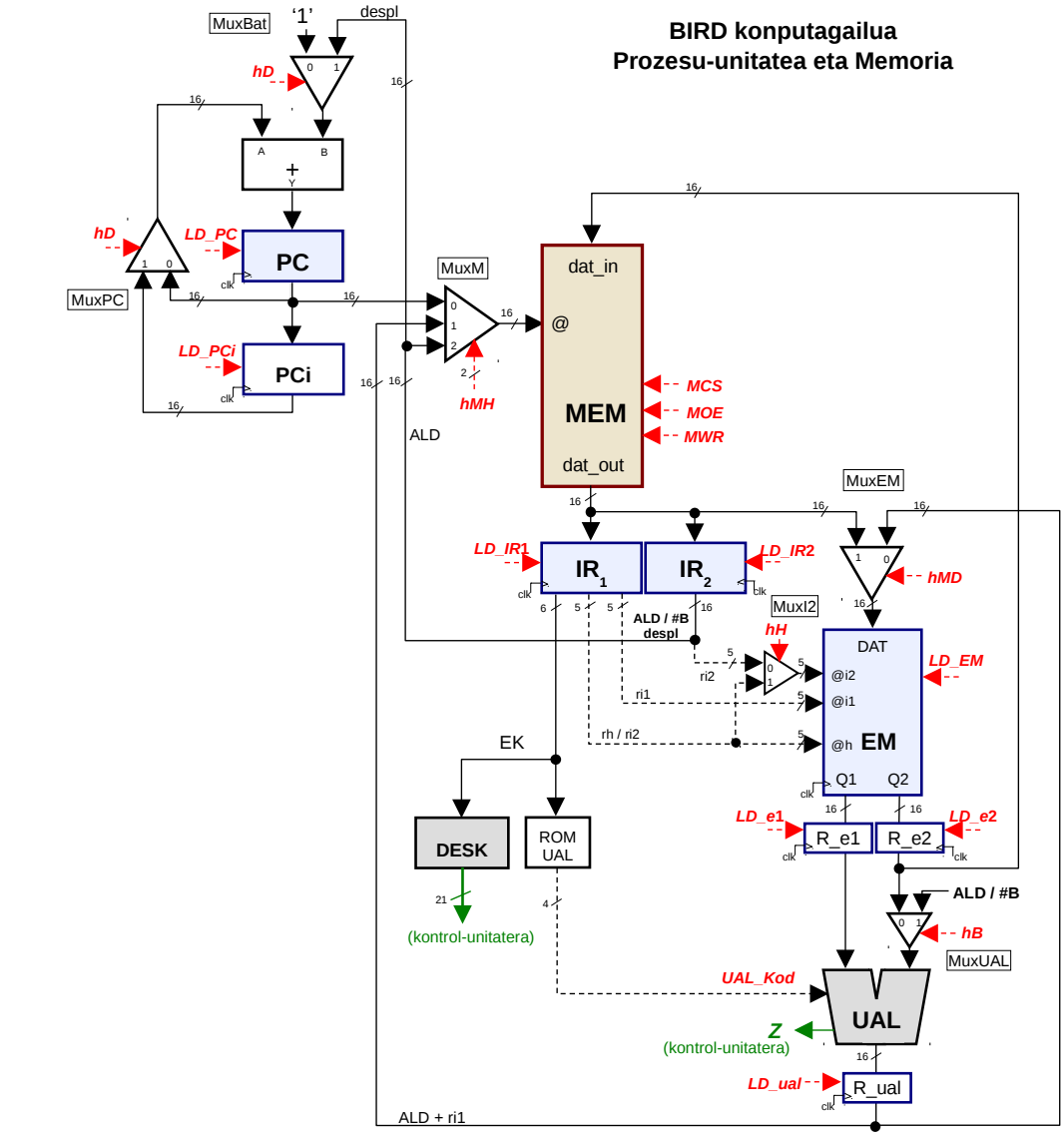
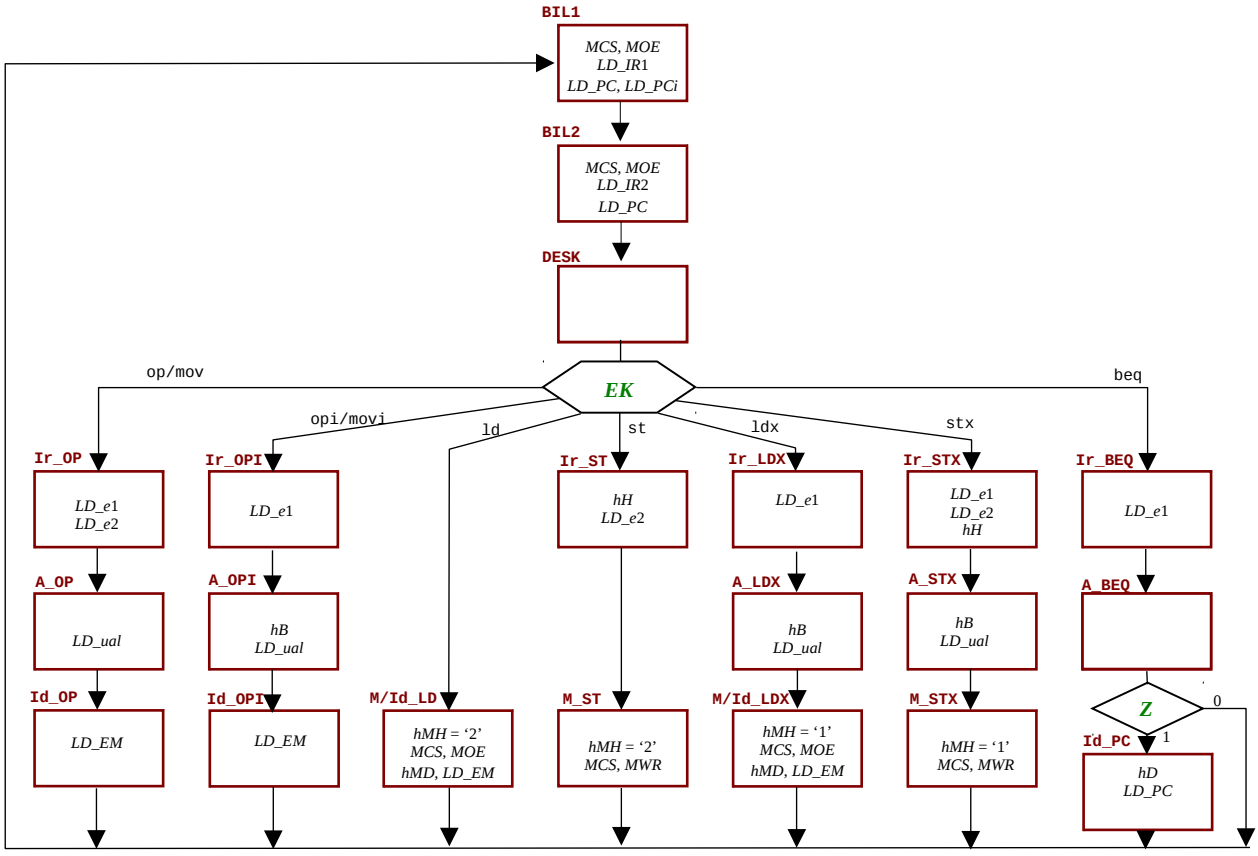
BIRD konputagailua
Prozesu-unitatea eta Memoria

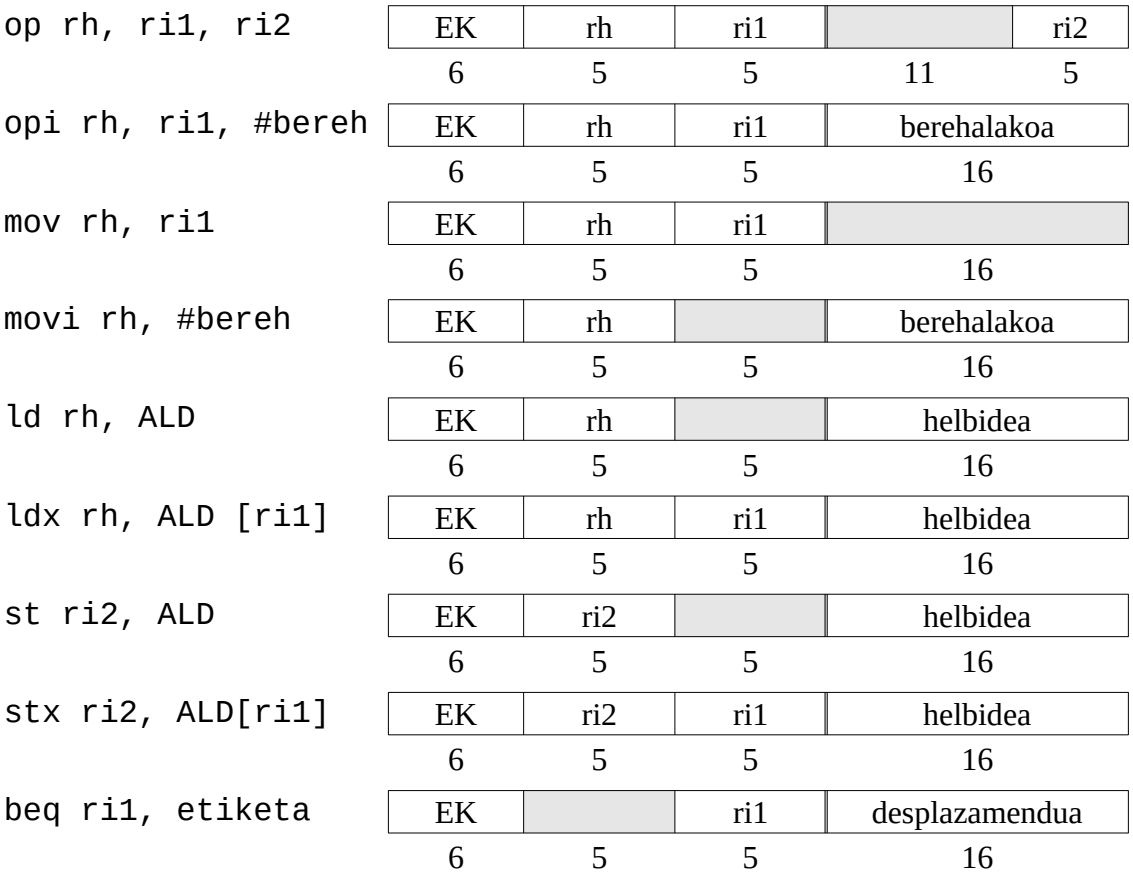




BIRD prozesadorearen UAL-aren eragiketa-kodeak:

Y	*UAL_Kod*	*Y*	*UAL_Kod*	*Y*	*UAL_Kod*
$A + B$	0000	A	0100	*A or B*	1000
$A - B$	0001	B	0101	*A and B*	1001
$A \times B$	0010	$A + 1$	0110	*A xor B*	1010
A / B	0011	$A - 1$	0111	Desp_ezk *A*	1011
				Desp_esk *A*	1100

BIRD prozesadorearen AGINDU-MULTZOA:



Aginduen eragiketa-kodeak (6 bitekoak):

`ld`↔ 0, `ldx`↔ 2, `st`↔ 3, `stx`↔ 5, `mov`↔ 7, `movi`↔ 8, `add`↔ 9, `addi`↔ 10, `sub`↔ 11, `subi`↔ 12, `mul`↔ 13, `muli`↔ 14, `div`↔ 15, `divi`↔ 16, `and`↔ 19, `andi`↔ 20, `or`↔ 21, `ori`↔ 22, `xor`↔ 23, `xori`↔ 24, `beq`↔ 26

PU of BIRD_euskara - Logisim-evolution v4.0.0

File Edit Project Simulate FPGA Window Help

Design Simulate

- BIRD_eusk...
  - PU
  - CU_microprogram
  - UAL
  - Erregistro_Multzoa
- Wiring
- Gates
- Plexers
- Arithmetic
- Memory
- Input/Output
- TTL
- TCL
- BFH mega function
- Input/Output Extra
- System On a Chip

Properties State

×1.3

Auto ... ...

BIRD Konputagailua

Exekuzioa eskuz debugeatzeko kontrola

Geldituu exekuzioa PCi-k balio hau jasotzean 0014

Jarraitu

PC PCi

MEM RAM 64K x 16

Memory Chip Select seinalea soberan dago simuladore honetan. Logisim-Evolution-en memonak ez duelako seinale honen beharrik

MUX EM MUX i2 MUX UAL

EM Erregistro_Multzoa

R_e1 R_e2 R_ual

UAL

IR1 IR2

ROM_UAL ROM 64 x 4

STATE CTR5

Jauzi_Taula ROM 64 x 5

BIRD prozesadorearen kontrol-unitate mikroprogramatua

CU_ROM ROM 32 x 25

BIRD prozesadorearen ASM kontrol-algoritmoa