

<b>ABBREVIATIONS</b>	<b>STANDS FOR</b>
<b>ENIAC</b>	<b>Electronic Numerical Integrator And Calculator</b>
<b>EDVAC</b>	<b>Electronic Discrete Variable Automatic Computer</b>
<b>EDSAC</b>	<b>Electronic Delay Storage Automatic Computer</b>
<b>UNIVAC</b>	<b>Universal Accounting Computer</b>
<b>CPU</b>	<b>Central Processing Unit</b>
<b>IC</b>	<b>Integrated Circuits</b>
<b>BASIC</b>	<b>Beginners All Purpose Symbolic Instructions Code</b>
<b>LSIC</b>	<b>Large Scale Integrated Circuits</b>
<b>VLSIC</b>	<b>Very Large Scale Integrated Circuits</b>
<b>AI</b>	<b>Artificial Intelligence</b>
<b>PC</b>	<b>Personal Computer</b>
<b>LAN</b>	<b>Local Area Network</b>
<b>WAN</b>	<b>Wide Area Network</b>
<b>ALU</b>	<b>Arithmetic Logical Unit</b>
<b>CU</b>	<b>Control Unit</b>
<b>RAM</b>	<b>Random Access Memory</b>
<b>SRAM</b>	<b>Static Random Access Memory</b>
<b>DRAM</b>	<b>Dynamic Random Access Memory</b>
<b>FPM-DRAM</b>	<b>Fast Page Mode Dynamic Random Access Memory</b>
<b>SDRAM</b>	<b>Synchronous Dynamic Random Access Memory</b>
<b>DDR-SDRAM</b>	<b>Double Data Rate Synchronous Dynamic Random Access Memory</b>
<b>DIMM</b>	<b>Dual In-Line Memory Module</b>
<b>SIMM</b>	<b>Single In-Line Memory Module</b>
<b>SODIMM</b>	<b>Small Out-Line Dual In-Line Memory Module</b>
<b>ROM</b>	<b>Read Only Memory</b>
<b>PROM</b>	<b>Programmable Read Only Memory</b>
<b>EPROM</b>	<b>Erasable Programmable Read Only Memory</b>
<b>EEPROM</b>	<b>Electrically Erasable Programmable Read Only Memory</b>
<b>CD</b>	<b>Compact Disk</b>
<b>MO</b>	<b>Magnetic Optical</b>
<b>PCI</b>	<b>Peripheral Component Interconnect</b>
<b>USB</b>	<b>Universal Serial Bus</b>
<b>AGP</b>	<b>Accelerated Graphic Port</b>
<b>ISA</b>	<b>Industry Standard Architecture</b>
<b>EISA</b>	<b>Extended Industry Standard Architecture</b>
<b>VLB</b>	<b>VESA Local Bus</b>
<b>VESA</b>	<b>Video Electronics Standards Association</b>
<b>NIC</b>	<b>Network Interface Card</b>

***HAWKAR OMER***