

Xander Yap

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About

I am a dedicated and detail-oriented undergraduate pursuing a degree in Information Technology and Electrical Engineering. My academic journey has helped me build a solid foundation, which I aim to leverage in advancing technology and its application in society.

Education

09/2022 - present	Bachelor of science, ETH Zürich <i>Department of Information Technology and Electrical Engineering</i> Electives: Introduction to machine learning, Embedded systems, Control Systems, VLSI 1: HDL Based design for FPGAs	Zürich, Switzerland
09/2015 - 05/2022	Secondary school (Abitur) <i>Thomas-Mann-Gymnasium</i> - Developed a memory game app using Android Studios	Munich, Germany

Study Projects

Group Project: The role of an external potential in a kernel charge equilibration

- Utilized a machine learning framework (q-pac) to model charge and force distributions of HfO_2
- Extended the framework by incorporating an external potential and study its effects
- Collaborated via GitLab and a remote server for model execution

FPGA in quantum computing with superconducting Qubits:

- Utilised a Red Pitaya to implement quantum signal processing using Xilinx and Verilog HDL

Memory Design: From Architecture down to Basic Cells:

- Analysed System Design, Circuit Design and Physical Design using Cadence

Professional Experience

11/2023 - 12/2023 ETH Zürich	Teaching assistant Led workshops with a fellow student to support first-year students in preparing for their exams	Zürich, Switzerland
01/2019 - 08/2022 Majestic GmbH	Gastronomy Provided general assistance as bartender, waiter and helped translate formal letters and documents	Munich, Germany
03/2019 - 03/2019 Wochenanzeiger	Internship Gained insights in customer service, public relations and logistics	Munich, Germany

Skills and interests

Languages:	English: Fluent, German: Native, Chinese: Native
Technical Skills:	Python (NumPy, Pandas, PyTorch, scikit-learn) C/C++ Git Assembly (RISC-V) Verilog/Systemverilog
Interests:	Electronics, Sports, Traveling, Cooking