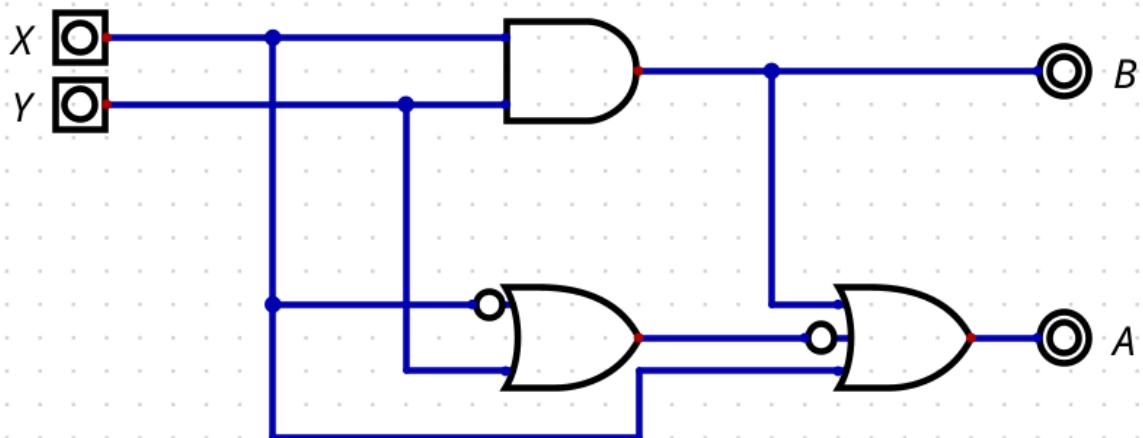
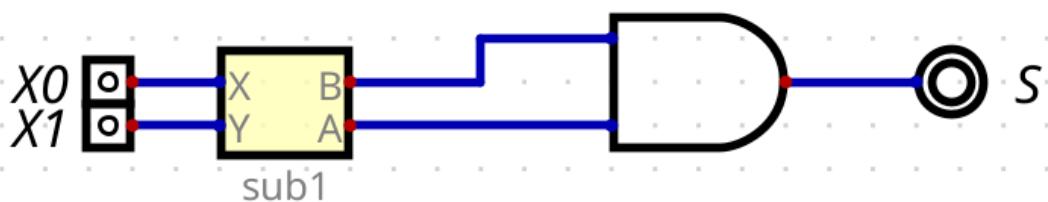


1-

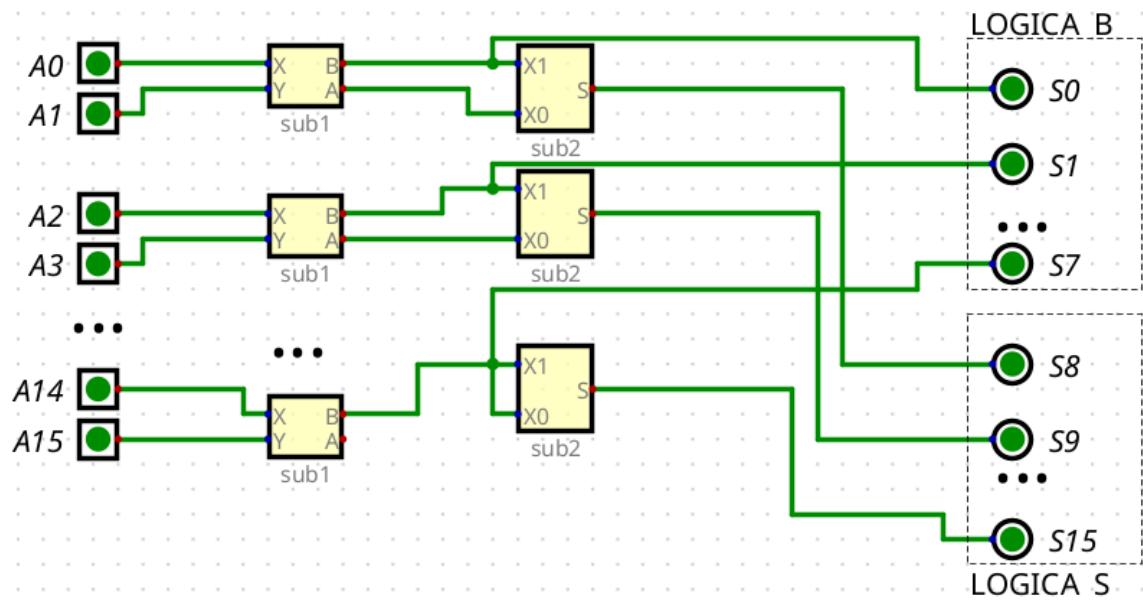
a) Implemente usando VHDL



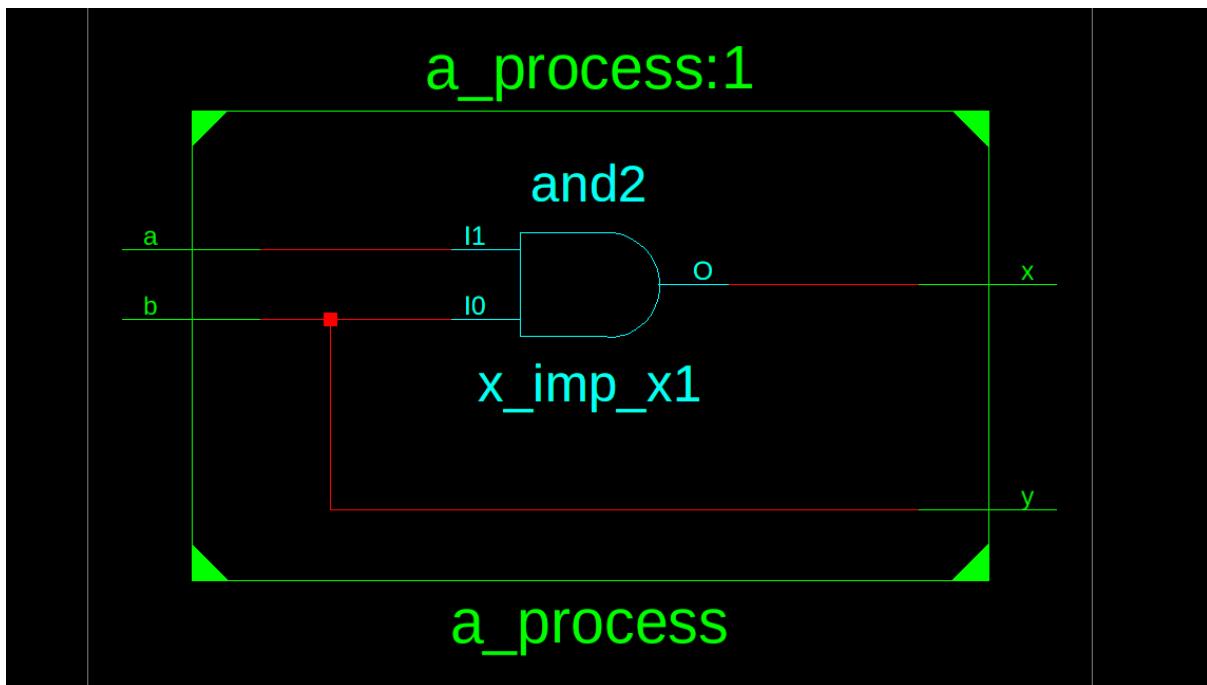
b) Implemente usando o circuito anterior



c) Implemente usando os dois últimos circuitos, depois crie um testbench colocando as entradas $a_{10}=1$ e $a_{11}=1$



2-



- a) implemente usando PROCESS
- b) configure na placa