fib_agc_top fub_seq_demux fub_rs232_rx fub_registerfile_ctrl data-raw-in RS232_RX strobe-in fub_flash fub-seq-in data-out data-in busy-out fub-data-out adress-out strobe-in fub-strobe-out busy-in busy-in fub-busy-in strobe-out adress-in fub-cfg-reg-in XOR_Err_o fub_fr_str_i timeout_Err_o flash_write_byte fub_fr_adr_i fub_fr_dat_o write_str_i CFG FLASH write_dat_i fub_f_busy_o write_busy_o fub_fw_str_i fub_seq_mux asdi_o fub_fw_adr_i fub_rs232_tx dclk_o fub_fw_dat_i **≺**RS232_TX data-raw-out fub fw strobe-out fub-seq-out busy-in data-out Flash RAM fub-data-in flash_read_byte data-in adress-out fub-strobe-in adress-in n_CS_o 、 busy_out read_str_o fub-busy-out busy-out strobe-out read_dat_o asdi_o fub-cfg-reg-out strobe-in read_busy_i dclk_o RAM_INTERFACE dclk_o dat_i normaly High dclk_o dat_i port A q_a | dat_i | adr_i | wr_en registerfile_ram_interface register-file port B SPI CH1 uC_Link_Connector (ADC Values) CONTROLLER PROCESS SPI CH2 Piggy_Connector (Channel Read Addresses)