

T-41-85

OPTOCOUPLER

Opto-isolator comprising an infrared emitting GaAs diode and a silicon npn Darlington phototransistor with accessible base. Plastic 6-lead dual-in line (DIL) envelope.

Features

- Very high output/input DC current transfer ratio
 - High isolation voltage of 3.12 kV RMS and 4.4 kV DC
 - Working voltage 2.5 kV DC
- A VDE and UL version is available; see CNX48U.

QUICK REFERENCE DATA

Diode

Continuous reverse voltage	V_R	max.	5 V
DC forward current (peak value); $t_p = 10 \mu s$; $\delta = 0.01$	I_F	max.	100 mA
Total power dissipation up to $T_{amb} = 25^\circ C$	P_{tot}	max.	200 mW

Transistor

Collector-emitter voltage (open base)	V_{CEO}	max.	30 V
Total power dissipation up to $T_{amb} = 25^\circ C$	P_{tot}	max.	200 mW

Optocoupler

Output/input DC current transfer ratio (CTR) $I_F = 1 \text{ mA}; V_{CE} = 1 \text{ V}; (I_B = 0)$	I_C/I_F	min.	5
Collector-cut-off current (dark) $V_{CC} = 10 \text{ V}; \text{working voltage} = 2.5 \text{ kV DC}$ diode: $I_F = 0$ (see also Fig. 2)	I_{CEW}	max.	1 μA

Isolation voltage

DC	V_{IORM}	min.	4.4 kV
AC (RMS value)			3.12 kV

MECHANICAL DATA

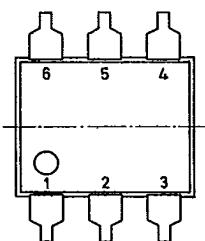
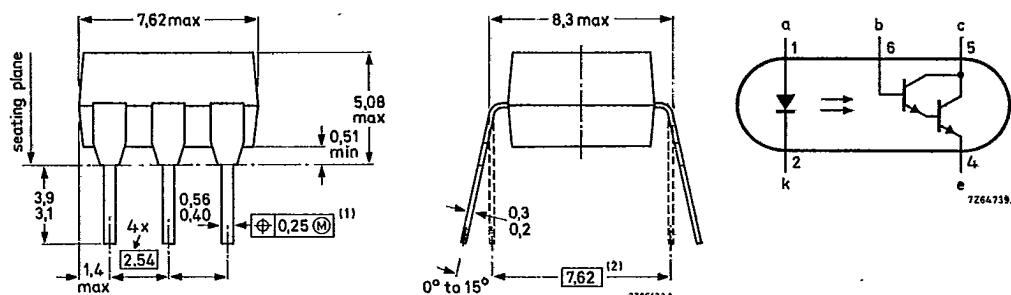
SOT90B (see Fig. 1).

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MECHANICAL DATA

Fig. 1 SOT90B.

Dimensions in mm



⊕ Positional accuracy.

(M) Maximum material condition.

(1) Centre-lines of all leads are within $\pm 0,125$ mm of the nominal position shown; in the worst case, the spacing between any two leads may deviate from nominal by 0,25 mm.

(2) When the leads are parallel, the tips are in position for automatic insertion.

RATINGS

Limiting values in accordance with the Absolute Maximum System (IEC 134)

Diode

Continuous reverse voltage

V_R max. 5 V

DC forward current

I_F max. 100 mA

(peak value); t_p = 10 μ s; δ = 0,01

I_{FRM} max. 3 A

Total power dissipation up to T_{amb} = 25 °C

P_{tot} max. 200 mW

Junction temperature

T_j max. 125 °C

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Transistor

Collector-emitter breakdown voltage $I_C = 1 \text{ mA}$	$V_{(\text{BR})\text{CEO}}$	min.	30 V
Collector-base breakdown voltage $I_C = 0.1 \text{ mA}$	$V_{(\text{BR})\text{CBO}}$	min.	30 V
Emitter-collector breakdown voltage $I_E = 0.1 \text{ mA}$	$V_{(\text{BR})\text{ECO}}$	min.	6 V
DC collector current	I_C	max.	100 mA
Total power dissipation up to $T_{\text{amb}} = 25^\circ\text{C}$	P_{tot}	max.	200 mW
Junction temperature	T_J	max.	125 °C

Optocoupler

Storage temperature range	T_{stg}	-55 to + 150 °C
Operating ambient temperature range	T_{amb}	-40 to + 100 °C
Lead soldering temperature up to the seating plane; $t_{\text{slid}} < 10 \text{ s}$	T_{slid}	max. 260 °C

THERMAL RESISTANCE

From junction to ambient in free air diode and transistor	$R_{\text{th j-a}}$	=	500 K/W
From junction to ambient, device mounted on a printed-circuit board diode and transistor	$R_{\text{th j-a}}$	=	400 K/W

ISOLATION RELATED VALUES

External air gap (clearance) input terminals to output terminals	$L(\text{IO1})$	min.	7.2 mm
External tracking path (creepage distance) input terminals to output terminals	$L(\text{IO2})$	min.	7.0 mm
Tracking resistance (KB value)			KB-100/A
Internal plastic gap (clearance) isolation thickness between emitter and receiver		min.	1 mm

CHARACTERISTICS $T_J = 25^\circ\text{C}$ unless otherwise specified**Diode**

Forward voltage $I_F = 10 \text{ mA}$	V_F	typ. max.	1.15 V 1.3 V
Reverse current $V_R = 5 \text{ V}$	I_R	max.	10 μA

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Transistor ($I_F = 0$)

Collector cut-off current (dark)

 $V_{CE} = 10 \text{ V}$ I_{CEO} typ. 20 nA $V_{CB} = 10 \text{ V}$ I_{CEO} max. 100 nACollector-emitter breakdown voltage
at $I_C = 1 \text{ mA}$ $V_{(BR)CEO}$ min. 30 VCollector-base breakdown voltage
at $I_C = 0.1 \text{ mA}$ $V_{(BR)CBO}$ min. 30 VEmitter-base breakdown voltage
at $I_E = 0.1 \text{ mA}$ $V_{(BR)ECO}$ min. 6 VOptocoupler ($I_B = 0$) (note 1)

Output/input DC current transfer ratio (CTR)

 $I_F = 0.5 \text{ mA}; V_{CE} = 1 \text{ V}$ I_C/I_F min. 3.5 $I_F = 1.0 \text{ mA}; V_{CE} = 1 \text{ V}$ I_C/I_F min. 5 $I_F = 10 \text{ mA}; V_{CE} = 1 \text{ V}$ I_C/I_F min. 6

Collector cut-off current (dark); see Fig. 2 (note 2)

 $V_{CC} = 10 \text{ V}; \text{working voltage} = 2.5 \text{ kV DC}$ I_{CEW} max. 1 μA $V_{CC} = 10 \text{ V}; \text{working voltage} = 2.5 \text{ kV DC}; T_j = 70^\circ\text{C}$ I_{CEW} max. 1000 μA

Collector-emitter saturation voltage

 $I_F = 5 \text{ mA}; I_C = 10 \text{ mA}$ V_{CEsat} max. 1 V

Isolation voltage (note 3)

 $t = 1 \text{ min}$

DC

AC (RMS value)

 V_{IORM} min. 4.4 kV
3.12 kVCollector capacitance at $f = 1 \text{ MHz}$ $I_E = I_e = 0; V_{CB} = 10 \text{ V}$ $C_{b'c}$ typ. 4.5 pF

Capacitance between input and output

 $I_F = 0; V = 0; f = 1 \text{ MHz}$ C_{io} typ. 0.6 pF

Insulation resistance between input and output

 $\pm V_{IO} = 1 \text{ kV}$ R_{IO} min. 10 G Ω typ. 1 T Ω

Switching times (see Figs 3 and 4)

 $I_{Fon} = 10 \text{ mA}; V_{CC} = 5 \text{ V}; R_E = 100 \Omega; R_{BE} = 1 \text{ M}\Omega$ t_{on} typ. 5 μs t_{off} typ. 30 μs $I_{Fon} = 1 \text{ mA}; V_{CC} = 5 \text{ V}; R_E = 1 \text{ k}\Omega; R_{BE} = 10 \text{ M}\Omega$ t_{on} typ. 50 μs t_{off} typ. 250 μs

Notes

- Where the phototransistor receives light from the diode the O (for open base) has been omitted from the symbols.
- As quality assurance (on a sample basis), these parameters are covered by a 1000 h reliability test.
- Every single product is tested by applying an isolation test voltage of 3750 V AC (RMS) for 2 seconds between the shorted input (diode) leads and the shorted output (phototransistor) leads.

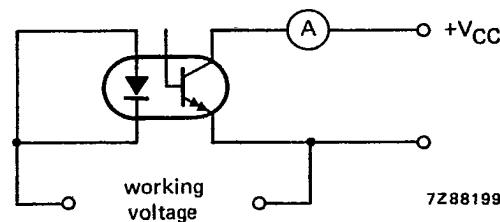


Fig. 2.

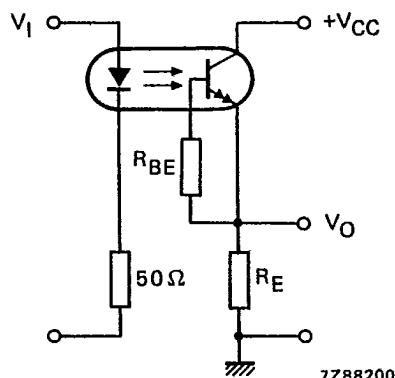


Fig. 3 Switching circuit.

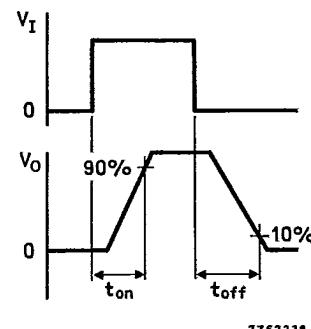


Fig. 4 Waveforms.

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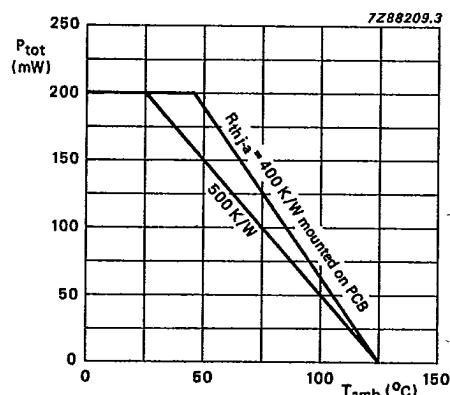


Fig. 5 Power derating curve for diode and transistor as a function of temperature.

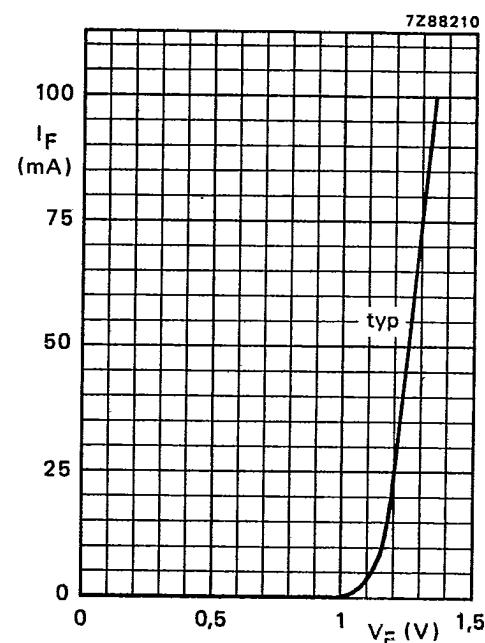
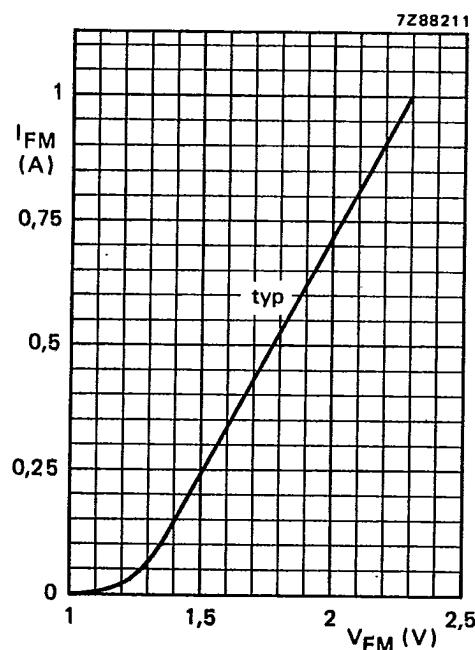
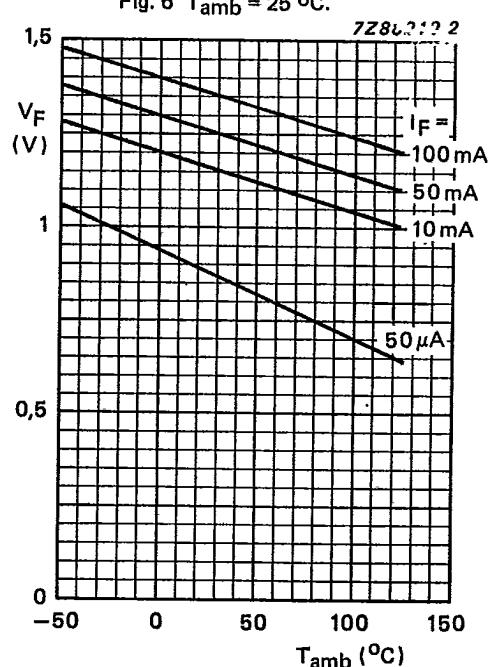
Fig. 6 $T_{amb} = 25 \text{ }^{\circ}\text{C}$.Fig. 7 $T_{amb} = 25 \text{ }^{\circ}\text{C}$; $t_p = 10 \mu\text{s}$; $\delta = 0.01$.

Fig. 8 Typical values.

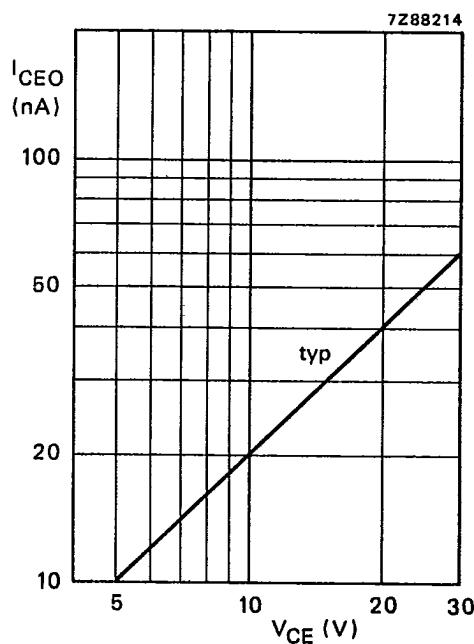
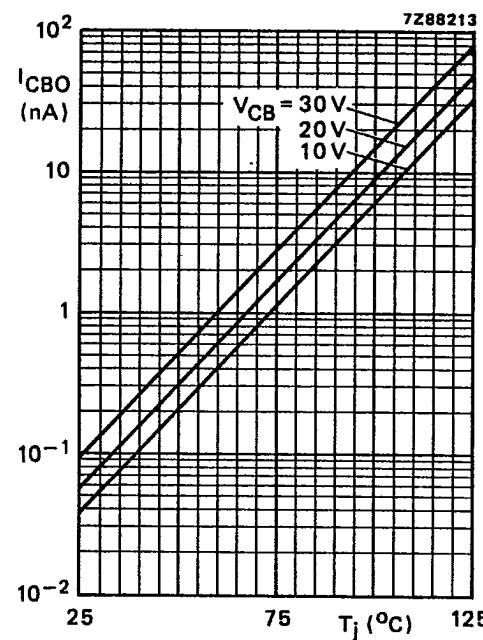
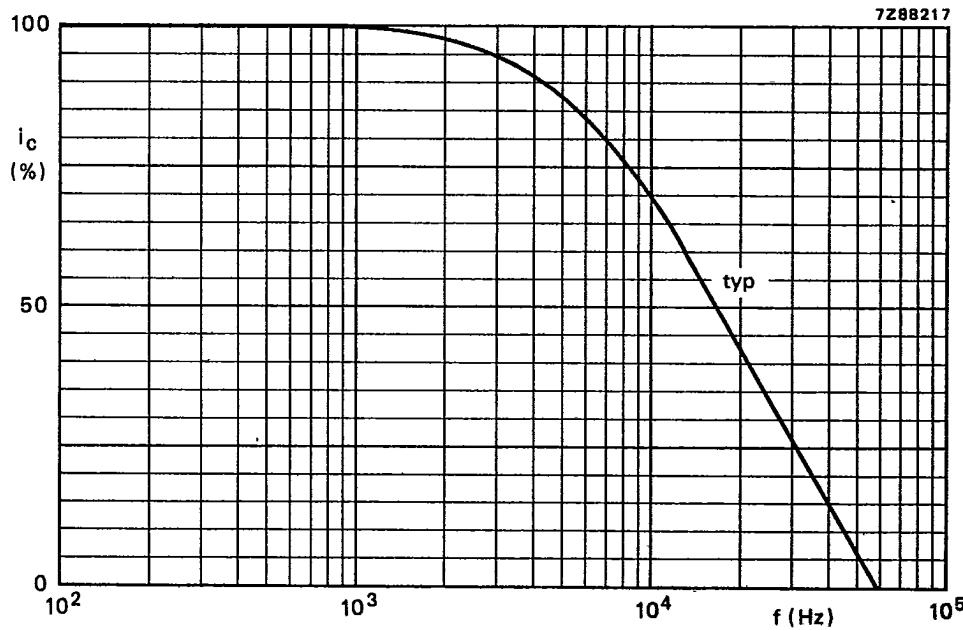
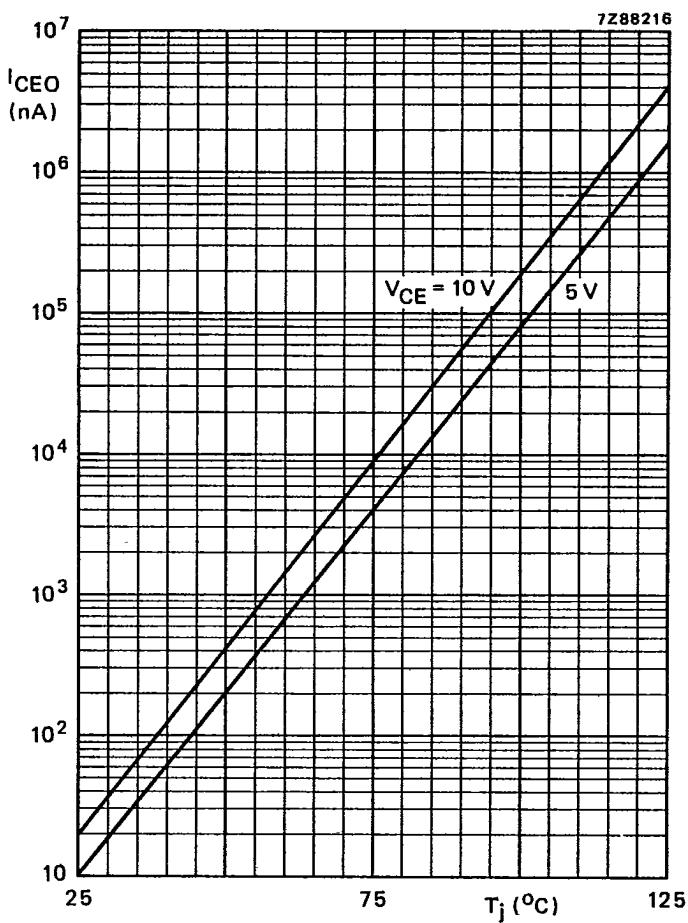
Fig. 9 $I_F = 0$; $T_j = 25^\circ\text{C}$.

Fig. 10 Typical values.

Fig. 11 $i_C = 10 \text{ mA}$; $V_{CC} = 5 \text{ V}$; $R_E = 100 \Omega$; $R_{BE} = 1 \text{ M}\Omega$; see also Fig. 4.

Fig. 12 $I_F = 0$; typical values.

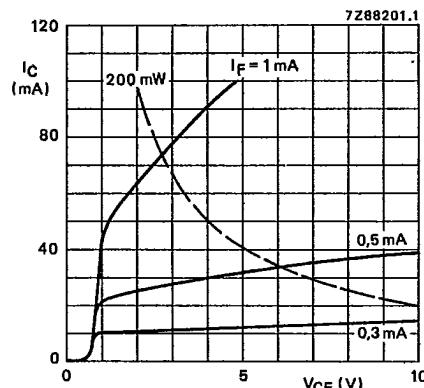


Fig. 13 Typical values; $T_{amb} = 25^\circ\text{C}$.

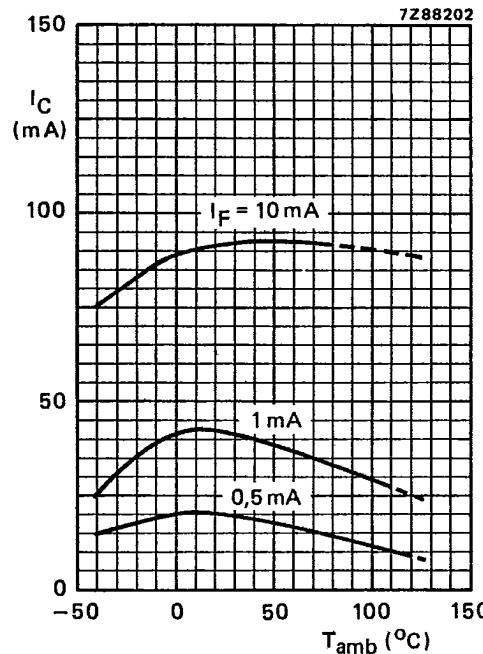


Fig. 14 Typical values; $I_B = 0$; $V_{CE} = 1 \text{ V}$.

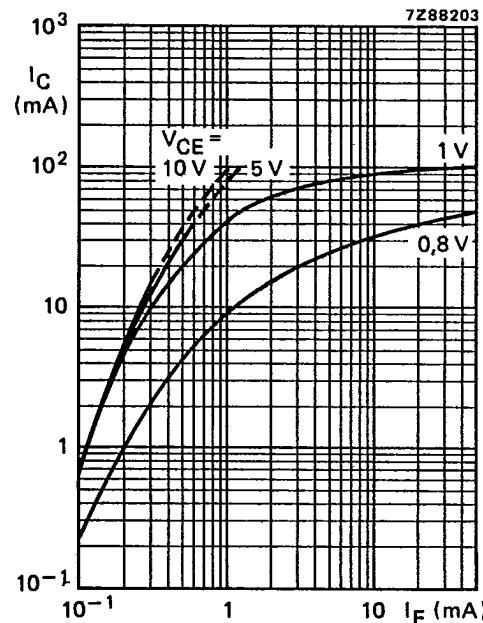


Fig. 15 Typical values; $I_B = 0$; $T_{amb} = 25^\circ\text{C}$.

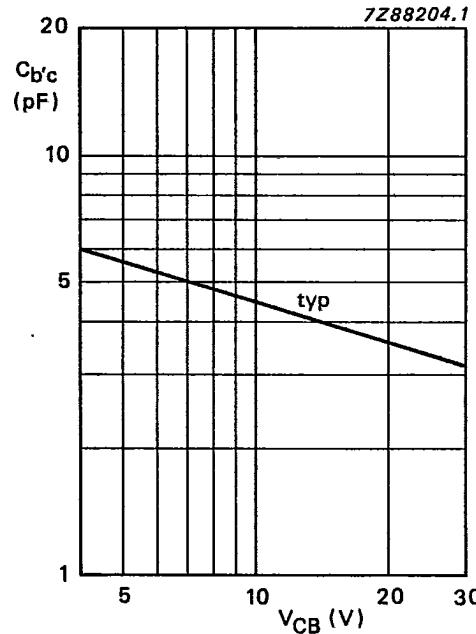
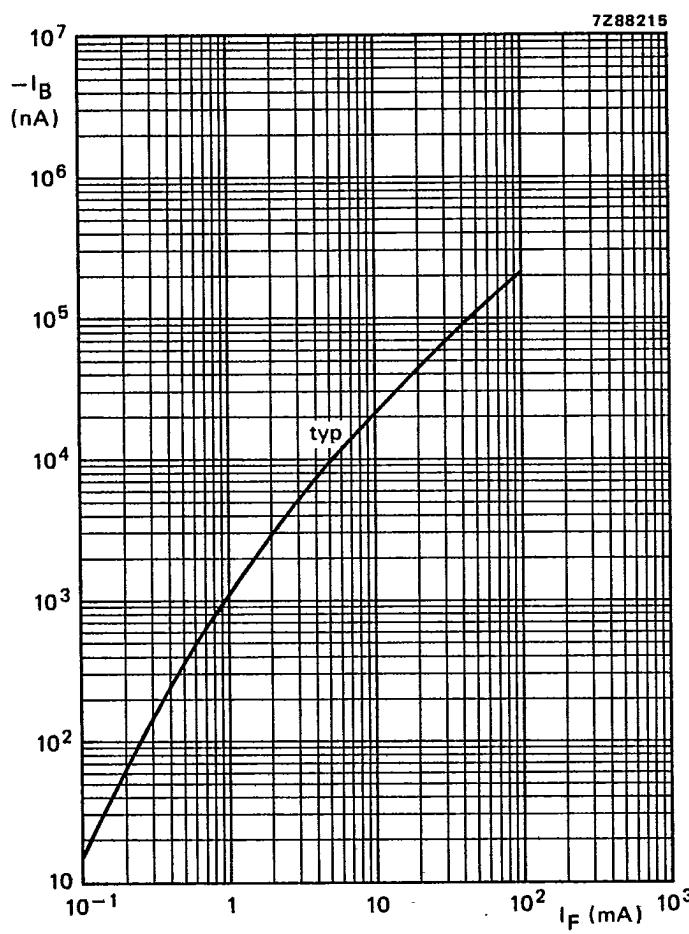


Fig. 16 $I_E = I_e = 0$; $f = 1 \text{ MHz}$; $T_{amb} = 25^\circ\text{C}$.

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Fig. 17 $I_E = 0$; $V_{CB} = 5$ V; $T_{amb} = 25$ °C.

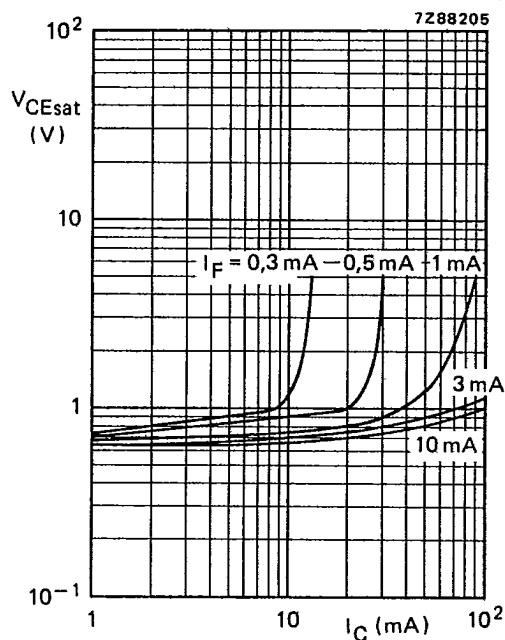


Fig. 18 Typical values; $I_B = 0$; $T_{amb} = 25^\circ\text{C}$.

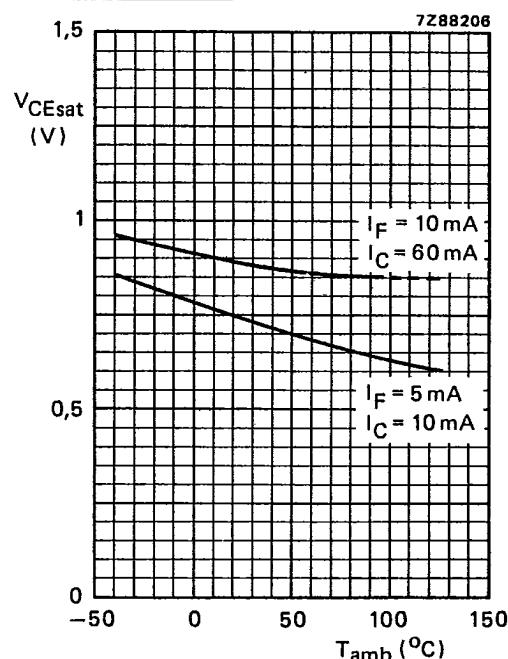


Fig. 19 Typical values; $I_B = 0$.

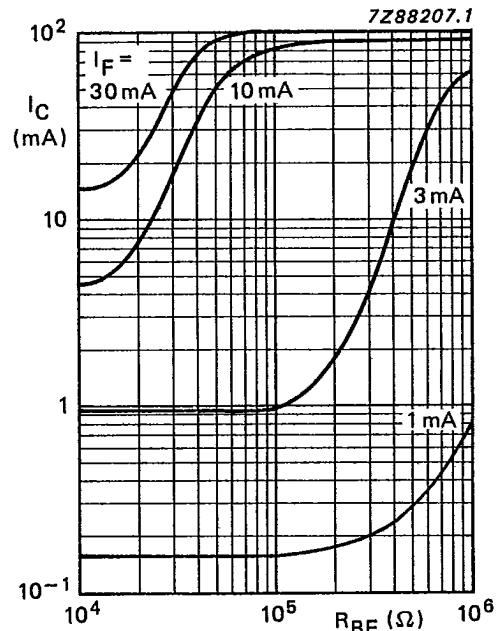


Fig. 20 Typ. values; $V_{CE} = 1 \text{ V}$; $T_{amb} = 25^\circ\text{C}$.

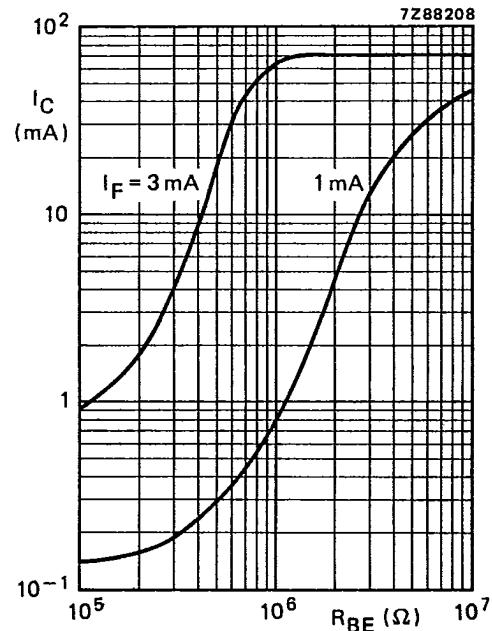


Fig. 21 Typ. values; $V_{CE} = 1 \text{ V}$; $T_{amb} = 25^\circ\text{C}$.