Communication between 1756-L62 and 5069-SERIAL

Contents

1.	L62 controller side	.1
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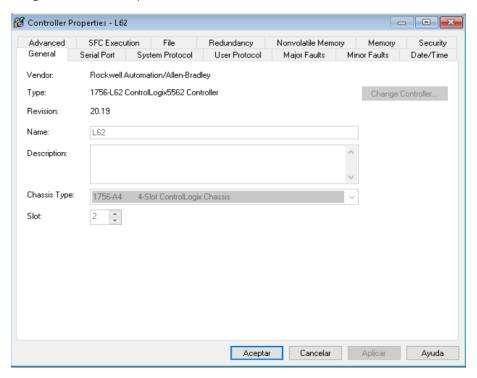
1. L62 controller side

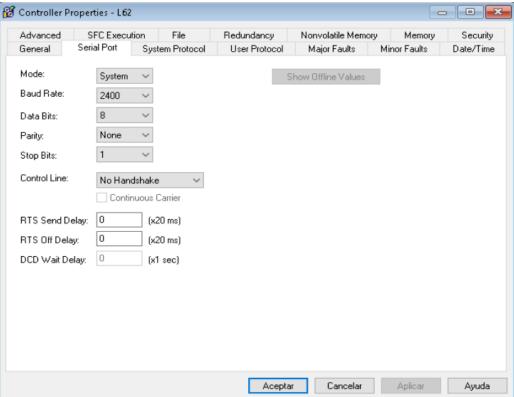


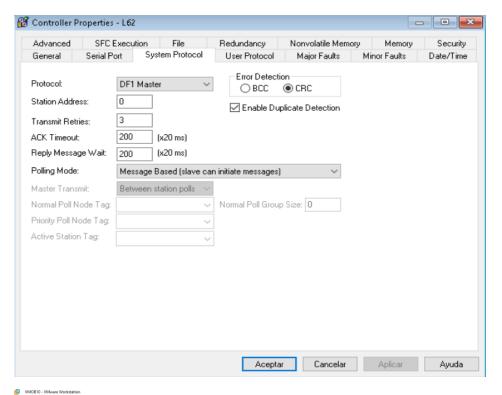
Slot 2

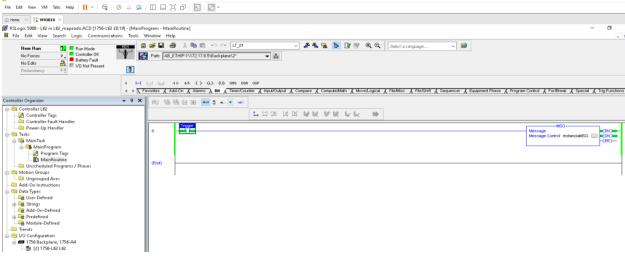
Firmware: 20.019

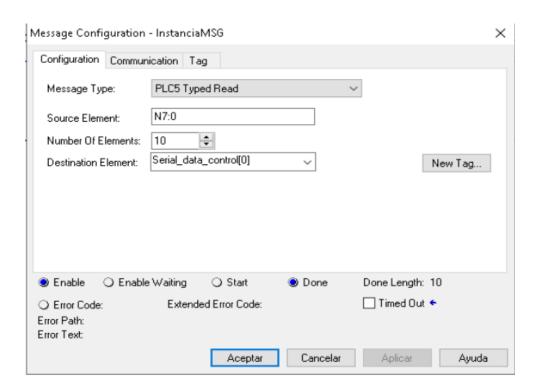
Programm: L62_mapeado.ACD



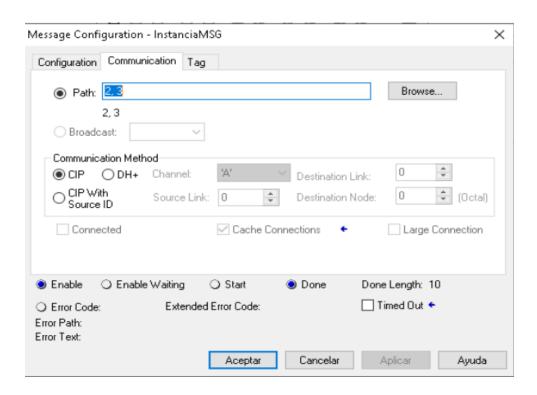








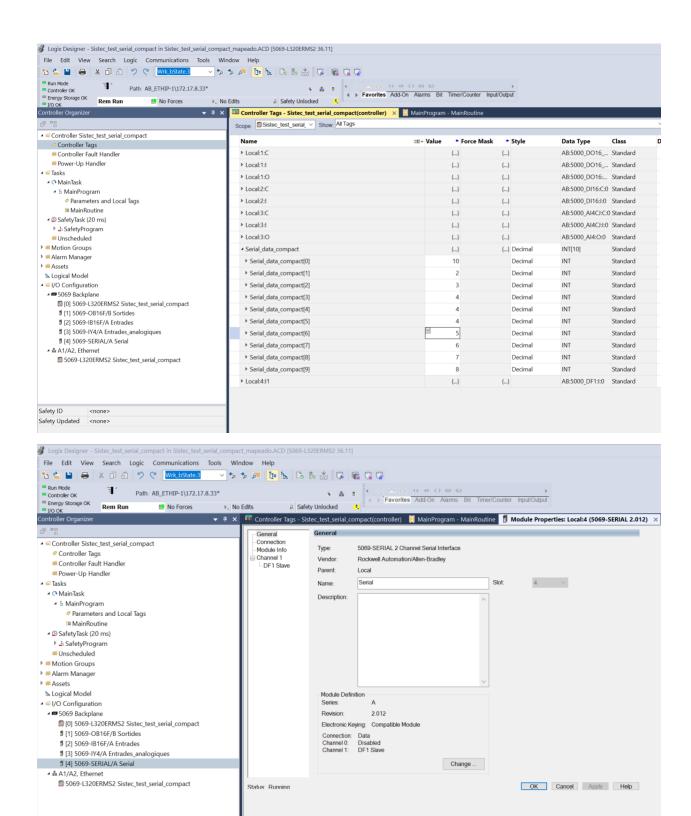
Path, salida por Puerto frontal RS232=2, estación 3

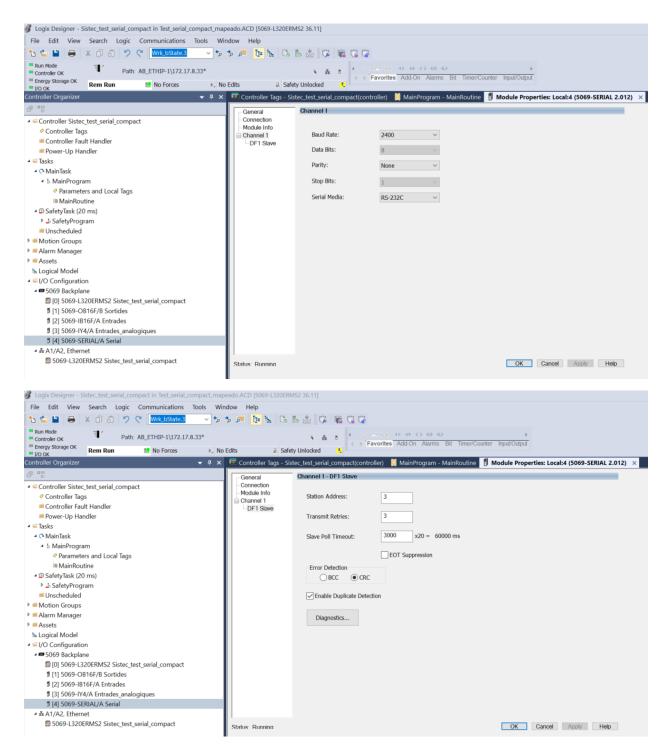


2. Compactlogix 5069-SERIAL side

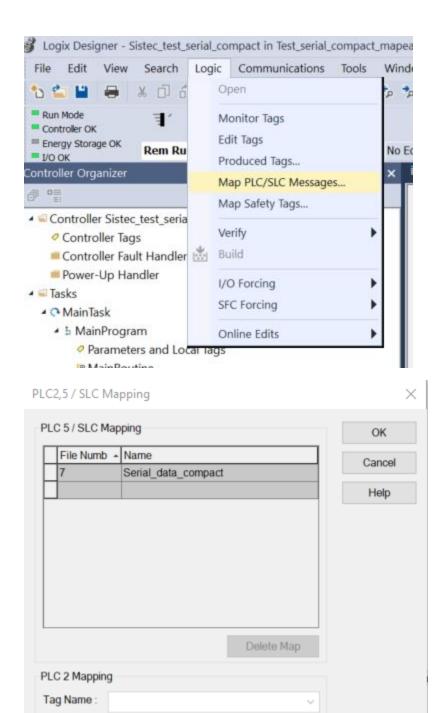


Programm: Test_serial_compact_mapeado

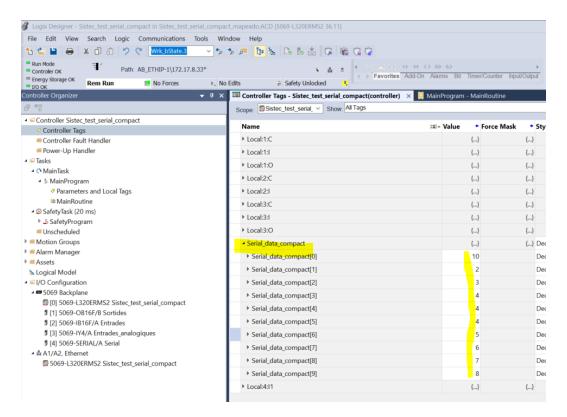




Memory Mapping to N7:0

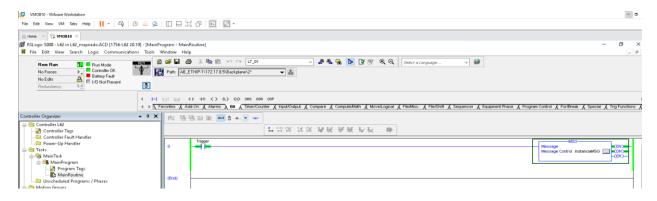


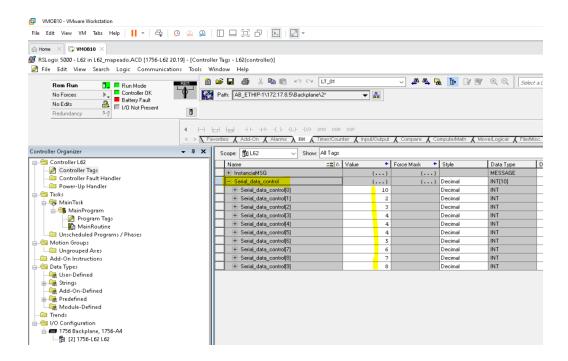
Voilà



Toggle bit

And wait until done



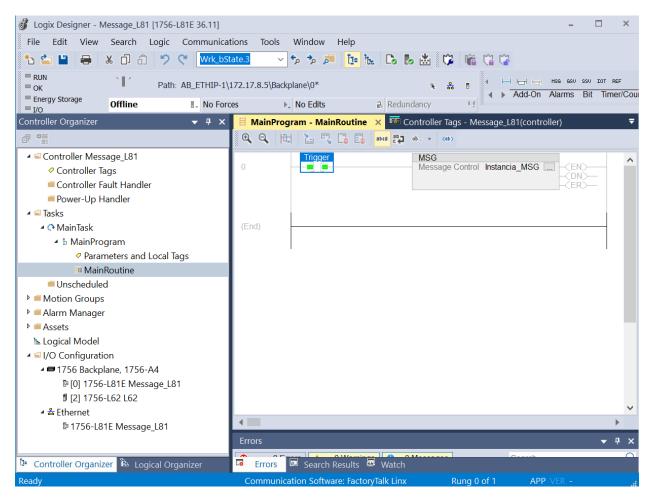


3. Routing from L84 thru backpanel

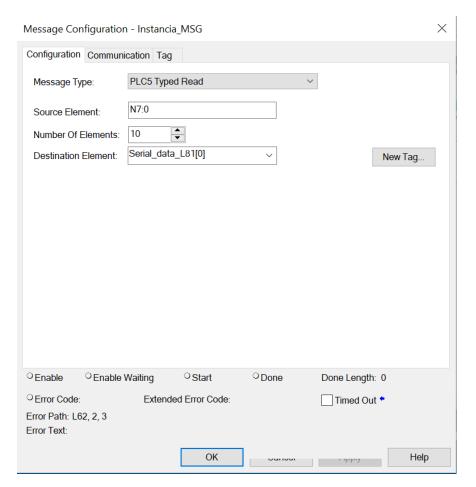
Let's execute the MSG instruction on the L81 processor on Slot 0



Keep same programs in RUN mode on the L62 and on the 5059 processors

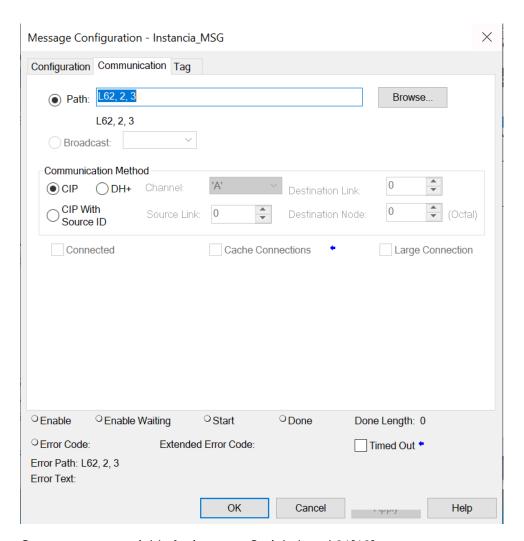


Configure MSG instruction this way



Path is 1,2,2,3

(outputting thru backplane, L62 slot position, outputting thru front connector, numer of serial remote station)



Create an array variable for instance Serial_data_L81[10]

Voila

