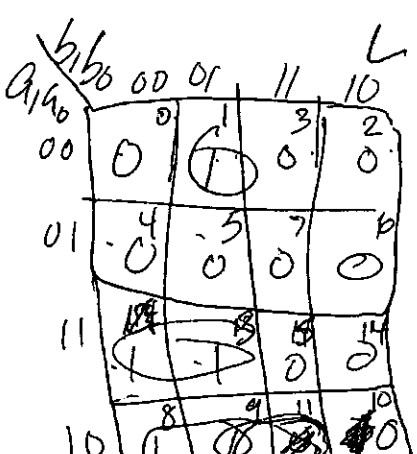
Xavier Richardson

ProLab Q1 Signed

L (A<B) MUX



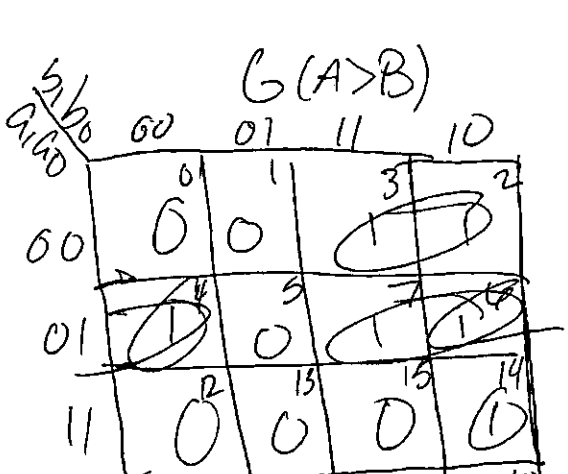
$Less\ 0 = \bar{b}_1 b_0$

$Less\ 1 = 0$

$Less\ 3 = b_0 + \bar{b}_1$

$Less\ 2 = \bar{b}_1$

G (A>B)



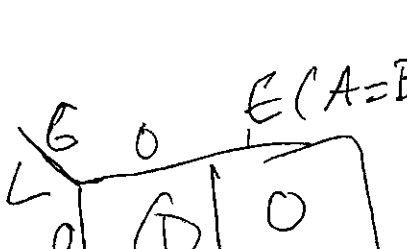
$Great\ 0 = b_1$

$Great\ 1 = b_1 + \bar{b}_0$

$Great\ 3 = b_1 b_0$

$Great\ 2 = 0$

E (A=B)



$EQ_1 = L \oplus G$

ECE 37000: DIGITAL SYSTEMS – LOGIC DESIGN
FALL 2017

LAB # 3

LAB DAY/TIME: Wed. 8:00 AM

LAB TITLE: Structual Modeling of Mux-based Combinational Logic Networks

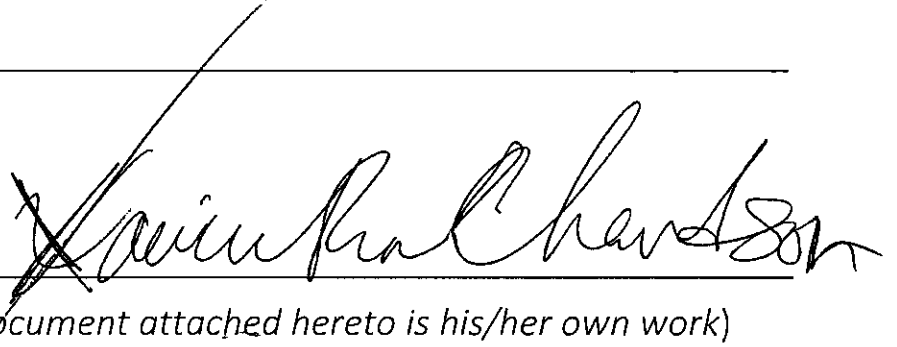
Due Date: 10-16-17

DEMO SIGNATURE: 

STUDENT NAME: Xavier Richardson

PUID: 0267870080

COLLABORATOR NAMES

STUDENT SIGNATURE: 

(Student attests that document attached hereto is his/her own work)

Signed

Xavier Richardson

Prelab 3 Q1

a1	a0	b1	b0	L(A<B)	E(A=B)	G(A>B)
0	0	0	0		*0*	*1*	*0*
0	0	0	1		*1*	*0*	*0*
0	0	1	0		*0*	*0*	*1*
0	0	1	1		*0*	*0*	*1*
0	1	0	0		*0*	*0*	*1*
0	1	0	1		*0*	*1*	*0*
0	1	1	0		*0*	*0*	*1*
0	1	1	1		*0*	*0*	*1*
1	0	0	0		*1*	*0*	*0*
1	0	0	1		*1*	*0*	*0*
1	0	1	0		*0*	*1*	*0*
1	0	1	1		*1*	*0*	*0*
1	1	0	0		*1*	*0*	*0*
1	1	0	1		*1*	*0*	*0*
1	1	1	0		*0*	*0*	*1*
1	1	1	1		*0*	*1*	*0*