

MATCHING  $\mu$ -LOGIC

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BY

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DISSERTATION

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## **Abstract**

We present matching  $\mu$ -logic, which is a unifying logic for specifying and reasoning about programs and programming languages. Matching  $\mu$ -logic uses its formulas, called patterns, to uniformly express programs' static structures, dynamic behaviors, and logical constraints. Programming languages can be formally defined as matching  $\mu$ -logic theories, which include patterns as axioms. The correctness of programming language implementations and tools can be proved using a fixed proof system. These proofs can be encoded as proof objects and automatically checked using a small proof checker.

An important feature of matching  $\mu$ -logic is its  $\mu$  operator, which provides direct support for specifying fixpoints and thus enables to specify and reason about induction and recursion.

We study the proof theory of matching  $\mu$ -logic and prove a few important completeness results. We study the expressive power of matching  $\mu$ -logic and show that many important logics, calculi, and foundations of computations, especially those featuring fixpoints/induction/recursion, can be defined as matching  $\mu$ -logic theories.

We study automated reasoning for matching  $\mu$ -logic, with a focus on fixpoint reasoning. We propose a set of high-level automated proof rules that can be applied to many matching  $\mu$ -logic theories, and thus enable automated reasoning in them.

We propose applicative matching  $\mu$ -logic, abbreviated as AML, as a simple instance of matching  $\mu$ -logic that remains all of its expressive power. AML is obtained by eliminating the features of matching  $\mu$ -logic that are definable using the other more basic and primitive infrastructure. We present an encoding of matching  $\mu$ -logic into AML and implement a 240-line proof checker for AML using Metamath.

We study proof-certifying program execution and formal verification, where the correctness of an execution/verification task is established by an AML proof object, serving as the machine-checkable correctness certificate. Our approach is based on the  $\mathbb{K}$  formal language semantics framework. We design and implement procedures that output AML proof objects for the language-agnostic program interpreter and formal verifier of  $\mathbb{K}$ , which are parametric in the formal semantics of a programming language. This way, the correctness of program execution or formal verification is reduced to checking the corresponding AML proof objects using the proof checker.

We hope to demonstrate that matching  $\mu$ -logic can serve as a unifying foundation for programming, where programming languages are defined as theories, and the correctness of language implementations and tools is established by machine-checkable proof objects.

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*To my parents.*

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## Chapter 1: INTRODUCTION

Unlike natural languages that allow vagueness and ambiguity, programming languages must be precise and unambiguous. Only with rigorous definitions of programming languages, called their formal semantics, can we guarantee the reliability, safety, and security of computing systems. Our vision is thus a unifying programming language framework based on the formal semantics of programming languages, as shown in Figure 1.1. In such an ideal language framework, language designers only need to define the formal semantics of their languages. All the implementations and tools of a given programming language are automatically generated from the formal semantics of the language by the framework.

A unifying language framework requires a unifying logical foundation, where the formal semantics of programming languages are defined as logical theories, consisting of logical formulas as axioms that specify the behaviors of programs. The correctness of language implementations and tools can be proved using a fixed formal proof system. These formal proofs can be encoded as proof objects and checked by a proof checker. A unifying language framework with a unifying logical foundation allows us to reduce the correctness of computation and programming in general to something as simple as proof checking.

Previous work has pursued the above vision with the  $\mathbb{K}$  framework [1] and matching logic [2].  $\mathbb{K}$  is a rewrite-based language framework that allows to define the formal semantics of programming languages using configurations and rewrite rules. From the formal semantics of any given programming language,  $\mathbb{K}$  automatically generates a set of language tools, including a parser, a program interpreter, a formal verifier, and a program equivalence checker [3, 4].  $\mathbb{K}$  has been used to define the complete executable formal semantics of many large languages, such as C [5], Java [6], JavaScript [7], Python [8], Ethereum virtual machines bytecode [9], and x86-64 [10]. Matching logic has served as the logical foundation for the static aspects of  $\mathbb{K}$ . The core of matching logic is a notion of its formulas called *patterns*, which can be used to uniformly specify and reason about program configurations and logical constraints.

However, matching logic has two major limitations that prevent it from being able to serve as the unifying logical foundation for programming. The first limitation is the lack of a universal proof system that supports formal reasoning in all matching logic theories. The known matching logic proof system  $\mathcal{P}$  proposed in [2] is not universal because it only supports formal reasoning in a subset of theories that feature *definedness*—a mathematical instrument that can be used to define equality. If the underlying theory does not feature definedness,  $\mathcal{P}$  cannot be used to do formal reasoning in it.

The second limitation of matching  $\mu$ -logic is the lack of ability to specify and reason about

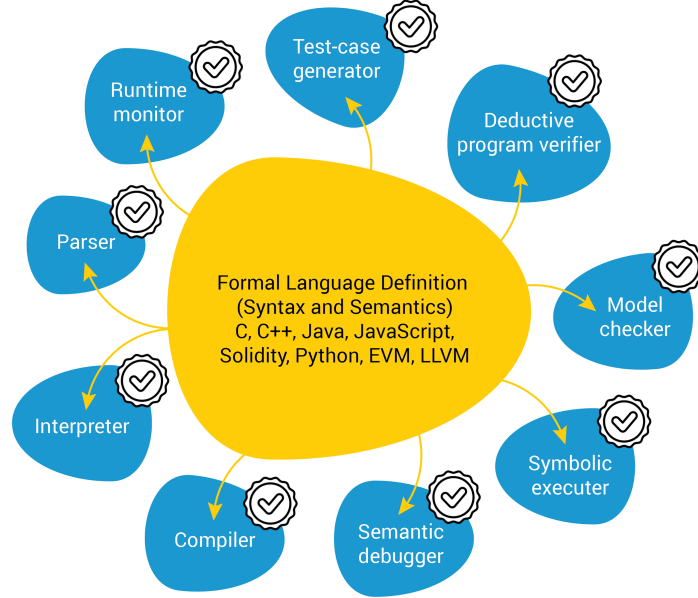


Figure 1.1: Vision of a Unifying Programming Language Framework

fixpoints. Fixpoints are ubiquitous and unavoidable in computer science. Without a direct support for fixpoints, matching logic is insufficient for dealing with topics such as inductive datatypes, induction principles, temporal properties about programs, or formal verification. To handle these fixpoints-related topics, one has to defer them outside matching logic to some other logics or frameworks such as Coq [11], or extend matching logic with additional infrastructure for fixpoints. For example, matching logic has been extended to reachability logic [12] that provides additional coinduction-based proof rules for formal verification.

This work addresses the above two limitations of matching logic. For the first limitation, we propose a new proof system  $\mathcal{H}$  for matching logic that is universal and works with all theories. We show that  $\mathcal{H}$  is sound for all theories, meaning that if a pattern (i.e., a matching logic formula) is provable using  $\mathcal{H}$  in a given theory, then it holds in that theory. The other direction is known as the completeness of  $\mathcal{H}$ . While we do not know whether  $\mathcal{H}$  is complete for all theories, we present two important completeness results. The first is the definedness completeness theorem (Theorem 3.8) which states that  $\mathcal{H}$  is complete for every theory that features definedness. The second is the local completeness theorem (Theorem 3.14), which states that  $\mathcal{H}$  is complete for the empty theory. We present proof system  $\mathcal{H}$  in Chapter 3.

For the second limitation, we extend matching logic to matching  $\mu$ -logic, by adding a  $\mu$  operator that builds least fixpoints. Greatest fixpoints are definable using least fixpoints. We also extend the proof system  $\mathcal{H}$  to  $\mathcal{H}_\mu$ , which has two proof rules dedicated to fixpoint reasoning, inspired from the Knaster-Tarski fixpoint theorem (Theorem 2.1). This way,



matching  $\mu$ -logic can specify and reason about fixpoints in a principled way. We present matching  $\mu$ -logic and its proof system  $\mathcal{H}_\mu$  in Chapter 4.

We then proceed to study the expressive power of matching  $\mu$ -logic. We show that many important logics, calculi, and foundations of computations, especially those featuring fixpoints, can be defined as matching  $\mu$ -logic theories. These includes FOL with least fixpoints, initial algebra semantics, separation logic with recursive predicates, modal  $\mu$ -calculus, various temporal logics, dynamic logic, reachability logic,  $\lambda$ -calculus, and type systems. We present the above results about the expressive power of matching  $\mu$ -logic in Chapter 5.

We study automated reasoning for matching  $\mu$ -logic with a focus on fixpoint reasoning. We propose a unifying proof framework that consists of high-level proof rules that are derivable using the proof system  $\mathcal{H}_\mu$ . Automated reasoning becomes proof search over the proposed high-level proof rules, with heuristics guiding the proof search for better performance. We present the above unifying proof framework based on matching  $\mu$ -logic in Chapter 6.

We propose and study applicative matching  $\mu$ -logic, abbreviated as AML, which is a simple instance of matching  $\mu$ -logic that remains all of its expressive power. AML is obtained by eliminating the features of matching  $\mu$ -logic that are definable using the other more basic and primitive infrastructure. For example, sorts in matching  $\mu$ -logic are definable so they are eliminated in AML. We present an encoding of matching  $\mu$ -logic into AML. We implement a proof checker for AML using Metamath [13] in 240 lines of code. AML proofs can be encoded as proof objects and checked by the 240-line proof checker, which serves as a small trust base of checking any AML proofs. We present AML and the proof checker in Chapter 7.

Finally, we put everything together and study proof-certifying program execution and formal verification, based on the  $\mathbb{K}$  framework. We implement proof generation procedures for the program interpreter and the formal verifier of  $\mathbb{K}$ , which are parametric in the formal semantics of a programming language. The proof generation procedures output AML proof objects as correctness certificates for the said interpreter and verifier. This way, we reduce the correctness of program execution and formal verification to checking the corresponding AML proof objects. We discuss proof-certifying program execution in Chapter 8 and proof-certifying formal verification in Chapter 9.

The vision of a unifying language framework and a unifying logic foundation for programming is a grand one. Related study started in the 1960s, with the proposal of various formal semantics notions and styles [14, 15, 16, 17, 18, 19, 20]. After more than half a century of research on the topic, great progress has been made in terms of the scalability, usability, robustness, popularity, reusability, and trustworthiness of semantics-based language tools, moving us closer to realizing the above vision, which we believe, with evidence present in this work, is in within our reach in the near future with matching  $\mu$ -logic.

## Chapter 2: PRELIMINARIES

This preliminary chapter consists of three parts. The first part is Section 2.1, where we review the basic definitions and notation in mathematics, such as sets, functions, and relations. The second part is Sections 2.2–2.14, where we introduce the logics, calculi, and foundations of computation that are relevant to this work. The third part is Section 2.15, where we present the  $\mathbb{K}$  formal language semantics framework.

### 2.1 BASIC MATHEMATICS

Let  $A$  be a set. The size or *cardinality* of  $A$  is denoted by  $\text{card}(A)$ . The *powerset* of  $A$  is denoted by  $\mathcal{P}(A)$ . The empty set is denoted by  $\emptyset$ .

Let  $A$  and  $B$  be two sets. The *intersection* of  $A$  and  $B$  is denoted by  $A \cap B$ . The *union* of  $A$  and  $B$  is denoted by  $A \cup B$ . The *set difference* of  $A$  and  $B$  is denoted by  $A \setminus B$  and defined by  $A \setminus B = \{a \in A \mid a \notin B\}$ . The *set symmetric difference* of  $A$  and  $B$  is denoted by  $A \triangle B$  and defined by  $A \triangle B = (A \setminus B) \cup (B \setminus A)$ . If  $A \cap B = \emptyset$ , we say that  $A$  and  $B$  are *disjoint*. We write  $A \dot{\cup} B$  to mean  $A \cup B$ , with the assumption that  $A$  and  $B$  are disjoint. We write  $A \subseteq B$  to mean that  $A$  is a *subset* of  $B$ . We write  $A \subsetneq B$  to mean that  $A$  is a *strict subset* of  $B$ , that is,  $A \subseteq B$  and  $A \neq B$ .

A *total function* or simply *function* from  $A$  to  $B$  is denoted by  $f: A \rightarrow B$ , whose *domain* is  $\text{domain}(f) = A$  and *codomain* is  $\text{codomain}(f) = B$ . The set of all functions from  $A$  to  $B$  is denoted by  $B^A$  or  $[A \rightarrow B]$ . The *image* of  $f$  is  $\text{image}(f) = \{f(a) \mid a \in A\}$ . We call  $f$  an *injective function* or *injection* iff  $f(a_1) = f(a_2)$  implies  $a_1 = a_2$  for any  $a_1, a_2 \in A$ . We call  $f$  a *surjective function* or *surjection* iff  $\text{image}(f) = \text{codomain}(f)$ . We call  $f$  a *bijective function* or *bijection* iff it is both injective and surjective. For a subset  $A_0 \subseteq A$ , the *restriction* of  $f$  over  $A_0$ , denoted by  $f|_{A_0}: A_0 \rightarrow B$ , is a function defined by

$$f|_{A_0}(a) = f(a) \quad \text{for all } a \in A_0$$

A *partial function* from  $A$  to  $B$  is denoted by  $f: A \rightharpoonup B$ , where  $\text{domain}(f) \subseteq A$ . Total functions are special instances of partial functions with  $\text{domain}(f) = A$ . The set of all partial functions from  $A$  to  $B$  is denoted by  $[A \rightharpoonup B]$ . We write  $f: A \rightharpoonup_{\text{fin}} B$  to mean that  $\text{domain}(f)$  is finite. The set of all finite-domain partial functions from  $A$  to  $B$  is denoted by  $[A \rightharpoonup_{\text{fin}} B]$ . We use  $\emptyset$  to denote a partial function with an empty domain. For  $a \in A \setminus \text{domain}(f)$ , we say that  $f$  is *undefined* at  $a$ , written  $f(a) = \perp$ .

For a function (or partial function)  $f$  from  $A$  to  $B$ , we use  $f[b_0/a_0]$  where  $a_0 \in A$  and

$b_0 \in B$  to denote the *updated function* (or *updated partial function*)  $f'$  such that  $f'(a_0) = b_0$  and  $f'(a) = f(a)$  for all  $a \in A \setminus \{a_0\}$ . For two functions (or partial functions)  $f$  and  $g$ , we write  $f \overset{a_0}{\approx} g$  where  $a_0 \in A$  to mean that  $f(a) = g(a)$  for all  $a \in A \setminus \{a_0\}$ . We write  $f \overset{A_0}{\approx} g$  where  $A_0 \subseteq A$  to mean that  $f \overset{a_0}{\approx} g$  for all  $a \in A \setminus A_0$ .

We say that partial functions  $f, g: A \rightarrow B$  are *disjoint*, if  $\text{domain}(f) \cap \text{domain}(g) = \emptyset$ . For disjoint partial functions  $f, g: A \rightarrow B$ , their *disjoint union* is a partial function  $f \dot{\cup} g: A \rightarrow B$ , given by

$$(f \dot{\cup} g)(a) = \begin{cases} f(a) & \text{if } a \in \text{domain}(f) \\ g(a) & \text{if } a \in \text{domain}(g) \\ \perp & \text{otherwise} \end{cases}$$

Note that  $\text{domain}(f \dot{\cup} g) = \text{domain}(f) \dot{\cup} \text{domain}(g)$ . For simplicity, we automatically require that  $f$  and  $g$  are disjoint whenever we write  $f \dot{\cup} g$ .

Given  $f: \mathcal{P}(A) \rightarrow \mathcal{P}(A)$ , a fixed point or *fixpoint* of  $f$  is a set  $A_0 \subseteq A$  such that  $f(A_0) = A_0$ . A *pre-fixpoint* (or *post-fixpoint*) of  $f$  is a set  $A_0 \subseteq A$  such that  $f(A_0) \subseteq A_0$  (or  $A_0 \subseteq f(A_0)$ ). Thus,  $A_0$  is a fixpoint iff it is a pre-fixpoint and a post-fixpoint. We say that  $f$  is *monotone* iff  $A_1 \subseteq A_2$  implies  $f(A_1) \subseteq f(A_2)$  for all  $A_1, A_2 \subseteq A$ .

**Theorem 2.1** (Knaster-Tarski fixpoint theorem [21]). Let  $f: \mathcal{P}(A) \rightarrow \mathcal{P}(A)$  be a monotone function. Then  $f$  has a unique least fixpoint **lfp**  $f$  and a unique greatest fixpoint **gfp**  $f$ , given as follows:

$$\mathbf{lfp} f = \bigcap \{A_0 \subseteq A \mid f(A_0) \subseteq A_0\} \quad \mathbf{gfp} f = \bigcup \{A_0 \subseteq A \mid A_0 \subseteq f(A_0)\}$$

In other words, the least fixpoint is also the least pre-fixpoint, and the great fixpoint is also the greatest post-fixpoint.

Let  $\Lambda$  be a set whose elements are called indices. A  $\Lambda$ -indexed set is denoted by  $A = \{A_\lambda\}_{\lambda \in \Lambda}$ , where  $A_\lambda$  is a set for each  $\lambda \in \Lambda$ . For simplicity, we often write  $a \in A$  to mean that  $a \in A_\lambda$  for some  $\lambda \in \Lambda$ . For two  $\Lambda$ -indexed sets  $A = \{A_\lambda\}_{\lambda \in \Lambda}$  and  $B = \{B_\lambda\}_{\lambda \in \Lambda}$ , we write  $f: A \rightarrow B$  to denote a  $\Lambda$ -indexed function, where  $f(a) \in B_\lambda$  for every  $\lambda \in \Lambda$  and  $a \in A_\lambda$ .

**Definition 2.1.** Given a function  $f: A \rightarrow \mathcal{P}(B)$ , we define its *pointwise extension*  $f^{\text{ext}}: \mathcal{P}(A) \rightarrow \mathcal{P}(B)$  by

$$f^{\text{ext}}(A_0) = \bigcup_{a \in A_0} f(a) \quad \text{for all } A_0 \subseteq A$$

Note that  $f^{\text{ext}}(\emptyset) = \emptyset$ . For any  $\Lambda$ -indexed set  $\{A_\lambda\}_{\lambda \in \Lambda}$ ,  $f^{\text{ext}}(\bigcup_{\lambda \in \Lambda} A_\lambda) = \bigcup_{\lambda \in \Lambda} f^{\text{ext}}(A_\lambda)$ . In particular, we have  $f^{\text{ext}}(A_1 \cup \dots \cup A_n) = f^{\text{ext}}(A_1) \cup \dots \cup f^{\text{ext}}(A_n)$  for any  $A_1, \dots, A_n \subseteq A$ .

Given sets  $A_1, \dots, A_n$  for  $n \geq 1$ , the set  $A_1 \times \dots \times A_n$  is the set of  $n$ -tuples, given by  $A_1 \times \dots \times A_n = \{(a_1, \dots, a_n) \mid a_i \in A_i, 1 \leq i \leq n\}$ . Note that the 1-tuples are simply the elements in  $A_1$ . The 2-tuples are called *pairs*. An  $n$ -ary relation over  $A_1, \dots, A_n$  is a subset of  $A_1 \times \dots \times A_n$ . When  $n = 1$ , we call it a *unary relation*. When  $n = 2$ , we call it a *binary relation*. For an  $n$ -ary relation  $R \subseteq A_1 \times \dots \times A_n$ , we say that  $R(a_1, \dots, a_n)$  holds iff  $(a_1, \dots, a_n) \in R$ . The set of all  $n$ -ary relations over  $A_1 \times \dots \times A_n$  is  $\mathcal{P}(A_1 \times \dots \times A_n)$ .

We define

$$A^n = \underbrace{A \times \dots \times A}_n \quad \text{for } n \geq 2$$

The set of all  $n$ -ary relations over  $A^n$  is  $\mathcal{P}(A^n)$ . The set of all relations over the elements/tuples of  $A$  is  $\bigcup_{i \geq 1} \mathcal{P}(A^i)$ .

The *identity relation* over  $A$  is denoted by  $\text{id}_A$  and defined by  $\text{id}_A = \{(a, a) \mid a \in A\}$ . For relations  $R_1 \subseteq A \times B$  and  $R_2 \subseteq B \times C$ , their *composition* is a relation  $R_1 \circ R_2 \subseteq A \times C$ , defined by

$$R_1 \circ R_2 = \{(a, c) \in A \times C \mid \text{there exists } b \in B \text{ such that } R_1(a, b) \text{ holds and } R_2(b, c) \text{ holds}\}$$

Note that  $\circ$  is associative so we can write  $R_1 \circ \dots \circ R_n$  without needing to put parentheses.

For a relation  $R \subseteq A \times A$ , we say that  $R$  is

1. *reflexive*, if  $R(a, a)$  holds for all  $a \in A$ ;
2. *symmetric*, if  $R(a_1, a_2)$  holds implies  $R(a_2, a_1)$  holds for all  $a_1, a_2 \in A$ ;
3. *transitive*, if  $R(a_1, a_2)$  holds and  $R(a_2, a_3)$  holds implies  $R(a_1, a_3)$  holds for all  $a_1, a_2, a_3 \in A$ .

We let  $R^n = \underbrace{R \circ \dots \circ R}_n$ . When  $n = 1$ , we let  $R^1 = R$ . When  $n = 0$ , we let  $R^0 = \text{id}_A$ . The *transitive closure* of  $R$  is denoted by  $R^+$  and defined by  $R^+ = \bigcup_{i \geq 1} R^i$ . The *reflexive and transitive closure* of  $R$  is denoted by  $R^*$  and defined by  $R^* = \bigcup_{i \geq 0} R^i$ .

## 2.2 FIRST-ORDER LOGIC

We review (many-sorted) first-order logic, abbreviated as FOL.

**Definition 2.2.** A *FOL signature*  $(S, F, \Pi)$  consists of a set  $S$  of *sorts*, an  $(S^* \times S)$ -indexed set  $F = \{F_{s_1 \dots s_n, s}\}_{s_1, \dots, s_n, s \in S}$  of *function symbols*, and an  $S^+$ -indexed set  $\Pi = \{\Pi_{s_1 \dots s_n}\}_{s_1, \dots, s_n \in S}$  of *predicate symbols*.

**Definition 2.3.** Given a FOL signature  $(S, F, \Pi)$  and an  $S$ -indexed set  $V = \{V_s\}_{s \in S}$  of (*many-sorted*) variables, denoted by  $x : s, y : s$ , etc, the syntax of FOL is given by the following grammar:

$$\begin{array}{ll}
 \text{FOL terms} & t_s ::= x : s \in V_s \\
 & \quad | f(t_{s_1}, \dots, t_{s_n}) \quad \text{with } f \in F_{s_1 \dots s_n, s} \\
 \text{FOL formulas} & \varphi ::= \pi(t_{s_1}, \dots, t_{s_n}) \quad \text{with } \pi \in \Pi_{s_1 \dots s_n} \\
 & \quad | \varphi_1 \wedge \varphi_2 \\
 & \quad | \neg \varphi \\
 & \quad | \exists x : s. \varphi
 \end{array}$$

We use  $\text{freeVar}(\varphi)$  to denote the set of free variables in  $\varphi$ . We write  $\varphi[t_s/x : s]$  for the result of substituting  $t_s$  for  $x : s$  in  $\varphi$ , where  $\alpha$ -renaming happens implicitly to avoid variable capture.

**Definition 2.4.** Given a FOL signature  $(S, F, \Pi)$ , a *FOL*  $(S, F, \Pi)$ -*model* or simply *FOL model* is a tuple

$$M = (\{M_s\}_{s \in S}, \{f_M\}_{f \in F}, \{\pi_M\}_{\pi \in \Pi})$$

where

1.  $M_s$  is a carrier set for every  $s \in S$ ;
2.  $f_M : M_{s_1} \times \dots \times M_{s_n} \rightarrow M_s$  is a function for every  $f \in F_{s_1 \dots s_n, s}$ ;
3.  $\pi_M \subseteq M_{s_1} \times \dots \times M_{s_n}$  is a relation for every  $\pi \in \Pi_{s_1 \dots s_n}$ .

**Definition 2.5.** Let  $M$  be a FOL model. A *FOL*  $M$ -*valuation* or simply *FOL valuation*  $\rho : V \rightarrow M$  is a function such that  $\rho(x : s) \in M_s$  for every  $s \in S$  and  $x : s \in V_s$ . The *term extension* of  $\rho$  is a function  $\bar{\rho}$  from the set of FOL terms to  $M$ , defined by

1.  $\bar{\rho}(x : s) = \rho(x : s)$ ;
2.  $\bar{\rho}(f(t_{s_1}, \dots, t_{s_n})) = f_M(\bar{\rho}(t_{s_1}), \dots, \bar{\rho}(t_{s_n}))$ .

Note that  $\bar{\rho}(t_s) \in M_s$  for every FOL term  $t_s$  whose sort is  $s$ .

**Definition 2.6.** Let  $M$  be a FOL model. The *FOL satisfaction relation*  $M, \rho \models_{\text{FOL}} \varphi$  is defined for all  $\rho$  as follows:

1.  $M, \rho \models_{\text{FOL}} \pi(t_{s_1}, \dots, t_{s_n})$  iff  $\pi_M(\bar{\rho}(t_{s_1}), \dots, \bar{\rho}(t_{s_n}))$  holds;

---

(PROPOSITIONAL TAUTOLOGY)	$\varphi$ , if $\varphi$ is a propositional tautology
(MODUS PONENS)	$\frac{\varphi_1 \quad \varphi_1 \rightarrow \varphi_2}{\varphi_2}$
(TERM SUBSTITUTION)	$\varphi[t_s/x : s] \rightarrow \exists x : s . \varphi$
( $\exists$ -GENERALIZATION)	$\frac{\varphi_1 \rightarrow \varphi_2}{(\exists x : s . \varphi_1) \rightarrow \varphi_2} \text{ if } x : s \notin \text{free Var}(\varphi_2)$

---

Figure 2.1: Sound and Complete Proof System of FOL

2.  $M, \rho \models_{\text{FOL}} \varphi_1 \wedge \varphi_2$  iff  $M, \rho \models_{\text{FOL}} \varphi_1$  and  $M, \rho \models_{\text{FOL}} \varphi_2$ ;
3.  $M, \rho \models_{\text{FOL}} \neg \varphi$  iff  $M, \rho \not\models_{\text{FOL}} \varphi$ ;
4.  $M, \rho \models_{\text{FOL}} \exists x : s . \varphi$  iff there exists  $a \in M_s$  such that  $M, \rho[a/x : s] \models_{\text{FOL}} \varphi$ .

We write  $M \models_{\text{FOL}} \varphi$  iff  $M, \rho \models_{\text{FOL}} \varphi$  for all  $\rho$ . A *FOL theory*  $\Gamma$  is a set of FOL formulas/axioms. We write  $M \models_{\text{FOL}} \Gamma$  iff  $M \models_{\text{FOL}} \psi$  for all  $\psi \in \Gamma$ . We write  $\Gamma \models_{\text{FOL}} \varphi$  iff  $M \models_{\text{FOL}} \Gamma$  implies  $M \models_{\text{FOL}} \varphi$  for all  $M$ .

FOL has a sound and complete Hilbert-style proof system as shown in Figure 2.1. The corresponding provability relation is denoted by  $\Gamma \vdash_{\text{FOL}} \varphi$ .

**Theorem 2.2.** For any FOL theory  $\Gamma$  and formula  $\varphi$ ,  $\Gamma \models_{\text{FOL}} \varphi$  iff  $\Gamma \vdash_{\text{FOL}} \varphi$ .

### 2.3 FIRST-ORDER LOGIC WITH LEAST FIXPOINTS

We review first-order logic with least fixpoints, abbreviated as LFP. LFP is an extension of FOL with predicate variables and an operator  $\text{lfp}$  that builds least fixpoints.

**Definition 2.7.** An *LFP signature*  $(S, F, \Pi)$  is the same as a FOL signature.

**Definition 2.8.** Given an LFP signature  $(S, F, \Pi)$ , an  $S$ -indexed set  $EV = \{EV_s\}_{s \in S}$  of *element variables*, and an  $S^+$ -indexed set  $PV = \{PV_{s_1 \dots s_n}\}_{s_1, \dots, s_n \in S}$  of *predicate variables*, the syntax of LFP extends the syntax of FOL with the following grammar rules:

$$\begin{aligned}
 \text{LFP formulas} \quad \varphi ::= & \text{(syntax of FOL formulas)} \\
 & \mid P(t_{s_1}, \dots, t_{s_n}) \quad \text{with } P \in PV_{s_1 \dots s_n} \\
 & \mid [\text{lfp}_{P, x_1 : s_1 \dots, x_n : s_n} \varphi](t_{s_1}, \dots, t_{s_n}) \quad \text{with } P \in PV_{s_1 \dots s_n}
 \end{aligned}$$

where  $[\text{lfp}_{P, x_1 : s_1 \dots, x_n : s_n} \varphi](t_{s_1}, \dots, t_{s_n})$  requires that  $\varphi$  is positive in  $P$ , that is, every sub-formula of  $\varphi$  that has form  $P(t'_{s_1}, \dots, t'_{s_n})$  must occur under an even number of  $\neg$ 's.

**Definition 2.9.** An LFP model  $M$  is the same as a FOL model. An *LFP  $M$ -valuation* or simply an *LFP valuation*  $\rho = (\rho_{EV}, \rho_{PV})$  is a pair, where  $\rho_{EV} : V \rightarrow M$  and  $\rho_{PV} : PV \rightarrow \{\mathcal{P}(M_{s_1} \times \dots \times M_{s_n})\}_{s_1, \dots, s_n \in S}$ . That is,  $\rho_{PV}(P)$  is an  $n$ -ary relation over  $M_{s_1}, \dots, M_{s_n}$ , for  $P \in PV_{s_1 \dots s_n}$ . Let  $\overline{\rho_{EV}}$  be the term extension of  $\rho_{EV}$  in Definition 2.5. The *LFP satisfaction relation*  $M, \rho \models_{\text{LFP}} \varphi$  is defined for all  $\rho$  by extending the FOL satisfaction relation with two additional rules:

1.  $M, \rho \models_{\text{LFP}} P(t_{s_1}, \dots, t_{s_n})$  iff  $\rho_{PV}(P)(\overline{\rho_{EV}}(t_{s_1}), \dots, \overline{\rho_{EV}}(t_{s_n}))$  holds;
2.  $M, \rho \models_{\text{LFP}} [\text{lfp}_{P, x_1 : s_1 \dots, x_n : s_n} \varphi](t_{s_1}, \dots, t_{s_n})$  iff  $(\overline{\rho_{EV}}(t_{s_1}), \dots, \overline{\rho_{EV}}(t_{s_n})) \in \bigcap \{R \subseteq M_{s_1} \times \dots \times M_{s_n} \mid \text{for all } a_i \in M_{s_i}, 1 \leq i \leq n, M, \rho[R/P, a_1/x_1 : s_1, \dots, a_n/x_n : s_n] \models_{\text{LFP}} \varphi \text{ implies } (a_1, \dots, a_n) \in R\}$ .

We write  $M \models_{\text{LFP}} \varphi$  iff  $M, \rho \models_{\text{LFP}} \varphi$  for all  $\rho$  and all the predicate variables of  $\varphi$  are bound by  $\text{lfp}$ . We write  $\models_{\text{LFP}} \varphi$  iff  $M \models_{\text{LFP}} \varphi$  for all  $M$ .

Our presentation of LFP is slightly different from the classical presentation. The classical presentation enforces all the predicate variables in an LFP formula to be bound by  $\text{lfp}$ . The semantics of predicate variables, which are needed for defining the semantics of  $[\text{lfp}_{R, x_1 : s_1 \dots, x_n : s_n} \varphi]$ , are given by a model of an extended signature, where all the predicate variables are added as predicate symbols and interpreted as relations. In our presentation, we do not extend the signature nor the model. Instead, we extend the valuation with  $\rho_{PV}$ , which maps predicate variables to relations. This modified yet equivalent presentation of LFP fits better in Section 5.2, where we will define LFP in matching  $\mu$ -logic.

## 2.4 SECOND-ORDER LOGIC

We review (many-sorted) second-order logic, abbreviated as SOL.

**Definition 2.10.** A *SOL signature*  $(S, C, \Pi)$  consists of a set  $S$  of sorts, an  $S$ -indexed set  $C$  of *constant symbols* that are function symbols of arity 0, and an  $S^*$ -indexed set  $\Pi$  of predicate symbols.

**Definition 2.11.** Given a SOL signature  $(S, C, \Pi)$ , an  $S$ -indexed set  $EV = \{EV_s\}_{s \in S}$  of element variables, and an  $S^+$ -indexed set  $PV = \{PV_{s_1 \dots s_n}\}_{s_1, \dots, s_n \in S}$  of predicate variables, the syntax of SOL is given by the following grammar:

$$\text{SOL terms} \quad t_s ::= x : s \in EV_s$$

$$\begin{array}{l}
 \text{SOL formulas} \quad | \quad c \in C_s \\
 \varphi ::= t_s = t'_s \\
 | \quad \pi(t_{s_1}, \dots, t_{s_n}) \quad \text{with } \pi \in \Pi_{s_1 \dots s_n} \\
 | \quad P(t_{s_1}, \dots, t_{s_n}) \quad \text{with } P \in PV_{s_1 \dots s_n} \\
 | \quad \varphi_1 \wedge \varphi_2 \\
 | \quad \neg \varphi \\
 | \quad \exists x : s . \varphi \\
 | \quad \exists P . \varphi \quad \text{with } P \in PV_{s_1 \dots s_n}
 \end{array}$$

**Definition 2.12.** Given a SOL signature  $(S, C, \Pi)$ , a *SOL*  $(S, C, \Pi)$ -*model* or simply *SOL model*  $M = (\{M_s\}_{s \in S}, \{c_M\}_{c \in C}, \{\pi_M\}_{\pi \in \Pi})$  is a tuple, where

1.  $M_s$  is a carrier set for every  $s \in S$ ;
2.  $c_M \in M_s$  for every  $c \in C_s$ ;
3.  $\pi_M \subseteq M_{s_1} \times \dots \times M_{s_n}$  for every  $\pi \in \Pi_{s_1 \dots s_n}$ .

**Definition 2.13.** Given a SOL model  $M$ , a *SOL*  $M$ -*valuation* or simply *SOL valuation*  $\rho = (\rho_{EV}, \rho_{PV})$  is the same as Definition 2.9. The SOL satisfaction relation  $M, \rho \models_{\text{SOL}} \varphi$  is defined for all  $\rho$  by the following rules:

1.  $M, \rho \models_{\text{SOL}} t_s = t'_s$  iff  $\overline{\rho_{EV}}(t_s) = \overline{\rho_{EV}}(t'_s)$ ;
2.  $M, \rho \models_{\text{SOL}} \pi(t_{s_1}, \dots, t_{s_n})$  iff  $\pi_M(\overline{\rho_{EV}}(t_{s_1}), \dots, \overline{\rho_{EV}}(t_{s_n}))$  holds;
3.  $M, \rho \models_{\text{SOL}} P(t_{s_1}, \dots, t_{s_n})$  iff  $\rho_{PV}(P)(\overline{\rho_{EV}}(t_{s_1}), \dots, \overline{\rho_{EV}}(t_{s_n}))$  holds;
4.  $M, \rho \models_{\text{SOL}} \varphi_1 \wedge \varphi_2$  iff  $M, \rho \models_{\text{SOL}} \varphi_1$  and  $M, \rho \models_{\text{SOL}} \varphi_2$ ;
5.  $M, \rho \models_{\text{SOL}} \neg \varphi$  iff  $M, \rho \not\models_{\text{SOL}} \varphi$ ;
6.  $M, \rho \models_{\text{SOL}} \exists x : s . \varphi$  iff there exists  $a \in M_s$  such that  $M, \rho[a/x : s] \models_{\text{SOL}} \varphi$ ;
7.  $M, \rho \models_{\text{SOL}} \exists P . \varphi$  iff there exists  $R \subseteq M_{s_1} \times \dots \times M_{s_n}$  such that  $M, \rho[R/P] \models_{\text{SOL}} \varphi$ , where  $P \in PV_{s_1 \dots s_n}$ .

We write  $M \models_{\text{SOL}} \varphi$  iff  $M, \rho \models_{\text{SOL}} \varphi$  for all  $\rho$ . A *SOL theory*  $\Gamma$  is a set of SOL formulas/axioms. We write  $M \models_{\text{SOL}} \Gamma$  iff  $M \models_{\text{SOL}} \psi$  for all  $\psi \in \Gamma$ . We write  $\Gamma \models_{\text{SOL}} \varphi$  iff  $M \models_{\text{SOL}} \Gamma$  implies  $M \models_{\text{SOL}} \varphi$  for all  $M$ .

Monadic SOL, abbreviated as MSOL, is an instance of SOL where all predicate variables are unary, i.e., taking only one argument.



## 2.5 EQUATIONAL SPECIFICATIONS AND INITIAL ALGEBRA SEMANTICS

Equational specifications (also known as algebraic specifications) and initial algebra semantics provide a generic and principled framework to study induction. We review the main definitions and notation about them following the standard many-sorted approach [22, 23].

**Definition 2.14.** Given a many-sorted signature  $(S, F)$ , an  $(S, F)$ -algebra or simply  $F$ -algebra  $A = (\{A_s\}_{s \in S}, \{f_A\}_{f \in F})$  is a pair, where

1.  $A_s$  is a carrier set for every  $s \in S$ ;
2.  $f_A : A_{s_1} \times \cdots \times A_{s_n} \rightarrow A_s$  is a function for every  $f \in F_{s_1 \dots s_n, s}$ .

Given an  $S$ -indexed set  $V = \{V_s\}_{s \in S}$  of variables, the syntax of  $(S, F)$ -terms or simply  $F$ -terms is the same as Definition 2.3. Let  $T_F(V) = \{T_{F,s}(V)\}_{s \in S}$  be an  $S$ -indexed set of terms. The set  $T_F(\emptyset) = \{T_{F,s}(\emptyset)\}_{s \in S}$  includes all the *ground terms*, i.e., terms with no variables. We often abbreviate  $T_F(\emptyset)$  as  $T_F$  and  $T_{F,s}(\emptyset)$  as  $T_{F,s}$ . An  $A$ -valuation  $\rho : V \rightarrow A$  and its term extension  $\bar{\rho}$  are the same as Definition 2.5. When  $V = \emptyset$ , there is a unique trivial valuation  $\emptyset : \emptyset \rightarrow A$ . We define  $\text{eval}_A(t) = \bar{\emptyset}(t)$  for  $t \in T_F$  and feel free to drop the subscript  $A$  when it is understood or not important.

**Definition 2.15.** Given a many-sorted signature  $(S, F)$ , an  $(S, F)$ -equation or simply  $F$ -equation is written  $\forall V . t_s = t'_s$ , where  $V$  is finite and  $t_s, t'_s \in T_{F,s}(V)$ . A *ground  $(S, F)$ -equation* or simply *ground  $F$ -equation*  $\forall \emptyset . t_s = t'_s$  is when  $V = \emptyset$  and  $t_s, t'_s \in T_{F,s}$ .

**Definition 2.16.** Given an  $(S, F)$ -algebra  $A$  and an  $(S, F)$ -equation  $\forall V . t = t'$ , we write  $A \models_{\text{EQ}} \forall V . t = t'$  iff  $\bar{\rho}(t) = \bar{\rho}(t')$  for all  $\rho$ . An *equational specification*  $(S, F, E)$  consists of a many-sorted signature  $(S, F)$  and a set  $E$  of  $(S, F)$ -equations. We often abbreviate  $(S, F, E)$  as  $(F, E)$  or simply  $E$ . We write  $A \models_{\text{EQ}} E$  iff  $A \models_{\text{EQ}} e$  for all  $e \in E$ , and we call  $A$  an  $(S, F, E)$ -algebra or simply  $(F, E)$ -algebra or  $E$ -algebra. We write  $E \models_{\text{EQ}} e$  iff  $A \models_{\text{EQ}} e$  for all  $E$ -algebras  $A$ .

Next, we review rules of equational deduction for many-sorted algebras. There are many equivalent definitions of equational deduction in the literature. We present one standard definition in Figure 2.2 and denote the corresponding provability relation by  $E \vdash_{\text{EQ}} e$ .

**Theorem 2.3.** For any equational specification  $E$  and equation  $e$ ,  $E \vdash_{\text{EQ}} e$  iff  $E \models_{\text{EQ}} e$ .

**Definition 2.17.** Let  $A$  be an  $(S, F)$ -algebra. A *congruence* on  $A$  is an  $S$ -indexed set  $R = \{R_s\}_{s \in S}$  of equivalence relations  $R_s \subseteq A_s \times A_s$  for  $s \in S$ , such that  $R_{s_i}(a_i, b_i)$  holds for all  $1 \leq i \leq n$  implies  $R_s(f_A(a_1, \dots, a_n), f_A(b_1, \dots, b_n))$  holds, for all  $f \in F_{s_1 \dots s_n, s}$  and  $a_i, b_i \in A_{s_i}$ ,

---

(AXIOM)	$\forall V . t_s = t'_s \quad \text{if } (\forall V . t_s = t'_s) \in E$
(REFLEXIVITY)	$\forall V . t_s = t_s$
(SYMMETRY)	$\frac{\forall V . t_s = t'_s}{\forall V . t'_s = t_s}$
(TRANSITIVITY)	$\frac{\forall V . t_s = t'_s \quad \forall V . t'_s = t''_s}{\forall V . t_s = t''_s}$
(CONGRUENCE)	$\frac{\forall V . t_{s_1} = t'_{s_1} \quad \dots \quad \forall V . t_{s_n} = t'_{s_n}}{\forall V . f(t_{s_1}, \dots, t_{s_n}) = f(t'_{s_1}, \dots, t'_{s_n})}$
(SUBSTITUTION)	$\frac{\forall V . t_s = t'_s}{\forall U . t_s \theta = t'_s \theta}$ with substitution $\theta: V \rightarrow T_F(U)$

---

Figure 2.2: Sound and Complete Proof Rules for Equational Deduction

$1 \leq i \leq n$ . The  $R$ -quotient algebra of  $A$  is an  $(S, F)$ -algebra  $A/R = (\{A/R_s\}_{s \in S}, \{f_{A/R}\}_{f \in F})$ , where

1.  $A/R_s = \{[a]_R \mid a \in A_s\}$  for every  $s \in S$ ; here,  $[a]_R = \{b \in A_s \mid R_s(a, b) \text{ holds}\}$  is the  $R$ -equivalence class of  $a$ ;
2.  $f_{A/R}: A/R_{s_1} \times \dots \times A/R_{s_n} \rightarrow A/R_s$  is defined by  $f_{A/R}([a_1]_R, \dots, [a_n]_R) = [f_A(a_1, \dots, a_n)]_R$  for all  $[a_i]_R \in A/R_{s_i}$ ,  $1 \leq i \leq n$ , for every  $f \in F_{s_1 \dots s_n, s}$ .

Note that  $f_{A/R}$  is well-defined because  $R$  is a congruence.

**Definition 2.18.** Given a many-sorted signature  $(S, F)$ , an  $(S, F)$ -term algebra or simply  $F$ -term algebra  $T_F = (\{T_{F,s}\}_{s \in S}, \{f_{T_F}\}_{f \in F})$  is an  $F$ -algebra, where

1.  $T_{F,s}$  is the set of ground terms of sort  $s$ , for every  $s \in S$ ;
2.  $f_{T_F}: T_{F,s_1} \times \dots \times T_{F,s_n} \rightarrow T_{F,s}$  is defined by  $f_{T_F}(t_{s_1}, \dots, t_{s_n}) = f(t_{s_1}, \dots, t_{s_n})$  for all  $t_{s_i} \in T_{F,s_i}$ ,  $1 \leq i \leq n$ , for every  $f \in F_{s_1 \dots s_n, s}$ .

Equational deduction generates a congruence on  $T_F$ .

**Proposition 2.4.** Let  $E$  be an equational specification. Define a relation  $\simeq_{E,s} \subseteq T_{F,s} \times T_{F,s}$  such that  $t_s \simeq_{E,s} t'_s$  iff  $E \vdash_{\text{EQ}} \forall \emptyset . t_s = t'_s$ , for all  $t_s, t'_s \in T_{F,s}$ . Then,  $\simeq_E = \{\simeq_{E,s}\}_{s \in S}$  is a congruence on  $T_F$ .

We use  $[t_s]_{\simeq_E}$ , or simply  $[t_s]_E$  or  $[t_s]$ , to denote the set of terms that are provably equal to  $t$ . We abbreviate  $t_s \simeq_{E,s} t'_s$  as  $t_s \simeq_E t'_s$  or  $t_s \simeq t'_s$ .

**Definition 2.19.** Given an equational specification  $(S, F, E)$ , the  $(S, F, E)$ -*quotient term algebra* or simply  $(F, E)$ -*quotient term algebra* or  $E$ -*quotient term algebra*, written  $T_{F/E}$ , is the  $\simeq_E$ -quotient algebra of  $T_F$ . Specifically,  $T_{F/E} = (\{T_{F/E,s}\}_{s \in S}, \{f_{T_{F/E}}\}_{f \in F})$ , where

1.  $T_{F/E,s} = \{[t_s]_E \mid t_s \in T_{F,s}\}$  for every  $s \in S$ ;
2.  $f_{T_{F/E}}: T_{F/E,s_1} \times \cdots \times T_{F/E,s_n} \rightarrow T_{F/E,s}$  is defined by  $f_{T_{F/E}}([t_{s_1}], \dots, [t_{s_n}]) = [f(t_{s_1}, \dots, t_{s_n})]$  for all  $[t_{s_i}] \in T_{F/E,s_i}$ ,  $1 \leq i \leq n$ , for every  $f \in F_{s_1 \dots s_n, s}$ .

Term algebras and quotient term algebras are the concrete examples of initial algebras, which are initial objects in the category of algebras. We first review the definition of algebra morphisms.

**Definition 2.20.** For  $(S, F)$ -algebras  $A$  and  $B$ , an *(algebra) morphism* is a function  $h: A \rightarrow B$  such that  $h(f_A(a_1, \dots, a_n)) = f_B(h(a_1), \dots, h(a_n))$  for all  $f \in F_{s_1 \dots s_n, s}$  and  $a_i \in A_{s_i}$ ,  $1 \leq i \leq n$ . If  $h$  is a morphism and its inverse  $h^{-1}: B \rightarrow A$  exists, then  $h$  is an *isomorphism* and  $A$  and  $B$  are *isomorphic*.

**Definition 2.21.** Given an equational specification  $(S, F, E)$ , an *initial  $(S, F, E)$ -algebra* or simply *initial  $(F, E)$ -algebra* or *initial  $E$ -algebra*, is an  $(S, F)$ -algebra  $I$  such that for every  $(F, E)$ -algebra  $A$ , there exists a unique morphism  $h_A: I \rightarrow A$ . An initial  $(S, F, \emptyset)$ -algebra is called an *initial  $(S, F)$ -algebra* or simply *initial  $F$ -algebra*.

**Theorem 2.5.** Any two initial  $(F, E)$ -algebras are isomorphic. In particular,  $T_F$  is an initial  $F$ -algebra and  $T_{F/E}$  is an initial  $(F, E)$ -algebra.

An equational specification states the existence of some data, operations, and equational properties. Its initial algebras are the minimal realization of the specification, in the sense that all of its elements are representable by terms and all the (equational) properties are derivable from  $E$ . An equivalent characterization of initiality is the famous “no junk, no confusion” slogan, firstly proposed in [24].

**Theorem 2.6.** Let  $A$  be an  $(S, F, E)$ -algebra. We define no-confusion and no-junk as follows:

1.  $A$  satisfies *no-confusion*, iff  $A \models_{\text{EQ}} \forall \emptyset. t_s = t'_s$  implies  $E \vdash_{\text{EQ}} \forall \emptyset. t_s = t'_s$  for all  $t_s, t'_s \in T_{F,s}$ .
2.  $A$  satisfies *no-junk*, iff for any  $a \in A_s$  there exists  $t_a \in T_{F,s}$  such that  $\text{eval}_A(t_a) = a$ .

Then,  $A$  is an initial  $(S, F, E)$ -algebra iff it satisfies no-junk and no-confusion.

If  $A$  satisfies no-confusion, then for any  $t_s, t'_s \in T_{F,s}$  that have the same semantics in  $A$ , we have  $E \vdash_{\text{EQ}} \forall \emptyset. t_s = t'_s$ , which implies that  $E \models_{\text{EQ}} \forall \emptyset. t_s = t'_s$  (Theorem 2.3). In other words, if  $t_s$  and  $t'_s$  have the same semantics in  $A$ , then they have the same semantics in all  $E$ -algebras. On the other hand, if  $A$  satisfies no-junk, then every element in  $A$  is representable by a ground term.

## 2.6 SEPARATION LOGIC

Separation logic [25], abbreviated as SL, is a logic specifically crafted for reasoning about heap structures. SL has many variants; the formalization that we consider here is adapted from [26]. The most characteristic construct in SL is separating conjunction  $\varphi_1 * \varphi_2$ , which specifies a conjunctive heap of two disjoint heaps. In addition, SL has the model of heaps (i.e., finite-domain maps) hard-wired in its semantics, which makes it a logic specifically crafted for heap reasoning.

**Definition 2.22.** Let  $V$  be a set of variables and  $RSymb$  be a finite set of *recursive symbols*. For each  $P \in RSymb$  we use  $\text{arity}(P) \geq 1$  to denote its arity. The syntax of SL is given by the following grammar:

<u>SL terms</u>	$t ::= x \in V$
	$\mid \text{nil}$
<u>SL formulas:</u>	$\varphi ::= (\text{syntax of FOL formulas})$
	$\mid \text{emp} \quad \quad \quad // \text{ the empty heap}$
	$\mid t_1 \mapsto t_2 \quad \quad // \text{ singleton heaps}$
	$\mid \varphi_1 * \varphi_2 \quad \quad // \text{ separating conjunction}$
	$\mid \varphi_1 \multimap \varphi_2 \quad // \text{ separating implication (the “magic wand”)}$
	$\mid P(t_1, \dots, t_n) \text{ with } P \in RSymb \text{ and } \text{arity}(p) = n$

A *recursive symbol definition*  $D$  is a set as follows:

$$D = \{P(x_1, \dots, x_n) =_{\text{ifp}} \psi_P \mid P \in RSymb \text{ and } \text{arity}(P) = n\}$$

where  $\text{freeVar}(\psi_P) \subseteq \{x_1, \dots, x_n\}$  and  $\psi_P$  is positive in  $P$ , the same as LFP (Definition 2.8). In this work, we do not consider mutually recursive symbols, so we require that  $P$  is the only recursive predicate symbol in  $\psi_P$ .

Many heap structures, especially those featuring induction, can be defined using recursive symbol definitions. For example, singly-linked lists can be defined by a recursive symbol `list` and the following recursive symbol definition:

$$\text{list}(x) =_{\mathbf{lfp}} (x = \text{nil}) \wedge \text{emp} \vee \exists y. (x \neq \text{nil}) \wedge x \mapsto y * \text{list}(y)$$

Intuitively, if  $x = \text{nil}$  then  $\text{list}(\text{nil})$  specifies the empty heap `emp`. Otherwise, there exists  $y$  such that  $x$  points to  $y$  (i.e.,  $x \mapsto y$ ), and in a separate heap segment there is a singly-linked list starting at  $y$ .

**Definition 2.23.** A *heap* is a partial function  $h: \mathbb{N}^+ \rightarrow_{\text{fin}} \mathbb{N}$ . The set of all heaps  $\mathbb{H} = [\mathbb{N}^+ \rightarrow_{\text{fin}} \mathbb{N}]$ . For heaps  $h_1$  and  $h_2$ , their disjoint union is denoted by  $h_1 \dot{\cup} h_2$  (Section 2.1). A *store* is a function  $s: V \rightarrow \mathbb{N}$ . Its term extension  $\bar{s}: V \rightarrow \mathbb{N}$  is given by  $\bar{s}(x) = s(x)$  for all  $x \in V$  and  $\bar{s}(\text{nil}) = 0$ .

**Definition 2.24.** Given a set of recursive symbols  $RSymb$  and a recursive symbol definition  $D$  in Definition 2.22, the SL satisfaction relation  $s, h \models_{\text{SL}} \varphi$  is defined for all heaps  $h$  and stores  $s$  by extending the FOL satisfaction relation with the following additional rules:

1.  $s, h \models_{\text{SL}} \text{emp}$  iff  $\text{domain}(h) = \emptyset$ ;
2.  $s, h \models_{\text{SL}} t_1 \mapsto t_2$  iff  $\bar{s}(t_1) \neq 0$ ,  $\text{domain}(h) = \{\bar{s}(t_1)\}$ , and  $h(\bar{s}(t_1)) = \bar{s}(t_2)$ ;
3.  $s, h \models_{\text{SL}} \varphi_1 * \varphi_2$  iff there exist  $h_1, h_2$  such that  $s, h_1 \models_{\text{SL}} \varphi_1$ ,  $s, h_2 \models_{\text{SL}} \varphi_2$ , and  $h = h_1 \dot{\cup} h_2$ ;
4.  $s, h \models_{\text{SL}} \varphi_1 \multimap \varphi_2$  iff for all  $h'$  such that  $h, h'$  are disjoint and  $s, h' \models_{\text{SL}} \varphi_1$ , we have  $s, h \dot{\cup} h' \models_{\text{SL}} \varphi_2$ ;
5.  $s, h \models_{\text{SL}} P(t_1, \dots, t_n)$  iff  $(\bar{s}(t_1), \dots, \bar{s}(t_n), h) \in |P|^{\text{SL}}$ , for  $P \in RSymb$  and  $\text{arity}(P) = n$ ,

where  $|P|^{\text{SL}}$  is given as follows. Define a function  $\mathcal{F}_P: \mathcal{P}(\mathbb{N}^n \times \mathbb{H}) \rightarrow \mathcal{P}(\mathbb{N}^n \times \mathbb{H})$  by letting

$$\mathcal{F}_P(R) = \{(\bar{s}(x_1), \dots, \bar{s}(x_n), h) \mid s, h \models_{\text{SL}, P} \psi_P\} \quad \text{for } R \subseteq \mathbb{N}^n \times \mathbb{H}$$

where  $\models_{\text{SL}, P}$  is the same as  $\models_{\text{SL}}$  except that  $|P|^{\text{SL}} = R$ . Since  $\psi_P$  is positive in  $P$ ,  $\mathcal{F}_P$  is monotone and its unique least fixpoint  $\mathbf{lfp} \mathcal{F}_P$  is given by Theorem 2.1. We let  $|P|^{\text{SL}} = \mathbf{lfp} \mathcal{F}_P$ . Given a SL formula  $\varphi$ , we write  $s, h \models_{\text{SL}} \varphi$  iff  $s, h \models_{\text{SL}} \varphi$  for all  $s$  and  $h$ .

## 2.7 MODAL LOGIC K

Modal logic is a big family of logics, with many variants and extensions. The formalization we consider here is called modal logic K with multiple modalities. We should not confuse modal logic K with the  $\mathbb{K}$  formal semantics framework (Section 2.15). We will always use the blackboard bold letter  $\mathbb{K}$  to refer to the latter.

**Definition 2.25.** Let  $AP$  be a set of *atomic propositions*, also known as propositional variables in the literature. Let  $L$  be a set of *labels*. The syntax of modal logic is given by the following grammar:

$$\begin{array}{l} \text{modal logic K formulas} \quad \varphi ::= p \in AP \\ \quad \quad \quad \quad \quad \quad \quad \quad | \varphi_1 \wedge \varphi_2 \\ \quad \quad \quad \quad \quad \quad \quad \quad | \neg \varphi \\ \quad \quad \quad \quad \quad \quad \quad \quad | [a]\varphi \text{ with } a \in L \end{array}$$

For each  $a \in L$  the dual of  $[a]$  is the modal operator  $\langle a \rangle$ , given by  $\langle a \rangle \varphi \equiv \neg[a]\neg\varphi$ .

Modal logic models are labeled transition systems.

**Definition 2.26.** Given a label set  $L$ , an  $L$ -labeled transition system or simply *labeled transition system* is a tuple  $T = (S, \{\xrightarrow{a}\}_{a \in L})$ , where  $S$  is a set of *states* and  $\xrightarrow{a} \subseteq S \times S$  is a binary relation, called a *transition relation*, for every  $a \in L$ . We write  $s_1 \xrightarrow{a} s_2$  to mean that  $(\xrightarrow{a})(s_1, s_2)$  holds, for  $s_1, s_2 \in S$ .

**Definition 2.27.** Given a label set  $L$  and an  $L$ -labeled transition system  $T = (S, \{\xrightarrow{a}\}_{a \in L})$ , a  $T$ -valuation is a function  $\rho: AP \rightarrow \mathcal{P}(S)$ . The modal logic satisfaction relation  $T, \rho, s \models_{\mathbb{K}} \varphi$  is defined for all  $\rho$  and  $s \in S$  as follows:

1.  $T, \rho, s \models_{\mathbb{K}} p$  iff  $s \in \rho(p)$ ;
2.  $T, \rho, s \models_{\mathbb{K}} \varphi_1 \wedge \varphi_2$  iff  $T, \rho, s \models_{\mathbb{K}} \varphi_1$  and  $T, \rho, s \models_{\mathbb{K}} \varphi_2$ ;
3.  $T, \rho, s \models_{\mathbb{K}} \neg \varphi$  iff  $T, \rho, s \not\models_{\mathbb{K}} \varphi$ ;
4.  $T, \rho, s \models_{\mathbb{K}} [a]\varphi$  iff for all  $s' \in S$ ,  $s \xrightarrow{a} s'$  implies  $T, \rho, s' \models_{\mathbb{K}} \varphi$ .

The derived semantics for  $\langle a \rangle \varphi$  is

$$T, \rho, s \models_{\mathbb{K}} \langle a \rangle \varphi \text{ iff there exists } s' \in S \text{ such that } s \xrightarrow{a} s' \text{ and } T, \rho, s' \models_{\mathbb{K}} \varphi$$

---

(PROPOSITIONAL TAUTOLOGY)	$\varphi$ , if $\varphi$ is a propositional tautology
(MODUS PONENS)	$\frac{\varphi_1 \quad \varphi_1 \rightarrow \varphi_2}{\varphi_2}$
(K)	$[a](\varphi_1 \rightarrow \varphi_2) \rightarrow ([a]\varphi_1 \rightarrow [a]\varphi_2)$
(N)	$\frac{\varphi}{[a]\varphi}$

---

Figure 2.3: Sound and Complete Proof System of Modal Logic K

We write  $T, \rho \models_K \varphi$  iff  $T, \rho, s \models_K \varphi$  for all  $s \in S$ . We write  $T \models_K \varphi$  iff  $T, \rho \models_K \varphi$  for all  $\rho$ . We write  $\models_K \varphi$  iff  $T \models_K \varphi$  for all  $T$ .

Modal logic K has a sound and complete proof system as shown in Figure 2.3. In the literature, (K) is also known as the distribution axiom and (N) is also known as the necessitation rule. We use  $\vdash_K \varphi$  to denote the corresponding provability relation.

**Theorem 2.7.** For any modal logic formula  $\varphi$ ,  $\models_K \varphi$  iff  $\vdash_K \varphi$ .

## 2.8 MODAL $\mu$ -CALCULUS

Modal  $\mu$ -calculus [27] is an extension of modal logic K with fixpoints.

**Definition 2.28.** The syntax of modal  $\mu$ -calculus extends the syntax of modal logic K with an additional grammar rule:

$$\begin{array}{l} \text{modal } \mu\text{-calculus formulas} \quad \varphi ::= (\text{syntax of modal logic K}) \\ \quad \mid \mu X . \varphi \quad \text{if } \varphi \text{ is positive in } X \end{array}$$

where  $X \in \text{AP}$  is also an atomic proposition. Following the convention, we use  $p$  for free atomic propositions and  $X$  when they are bound by  $\mu$ .

The operator  $\mu$  is the least fixpoint operator. Its dual is  $\nu$ , which is the greatest fixpoint operator, given by  $\nu X . \varphi \equiv \neg \mu X . \neg \varphi[\neg X/X]$ .

**Definition 2.29.** The modal  $\mu$ -calculus satisfaction relation  $T, \rho, s \models_{L\mu} \varphi$  extends the modal logic satisfaction relation  $\models_K$  by adding a rule for  $\mu$ . Firstly, we introduce the notation

$$|\varphi|_{T,\rho}^{L\mu} = \{s \in S \mid T, \rho, s \models_{L\mu} \varphi\}$$

---

(MODAL LOGIC K)	all proof rules in Figure 2.3
(PRE-FIXPOINT)	$\varphi[\mu X . \varphi / X] \rightarrow \mu X . \varphi$
	$\frac{\varphi[\psi / X] \rightarrow \psi}{\mu X . \varphi \rightarrow \psi}$
(KNASTER TARSKI)	

---

Figure 2.4: Sound and Complete Proof System of Modal  $\mu$ -Calculus

Then, we add the following rule for  $\mu$ :

$$|\mu X . \varphi|_{T,\rho}^{L\mu} = \bigcap \{A \subseteq S \mid |\varphi|_{T,\rho[A/X]}^{L\mu} \subseteq A\}$$

The derived rule for  $\nu$  is

$$|\nu X . \varphi|_{T,\rho}^{L\mu} = \bigcup \{A \subseteq S \mid A \subseteq |\varphi|_{T,\rho[A/X]}^{L\mu}\}$$

We write  $\models_{L\mu} \varphi$  iff  $|\varphi|_{T,\rho}^{L\mu} = S$  for all  $T$  and  $\rho$ , that is,  $T, \rho, s \models_{L\mu} \varphi$  for all  $T$ ,  $\rho$ , and  $s$ .

Modal  $\mu$ -calculus has a sound and complete proof system, as shown in Figure 2.4. We use  $\vdash_{L\mu} \varphi$  to denote the corresponding provability relation.

**Theorem 2.8.** For any modal  $\mu$ -calculus formula  $\varphi$ ,  $\models_{L\mu} \varphi$  iff  $\vdash_{L\mu} \varphi$ .

## 2.9 TEMPORAL LOGICS

We review three temporal logics: infinite-trace linear temporal logic (infinite-trace LTL), finite-trace linear temporal logic (finite-trace LTL), and computation tree logic (CTL).

### 2.9.1 Infinite-trace LTL

**Definition 2.30.** Let  $AP$  be a set of atomic propositions. The syntax of infinite-trace LTL is given by the following grammar:

$$\begin{array}{ll} \text{infinite-trace LTL formulas} & \varphi ::= p \in AP \\ & \mid \varphi_1 \wedge \varphi_2 \\ & \mid \neg \varphi \\ & \mid \circ \varphi \qquad \qquad \qquad // \text{“next } \varphi\text{”} \end{array}$$



$$\mid \varphi_1 U \varphi_2 \quad // \text{“}\varphi_1 \text{ until } \varphi_2\text{”}$$

The other modal operators such as  $\diamond\varphi$  (“eventually  $\varphi$ ”) and  $\Box\varphi$  (“always  $\varphi$ ”) can be defined as follows:

$$\diamond\varphi \equiv \top U \varphi \qquad \Box\varphi \equiv \neg \diamond \neg \varphi$$

where  $\top$  is a formula that always holds, which can be defined by  $\top \equiv p \vee \neg p$ .

The models of infinite-trace LTL are infinite traces over  $\mathcal{P}(AP)$ . We use  $\alpha = \alpha_0\alpha_1\alpha_2\ldots$  to denote an infinite trace, where  $\alpha_i \subseteq AP$  for every  $i \geq 0$ . We use  $\alpha_{\geq i}$  to denote the suffix trace  $\alpha_i\alpha_{i+1}\alpha_{i+2}\ldots$ .

**Definition 2.31.** The infinite-trace LTL satisfaction relation  $\alpha \models_{\text{infLTL}} \varphi$  is defined as follows:

1.  $\alpha \models_{\text{infLTL}} p$  iff  $p \in \alpha_0$  for every  $p \in AP$ ;
2.  $\alpha \models_{\text{infLTL}} \varphi_1 \wedge \varphi_2$  iff  $\alpha \models_{\text{infLTL}} \varphi_1$  and  $\alpha \models_{\text{infLTL}} \varphi_2$ ;
3.  $\alpha \models_{\text{infLTL}} \neg\varphi$  iff  $\alpha \not\models_{\text{infLTL}} \varphi$ ;
4.  $\alpha \models_{\text{infLTL}} \circ\varphi$  iff  $\alpha_{\geq 1} \models_{\text{infLTL}} \varphi$ ;
5.  $\alpha \models_{\text{infLTL}} \varphi_1 U \varphi_2$  iff there exists  $j \geq 0$  such that  $\alpha_{\geq j} \models_{\text{infLTL}} \varphi_2$  and for every  $i < j$ ,  $\alpha_{\geq i} \models_{\text{infLTL}} \varphi_1$ .

We write  $\models_{\text{infLTL}} \varphi$  to mean  $\alpha \models_{\text{infLTL}} \varphi$  for all  $\alpha$ .

Infinite-trace LTL has a sound and complete proof system, as shown in Figure 2.5. We use  $\vdash_{\text{infLTL}} \varphi$  to denote the corresponding provability relation.

**Theorem 2.9.** For any infinite-trace LTL formula  $\varphi$ ,  $\models_{\text{infLTL}} \varphi$  iff  $\vdash_{\text{infLTL}} \varphi$ .

### 2.9.2 Finite-trace LTL

Finite execution traces play an important role in program verification and monitoring. Unlike infinite-trace LTL, finite-trace LTL use finite traces as its models.

**Definition 2.32.** Let  $AP$  be a set of atomic propositions. The syntax of finite-trace LTL is given by the following grammar:

$$\text{finite-trace LTL formulas} \quad \varphi ::= p \in AP$$

---

(PROPOSITIONAL TAUTOLOGY)	$\varphi$ , if $\varphi$ is a propositional tautology
(MODUS PONENS)	$\frac{\varphi_1 \quad \varphi_1 \rightarrow \varphi_2}{\varphi_2}$
(K <sub>○</sub> )	$\circ(\varphi_1 \rightarrow \varphi_2) \rightarrow (\circ\varphi_1 \rightarrow \circ\varphi_2)$
(N <sub>○</sub> )	$\frac{\varphi}{\circ\varphi}$
(K <sub>□</sub> )	$\Box(\varphi_1 \rightarrow \varphi_2) \rightarrow (\Box\varphi_1 \rightarrow \Box\varphi_2)$
(N <sub>□</sub> )	$\frac{\varphi}{\Box\varphi}$
(FUN)	$\circ\varphi \leftrightarrow \neg(\circ\neg\varphi)$
(U1)	$(\varphi_1 U \varphi_2) \rightarrow \Diamond\varphi_2$
(U2)	$(\varphi_1 U \varphi_2) \leftrightarrow (\varphi_2 \vee (\varphi_1 \wedge \circ(\varphi_1 U \varphi_2)))$
(IND)	$\Box(\varphi \rightarrow \circ\varphi) \rightarrow (\varphi \rightarrow \Box\varphi)$

---

Figure 2.5: Sound and Complete Proof System of Infinite-Trace LTL

$\varphi_1 \wedge \varphi_2$	
$\neg\varphi$	
$\circ\varphi$	// “next $\varphi$ ”
$\varphi_1 W \varphi_2$	// “ $\varphi_1$ weak-until $\varphi_2$ ”

Unlike  $\varphi_1 U \varphi_2$  in infinite-trace LTL,  $\varphi_1 W \varphi_2$  only requires that  $\varphi_1$  remains true until  $\varphi_2$  becomes true, but it does not require that  $\varphi_2$  eventually becomes true. Therefore, it is possible that  $\varphi_2$  remains false until the end of the trace.

**Definition 2.33.** The finite-trace LTL satisfaction relation  $\alpha_0 \dots \alpha_n \models_{\text{finLTL}} \varphi$  with  $n \geq 0$  is defined as follows:

1.  $\alpha_0 \dots \alpha_n \models_{\text{finLTL}} p$  iff  $p \in \alpha_0$  for every  $p \in AP$ ;
2.  $\alpha_0 \dots \alpha_n \models_{\text{finLTL}} \varphi_1 \wedge \varphi_2$  iff  $\alpha_0 \dots \alpha_n \models_{\text{finLTL}} \varphi_1$  and  $\alpha_0 \dots \alpha_n \models_{\text{finLTL}} \varphi_2$ ;
3.  $\alpha_0 \dots \alpha_n \models_{\text{finLTL}} \neg\varphi$  iff  $\alpha_0 \dots \alpha_n \not\models_{\text{finLTL}} \varphi$ ;

---

(PROPOSITIONAL TAUTOLOGY)	$\varphi$ , if $\varphi$ is a propositional tautology
(MODUS PONENS)	$\frac{\varphi_1 \quad \varphi_1 \rightarrow \varphi_2}{\varphi_2}$
(K <sub>○</sub> )	$\circ(\varphi_1 \rightarrow \varphi_2) \rightarrow (\circ\varphi_1 \rightarrow \circ\varphi_2)$
(N <sub>○</sub> )	$\frac{\varphi}{\circ\varphi}$
(K <sub>□</sub> )	$\Box(\varphi_1 \rightarrow \varphi_2) \rightarrow (\Box\varphi_1 \rightarrow \Box\varphi_2)$
(N <sub>□</sub> )	$\frac{\varphi}{\Box\varphi}$
(¬○)	$\neg \circ \varphi \rightarrow \circ \neg \varphi$
(COIND)	$\frac{\circ\varphi \rightarrow \varphi}{\varphi}$
(FIX)	$(\varphi_1 \, W \, \varphi_2) \leftrightarrow (\varphi_2 \vee (\varphi_1 \wedge \circ(\varphi_1 \, W \, \varphi_2)))$

---

Figure 2.6: Sound and Complete Proof System of Finite-Trace LTL

4.  $\alpha_0 \dots \alpha_n \models_{\text{finLTL}} \circ\varphi$  iff  $n = 0$  or  $\alpha_1 \dots \alpha_n \models_{\text{finLTL}} \varphi$ ;
5.  $\alpha_0 \dots \alpha_n \models_{\text{finLTL}} \varphi_1 \, W \, \varphi_2$  iff one of the following holds:
  - (a) for every  $i \leq n$ ,  $\alpha_i \dots \alpha_n \models_{\text{finLTL}} \varphi_1$ ;
  - (b) there is  $j \leq n$  such that  $\alpha_j \dots \alpha_n \models_{\text{finLTL}} \varphi_2$  and for every  $i < j$ ,  $\alpha_i \dots \alpha_n \models_{\text{finLTL}} \varphi_1$ .

We write  $\models_{\text{finLTL}} \varphi$  to mean that  $\alpha_0 \dots \alpha_n \models_{\text{finLTL}} \varphi$  for all  $n$  and  $\alpha_0 \dots \alpha_n$ .

Finite-trace LTL has a sound and complete proof system, as shown in Figure 2.6. We use  $\vdash_{\text{finLTL}} \varphi$  to denote its provability relation.

**Theorem 2.10.** For any finite-trace LTL formula  $\varphi$ ,  $\models_{\text{finLTL}} \varphi$  iff  $\vdash_{\text{finLTL}} \varphi$ .

### 2.9.3 CTL

CTL is a branching-time logic whose time model has a tree-like structure.

**Definition 2.34.** Let  $AP$  be a set of atomic propositions. The syntax of CTL is given by the following grammar:

<u>CTL formulas</u>	$\varphi ::= p \in AP$	
	$  \varphi_1 \wedge \varphi_2$	
	$  \neg \varphi$	
	$  AX\varphi$	// “on all paths, next $\varphi$ ”
	$  EX\varphi$	// “on one path, next $\varphi$ ”
	$  \varphi_1 AU \varphi_2$	// “on all paths, $\varphi_1$ until $\varphi_2$ ”
	$  \varphi_1 EU \varphi_2$	// “on one path, $\varphi_1$ until $\varphi_2$ ”

Other modal operators can be defined as follows:

$EF\varphi \equiv \top EU \varphi$	// “on one path, eventually $\varphi$ ”
$AF\varphi \equiv \top AU \varphi$	// “on all paths, eventually $\varphi$ ”
$AG\varphi \equiv \neg EF\neg\varphi$	// “on all paths, always $\varphi$ ”
$EG\varphi \equiv \neg AF\neg\varphi$	// “on one path, always $\varphi$ ”

As we can see, every CTL operator consists of a path quantifier (A or E) and a trace quantifier (X, U, F, or G). The path quantifiers specify whether a property should hold on all paths (A) or one path (E). The trace quantifiers have the same meaning as their infinite-trace LTL counterparts, where X is “next”, U is “until”, F is “eventually”, and G is “always”.

CTL models are infinite trees over  $\mathcal{P}(AP)$ . Given an infinite tree  $\tau$ , we use  $\text{root}(\tau) \subseteq AP$  to denote its root and  $\tau \rightarrow_{\text{subtree}} \tau'$  to indicate that  $\tau'$  is an immediate sub-tree of  $\tau$ .

**Definition 2.35.** The CTL satisfaction relation  $\tau \models_{\text{CTL}} \varphi$  is defined as follows:

- $\tau \models_{\text{CTL}} p$  iff  $p \in \text{root}(\tau)$  for every  $p \in AP$ ;
- $\tau \models_{\text{CTL}} \varphi_1 \wedge \varphi_2$  iff  $\tau \models_{\text{CTL}} \varphi_1$  and  $\tau \models_{\text{CTL}} \varphi_2$ ;
- $\tau \models_{\text{CTL}} \neg\varphi$  iff  $\tau \not\models_{\text{CTL}} \varphi$ ;
- $\tau \models_{\text{CTL}} AX\varphi$  iff for all  $\tau'$  such that  $\tau \rightarrow_{\text{subtree}} \tau'$ ,  $\tau' \models_{\text{CTL}} \varphi$ ;
- $\tau \models_{\text{CTL}} EX\varphi$  iff there exists  $\tau'$  such that  $\tau \rightarrow_{\text{subtree}} \tau'$  and  $\tau' \models_{\text{CTL}} \varphi$ ;
- $\tau \models_{\text{CTL}} \varphi_1 AU \varphi_2$  if for all  $\tau_0, \tau_1, \dots$  such that  $\tau = \tau_0 \rightarrow_{\text{subtree}} \tau_1 \rightarrow_{\text{subtree}} \dots$ , there exists  $i \geq 0$  such that  $\tau_i \models_{\text{CTL}} \varphi_2$  and for all  $j < i$ ,  $\tau_j \models_{\text{CTL}} \varphi_1$ ;

---

(PROPOSITIONAL TAUTOLOGY)	$\varphi$ , if $\varphi$ is a propositional tautology
(MODUS PONENS)	$\frac{\varphi_1 \quad \varphi_1 \rightarrow \varphi_2}{\varphi_2}$
(CTL <sub>1</sub> )	$\text{EX}(\varphi_1 \vee \varphi_2) \leftrightarrow \text{EX}\varphi_1 \vee \text{EX}\varphi_2$
(CTL <sub>2</sub> )	$\text{AX}\varphi \leftrightarrow \neg(\text{EX}\neg\varphi)$
(CTL <sub>3</sub> )	$\varphi_1 \text{ EU } \varphi_2 \leftrightarrow \varphi_2 \vee (\varphi_1 \wedge \text{EX}(\varphi_1 \text{ EU } \varphi_2))$
(CTL <sub>4</sub> )	$\varphi_1 \text{ AU } \varphi_2 \leftrightarrow \varphi_2 \vee (\varphi_1 \wedge \text{AX}(\varphi_1 \text{ AU } \varphi_2))$
(CTL <sub>5</sub> )	$\text{EX}\top \wedge \text{AX}\top$
(CTL <sub>6</sub> )	$\text{AG}(\varphi_3 \rightarrow (\neg\varphi_2 \wedge \text{EX}\varphi_3)) \rightarrow (\varphi_3 \rightarrow \neg(\varphi_1 \text{ AU } \varphi_2))$
(CTL <sub>7</sub> )	$\text{AG}(\varphi_3 \rightarrow (\neg\varphi_2 \wedge (\varphi_1 \rightarrow \text{AX}\varphi_3))) \rightarrow (\varphi_3 \rightarrow \neg(\varphi_1 \text{ EU } \varphi_2))$
(CTL <sub>8</sub> )	$\text{AG}(\varphi_1 \rightarrow \varphi_2) \rightarrow (\text{EX}\varphi_1 \rightarrow \text{EX}\varphi_2)$

---

Figure 2.7: Sound and Complete Proof System of CTL

- $\tau \models_{\text{CTL}} \varphi_1 \text{ EU } \varphi_2$  iff there exists  $\tau_0, \tau_1, \dots$  such that  $\tau = \tau_0 \rightarrow_{\text{subtree}} \tau_1 \rightarrow_{\text{subtree}} \dots$ , and there exists  $i \geq 0$  such that  $\tau_i \models_{\text{CTL}} \varphi_2$  and for all  $j < i$ ,  $\tau_j \models_{\text{CTL}} \varphi_1$ .

We write  $\models_{\text{CTL}} \varphi$  iff  $\tau \models_{\text{CTL}} \varphi$  for all  $\tau$ .

CTL has a sound and complete proof system, as shown in Figure 2.7. We use  $\vdash_{\text{CTL}} \varphi$  to denote the corresponding provability relation.

**Theorem 2.11.** For any CTL formula  $\varphi$ ,  $\models_{\text{CTL}} \varphi$  iff  $\vdash_{\text{CTL}} \varphi$ .

## 2.10 DYNAMIC LOGIC

Dynamic logic, abbreviated as DL, is a common logic for program reasoning [14, 28, 29, 30].

**Definition 2.36.** Let  $AP$  be a set of atomic propositions and  $APgm$  be a set of *atomic programs*. The syntax of DL is given by the following grammar:

$$\begin{aligned} \text{DL formulas } \varphi ::= & p \in AP \\ & | \varphi_1 \wedge \varphi_2 \end{aligned}$$

	$  \neg\varphi$	
	$  [\alpha]\varphi$	
<u>DL programs</u>	$\alpha ::= a \in APgm$	
	$  \alpha_1 ; \alpha_2$	// sequence
	$  \alpha_1 \cup \alpha_2$	// choice
	$  \alpha^*$	// iteration
	$  \varphi?$	// test

The dual of  $[\alpha]$  is  $\langle\alpha\rangle$ , given by  $\langle\alpha\rangle\varphi \equiv \neg[\alpha](\neg\varphi)$ . Other program constructs such as if-then-else, while-do, etc., can be defined as derived constructs [28, 29, 30].

**Definition 2.37.** Given an atomic program set  $APgm$ , a *DL model* is a tuple  $T = (S, \{\xrightarrow{a}_{a \in APgm}\})$  where  $S$  is a set of states and  $\xrightarrow{a} \subseteq S \times S$  is a transition relation for every  $a \in APgm$ . A *DL  $T$ -valuation* is a function  $\rho: AP \rightarrow \mathcal{P}(S)$ . The DL satisfaction relation  $T, \rho, s \models_{DL} \varphi$  is defined as follows. Firstly, we introduce the notation

$$|\varphi|_{T,\rho}^{DL} = \{s \in S \mid T, \rho, s \models_{DL} \varphi\}$$

Then, we define  $|\varphi|_{T,\rho}^{DL} \subseteq S$  and  $|\alpha|_{T,\rho}^{DL} \subseteq S \times S$  for all  $\varphi, \alpha$ , and  $\rho$  using the following rules:

1.  $|p|_{T,\rho}^{DL} = \rho(p)$  for  $p \in AP$ ;
2.  $|\varphi_1 \wedge \varphi_2|_{T,\rho}^{DL} = |\varphi_1|_{T,\rho}^{DL} \cap |\varphi_2|_{T,\rho}^{DL}$ ;
3.  $|\neg\varphi|_{T,\rho}^{DL} = S \setminus |\varphi|_{T,\rho}^{DL}$ ;
4.  $|[\alpha]\varphi|_{T,\rho}^{DL} = \{s \in S \mid \text{for all } t \in S, (s, t) \in |\alpha|_{T,\rho}^{DL} \text{ implies } t \in |\varphi|_{T,\rho}^{DL}\}$ ;
5.  $|a|_{T,\rho}^{DL} = (\xrightarrow{a})$  for  $a \in APgm$ ;
6.  $|\alpha_1 ; \alpha_2|_{T,\rho}^{DL} = |\alpha_1|_{T,\rho}^{DL} \circ |\alpha_2|_{T,\rho}^{DL}$ ;
7.  $|\alpha_1 \cup \alpha_2|_{T,\rho}^{DL} = |\alpha_1|_{T,\rho}^{DL} \cup |\alpha_2|_{T,\rho}^{DL}$ ;
8.  $|\alpha^*|_{T,\rho}^{DL} = (|\alpha|_{T,\rho}^{DL})^*$ ;
9.  $|\varphi?|_{T,\rho}^{DL} = \{(s, s) \mid s \in |\varphi|_{T,\rho}^{DL}\}$ .

Recall that  $|\alpha_1|_{T,\rho}^{DL} \circ |\alpha_2|_{T,\rho}^{DL}$  is the composition of  $|\alpha_1|_{T,\rho}^{DL}$  and  $|\alpha_2|_{T,\rho}^{DL}$ , and  $(|\alpha|_{T,\rho}^{DL})^*$  is the reflexive and transitive closure of  $|\alpha|_{T,\rho}^{DL}$ , as defined in Section 2.1. We write  $\models_{DL} \varphi$  iff  $|\varphi|_{T,\rho}^{DL} = S$  for all  $T$  and  $\rho$ .

---

(PROPOSITIONAL TAUTOLOGY)	$\varphi$ , if $\varphi$ is a propositional tautology
(MODUS PONENS)	$\frac{\varphi_1 \quad \varphi_1 \rightarrow \varphi_2}{\varphi_2}$
(DL <sub>1</sub> )	$[\alpha](\varphi_1 \rightarrow \varphi_2) \rightarrow ([\alpha]\varphi_1 \rightarrow [\alpha]\varphi_2)$
(DL <sub>2</sub> )	$[\alpha](\varphi_1 \wedge \varphi_2) \leftrightarrow ([\alpha]\varphi_1 \wedge [\alpha]\varphi_2)$
(DL <sub>3</sub> )	$[\alpha \cup \beta]\varphi \leftrightarrow [\alpha]\varphi \wedge [\beta]\varphi$
(DL <sub>4</sub> )	$[\alpha ; \beta]\varphi \leftrightarrow [\alpha][\beta]\varphi$
(DL <sub>5</sub> )	$[\psi?]\varphi \leftrightarrow (\psi \rightarrow \varphi)$
(DL <sub>6</sub> )	$\varphi \wedge [\alpha][\alpha^*]\varphi \leftrightarrow [\alpha^*]\varphi$
(DL <sub>7</sub> )	$\varphi \wedge [\alpha^*](\varphi \rightarrow [\alpha]\varphi) \rightarrow [\alpha^*]\varphi$
(GEN)	$\frac{\varphi}{[\alpha]\varphi}$

---

Figure 2.8: Sound and Complete Proof System of DL

DL has sound and complete proof system, as shown in Figure 2.8. We use  $\vdash_{\text{DL}} \varphi$  to denote the corresponding provability relation.

**Theorem 2.12.** For any DL formula  $\varphi$ ,  $\models_{\text{DL}} \varphi$  iff  $\vdash_{\text{DL}} \varphi$ .

## 2.11 $\lambda$ -CALCULUS

$\lambda$ -calculus [31] is a Turing-complete foundation of computation based on function abstraction and application.

**Definition 2.38.** Let  $V$  be a set of variables denoted by  $x, y$ , etc. The syntax of  $\lambda$ -calculus is as follows:

<u><math>\lambda</math>-expressions</u>	$e ::= x$	
	$  e_1 e_2$	// function application
	$  \lambda x . e$	// function abstraction

where  $\lambda$  is a binder. We use  $\text{freeVar}(e) \subseteq V$  to denote the free variables of  $e$  and  $e[e'/x]$  to denote the result of substituting  $e'$  for  $x$  in  $e$ , where  $\alpha$ -renaming implicitly happens to avoid variable capture. Let  $\Lambda$  be the set of all  $\lambda$ -expressions.

In  $\lambda$ -calculus, we are interested in proving equations between  $\lambda$ -expressions. Equational reasoning in  $\lambda$ -calculus includes the standard reflexivity, symmetry, transitivity, and congruence proof rules in Figure 2.2, plus a distinguished  $(\beta)$  axiom schema that specifies the result of function application:

$$(\beta) \quad (\lambda x . e) e' = e[e'/x] \quad \text{for all } x \in V \text{ and } e, e' \in \Lambda$$

We write  $\vdash_\lambda e_1 = e_2$  to mean that  $e_1 = e_2$  is provable.

$\lambda$ -calculus has many notions of models. Here, we review the concrete Cartesian closed category models, abbreviated as concrete ccc models (see [32, Definition 5.5.9]).

Firstly, we define application structures.

**Definition 2.39.** An *application structure* is a tuple  $(A, \_ \bullet_A \_)$  where  $A$  is a set and  $\_ \bullet_A \_ : A \times A \rightarrow A$  is a binary function.

Next, we define pre-models.

**Definition 2.40.** Given an applicative structure  $(A, \_ \bullet_A \_)$  and  $a \in A$ , we define a function  $\mathbb{A}(a) : A \rightarrow A$  given by  $\mathbb{A}(a)(b) = a \bullet_A b$  for all  $b \in A$ . Let  $R(A) = \text{codomain}(\mathbb{A})$ , which is called the set of *representable functions*; that is,

$$R(A) = \{f : A \rightarrow A \mid \text{there is } a \in A \text{ such that } f = \mathbb{A}(a)\}$$

If there exists  $\mathcal{G} : R(A) \rightarrow A$  such that  $\mathbb{A} \circ \mathcal{G}$  is the identity function over  $R(A)$ , we call  $\mathcal{G}$  a *retraction function*, and  $(A, \_ \bullet_A \_, \mathcal{G})$  a *pre-model*.

Finally, we define concrete ccc models.

**Definition 2.41.** A *concrete ccc model* is a pre-model in Definition 2.40, if the following rules for defining  $|e|_\rho^\lambda$  for all  $\rho : V \rightarrow A$  are well-defined:

1.  $|x|_\rho^\lambda = \rho(x)$ ;
2.  $|e_1 e_2|_\rho^\lambda = |e_1|_\rho^\lambda \bullet_A |e_2|_\rho^\lambda$ ;
3.  $|\lambda x . e|_\rho^\lambda = \mathcal{G}(f_{e,x}^\rho)$  where  $f_{e,x}^\rho(a) = |e|_{\rho[a/x]}^\lambda$  for  $a \in A$  and  $f_{e,x}^\rho \in R(A)$ .



We write  $A \models_\lambda e_1 = e_2$  iff  $|e_1|_\rho^\lambda = |e_2|_\rho^\lambda$  for all  $\rho$ . We write  $\models_\lambda e_1 = e_2$  iff  $A \models_\lambda e_1 = e_2$  for all concrete ccc models  $A$ .

Concrete ccc models are sound and complete with respect to  $\models_\lambda$ , which denotes equational reasoning over  $\lambda$ -expressions.

**Theorem 2.13.** For any  $\lambda$ -expressions  $e_1$  and  $e_2$ ,  $\models_\lambda e_1 = e_2$  iff  $\vdash_\lambda e_1 = e_2$ .

## 2.12 TERM-GENERIC FIRST-ORDER LOGIC

Term-generic first-order logic [33], or simply term-generic logic (abbreviated as TGL), is a variant of many-sorted FOL whose syntax is parametric in a set of generic terms. Generic terms are generalization of FOL terms in Definition 2.3, which are inductively built using function symbols. Generic terms are not built using function symbols, but defined axiomatically. There are two operations related to generic terms: free variables  $\text{freeVar}(e)$  and capture-free substitution  $e[e'/x]$ . These operations should satisfy certain conditions [33, Definition 2.1]. TGL formulas are built in the same way as FOL formulas, except that FOL terms are now replaced by generic terms.

TGL aims at defining various logics and calculi that feature bindings, such as  $\lambda$ -calculus. These bindings-featuring systems usually cannot be naturally defined as FOL theories. On the other hand, the generic terms in TGL can be instantiated to different kinds of concrete terms, such as the expressions of  $\lambda$ -calculus. This way, many bindings-featuring systems can be naturally defined as TGL theories.

In this work, we do not need TGL in its full generality. Instead, we present a concrete instance of TGL where the generic terms are inductively built from a syntax that features binders of the form  $b(x : s_1, t_{s_2})$ , where  $x : s_1$  is bound by  $b$  in  $t_{s_2}$ . The semantics and proof system of TGL will also be introduced using this concrete instance.

**Definition 2.42.** A (*many-sorted*) *binder signature* is a tuple  $(S, F, B, \Pi)$ , where  $(S, F, \Pi)$  is a FOL signature and  $B = \{B_{s_1, s_2, s}\}_{s_1, s_2, s \in S}$  is an  $S^3$ -indexed set of *binders*. Let  $V = \{V_s\}_{s \in S}$  be an  $S$ -indexed set of variables. The syntax of TGL is given by the following grammar:

$$\begin{array}{ll}
 \underline{\text{TGL } (S, F, B, \Pi)\text{-terms}} & t_s ::= (\text{syntax of FOL terms}) \\
 & \quad | b(x : s_1, t_{s_2}) \text{ with } b \in B_{s_1, s_2, s} \\
 \underline{\text{TGL formulas}} & \varphi ::= (\text{syntax of FOL formulas}) \\
 & \quad | t_s = t'_s
 \end{array}$$

We use  $\text{TGLTERM}$  and  $\text{TGLFORM}$  to denote the sets of TGL terms and formulas, respectively.

Unlike FOL, the semantics of TGL has a Henkin-style definition, where terms and formulas are interpreted at the same time.

**Definition 2.43.** Let  $A = \{A_s\}_{s \in S}$  be an  $S$ -indexed set. A *TGL  $A$ -valuation* or simply *TGL valuation* is a function  $\rho: V \rightarrow A$ . Let  $\text{TGLVAL} = [V \rightarrow A]$  be the set of all TGL valuations. A *TGL model* is a tuple  $(\{A_s\}_{s \in S}, \{A_t\}_{t \in \text{TGLTERM}}, \{A_\pi\}_{\pi \in \Pi})$ , where

1.  $A_{t_s}: \text{TGLVAL} \rightarrow A_s$  is a function for every  $t_s \in \text{TGLTERM}$ ; in addition, the following conditions should hold for all  $x:s \in V_s$ ,  $t_s, t'_s \in \text{TGLTERM}$ , and  $\rho \in \text{TGLVAL}$ :

- (a)  $A_{x:s}(\rho) = \rho(x:s)$ .
- (b)  $A_{t_s[t'_s/x:s]}(\rho) = A_{t_s}(\rho[A_{t'_s}(\rho)/x:s]);$

2.  $A_\pi \subseteq A_{s_1} \times \cdots \times A_{s_n}$  for every  $\pi \in \Pi_{s_1 \dots s_n}$ .

**Definition 2.44.** Under the notation of Definition 2.43, we define  $A_\varphi \subseteq \text{TGLVAL}$  for every  $\varphi \in \text{TGLFORM}$  using the following rules:

1.  $\rho \in A_{t_s=t'_s}$  iff  $A_{t_s}(\rho) = A_{t'_s}(\rho)$ ;
2.  $\rho \in A_{\pi(t_{s_1}, \dots, t_{s_n})}$  iff  $A_\pi(A_{t_{s_1}}(\rho), \dots, A_{t_{s_n}}(\rho))$  holds;
3.  $\rho \in A_{\varphi_1 \wedge \varphi_2}$  iff  $\rho \in A_{\varphi_1}$  and  $\rho \in A_{\varphi_2}$ ;
4.  $\rho \in A_{\neg \varphi}$  iff  $\rho \notin A_\varphi$ ;
5.  $\rho \in A_{\exists x:s.\varphi}$  iff there exists  $a \in A_s$  such that  $\rho[a/x:s] \in A_\varphi$ .

The TGL satisfaction relation  $A, \rho \models_{\text{TGL}} \varphi$  is defined by  $\rho \in A_\varphi$ . We write  $A \models_{\text{TGL}} \varphi$  iff  $A, \rho \models_{\text{TGL}} \varphi$  for all  $\rho$ , that is,  $A_\varphi = \text{TGLVAL}$ . Given a set  $\Gamma$  of TGL formulas, we write  $A \models_{\text{TGL}} \Gamma$  iff  $A \models_{\text{TGL}} \psi$  for all  $\psi \in \Gamma$ . For two sets  $\Delta_1$  and  $\Delta_2$ , we write  $\Gamma \models_{\text{TGL}} \Delta_1 \triangleright \Delta_2$  iff  $\bigcap_{\varphi \in \Delta_1} A_\varphi \subseteq \bigcup_{\varphi \in \Delta_2} A_\varphi$  for all  $A \models_{\text{TGL}} \Gamma$ . Intuitively,  $\Delta_1 \triangleright \Delta_2$  states that if all the formulas in  $\Delta_1$  hold, then one of the formulas in  $\Delta_2$  holds.

TGL has a sound and complete Gentzen-style proof system, as shown in Figure 2.9. The proof system derives sequents of the form  $\Gamma \vdash_{\text{TGL}} \Delta_1 \triangleright \Delta_2$ , where  $\Gamma, \Delta_1, \Delta_2 \subseteq \text{TGLFORM}$ . Following the convention of writing Gentzen-style proof rules, we write  $\Delta, \varphi$  to mean  $\Delta \cup \{\varphi\}$ . We require that all the formulas in  $\Gamma$  are closed (i.e., ) and  $\Delta_1, \Delta_2$  are finite. These requirements are needed for Theorem 2.14.

**Theorem 2.14** ([33, Theorem 3.1]). Let  $\Gamma$  be a set of closed TGL formulas. For any finite  $\Delta_1, \Delta_2 \in \text{TGLFORM}$ ,  $\Gamma \models_{\text{TGL}} \Delta_1 \triangleright \Delta_2$  iff  $\Gamma \vdash_{\text{TGL}} \Delta_1 \triangleright \Delta_2$ .

(AX)	$\frac{\cdot}{\Delta_1 \triangleright \Delta_2} \text{ if } \Delta_1 \cap \Delta_2 \neq \emptyset$
(LEFT $\rightarrow$ )	$\frac{\Delta_1 \triangleright \Delta_2, \varphi_1 \quad \Delta_1, \varphi_2 \triangleright \Delta_2}{\Delta_1, (\varphi_1 \rightarrow \varphi_2) \triangleright \Delta_2}$
(RIGHT $\rightarrow$ )	$\frac{\Delta_1, \varphi \triangleright \Delta_2, \varphi_2}{\Delta_1 \triangleright \Delta_2, (\varphi_1 \rightarrow \varphi_2)}$
(LEFT $\wedge$ )	$\frac{\Delta_1, \varphi_1, \varphi_2 \triangleright \Delta_2}{\Delta_1, (\varphi_1 \wedge \varphi_2) \triangleright \Delta_2}$
(RIGHT $\wedge$ )	$\frac{\Delta_1 \triangleright \Delta_2, \varphi_1 \quad \Delta_1 \triangleright \Delta_2, \varphi_2}{\Delta_1 \triangleright \Delta_2, (\varphi_1 \wedge \varphi_2)}$
(LEFT $\forall$ )	$\frac{\Delta_1, \forall x. \varphi, \varphi[t/x] \triangleright \Delta_2}{\Delta_1, \forall x. \varphi \triangleright \Delta_2}$
(RIGHT $\forall$ )	$\frac{\Delta_1 \triangleright \Delta_2, \varphi[y/x]}{\Delta_1 \triangleright \Delta_2, \forall x. \varphi} \text{ if } y \text{ is fresh}$
(REFLEXIVITY)	$\frac{\Delta_1, t = t \triangleright \Delta_2}{\Delta_1 \triangleright \Delta_2}$
(SYMMETRY)	$\frac{\Delta_1 \triangleright \Delta_2, t_1 = t_2 \quad \Delta_1, t_2 = t_1 \triangleright \Delta_2}{\Delta_1 \triangleright \Delta_2}$
(TRANSITIVITY)	$\frac{\Delta_1 \triangleright \Delta_2, t_1 = t_2 \quad \Delta_1 \triangleright \Delta_2, t_2 = t_3 \quad \Delta_1, t_1 = t_3 \triangleright \Delta_2}{\Delta_1 \triangleright \Delta_2}$
(CMP $_{\pi}$ )	$\frac{\Delta_1 \triangleright \Delta_2, t_i = t'_i \text{ for all } 1 \leq i \leq n \quad \Delta_1 \triangleright \Delta_2, \pi(t_1, \dots, t_n) \quad \Delta_1, \pi(t'_1, \dots, t'_n) \triangleright \Delta_2}{\Delta_1 \triangleright \Delta_2}$
(SBS)	$\frac{\Delta_1 \triangleright \Delta_2, t_1 = t_2 \quad \Delta_1, t[t_1/x] = t[t_2/x] \triangleright \Delta_2}{\Delta_1 \triangleright \Delta_2}$
(BINDER)	$\frac{\Delta_1 \triangleright \Delta_2, t = t' \quad \Delta_1, b(x, t) = b(x, t') \triangleright \Delta_2}{\Delta_1 \triangleright \Delta_2}$

Figure 2.9: Sound and Complete Proof System of TGL [33, Figs. 1-2] Plus (BINDER)



Like in FOL,  $\exists$  and  $\forall$  are binders. We adopt the standard notions of free variables,  $\alpha$ -renaming, and capture-avoiding substitution. We let  $\text{freeVar}(\varphi)$  denote the set of free variables in  $\varphi$ . When  $\text{freeVar}(\varphi) = \emptyset$ , we say  $\varphi$  is closed. We regard  $\alpha$ -equivalent patterns  $\varphi$  and  $\varphi'$  as the same, and write  $\varphi \equiv \varphi'$ . We let  $\varphi[\psi/x]$  be the result of substituting  $\psi$  for every free occurrence of  $x$  in  $\varphi$ , where  $\alpha$ -renaming happens implicitly to prevent variable capture. We let  $\varphi[\psi_1/x_1, \dots, \psi_n/x_n]$  be the result of simultaneously substituting  $\psi_1, \dots, \psi_n$  for  $x_1, \dots, x_n$ .

**Definition 2.46.** Given a matching logic signature  $(S, \Sigma)$ , a *matching logic  $(S, \Sigma)$ -model* or simply a *model* is a tuple  $M = (\{M_s\}_{s \in S}, \{\sigma_M\}_{\sigma \in \Sigma})$ , where

- $M_s$  is a nonempty carrier set, for every  $s \in S$ ;
- $\sigma_M: M_{s_1} \times \dots \times M_{s_n} \rightarrow \mathcal{P}(M_s)$  is a function, for every  $\sigma \in \Sigma_{s_1 \dots s_n, s}$ .

FOL function symbols can be regarded as a special instance of matching logic symbols, where  $\text{card}(\sigma_M(a_1, \dots, a_n)) = 1$  for all  $a_1 \in M_{s_1}, \dots, a_n \in M_{s_n}$ . Similarly, partial functions in partial FOL [34] can be regarded as a special instance, where  $\text{card}(\sigma_M(a_1, \dots, a_n)) \geq 1$  for all  $a_1 \in M_{s_1}, \dots, a_n \in M_{s_n}$ . The undefinedness of  $\sigma_M$  at  $a_1, \dots, a_n$  is captured by letting  $\sigma_M(a_1, \dots, a_n) = \emptyset$ .

**Definition 2.47.** Given a matching logic  $(S, \Sigma)$  and an  $(S, \Sigma)$ -model  $M$ . An  *$M$ -valuation* or simply *valuation* is a function  $\rho: V \rightarrow M$ . The matching logic interpretation function  $|\_|\_{M, \rho}: \text{MLPATTERN} \rightarrow \mathcal{P}(M)$  is inductively defined as follows:

- $|x : s|\_{M, \rho} = \{\rho(x : s)\}$  for all  $x : s \in V_s$ ;
- $|\varphi_1 \wedge \varphi_2|\_{M, \rho} = |\varphi_1|\_{M, \rho} \cap |\varphi_2|\_{M, \rho}$ ;
- $|\neg \varphi|\_{M, \rho} = M_s \setminus |\varphi|\_{M, \rho}$  for every  $\varphi_s \in \text{MLPATTERN}_s$ ;
- $|\exists x . \varphi|\_{M, \rho} = \bigcup_{a \in M_{s'}} |\varphi|\_{M, \rho[a/x]}$ ;
- $|\sigma(\varphi_1, \dots, \varphi_n)|\_{M, \rho} = \sigma_M^{\text{ext}}(|\varphi_1|\_{M, \rho}, \dots, |\varphi_n|\_{M, \rho})$  for  $\sigma \in \Sigma_{s_1 \dots s_n, s}$ ;

where  $\sigma_M^{\text{ext}}$  is the pointwise extension of  $\sigma_M$  in Section 2.1. We feel free to drop the subscripts  $M$  and  $\rho$  when they are not important or relevant. We say that  $\varphi_s$  is *valid* in  $M$ , written  $M \models \varphi_s$ , iff  $|\varphi_s|\_{M, \rho} = M_s$  for all  $\rho$ .

Intuitively,  $|\varphi|\_{M, \rho}$  is the set of elements that match  $\varphi$  under  $M$  and  $\rho$ . There is a close relation between the semantics of patterns and set operations. For example,  $\varphi_1 \wedge \varphi_2$  is matched

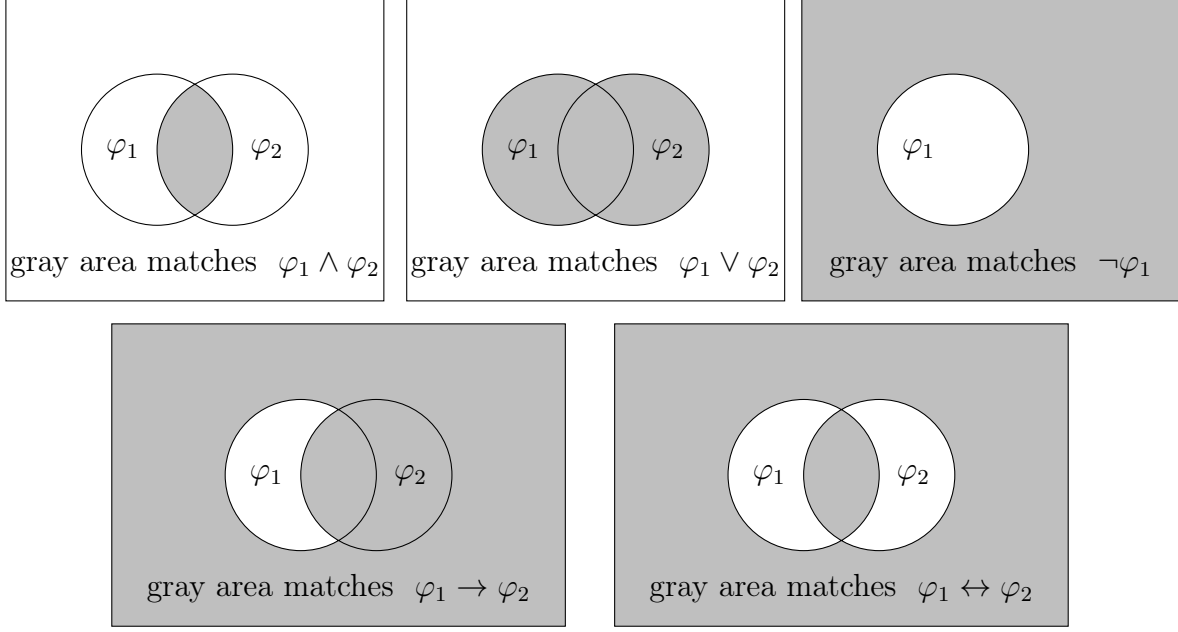


Figure 2.10: Matching Logic Semantics Illustration [2, Fig. 4]

by those elements that match both  $\varphi_1$  and  $\varphi_2$ . Therefore,  $|\varphi_1 \wedge \varphi|_{M,\rho} = |\varphi_1|_{M,\rho} \cap |\varphi|_{M,\rho}$ . In other words, conjunction ( $\wedge$ ) means set intersection. Similarly, disjunction ( $\vee$ ) means set union and negation ( $\neg$ ) means set complement.

**Proposition 2.15.**  $|\exists x : s . x : s|_{M,\rho} = M_s$ .

*Proof.* By definition,  $|x : s . x : s|_{M,\rho} = \bigcup_{a \in M_s} |x : s|_{M,\rho} = \bigcup_{a \in M_s} \{a\} = M_s$ . QED.

**Definition 2.48.** Given a matching logic signature  $(S, \Sigma)$ , a *matching logic*  $(S, \Sigma)$ -*theory* or simply *theory* is a tuple  $(S, \Sigma, \Gamma)$ , where  $\Gamma$  is a set of  $(S, \Sigma)$ -patterns/axioms. When  $(S, \Sigma)$  is understood, we simply use  $\Gamma$  to denote the theory  $(S, \Sigma, \Gamma)$ . We write  $M \models \Gamma$  iff  $M \models \psi$  for all  $\psi \in \Gamma$ . We write  $\Gamma \models \varphi$  iff  $M \models \varphi$  for all  $M \models \Gamma$ .

### 2.13.2 Important theories

Several mathematical instruments of practical importance, such as definedness, equality, membership, and functions, can be defined as matching logic theories.

**Definition 2.49.** For a set  $S$  of sorts, let  $\Sigma^{\text{definedness}}$  and  $\Gamma^{\text{definedness}}$  be as follows:

$$\begin{aligned} \Sigma^{\text{definedness}} &= \{ \lceil \_ \rceil_s^{s'} \mid s, s' \in S \} && // \text{ definedness symbols} \\ \Gamma^{\text{definedness}} &= \{ \lceil x : s \rceil_s^{s'} \mid s, s' \in S \} && // \text{ (DEFINEDNESS) axioms} \end{aligned}$$

Furthermore, we introduce the following notation:

$$\begin{aligned}
 \lfloor \varphi \rfloor_s^{s'} &\equiv \neg \lceil \neg \varphi \rceil_s^{s'} && // \text{ totality} \\
 \varphi_1 =_s^{s'} \varphi_2 &\equiv \lfloor \varphi_1 \leftrightarrow \varphi_2 \rfloor_s^{s'} && // \text{ equality} \\
 x \in_s^{s'} \varphi &\equiv \lceil x \wedge \varphi \rceil_s^{s'} && // \text{ membership} \\
 \varphi_1 \subseteq_s^{s'} \varphi_2 &\equiv \lfloor \varphi_1 \rightarrow \varphi_2 \rfloor_s^{s'} && // \text{ set inclusion}
 \end{aligned}$$

and feel free to drop the sort superscripts/subscripts when they are not important.

**Proposition 2.16.** *For any  $M \models \Gamma^{\text{definedness}}$ , the following hold:*

- $(\lfloor \_ \rfloor_s^{s'})_M(a) = M_{s'}$  for all  $a \in M_s$ ;
- $\lceil \varphi \rceil_s^{s'}|_{M,\rho} = M_{s'}$  if  $|\varphi|_{M,\rho} \neq \emptyset$ ; otherwise,  $\lceil \varphi \rceil_s^{s'}|_{M,\rho} = \emptyset$ ;
- $\lfloor \varphi \rfloor_s^{s'}|_{M,\rho} = M_{s'}$  if  $|\varphi|_{M,\rho} = M_s$ ; otherwise,  $\lfloor \varphi \rfloor_s^{s'}|_{M,\rho} = \emptyset$ ;
- $|\varphi_1 =_s^{s'} \varphi_2|_{M,\rho} = M_{s'}$  if  $|\varphi_1|_{M,\rho} = |\varphi_2|_{M,\rho}$ ; otherwise,  $|\varphi_1 =_s^{s'} \varphi_2|_{M,\rho} = \emptyset$ ;
- $|x \in_s^{s'} \varphi|_{M,\rho} = M_{s'}$  if  $\rho(x) \in |\varphi|_{M,\rho}$ ; otherwise,  $|x \in_s^{s'} \varphi|_{M,\rho} = \emptyset$ ;
- $|\varphi_1 \subseteq_s^{s'} \varphi_2|_{M,\rho} = M_{s'}$  if  $|\varphi_1|_{M,\rho} \subseteq |\varphi_2|_{M,\rho}$ ; otherwise,  $|\varphi_1 \subseteq_s^{s'} \varphi_2|_{M,\rho} = \emptyset$ ; in particular,  $|x \subseteq_s^{s'} \varphi|_{M,\rho} = |x \in_s^{s'} \varphi|_{M,\rho}$ ;
- $M \models \varphi_1 =_s^{s'} \varphi_2$  if and only if  $M \models \varphi_1 \leftrightarrow \varphi_2$ ;
- $M \models \varphi_1 \subseteq_s^{s'} \varphi_2$  if and only if  $M \models \varphi_1 \rightarrow \varphi_2$ .

In Section 2.13.1, we have shown that functions (or partial functions) can be regarded as a special instance of matching logic symbols where  $\text{card}(\sigma_M(a_1, \dots, a_n)) = 1$  (or  $\leq 1$ ). We can enforce the function (or partial function) semantics of a matching logic symbol using patterns/axioms.

**Definition 2.50.** Given a matching logic symbol  $\sigma \in \Sigma_{s_1 \dots s_n, s}$ , let  $\Gamma^{\text{function}(\sigma)}$  and  $\Gamma^{\text{pfunction}(\sigma)}$  be as follows:

$$\begin{aligned}
 \Gamma^{\text{function}(\sigma)} &= \{\exists y : s. \sigma(x_1 : s_1, \dots, x_n : s_n) = y : s\} && // \text{ (FUNCTION) axiom} \\
 \Gamma^{\text{pfunction}(\sigma)} &= \{\exists y : s. \sigma(x_1 : s_1, \dots, x_n : s_n) \subseteq y : s\} && // \text{ (PARTIAL FUNCTION) axiom}
 \end{aligned}$$

For brevity, we use  $\sigma : s_1 \times \dots \times s_n \rightarrow s$  and  $\sigma : s_1 \times \dots \times s_n \rightharpoonup s$  to denote (FUNCTION) and (PARTIAL FUNCTION), respectively, and we feel free to drop the sorts when they are not

important. Given  $F \subseteq \Sigma$ , which is a set of symbols to be interpreted as functions (or partial functions), we let  $\Gamma^{\text{function}(F)} = \bigcup_{f \in F} \Gamma^{\text{function}(f)}$  (or  $\Gamma^{\text{pfunction}(F)} = \bigcup_{f \in F} \Gamma^{\text{pfunction}(f)}$ ) to denote the corresponding set of axioms.

Unlike FOL where formulas are two-valued ( $\top$  or  $\perp$ ), matching logic patterns can be evaluated to any subsets of the underlying carrier sets. However, we can restore the FOL semantics by letting  $\top_s$  denote logical truth and  $\perp_s$  denote logical false in sort  $s$ . A FOL predicate symbol over  $s_1 \dots s_n$  is a special instance of a matching logic symbol  $\sigma \in \Sigma_{s_1 \dots s_n, \text{Formula}}$  where **Formula** is a distinguished sort for formulas and  $\sigma_M(a_1, \dots, a_n) \in \{\emptyset, M_{\text{Formula}}\}$  for all  $a_i \in M_{s_i}$ ,  $1 \leq i \leq n$ . Note that if  $M_{\text{Formula}}$  is a singleton set, the above condition is automatically satisfied for any symbol whose return sort is **Formula**. Following this idea, we can define FOL in matching logic in the following way, which is a slightly modified version of the definition given in [2, Section 7].

**Definition 2.51.** Given a FOL signature  $(S, F, \Pi)$ , we define the corresponding matching logic theory  $(S^{\text{FOL}}, \Sigma^{\text{FOL}}, \Gamma^{\text{FOL}})$ , or simply  $(S^{\text{FOL}}, \Sigma^{\text{FOL}}, \Gamma^{\text{FOL}})$ , as follows:

$$\begin{aligned} S^{\text{FOL}} &= S \cup \{\text{Formula}\} \\ \Sigma^{\text{FOL}} &= \Gamma^{\text{definedness}} \cup F \cup \{\pi \in \Sigma_{s_1 \dots s_n, \text{Formula}}^{\text{FOL}} \mid \pi \in \Pi_{s_1 \dots s_n}\} \\ \Gamma^{\text{FOL}} &= \Gamma^{\text{definedness}} \cup \Gamma^{\text{function}(F)} \cup \{x : \text{Formula}\} \end{aligned}$$

That is, we add all the FOL sorts to matching logic, with an additional sort **Formula** for FOL formulas. We include all the definedness symbols and axioms, which are necessary for defining functions. We add FOL function symbols as matching logic symbols of the same arities and define them using the function axioms. We add FOL predicate symbols as matching logic symbols with the return sort **Formula**. The axiom  $\{x : \text{Formula}\}$  enforces the carrier set of **Formula** to be a singleton set, so we do not need any axioms for  $\pi \in \Sigma_{s_1 \dots s_n, \text{Formula}}^{\text{FOL}}$ .

This way, all FOL formulas are matching logic  $\Sigma^{\text{FOL}}$ -patterns of sort **Formula**.

**Proposition 2.17** ([2, Proposition 7.1]). *Under the notation of Definition 2.51,  $\models_{\text{FOL}} \varphi$  iff  $\Gamma^{\text{FOL}} \models \varphi$  for every FOL formula  $\varphi$ .*

Our definition of FOL in Definition 2.51 is different from [2, Section 7] in that we enforce **Formula** to be a singleton set using an additional axiom  $\{x : \text{Formula}\}$ . Proposition 2.17 still holds, because for the “only if” direction, we only have fewer matching logic models. For the “if” direction, the original proof in [2] only needs matching logic models where the carrier set of **Formula** is a singleton set, anyway. We prefer Definition 2.51 because it is easier to extend it to other logics, such as LFP (see Section 5.2).



Another important result about the expressive power of matching logic is its ability to define separation logic (SL) as an instance, where we fix the underlying model to be the model of finite maps. However, the following result does not consider recursive symbols in SL, which will be discussed in Section 5.3.

**Definition 2.52.** We define the matching logic theory  $(S^{\text{Map}}, \Sigma^{\text{Map}}, \Gamma^{\text{Map}})$  for maps as follows:

$$\begin{aligned} S^{\text{Map}} &= \{\text{Nat}, \text{Map}\} \\ \Sigma^{\text{Map}} &= \Sigma^{\text{definedness}} \cup \{\text{nil}, \text{emp}, (\_ \mapsto \_), (\_ * \_)\} \end{aligned}$$

and  $\Gamma^{\text{Map}}$  includes  $\Gamma^{\text{definedness}}$  plus the following axioms:

$$\begin{aligned} \text{nil} &: \rightarrow \text{Nat} \\ \text{emp} &: \rightarrow \text{Map} \\ \_ \mapsto \_ &: \text{Nat} \times \text{Nat} \rightarrow \text{Map} \\ \_ * \_ &: \text{Map} \times \text{Map} \rightarrow \text{Map} \\ \text{emp} * h &= h \\ h_1 * h_2 &= h_2 * h_1 \\ (h_1 * h_2) * h_3 &= h_1 * (h_2 * h_3) \\ \text{nil} \mapsto x &= \perp \\ x \mapsto y * x \mapsto z &= \perp \end{aligned}$$

Furthermore, we define  $\varphi_1 \multimap \varphi_2 \equiv \exists h . h \wedge [h * \varphi_1 \rightarrow \varphi_2]$ .

This way, all SL formulas (without recursive symbols) are patterns of sort **Map**.

**Definition 2.53.** The *standard map model* is an  $(S^{\text{Map}}, \Sigma^{\text{Map}})$ -model (where the standard interpretation for the definiteness symbols are omitted)

$$M^{\text{Map}} = (\{M_{\text{Nat}}^{\text{Map}}, M_{\text{Map}}^{\text{Map}}\}, \{\text{nil}_{M^{\text{Map}}}, \text{emp}_{M^{\text{Map}}}, (\_ \mapsto \_)_{M^{\text{Map}}}, (\_ * \_)_{M^{\text{Map}}}\})$$

where

1.  $M_{\text{Nat}}^{\text{Map}} = \mathbb{N}$  and  $M_{\text{Map}}^{\text{Map}} = \mathbb{H}$ , defined in Section 2.6;
2.  $\text{nil}_{M^{\text{Map}}} = \{0\}$ ;
3.  $\text{emp}_{M^{\text{Map}}} = \{\emptyset\}$ , where  $\emptyset \in \mathbb{H}$  denotes the empty heap;

4.  $(\_ \mapsto \_)_{M^{\text{Map}}}(m, n) = \{h_{m,n}\}$  for every  $m, n \in \mathbb{N}$  with  $m \neq 0$ , where  $h_{m,n}$  is the partial function that maps  $m$  to  $n$  and is undefined anywhere else;
5.  $(\_ \mapsto \_)_{M^{\text{Map}}}(0, n) = \emptyset$  for every  $n \in \mathbb{N}$ ;
6.  $(\_ * \_)_{M^{\text{Map}}}(h_1, h_2) = h_1 \dot{\cup} h_2$  for every  $h_1, h_2 \in \mathbb{H}$  that are disjoint;
7.  $(\_ * \_)_{M^{\text{Map}}}(h_1, h_2) = \emptyset$  for every  $h_1, h_2 \in \mathbb{H}$  that are not disjoint.

**Proposition 2.18** ([2, Proposition 9.2]).  $M^{\text{Map}} \models \Gamma^{\text{Map}}$ . In addition,  $\models_{SL} \varphi$  iff  $M^{\text{Map}} \models \varphi$  for any SL formula  $\varphi$ .

In other words, SL can be regarded as an instance/fragment of matching logic when we fix the underlying model to be  $M^{\text{Map}}$ .

### 2.13.3 Matching logic proof system $\mathcal{P}$

Matching logic has a conditional sound and complete Hilbert-style proof system  $\mathcal{P}$ , as shown in Figure 2.11. We use  $\Gamma \vdash_{\mathcal{P}} \varphi$  to denote the corresponding provability relation. We call  $\mathcal{P}$  a conditional proof system because it requires the definedness symbols and axioms in Definition 2.49. There are proof rules of  $\mathcal{P}$  that use equality “=” and membership “ $\in$ ”, both requiring the definedness symbols and axioms. Therefore,  $\mathcal{P}$  cannot be used on theories that do not have the definedness symbols or axioms. The completeness of  $\mathcal{P}$  is proved by a reduction from matching logic to pure predicate logic with equality, which is FOL extended with a built-in equality symbol that has no function symbols. Through the reduction, the completeness of matching logic is reduced to the completeness of pure predicate logic with equality. The definedness symbols and axioms are needed for defining equality and membership, which are needed for mimicking the proofs of pure predicate logic with equality in matching logic.

**Definition 2.54.** For a matching logic symbol  $\sigma \in \Sigma$ , we write  $C_{\sigma}[\varphi]$  to mean a pattern of the form  $\sigma(\psi_1, \dots, \psi_{i-1}, \varphi, \psi_{i+1}, \dots, \psi_n)$ .

**Theorem 2.19** ([2, Theorem 11.2]). For any matching logic theory  $\Gamma$  that includes the definedness symbols and axioms in Definition 2.49,  $\Gamma \models \varphi$  iff  $\Gamma \vdash_{\mathcal{P}} \varphi$ , for any matching logic pattern  $\varphi$ .

(PROPOSITIONAL TAUTOLOGY)	$\varphi$ , if $\varphi$ is a proposition tautology
(MODUS PONENS)	$\frac{\varphi_1 \quad \varphi_1 \rightarrow \varphi_2}{\varphi_2}$
(FUNCTIONAL SUBSTITUTION)	$(\forall x . \varphi) \wedge (\exists y . \varphi' = y) \rightarrow \varphi[\varphi'/x]$ if $y \notin \text{freeVar}(\varphi')$
( $\forall$ )	$\forall x . (\varphi_1 \rightarrow \varphi_2) \rightarrow (\varphi_1 \rightarrow \forall x . \varphi_2)$ if $x \notin \text{freeVar}(\varphi_1)$
(UNIVERSAL GENERALIZATION)	$\frac{\varphi}{\forall x . \varphi}$
(EQUALITY INTRODUCTION)	$\varphi = \varphi$
(EQUALITY ELIMINATION)	$(\varphi_1 = \varphi_2) \wedge \psi[\varphi_1/x] \rightarrow \psi[\varphi_2/x]$
(MEMBERSHIP INTRODUCTION)	$\frac{\varphi}{\forall x . (x \in \varphi)}$ if $x \notin \text{freeVar}(\varphi)$
(MEMBERSHIP ELIMINATION)	$\frac{\forall x . (x \in \varphi)}{\varphi}$ if $x \notin \text{freeVar}(\varphi)$
(MEMBERSHIP VARIABLE)	$(x \in y) = (x = y)$
(MEMBERSHIP $_{\neg}$ )	$(x \in \neg\varphi) = \neg(x \in \varphi)$
(MEMBERSHIP $_{\wedge}$ )	$(x \in \varphi_1 \wedge \varphi_2) = (x \in \varphi_1) \wedge (x \in \varphi_2)$
(MEMBERSHIP $_{\exists}$ )	$(x \in \exists y . \varphi) = \exists y . (x \in \varphi)$ , if $x$ and $y$ are distinct.
(MEMBERSHIP SYMBOL)	$x \in C_{\sigma}[\varphi] = \exists y . (y \in \varphi) \wedge (x \in C_{\sigma}[y])$ if $y \notin \text{freeVar}(C_{\sigma}[\varphi])$

Figure 2.11: Conditional Sound and Complete Proof System  $\mathcal{P}$  of Matching Logic

## 2.14 REACHABILITY LOGIC

Reachability logic [12], abbreviated as RL, is an approach to program verification using operational semantics. Unlike the other approaches such as Hoare-style verification, RL has a language-independent proof system that offers sound and relatively complete deduction for all programming languages. RL is the logic underlying the  $\mathbb{K}$  framework (Section 2.15), which has been used to define the formal semantics of many large programming languages, from which their sound and relatively complete program verifiers are obtained using RL at no additional cost [3].

RL is parametric in a matching logic model for computation configurations. Specifically, fix a signature (of static program configurations)  $\Sigma^{\text{Cfg}}$  which may have various sorts and symbols, among which there is a distinguished sort  $\text{Cfg}$ . Fix a  $\Sigma^{\text{Cfg}}$ -model  $M^{\text{Cfg}}$  called the *configuration model*, where  $M_{\text{Cfg}}^{\text{Cfg}}$  is the set of all computation configurations. RL formulas are called *reachability rules*, or simply *rules*, and have the form  $\varphi_1 \Rightarrow \varphi_2$  where  $\varphi_1, \varphi_2$  are matching logic  $\Sigma^{\text{Cfg}}$ -patterns. A *reachability system*  $S$  is a finite set of rules, which yields a transition system  $T = (M_{\text{Cfg}}^{\text{Cfg}}, \xrightarrow{T})$  where  $s \xrightarrow{T} t$  iff there exist  $(\varphi_1 \Rightarrow \varphi_2) \in S$  and an  $M^{\text{Cfg}}$ -valuation  $\rho$  such that  $s \in |\varphi_1|_{T,\rho}$  and  $t \in |\varphi_2|_{T,\rho}$ . A rule  $\psi_1 \Rightarrow \psi_2$  is *S-valid*, written  $S \models_{\text{RL}} \psi_1 \Rightarrow \psi_2$ , iff for all  $M_{\text{Cfg}}^{\text{Cfg}}$ -valuations  $\rho$  and  $s \in |\psi_1|_{T,\rho}$ , either there is an infinite trace  $s \xrightarrow{T} t_1 \xrightarrow{T} t_2 \xrightarrow{T} \dots$  in  $T$  or there exists  $t \in T$  such that  $s (\xrightarrow{T})^* r$  and  $t \in |\psi_2|_{T,\rho}$ . Recall that  $(\xrightarrow{T})^*$  is the reflexive and transitive closure of  $\xrightarrow{T}$  in Section 2.1.

RL has a sound and relatively complete proof system, as shown in Figure 2.12. The proof system derives sequents of the form  $A \vdash_C \varphi_1 \Rightarrow \varphi_2$ , where  $A$  (called *axioms*) and  $C$  (called *circularities*) are finite sets of rules. The corresponding provability relation  $S \vdash_{\text{RL}} \psi_1 \Rightarrow \psi_2$  is given by  $S \vdash_{\emptyset} \psi_1 \Rightarrow \psi_2$ . In other words, we start with  $A = S$  and  $C = \emptyset$  in any RL proof. As the proof proceeds, more rules can be added to  $C$  via (CIRCULARITY) and then moved to  $A$  via (TRANSITIVITY), which can then be used via (AXIOM). Note that (CONSEQUENCE) consults the underlying configuration model  $M^{\text{Cfg}}$  for the semantic satisfaction relation, so the completeness of the RL proof system is relative to  $M^{\text{Cfg}}$ .

**Theorem 2.20.** Let  $S$  be a reachability system that satisfies the technical assumptions in [12]. For any  $\psi_1 \Rightarrow \psi_2$ ,  $S \models_{\text{RL}} \psi_1 \Rightarrow \psi_2$  iff  $S \vdash_{\text{RL}} \psi_1 \Rightarrow \psi_2$ .

## 2.15 $\mathbb{K}$ FRAMEWORK

$\mathbb{K}$  framework is an effort in realizing the ideal language framework vision in Figure 1.1. An easy way to understand  $\mathbb{K}$  is to look at it as a meta-language that can define other programming languages. In Figure 2.13, we show an example  $\mathbb{K}$  language definition of an

	$\frac{\varphi \Rightarrow \varphi' \in A}{A \vdash_C \varphi \Rightarrow \varphi'}$
(AXIOM)	
	$\frac{}{A \vdash_\emptyset \varphi \Rightarrow \varphi}$
(REFLEXIVITY)	
	$\frac{A \vdash_C \varphi_1 \Rightarrow \varphi_2 \quad A \cup C \vdash \varphi_2 \Rightarrow \varphi_3}{A \vdash_C \varphi_1 \Rightarrow \varphi_3}$
(TRANSITIVITY)	
	$\frac{A \vdash_C \varphi \Rightarrow \varphi' \quad \psi \text{ is a FOL formula}}{A \vdash_C \varphi \wedge \psi \Rightarrow \varphi' \wedge \psi}$
(LOGIC FRAMING)	
	$\frac{M^{\text{Cfg}} \models \varphi_1 \rightarrow \varphi'_1 \quad A \vdash_C \varphi'_1 \Rightarrow \varphi'_2 \quad M^{\text{Cfg}} \models \varphi'_2 \rightarrow \varphi_2}{A \vdash_C \varphi_1 \Rightarrow \varphi_2}$
(CONSEQUENCE)	
	$\frac{A \vdash_C \varphi_1 \Rightarrow \varphi \quad A \vdash_C \varphi_2 \Rightarrow \varphi}{A \vdash_C \varphi_1 \vee \varphi_2 \Rightarrow \varphi}$
(CASE ANALYSIS)	
	$\frac{A \vdash_C \varphi \Rightarrow \varphi' \quad X \cap \text{free Var}(\varphi') = \emptyset}{A \vdash_C \exists X. \varphi \Rightarrow \varphi'}$
(ABSTRACTION)	
	$\frac{A \vdash_{C \cup \{\varphi \Rightarrow \varphi'\}} \varphi \Rightarrow \varphi'}{A \vdash_C \varphi \Rightarrow \varphi'}$
(CIRCULARITY)	

Figure 2.12: Sound and Relatively Complete Proof System of RL

imperative language IMP. In the 39-line definition, we completely define the formal syntax and the (executable) formal semantics of IMP, using a front end language that is easy to understand. From this language definition,  $\mathbb{K}$  can generate all language tools for IMP, including its parser, interpreter, verifier, etc.

We use IMP as an example to illustrate the main  $\mathbb{K}$  features. There are two modules: **IMP-SYNTAX** defines the syntax and **IMP** defines the semantics using rewrite rules. Syntax is defined as BNF grammars. The keyword **syntax** leads production rules that can have attributes that specify the additional syntactic and/or semantic information. For example, in lines 11-12, we define the syntax of if-statements, as expected. A production rule can be associated with attributes, which are written in brackets. For example, the syntax of **if**-statements is defined in lines 11-12 and has the attribute **[strict(1)]**, meaning that the evaluation order is strict in the first argument, i.e., the condition of an **if**-statement. There are many other attributes. Some attributes (like **[strict(1)]**) have semantic meaning while the others are only used for parsing. For example, the attribute **[left]** in line 6 means that the binary construct “+” is left associative.

In the module **IMP**, we define the *configurations* of IMP and its formal semantics. A configuration (lines 23-25) is a constructor term that has all semantic information needed to

```

1  module IMP-SYNTAX
2    imports DOMAINS-SYNTAX
3    syntax Exp ::=
4      Int
5      | Id
6      | Exp "+" Exp    [left, strict]
7      | Exp "-" Exp    [left, strict]
8      | "(" Exp ")"    [bracket]
9    syntax Stmt ::=
10     Id "=" Exp ";" [strict(2)]
11     | "if" "(" Exp ")"
12     | Stmt Stmt    [strict(1)]
13     | "while" "(" Exp ")" Stmt
14     | "{" Stmt "}" [bracket]
15     | "{" "}"
16     > Stmt Stmt    [left, strict(1)]
17   syntax Pgm ::= "int" Ids ";" Stmt
18   syntax Ids ::= List{Id, ","}
19   endmodule

20 module IMP imports IMP-SYNTAX
21   imports DOMAINS
22   syntax KResult ::= Int
23   configuration
24     <T> <k> $PGM:Pgm </k>
25     <state> .Map </state> </T>
26   rule <k> X:Id => I ...</k>
27     <state>... X |-> I ...</state>
28   rule I1 + I2 => I1 +Int I2
29   rule I1 - I2 => I1 -Int I2
30   rule <k> X = I:Int => I ...</k>
31     <state>... X |-> ( _ => I ) ...</state>
32   rule {} S:Stmt => S
33   rule if(I) S _ => S requires I /=Int 0
34   rule if(0) _ S => S
35   rule while(B) S => if(B) {S while(B) S} {}
36   rule <k> int (X, Xs => Xs) ; S </k>
37     <state>... ( . => X |-> 0 ) </state>
38   rule int .Ids ; S => S
39   endmodule

```

Figure 2.13: Complete Formal Semantics of IMP in  $\mathbb{K}$ 

execute programs. IMP configurations are simple, consisting of the IMP code and a program state that maps variables to values. We organize configurations using (semantic) cells:  $\langle /k \rangle$  is the cell of IMP code and  $\langle /state \rangle$  is the cell of program states. In the initial configuration (lines 24-25),  $\langle /state \rangle$  is empty and  $\langle /k \rangle$  contains the IMP program that we pass to  $\mathbb{K}$  for execution (represented by the special  $\mathbb{K}$  variable  $\$PGM$ ).

We define formal semantics using rewrite rules. In lines 26-27, we define the semantics of variable lookup, where we match on a variable  $X$  in the  $\langle /k \rangle$  cell and look up its value  $I$  in the  $\langle /state \rangle$  cell, by matching on the binding  $X \mapsto I$ . Then, we rewrite  $X$  to  $I$ , denoted by  $X \Rightarrow I$  in the  $\langle /k \rangle$  cell in line 26. Rewrite rules in  $\mathbb{K}$  are similar to those in the rewrite engines such as Maude [35].

## Chapter 3: TWO COMPLETENESS THEOREMS FOR MATCHING LOGIC

As we have seen in Section 2.13.3, the proof system  $\mathcal{P}$  requires the definedness symbols and axioms. A natural question is: Is there a proof system of matching logic that does not require the definedness symbols or axioms and can be used to do formal reasoning in any theories?

We will present such a proof system, which we refer to as the proof system  $\mathcal{H}$ . Unlike  $\mathcal{P}$ ,  $\mathcal{H}$  does not require the definedness symbols or axioms so it can be used to do formal reasoning in any matching logic theories. We will prove the soundness of  $\mathcal{H}$  (Theorem 3.1).

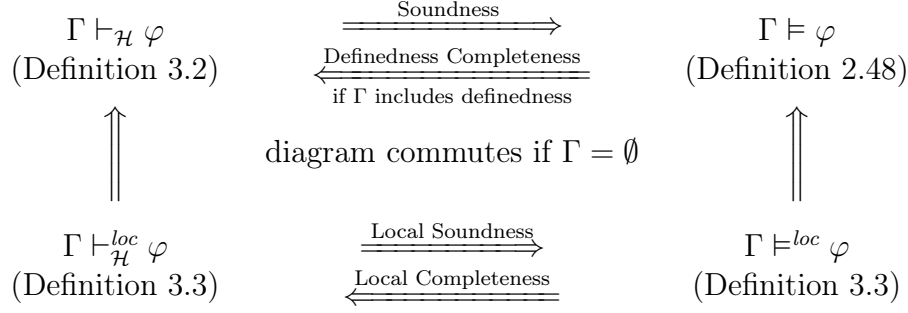
As for completeness of  $\mathcal{H}$ , we prove two important results. The first result is the definedness completeness of  $\mathcal{H}$ , stated in Theorem 3.8. It says that  $\mathcal{H}$  is complete for any theory that includes the definedness symbols and axioms. Therefore,  $\mathcal{H}$  is at least as good as  $\mathcal{P}$ . We prove the definedness completeness result by showing that all the proof rules of  $\mathcal{P}$  are derivable using  $\mathcal{H}$  and the definedness axioms.

The second completeness result is the local completeness of  $\mathcal{H}$ , stated in Theorem 3.13. For this result, we define two new relations  $\Gamma \models^{loc} \varphi$  and  $\Gamma \vdash_{\mathcal{H}}^{loc} \varphi$  (Definition 3.3). We call  $\Gamma \models^{loc} \varphi$  the local validity relation and  $\Gamma \vdash_{\mathcal{H}}^{loc} \varphi$  the local provability relation. These local relations are stronger than their (global) counterparts  $\Gamma \models \varphi$  and  $\Gamma \vdash_{\mathcal{H}} \varphi$ , respectively, and when  $\Gamma = \emptyset$ , they are equivalent to their global counterparts. The local (soundness) and completeness result states that  $\Gamma \models^{loc} \varphi$  iff  $\Gamma \vdash_{\mathcal{H}}^{loc} \varphi$ .

We summarize the soundness, the definedness completeness, and the local completeness of  $\mathcal{H}$  in Figure 3.1. We know that the diagram commutes if  $\Gamma = \emptyset$ . We also know that the diagram does not commute for an arbitrary  $\Gamma$ . There exists a (nonempty)  $\Gamma$  and a pattern  $\varphi$  such that  $\Gamma \models \varphi$  and  $\Gamma \vdash_{\mathcal{H}} \varphi$ , but  $\Gamma \not\models^{loc} \varphi$  and  $\Gamma \not\vdash_{\mathcal{H}}^{loc} \varphi$ , and we give such a counterexample in Section 3.3. The local soundness and completeness of  $\mathcal{H}$  shows that the two local relations are equivalent for all  $\Gamma$  and  $\varphi$ . However, we do not know whether the two global relations are also equivalent for all  $\Gamma$  and  $\varphi$ . More precisely, we do not know whether  $\Gamma \models \varphi$  always implies  $\Gamma \vdash_{\mathcal{H}} \varphi$ ; we call it the global completeness of  $\mathcal{H}$ . We only know that the implication holds when  $\Gamma = \emptyset$ , which is the local completeness result, and when  $\Gamma$  includes definedness, which is the definedness completeness result. Global completeness of  $\mathcal{H}$  is still an open problem.

### 3.1 MATCHING LOGIC PROOF SYSTEM $\mathcal{H}$

We first need the following definition of application contexts.

Figure 3.1: Known Relation among  $\models$ ,  $\models^{loc}$ ,  $\vdash_{\mathcal{H}}$ , and  $\vdash_{\mathcal{H}}^{loc}$ 

**Definition 3.1.** Let  $C$  be a pattern and  $\square$  be a distinguished variable that occurs exactly once in  $C$ . We call  $C$  an *application context* if  $\square$  appears within a number of (nested) symbols. Formally,  $C$  is an application context if

1.  $C$  is  $\square$ ; or
2.  $C$  is  $C_{\sigma}[C']$  and  $C'$  is an application context. Note that  $C_{\sigma}[C']$  is the shortcut of  $\sigma(\varphi_1, \dots, \varphi_{i-1}, C', \varphi_{i+1}, \dots, \varphi_n)$  in Definition 2.54.

We write  $C[\varphi]$  to mean  $C[\varphi/\square]$ .

The proof system  $\mathcal{H}$  is shown in Figure 3.2. It has nine proof rules that can be divided to three categories. The first category consists of four proof rules: (PROPOSITIONAL TAUTOLOGY), (MODUS PONENS), ( $\exists$ -QUANTIFIER), and ( $\exists$ -GENERALIZATION). These four proof rules belong to the complete axiomatization of pure predicate logic; see, e.g., [36]. The second category consists of three proof rules: (PROPAGATION $_{\vee}$ ), (PROPAGATION $_{\exists}$ ), and (FRAMING). These three proof rules characterize the behaviors of symbols and allow us to propagate logical reasoning through symbols. The third category contains two technical rules that are necessary for proving definedness completeness (Theorem 3.8) and local completeness (Theorem 3.14).

**Definition 3.2.** We use  $\Gamma \vdash_{\mathcal{H}} \varphi$  to denote the provability relation defined by  $\mathcal{H}$ .

Note that all proof rules of  $\mathcal{H}$  are general rules and do not depend on any special symbols such as the definedness symbols. Therefore,  $\mathcal{H}$  can be used to do formal reasoning in any theories.

### 3.1.1 Soundness of $\mathcal{H}$

We will show that  $\mathcal{H}$  is sound, that is,  $\Gamma \vdash_{\mathcal{H}} \varphi$  implies  $\Gamma \models \varphi$ , stated in Theorem 3.1. We first prove Lemma 3.1, known as the substitution lemma.



---

(PROPOSITIONAL TAUTOLOGY)	$\varphi$ if $\varphi$ is a propositional tautology over patterns
(MODUS PONENS)	$\frac{\varphi_1 \quad \varphi_1 \rightarrow \varphi_2}{\varphi_2}$
( $\exists$ -QUANTIFIER)	$\varphi[y/x] \rightarrow \exists x . \varphi$
( $\exists$ -GENERALIZATION)	$\frac{\varphi_1 \rightarrow \varphi_2}{(\exists x . \varphi_1) \rightarrow \varphi_2} \text{ if } x \notin \text{freeVar}(\varphi_2)$

---

(PROPAGATION $_{\vee}$ )	$C_{\sigma}[\varphi_1 \vee \varphi_2] \rightarrow C_{\sigma}[\varphi_1] \vee C_{\sigma}[\varphi_2]$
(PROPAGATION $_{\exists}$ )	$C_{\sigma}[\exists x . \varphi] \rightarrow \exists x . C_{\sigma}[\varphi] \quad \text{if } x \notin \text{freeVar}(C_{\sigma}[\exists x . \varphi])$
(FRAMING)	$\frac{\varphi_1 \rightarrow \varphi_2}{C_{\sigma}[\varphi_1] \rightarrow C_{\sigma}[\varphi_2]}$

---

(EXISTENCE)	$\exists x . x$
(SINGLETON VARIABLE)	$\neg(C_1[x \wedge \varphi] \wedge C_2[x \wedge \neg\varphi])$ $C_1$ and $C_2$ are application contexts

---

Figure 3.2: Sound and Complete Proof System  $\mathcal{H}$  of Matching Logic

**Lemma 3.1.** For any  $M$  and  $M$ -valuation  $\rho$ ,  $|\varphi[y/x]|_{M,\rho} = |\varphi|_{M,\rho[\rho(y)/x]}$ .

*Proof.* We do structural induction on  $\varphi$ .

If  $\varphi$  is  $z$ , distinct from  $x$ , we have  $|z[y/x]|_{M,\rho} = |z|_{M,\rho} = \{\rho(z)\}$  and  $|z|_{M,\rho[\rho(y)/x]} = \{\rho(z)\}$ .

If  $\varphi$  is  $x$ , we have  $|x[y/x]|_{M,\rho} = |y|_{M,\rho} = \{\rho(y)\}$  and  $|x|_{M,\rho[\rho(y)/x]} = \{\rho(y)\}$ .

If  $\varphi$  is  $\sigma(\varphi_1, \dots, \varphi_n)$ , we have

$$\begin{aligned}
 |\sigma(\varphi_1, \dots, \varphi_n)[y/x]|_{M,\rho} &= |\sigma(\varphi_1[y/x], \dots, \varphi_n[y/x])|_{M,\rho} \\
 &= \sigma_M(|\varphi_1[y/x]|_{M,\rho}, \dots, |\varphi_n[y/x]|_{M,\rho}) \\
 &= \sigma_M(|\varphi_1|_{M,\rho[\rho(y)/x]}, \dots, |\varphi_n|_{M,\rho[\rho(y)/x]}) \\
 &= |\sigma(\varphi_1, \dots, \varphi_n)|_{M,\rho[\rho(y)/x]}
 \end{aligned}$$

If  $\varphi$  is  $\varphi_1 \wedge \varphi_2$ , we have  $|(\varphi_1 \wedge \varphi_2)[y/x]|_{M,\rho} = |\varphi_1[y/x] \wedge \varphi_2[y/x]|_{M,\rho} = |\varphi_1[y/x]|_{M,\rho} \cap |\varphi_2[y/x]|_{M,\rho} = |\varphi_1|_{M,\rho[\rho(y)/x]} \cap |\varphi_2|_{M,\rho[\rho(y)/x]} = |\varphi_1 \wedge \varphi_2|_{M,\rho[\rho(y)/x]}$ .

If  $\varphi$  is  $\neg\varphi_1$ , we have  $|(\neg\varphi_1)[y/x]|_{M,\rho} = |\neg(\varphi_1[y/x])|_{M,\rho} = M \setminus |\varphi_1[y/x]|_{M,\rho} = M \setminus |\varphi_1|_{M,\rho[\rho(y)/x]} = |\neg\varphi_1|_{M,\rho[\rho(y)/x]}$ .

If  $\varphi$  is  $\exists z . \varphi_1$ , we can assume that  $z$  is distinct from  $x$  or  $y$  by  $\alpha$ -renaming. Then we have

$$\begin{aligned}
 |(\exists z . \varphi_1)[y/x]|_{M,\rho} &= |\exists z . (\varphi_1[y/x])|_{M,\rho} \\
 &= \bigcup_{a \in M} |\varphi_1[y/x]|_{M,\rho[a/z]} \\
 &= \bigcup_{a \in M} |\varphi_1|_{M, [\rho[a/z](y)/x]} \\
 &= \bigcup_{a \in M} |\varphi_1|_{M, \rho[a/z][\rho(y)/x]} \\
 &= \bigcup_{a \in M} |\varphi_1|_{M, \rho[\rho(y)/x][a/z]} \\
 &= |\exists z . \varphi_1|_{M, \rho[\rho(y)/x]}
 \end{aligned}$$

Therefore, the conclusion holds by structural induction. QED.

**Lemma 3.2.** Let  $C$  be an application context. For any  $M$  and  $M$ -valuation  $\rho$ , we have

1.  $|C[\perp]|_{M,\rho} = \emptyset$ ;
2.  $|C[\varphi_1 \vee \varphi_2]|_{M,\rho} = |\varphi_1|_{M,\rho} \cup |\varphi_2|_{M,\rho}$ ;
3.  $|C[\exists x . \varphi]|_{M,\rho} = \bigcup_{a \in M} |C[\varphi]|_{M,\rho[a/x]}$  if  $x \notin \text{free Var}(C[\exists x . \varphi])$ ;
4.  $|\varphi_1|_{M,\rho} \subseteq |\varphi_2|_{M,\rho}$  implies  $|C[\varphi_1]|_{M,\rho} \subseteq |C[\varphi_2]|_{M,\rho}$ ;
5.  $|C[x \wedge \varphi]|_{M,\rho} \cap |C[x \wedge \neg \varphi]|_{M,\rho} = \emptyset$ .

*Proof.* We do structural induction on  $C$ .

(Base Case). In this case,  $C[\Box]$  is  $\Box$  and  $C[\varphi]$  is just  $\varphi$ . All propositions hold.

(Induction Step). Let us assume  $C[\Box] \equiv C_\sigma[C_1[\Box]]$ , where

$$C_\sigma[\Box] \equiv \sigma(\psi_1, \dots, \psi_{i-1}, \Box, \psi_{i+1}, \dots, \psi_n)$$

for some  $\sigma \in \Sigma$  and  $C$  is an application context. By the inductive hypotheses, all propositions hold for  $C_1$ . For simplicity, let us define

$$\sigma_M^i(A) = \sigma_M(|\psi_1|_{M,\rho}, \dots, |\psi_{i-1}|_{M,\rho}, A, |\psi_{i+1}|_{M,\rho}, \dots, |\psi_n|_{M,\rho})$$

for  $A \subseteq M$ . Note that  $\sigma_M^i$  is monotone, that is,  $\sigma_M^i(A_1) \subseteq \sigma_M^i(A_2)$  if  $A_1 \subseteq A_2$ . Under the above notation,  $|C_\sigma[\varphi]|_{M,\rho} = \sigma_M^i(|\varphi|_{M,\rho})$ . We now prove (1)–(5).

For (1), we have  $|C_\sigma[C_1[\perp]]|_{M,\rho} = \sigma_M^i(|C_1[\perp]|_{M,\rho}) = \sigma_M^i(\emptyset) = \emptyset$ .

For (2), we have  $|C_\sigma[C_1[\varphi_1 \vee \varphi_2]]|_{M,\rho} = \sigma_M^i(|C_1[\varphi_1 \vee \varphi_2]|_{M,\rho}) = \sigma_M^i(|C_1[\varphi_1]|_{M,\rho} \cup |C_1[\varphi_2]|_{M,\rho}) = \sigma_M^i(|C_1[\varphi_1]|_{M,\rho}) \cup \sigma_M^i(|C_1[\varphi_2]|_{M,\rho}) = |C_\sigma[C_1[\varphi_1]]|_{M,\rho} \cup |C_\sigma[C_1[\varphi_2]]|_{M,\rho}$ .

For (3), we have  $|C_\sigma[C_1[\exists x. \varphi]]|_{M,\rho} = \sigma_M^i(|C_1[\exists x. \varphi]|_{M,\rho}) = \sigma_M^i(\bigcup_a |C_1[\varphi]|_{M,\rho[a/x]})$ . Because  $x \notin \text{free Var}(C_\sigma[C_1[\exists x. \varphi]])$ , we have  $\sigma_M^i(\bigcup_a |C_1[\varphi]|_{M,\rho[a/x]}) = \bigcup_a \sigma_M^i(|C_1[\varphi]|_{M,\rho[a/x]}) = \bigcup_a |C_\sigma[C_1[\varphi]]|_{M,\rho[a/x]}$ .

For (4), we need to prove that  $|C_\sigma[C_1[\varphi_1]]|_{M,\rho} \subseteq |C_\sigma[C_1[\varphi_2]]|_{M,\rho}$ , that is,  $\sigma_M^i(|C_1[\varphi_1]|_{M,\rho}) \subseteq \sigma_M^i(|C_1[\varphi_2]|_{M,\rho})$ . Since  $\sigma_M^i$  is monotone, we only need to prove that  $|C_1[\varphi_1]|_{M,\rho} \subseteq |C_1[\varphi_2]|_{M,\rho}$ . The latter holds by the inductive hypotheses and  $|\varphi_1|_{M,\rho} \subseteq |\varphi_2|_{M,\rho}$ .

For (5), we do a case analysis. If  $\rho(x) \in |\varphi|_{M,\rho}$ , we have  $|x \wedge \varphi|_{M,\rho} = \emptyset$ , and thus  $|C[x \wedge \varphi]|_{M,\rho} = \emptyset$ . Otherwise, we have  $|x \wedge \neg\varphi|_{M,\rho} = \emptyset$ , and thus  $|C[x \wedge \neg\varphi]|_{M,\rho} = \emptyset$ .

Therefore, all propositions hold by structural induction. QED.

**Lemma 3.3.** For any model  $M$ , the following propositions hold:

1.  $M \models \varphi$  for propositional tautology  $\varphi$  over patterns of the same sort;
2.  $M \models \varphi_1$  and  $M \models \varphi_1 \rightarrow \varphi_2$  imply  $M \models \varphi_2$ ;
3.  $M \models \varphi[y/x] \rightarrow \exists x. \varphi$ ;
4.  $M \models \varphi_1 \rightarrow \varphi_2$  implies  $M \models (\exists x. \varphi_1) \rightarrow \varphi_2$  if  $x \notin \text{free Var}(\varphi_2)$ ;
5.  $M \models C_\sigma[\perp] \rightarrow \perp$ ;
6.  $M \models C_\sigma[\varphi_1 \vee \varphi_2] \rightarrow C_\sigma[\varphi_1] \vee C_\sigma[\varphi_2]$ ;
7.  $M \models C_\sigma[\exists x. \varphi] \rightarrow \exists x. C_\sigma[\varphi]$  if  $x \notin \text{free Var}(C_\sigma[\exists x. \varphi])$ ;
8.  $M \models \varphi_1 \rightarrow \varphi_2$  implies  $M \models C_\sigma[\varphi_1] \rightarrow C_\sigma[\varphi_2]$ ;
9.  $M \models \exists x. x$ ;
10.  $M \models \neg(C_1[x \wedge \varphi] \wedge C_2[x \wedge \neg\varphi])$

*Proof.* (1) and (2) are proved in [2, Proposition 2.8]. Note that  $M \models \varphi_1 \rightarrow \varphi_2$  iff  $|\varphi_1|_{M,\rho} \subseteq |\varphi_2|_{M,\rho}$  for all  $\rho$  (see [2, Proposition 2.6]). We will use this property to prove (3)–(8). In the following, let  $\rho$  be any valuation.

(3). By Lemma 3.1,  $|\varphi[y/x]|_{M,\rho} = |\varphi|_{M,\rho[\rho(y)/x]} \subseteq \bigcup_a |\varphi|_{M,\rho[a/x]} = |\exists x. \varphi|_{M,\rho}$ .

(4). We need to prove that  $|\exists x. \varphi_1|_{M,\rho} \subseteq |\varphi_2|_{M,\rho}$ , that is,  $\bigcup_a |\varphi_1|_{M,\rho[a/x]} \subseteq |\varphi_2|_{M,\rho}$ . We only need to prove that  $|\varphi_1|_{M,\rho[a/x]} \subseteq |\varphi_2|_{M,\rho}$  for all  $a \in M$ . Because  $x \notin \text{free Var}(\varphi_1)$ , we have  $|\varphi_1|_{M,\rho[a/x]} = |\varphi_1|_{M,\rho}$ . Thus, we only need to prove that  $|\varphi_1|_{M,\rho} \subseteq |\varphi_2|_{M,\rho}$ . The latter holds by assumption.

(5)–(8) and (10). These propositions are direct consequences of Lemma 3.2.

(9). We have  $|\exists x . x|_{M,\rho} = \bigcup_a |x|_{M,\rho[a/x]} = \bigcup_a \{a\} = M$ . QED.

We now state and prove the soundness of  $\mathcal{H}$  in Theorem 3.1.

**Theorem 3.1.**  $\mathcal{H}$  is sound, that is,  $\Gamma \vdash_{\mathcal{H}} \varphi$  implies  $\Gamma \models \varphi$ .

*Proof.* The proof is standard. Because  $\Gamma \vdash_{\mathcal{H}} \varphi$ , there exists a Hilbert-style proof for  $\varphi$  under  $\Gamma$ . We do mathematical induction on the length of the Hilbert-style proof for  $\varphi$  under  $\Gamma$ .

(Base Case). In this case,  $n = 1$ . Therefore,  $\varphi$  is an axiom of  $\mathcal{H}$  or  $\varphi \in \Gamma$ . If  $\varphi$  is an axiom of  $\mathcal{H}$ , we have  $\Gamma \models \varphi$  by Lemma 3.3. If  $\varphi \in \Gamma$ , then we have  $\Gamma \models \varphi$  by Definition 2.48.

(Induction Step). Suppose the proof length is  $n + 1$  for some  $n \geq 1$ , as shown in the following:

$$\varphi_1, \dots, \varphi_n, \varphi_{n+1} \quad \text{where } \varphi_{n+1} \equiv \varphi.$$

If  $\varphi_{n+1}$  is an axiom of  $\mathcal{H}$  or  $\varphi_{n+1} \in \Gamma$ , we have  $\Gamma \models \varphi_{n+1}$  as in the base case. If  $\varphi_{n+1}$  is the result of applying (MODUS PONENS), ( $\exists$ -GENERALIZATION), or (FRAMING), we have  $\Gamma \models \varphi$  by Lemma 3.3.

Therefore, we prove the soundness of  $\mathcal{H}$  by induction. QED.

### 3.1.2 Important properties of $\mathcal{H}$

Firstly, note that the proof rules (PROPOSITIONAL TAUTOLOGY), (MODUS PONENS), ( $\exists$ -QUANTIFIER), and ( $\exists$ -GENERALIZATION) form a complete axiomatization of (pure) predicate logic, which is FOL without function symbols. It leads us to Proposition 3.2.

**Proposition 3.2.** *Predicate logic reasoning is sound for matching logic.*

Throughout this thesis, we will say “by FOL reasoning” to mean that a certain reasoning step in matching logic can be accomplished by applying the four proof rules: (PROPOSITIONAL TAUTOLOGY), (MODUS PONENS), ( $\exists$ -QUANTIFIER), and ( $\exists$ -GENERALIZATION).

Secondly, we prove that frame reasoning is sound for matching logic.

**Proposition 3.3.** *The following propositions hold:*

1. If  $\Gamma \vdash_{\mathcal{H}} \varphi_i \rightarrow \varphi'_i$  for  $1 \leq i \leq n$ , then  $\Gamma \vdash_{\mathcal{H}} \sigma(\varphi_1, \dots, \varphi_n) \rightarrow \sigma(\varphi'_1, \dots, \varphi'_n)$ ;
2. If  $\Gamma \vdash_{\mathcal{H}} \varphi \rightarrow \varphi'$ , then  $\Gamma \vdash_{\mathcal{H}} C[\varphi] \rightarrow C[\varphi']$ , where  $C$  is an application context.

*Proof.* To prove (1), we only need to prove all the following propositions:

$$\begin{aligned}
 \Gamma \vdash_{\mathcal{H}} \sigma(\varphi_1, \varphi_2, \dots, \varphi_{n-1}, \varphi_n) &\rightarrow \sigma(\varphi'_1, \varphi_2, \dots, \varphi_{n-1}, \varphi_n) \\
 \Gamma \vdash_{\mathcal{H}} \sigma(\varphi'_1, \varphi_2, \dots, \varphi_{n-1}, \varphi_n) &\rightarrow \sigma(\varphi'_1, \varphi'_2, \dots, \varphi_{n-1}, \varphi_n) \\
 &\dots \\
 \Gamma \vdash_{\mathcal{H}} \sigma(\varphi'_1, \varphi'_2, \dots, \varphi'_{n-1}, \varphi_n) &\rightarrow \sigma(\varphi'_1, \varphi'_2, \dots, \varphi'_{n-1}, \varphi'_n)
 \end{aligned}$$

These propositions can be directly proved by (FRAMING).

To prove (2), we do structural induction on  $C$ .

(Base Case). Suppose  $C$  is  $\square$ . In this case, the proposition holds.

(Induction Step). Suppose  $C \equiv C_\sigma[C_1]$ , where  $\sigma \in \Sigma$  and  $C_1$  is an application context. Then we have

$$\begin{aligned}
 \Gamma \vdash_{\mathcal{H}} \varphi &\rightarrow \varphi' && // \text{ assumption} \\
 \Gamma \vdash_{\mathcal{H}} C_1[\varphi] &\rightarrow C_1[\varphi'] && // \text{ inductive hypothesis} \\
 \Gamma \vdash_{\mathcal{H}} C_\sigma[C_1[\varphi]] &\rightarrow C_\sigma[C_1[\varphi']] && // \text{ (FRAMING)}
 \end{aligned}$$

Therefore, (2) holds by structural induction. QED.

Thirdly, we show that certain logical reasoning can be propagated through application contexts. More specifically, logical reasoning that has a “disjunctive” semantics can be propagated through application contexts. This includes  $\vee$  (disjunction) whose semantics is set union,  $\exists$  (existential quantification) whose semantics is also set union, and  $\perp$ , which is the unit of disjunction.

**Proposition 3.4.** *Let  $C$  be an application context. The following propositions hold:*

1.  $\Gamma \vdash_{\mathcal{H}} C[\perp] \leftrightarrow \perp$ ;
2.  $\Gamma \vdash_{\mathcal{H}} C[\varphi_1 \vee \varphi_2] \leftrightarrow C[\varphi_1] \vee C[\varphi_2]$ ;
3.  $\Gamma \vdash C[\exists x. \varphi] \leftrightarrow \exists x. C[\varphi]$ , if  $x \notin \text{freeVar}(C[\exists x. \varphi])$ ;
4.  $\Gamma \vdash_{\mathcal{H}} C[\varphi_1 \vee \varphi_2] \text{ iff } \Gamma \vdash_{\mathcal{H}} C[\varphi_1] \vee C[\varphi_2]$ ;
5.  $\Gamma \vdash C[\exists x. \varphi] \text{ iff } \Gamma \vdash_{\mathcal{H}} \exists x. C[\varphi]$ , if  $x \notin \text{freeVar}(C[\exists x. \varphi])$ .

*Proof.* We do structural induction on  $C$ .

(Base Case). Suppose  $C$  is  $\square$ . In this case, all propositions hold.

(Induction Step). Suppose  $C$  is  $C_\sigma[C_1]$  where  $C_1$  is an application context. We prove (1)–(5) using the induction hypothesis about  $C_1$ .

(1, “ $\rightarrow$ ”). By the inductive hypothesis, we have  $\Gamma \vdash_{\mathcal{H}} C_1[\perp] \rightarrow \perp$ . By (FRAMING), we have  $\Gamma \vdash_{\mathcal{H}} C_{\sigma}[C_1[\perp]] \rightarrow C_{\sigma}[\perp]$ , i.e.,  $\Gamma \vdash_{\mathcal{H}} C[\perp] \rightarrow C_{\sigma}[\perp]$ . Therefore, we only need to prove that  $\Gamma \vdash_{\mathcal{H}} C_{\sigma}[\perp] \rightarrow \perp$ . Let  $x$  be any variable and  $\psi$  be any pattern.<sup>1</sup> We have  $\Gamma \vdash_{\mathcal{H}} \perp \rightarrow (x \wedge \psi)$  and  $\Gamma \vdash_{\mathcal{H}} \perp \rightarrow (x \wedge \neg\psi)$ . By (FRAMING), we have  $\Gamma \vdash_{\mathcal{H}} C_{\sigma}[\perp] \rightarrow C_{\sigma}[x \wedge \psi]$  and  $\Gamma \vdash_{\mathcal{H}} C_{\sigma}[\perp] \rightarrow C_{\sigma}[x \wedge \neg\psi]$ . Therefore, we have  $\Gamma \vdash_{\mathcal{H}} C_{\sigma}[\perp] \rightarrow (C_{\sigma}[x \wedge \psi] \wedge C_{\sigma}[x \wedge \neg\psi])$ . On the other hand, by (SINGLETON VARIABLE), we have  $\Gamma \vdash_{\mathcal{H}} \neg(C_{\sigma}[x \wedge \psi] \wedge C_{\sigma}[x \wedge \neg\psi])$ . Therefore,  $\Gamma \vdash_{\mathcal{H}} C_{\sigma}[\perp] \rightarrow \perp$ .

(1, “ $\leftarrow$ ”). By FOL reasoning.

(2, “ $\rightarrow$ ”). Same as (1, “ $\rightarrow$ ”) except that we use (PROPAGATION <sub>$\vee$</sub> ).

(2, “ $\leftarrow$ ”). We only need to prove  $\Gamma \vdash_{\mathcal{H}} C[\varphi_i] \rightarrow C[\varphi_1 \vee \varphi_2]$  for  $i \in \{1, 2\}$ . They can be proved by applying frame reasoning (Proposition 3.3) on  $\Gamma \vdash_{\mathcal{H}} \varphi_i \rightarrow \varphi_1 \vee \varphi_2$ .

(3, “ $\rightarrow$ ”). Same as (1, “ $\rightarrow$ ”) except that we use (PROPAGATION <sub>$\exists$</sub> ).

(3, “ $\leftarrow$ ”). We only need to prove  $\Gamma \vdash_{\mathcal{H}} (\exists x. C[\varphi]) \rightarrow C[\exists x. \varphi]$ . By ( $\exists$ -GENERALIZATION), we only need to prove  $\Gamma \vdash_{\mathcal{H}} C[\varphi] \rightarrow C[\exists x. \varphi]$ . It can be proved by applying frame reasoning (Proposition 3.3) on  $\Gamma \vdash_{\mathcal{H}} \varphi \rightarrow \exists x. \varphi$ .

(4) and (5) are direct consequences of (1)–(3).

Therefore, the propositions hold by structural induction. QED.

**Lemma 3.4.** For an application context  $C$ ,  $\Gamma \vdash_{\mathcal{H}} \varphi$  implies  $\Gamma \vdash_{\mathcal{H}} \neg C[\neg\varphi]$ .

*Proof.*

1	$\varphi$	hypothesis
2	$\neg\varphi \rightarrow \perp$	by 1, FOL reasoning
3	$C[\neg\varphi] \rightarrow C[\perp]$	by 2, (FRAMING)
4	$C[\perp] \rightarrow \perp$	by (PROPAGATION)
5	$C[\neg\varphi] \rightarrow \perp$	by 3 and 4, FOL reasoning
6	$\neg C[\neg\varphi]$	by 5, FOL reasoning

QED.

Finally, we show that logical equivalence propagates through any context, as expected. A *context*  $C$  (not just an application context) is a pattern with a distinguished variable  $\square$ . We use  $C[\varphi]$  to denote the result of in-place replacing  $\square$  with  $\varphi$ .

**Proposition 3.5.** For any context  $C$  (not just an application context),  $\Gamma \vdash_{\mathcal{H}} \varphi_1 \leftrightarrow \varphi_2$  implies  $\Gamma \vdash_{\mathcal{H}} C[\varphi_1] \leftrightarrow C[\varphi_2]$ .

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<sup>1</sup>The proof of  $\Gamma \vdash_{\mathcal{H}} C_{\sigma}[\perp] \rightarrow \perp$  presented here is credited to Mircea Sebe.

*Proof.* We do structural induction on  $C$ . If  $C$  is  $\Box$ , the conclusion holds. If  $C$  has one of the following forms:  $\neg C'$ ,  $\psi \wedge C'$ ,  $C' \wedge \psi$ , or  $\exists x.C'$ , where  $C'$  is a context, the conclusion holds by FOL reasoning. If  $C$  has the form  $C_\sigma[C']$ , the conclusion holds by Proposition 3.3. Therefore, the conclusion holds by structural induction. QED.

Proposition 3.5 allows us to replace any two logically equivalent patterns in any context.

### 3.1.3 Relation to modal logic proof rules

There is a close relation between matching logic and modal logic. More specifically, matching logic symbols and modal operators are dual to each other.

**Theorem 3.6.** Given a matching logic symbol  $\sigma$ , we define its dual as  $\sigma^d(\varphi_1, \dots, \varphi_n) \equiv \neg\sigma(\neg\varphi_1, \dots, \neg\varphi_n)$ . Then we have:

- (K):  $\emptyset \vdash_{\mathcal{H}} \sigma^d(\varphi_1 \rightarrow \varphi'_1, \dots, \varphi_n \rightarrow \varphi'_n) \rightarrow (\sigma^d(\varphi_1, \dots, \varphi_n) \rightarrow \sigma^d(\varphi'_1, \dots, \varphi'_n))$ ;
- (N):  $\emptyset \vdash_{\mathcal{H}} \varphi_i$  implies  $\emptyset \vdash_{\mathcal{H}} \sigma^d(\varphi_1, \dots, \varphi_i, \dots, \varphi_n)$ .
- (BARCAN):  $\emptyset \vdash_{\mathcal{H}} (\forall x. \sigma^d(\dots, \varphi_i, \dots)) \rightarrow \sigma^d(\dots, \forall x. \varphi_i, \dots)$  if  $x$  does not occur free in the “...” part.

*Proof.* Let  $C_\sigma[\Box] = \sigma(\varphi_1, \dots, \varphi_{i-1}, \Box, \varphi_{i+1}, \dots, \varphi_n)$ .

(K). By FOL reasoning, we only need to prove the case of one argument, that is,  $\emptyset \vdash_{\mathcal{H}} \neg C_\sigma[\neg(\varphi \rightarrow \varphi')] \rightarrow (\neg C_\sigma[\neg\varphi] \rightarrow \neg C_\sigma[\neg\varphi'])$ . By FOL reasoning, we only need to prove  $\emptyset \vdash_{\mathcal{H}} C_\sigma[\varphi \wedge \varphi'] \vee C_\sigma[\neg\varphi] \vee \neg C_\sigma[\neg\varphi']$ . By Proposition 3.4, we need to prove  $\emptyset \vdash_{\mathcal{H}} C_\sigma[(\varphi \wedge \varphi') \vee \neg\varphi] \vee \neg C_\sigma[\neg\varphi']$ , i.e.,  $\emptyset \vdash_{\mathcal{H}} C_\sigma[\varphi' \vee \neg\varphi] \vee \neg C_\sigma[\neg\varphi']$ . By Proposition 3.4, we need to prove  $\vdash_{\mathcal{H}} C_\sigma[\varphi'] \vee C_\sigma[\neg\varphi] \vee \neg C_\sigma[\neg\varphi']$ . The latter holds by FOL reasoning.

(N). It is a direct consequence of Lemma 3.4, where we let  $C$  to be  $C_\sigma$ .

(BARCAN). By unfolding  $\forall x$  to  $\neg\exists x\neg$ , we need to prove that  $\emptyset \vdash_{\mathcal{H}} (\neg\exists x. C_\sigma[\neg\varphi_i]) \rightarrow \neg C_\sigma[\exists x. \neg\varphi_i]$ . Therefore, we need to prove that  $\emptyset \vdash_{\mathcal{H}} C_\sigma[\exists x. \neg\varphi_i] \rightarrow \exists x. C_\sigma[\neg\varphi_i]$ . The latter is provable by (PROPAGATION $_{\exists}$ ). QED.

These above proof rules are also proof rules of polyadic modal logic and hybrid logic [37, 38]. If we let  $n = 1$ , we obtain the standard (K) rule and (N) rule of modal logic K (Figure 2.3).

### 3.2 DEFINEDNESS COMPLETENESS

We will prove that the proof system  $\mathcal{H}$  is complete for every theory that contains the following definedness symbols and axioms in Definition 2.49:

$$\begin{array}{ll} [\_ ]_s^{s'} \in \Sigma_{s,s'} & // \text{ definedness symbols} \\ [x : s] & // (\text{DEFINEDNESS}) \text{ axioms} \end{array}$$

This result is called definedness completeness, stated in Theorem 3.8. In other words,  $\mathcal{H}$  is as good as the conditional sound and complete proof system  $\mathcal{P}$  in Section 2.13.3, but unlike  $\mathcal{P}$ , it does not rely on the existence of definedness symbols or axioms and can be used to do formal reasoning with any theory. In fact, we will prove definedness completeness by showing that all the proof rules of  $\mathcal{P}$  are derivable using  $\mathcal{H}$  and the definedness axioms, that is:

$$[x : s] \vdash_{\mathcal{H}} (\text{all the proof rules of } \mathcal{P})$$

Throughout this section we will assume that  $\Gamma$  is a theory that includes the definedness axioms. To simplify our notation we feel free to drop the sorts when they are not important.

Let us first go through all the proof rules of  $\mathcal{P}$  and see which of them are already known to be derivable using  $\mathcal{H}$ . The proof system  $\mathcal{P}$  has 14 proof rules in total (Figure 2.11). (PROPOSITIONAL TAUTOLOGY) and (MODUS PONENS) are also proof rules of  $\mathcal{H}$  so they are derivable. ( $\forall$ ) and (UNIVERSAL GENERALIZATION) are derivable by FOL reasoning. Therefore, we only need to consider the (FUNCTIONAL SUBSTITUTION) rule, two (EQUALITY) rules, and seven (MEMBERSHIP) rules.

**Lemma 3.5.**  $\Gamma \vdash_{\mathcal{H}} \varphi_1 \leftrightarrow \varphi_2$  implies  $\Gamma \vdash_{\mathcal{H}} \varphi_1 = \varphi_2$ .

*Proof.*

$$\begin{array}{ll} 1 & \left| \begin{array}{ll} \varphi_1 \leftrightarrow \varphi_2 & \text{hypothesis} \\ \neg[\neg(\varphi_1 \leftrightarrow \varphi_2)] & \text{by 1, Lemma 3.4} \\ \varphi_1 = \varphi_2 & \text{by 2, definition of equality} \end{array} \right. \end{array}$$

QED.

**Lemma 3.6.** (EQUALITY INTRODUCTION) can be proved in  $\mathcal{H}$ .

*Proof.*

$$\begin{array}{ll} 1 & \left| \begin{array}{ll} \varphi \leftrightarrow \varphi & \text{propositional tautology} \\ \varphi = \varphi & \text{by 1, Lemma 3.5} \end{array} \right. \end{array}$$



QED.

**Lemma 3.7.** (MEMBERSHIP INTRODUCTION) can be proved in  $\mathcal{H}$ .

*Proof.*

1	$\varphi$	hypothesis
2	$\varphi \rightarrow (x \rightarrow \varphi)$	(PROPOSITIONAL TAUTOLOGY)
3	$x \rightarrow \varphi$	by 1 and 2, (MODUS PONENS)
4	$x \rightarrow x$	(PROPOSITIONAL TAUTOLOGY)
5	$x \rightarrow x \wedge \varphi$	by 3 and 4, FOL reasoning
6	$[x] \rightarrow [x \wedge \varphi]$	by 5, (FRAMING)
7	$[x]$	definedness axiom
8	$[x \wedge \varphi]$	by 6 and 7, (MODUS PONENS)
9	$x \in \varphi$	by 8, definition of membership
10	$\forall x . (x \in \varphi)$	by 9, FOL reasoning

QED.

**Lemma 3.8.** (MEMBERSHIP ELIMINATION) can be proved in  $\mathcal{H}$ .

*Proof.*

1	$\forall x . (x \in \varphi)$	hypothesis
2	$(\forall x . (x \in \varphi)) \rightarrow x \in \varphi$	FOL reasoning
3	$x \in \varphi$	by 1 and 2, (MODUS PONENS)
4	$[x \wedge \varphi]$	by 3, definition of membership
5	$\neg([x \wedge \varphi] \wedge (x \wedge \neg\varphi))$	(SINGLETON VARIABLE)
6	$[x \wedge \varphi] \rightarrow (x \rightarrow \varphi)$	by 5, FOL reasoning
7	$x \rightarrow \varphi$	by 4 and 6, (MODUS PONENS)
8	$\forall x . (x \rightarrow \varphi)$	by 7, FOL reasoning
9	$(\exists x . x) \rightarrow \varphi$	by 8, FOL reasoning
10	$\exists x . x$	(EXISTENCE)
11	$\varphi$	by 10 and 9, (MODUS PONENS)

QED.

**Lemma 3.9.** (MEMBERSHIP VARIABLE) can be proved in  $\mathcal{H}$ .

*Proof.* By Lemma 3.5, we to prove  $\vdash_{\mathcal{H}} (x \in y) \rightarrow (x = y)$  and  $\vdash_{\mathcal{H}} (x = y) \rightarrow (x \in y)$ . We first prove  $\vdash_{\mathcal{H}} (x = y) \rightarrow (x \in y)$ .

1	$\lceil x \rceil$	definedness axiom
2	$\lceil x \rceil \vee \lceil y \rceil$	by 1, FOL reasoning
3	$\lceil x \vee y \rceil$	by 2, Proposition 3.4
4	$\lceil \neg(x \leftrightarrow y) \vee (x \wedge y) \rceil$	by 3, FOL reasoning
5	$\lceil \neg(x \leftrightarrow y) \rceil \vee \lceil x \wedge y \rceil$	by 4, Proposition 3.4
6	$\neg \lceil \neg(x \leftrightarrow y) \rceil \rightarrow \lceil x \wedge y \rceil$	by 5, FOL reasoning
7	$(x = y) \rightarrow (x \in y)$	by 6, definition

We then prove  $\vdash_{\mathcal{H}} (x \in y) \rightarrow (x = y)$ .

1	$\neg(\lceil x \wedge y \rceil \wedge \lceil x \wedge \neg y \rceil)$	by (SINGLETON VARIABLE)
2	$\neg(\lceil x \wedge y \rceil \wedge \lceil \neg x \wedge y \rceil)$	by (SINGLETON VARIABLE)
3	$\lceil x \wedge y \rceil \rightarrow \neg \lceil x \wedge \neg y \rceil$	by 1, FOL reasoning
4	$\lceil x \wedge y \rceil \rightarrow \neg \lceil \neg x \wedge y \rceil$	by 2, FOL reasoning
5	$\lceil x \wedge y \rceil \rightarrow \neg \lceil x \wedge \neg y \rceil \wedge \neg \lceil \neg x \wedge y \rceil$	by 3 and 4, FOL reasoning
6	$\lceil x \wedge y \rceil \rightarrow \neg(\lceil x \wedge \neg y \rceil \vee \lceil \neg x \wedge y \rceil)$	by 5, FOL reasoning
7	$\lceil x \wedge y \rceil \rightarrow \neg \lceil (x \wedge \neg y) \vee (\neg x \wedge y) \rceil$	by 6, Proposition 3.4
8	$\lceil x \wedge y \rceil \rightarrow \neg \lceil \neg(x \leftrightarrow y) \rceil$	by 7, FOL reasoning
9	$(x \in y) \rightarrow (x = y)$	by 8, definition

QED.

**Lemma 3.10.** (MEMBERSHIP<sub>¬</sub>) can be proved in  $\mathcal{H}$ .

*Proof.* We first prove  $\vdash_{\mathcal{H}} (x \in \neg\varphi) \rightarrow \neg(x \in \varphi)$ .

1	$\neg(\lceil x \wedge \varphi \rceil \wedge \lceil x \wedge \neg\varphi \rceil)$	by (SINGLETON VARIABLE)
2	$\lceil x \wedge \neg\varphi \rceil \rightarrow \neg \lceil x \wedge \varphi \rceil$	by 1, FOL reasoning
3	$(x \in \neg\varphi) \rightarrow \neg(x \in \varphi)$	by 2, definition

We then prove  $\vdash_{\mathcal{H}} \neg(x \in \varphi) \rightarrow (x \in \neg\varphi)$ .

1	$\lceil x \rceil$	definedness axiom
2	$\lceil (x \wedge \varphi) \vee (x \wedge \neg\varphi) \rceil$	by 1, FOL reasoning
3	$\lceil x \wedge \varphi \rceil \vee \lceil x \wedge \neg\varphi \rceil$	by 2, Proposition 3.4
4	$\neg \lceil x \wedge \varphi \rceil \rightarrow \lceil x \wedge \neg\varphi \rceil$	by 3, FOL reasoning
5	$\neg(x \in \varphi) \rightarrow (x \in \neg\varphi)$	by 4, definition

QED.

**Lemma 3.11.**  $\vdash_{\mathcal{H}} (x \in (\varphi_1 \vee \varphi_2)) \leftrightarrow (x \in \varphi_1) \vee (x \in \varphi_2)$ .

*Proof.* Use (PROPAGATION<sub>∨</sub>) and FOL reasoning.

QED.

**Lemma 3.12.** ( $\text{MEMBERSHIP}_\wedge$ ) can be proved in  $\mathcal{H}$ .

*Proof.* Use Lemma 3.10 and 3.11, and the fact that  $\vdash_{\mathcal{H}} \varphi_1 \wedge \varphi_2 \leftrightarrow \neg(\neg\varphi_1 \vee \neg\varphi_2)$ . QED.

**Lemma 3.13.** ( $\text{MEMBERSHIP}_\exists$ ) can be proved in  $\mathcal{H}$ .

*Proof.* Use ( $\text{PROPAGATION}_\exists$ ) and FOL reasoning. QED.

The following is a useful lemma about definedness symbols.

**Lemma 3.14.**  $\vdash_{\mathcal{H}} C[\varphi] \rightarrow [\varphi]$  for any application context  $C$ .

*Proof.* Let  $x$  be a fresh variable in the following proof.

1	$[x]$	definedness axiom
2	$[x] \vee [\varphi]$	by 1, FOL reasoning
3	$[x \vee \varphi]$	by 2, Proposition 3.4
4	$[x \wedge \neg\varphi \vee \varphi]$	by 3, FOL reasoning
5	$[x \wedge \neg\varphi] \vee [\varphi]$	by 4, Proposition 3.4
6	$C[x \wedge \varphi] \rightarrow \neg[x \wedge \neg\varphi]$	by ( $\text{SINGLETON VARIABLE}$ )
7	$\neg[x \wedge \neg\varphi] \rightarrow [\varphi]$	by 5, FOL reasoning
8	$C[x \wedge \varphi] \rightarrow [\varphi]$	by 6 and 7, FOL reasoning
9	$\forall x. (C[x \wedge \varphi] \rightarrow [\varphi])$	by 8, FOL reasoning
10	$(\exists x. C[x \wedge \varphi]) \rightarrow [\varphi]$	by 9, FOL reasoning
11	$\varphi \rightarrow (\exists x. x) \wedge \varphi$	by ( $\text{EXISTENCE}$ )
12	$\varphi \rightarrow \exists x. (x \wedge \varphi)$	by 11, FOL reasoning
13	$C[\varphi] \rightarrow C[\exists x. (x \wedge \varphi)]$	by 12, ( $\text{FRAMING}$ )
14	$C[\exists x. (x \wedge \varphi)] \rightarrow [\varphi]$	by 10, Proposition 3.4
15	$C[\varphi] \rightarrow [\varphi]$	by 13, 14, FOL reasoning

QED.

**Corollary 3.1.**  $\vdash_{\mathcal{H}} C_\sigma[\varphi] \rightarrow [\varphi]$  and  $\vdash_{\mathcal{H}} [\varphi] \rightarrow \neg C_\sigma[\neg\varphi]$  for every symbol  $\sigma$ . Also,  $\vdash_{\mathcal{H}} \varphi \rightarrow [\varphi]$  and  $\vdash_{\mathcal{H}} [\varphi] \rightarrow \varphi$ .

*Proof.* Let  $C$  be  $C_\sigma$  and  $\Box$  in Lemma 3.14, respectively. QED.

We state and prove a deduction theorem of  $\mathcal{H}$ .

**Theorem 3.7.** Let  $\Gamma$  be a theory that contains definedness. For any  $\varphi$  and  $\psi$ , if  $\Gamma \cup \{\psi\} \vdash_{\mathcal{H}} \varphi$  and the proof does not use ( $\exists$ -GENERALIZATION) on any free variables of  $\psi$ , then  $\Gamma \vdash_{\mathcal{H}} [\psi] \rightarrow \varphi$ . In particular, if  $\psi$  is closed, then  $\Gamma \cup \{\psi\} \vdash_{\mathcal{H}} \varphi$  implies  $\Gamma \vdash_{\mathcal{H}} [\psi] \rightarrow \varphi$ .

The condition regarding ( $\exists$ -GENERALIZATION) is standard. For example, the deduction theorem for FOL also has a similar condition [39]. The proof of Theorem 3.7 is standard, by using mathematical induction on the length of the proof of  $\Gamma \cup \{\psi\} \vdash_{\mathcal{H}} \varphi$ . In the following, we give a semantic argument and explain why the axiom  $\psi$  becomes  $\lfloor \psi \rfloor$  when we move it from the left-hand side of  $\vdash_{\mathcal{H}}$  to the right-hand side.

Suppose  $\Gamma \cup \{\psi\} \models \varphi$  where  $\psi$  is closed. By definition,  $M \models \Gamma$  and  $M \models \psi$  implies  $M \models \varphi$  for every  $M$ . In other words, if  $M \models \Gamma$ , then we have

$$\text{“}\psi \text{ holds in } M\text{”} \quad \text{implies} \quad \text{“}\varphi \text{ holds in } M\text{”}$$

The above implication can be equivalently stated as  $M \models \lfloor \psi \rfloor \rightarrow \varphi$ , because if  $\psi$  does not hold in  $M$ ,  $\lfloor \psi \rfloor$  is equivalent to  $\perp$ , and the implication holds. Otherwise,  $\lfloor \psi \rfloor$  is equivalent to  $\top$ , and the implication holds iff  $\varphi$  holds. Therefore, an equivalent statement of  $\Gamma \cup \{\psi\} \models \varphi$  is that for every  $M$ , if  $M \models \Gamma$  then  $M \models \lfloor \psi \rfloor \rightarrow \varphi$ . The latter is equivalent to  $\Gamma \models \lfloor \psi \rfloor \rightarrow \varphi$  by definition.

Note that  $\Gamma \models \psi \rightarrow \varphi$  is too strong as a conclusion, for it requires that  $\psi$  is always included by  $\varphi$ , even in models where  $\psi$  does not hold. Here is a simple counterexample:  $\Gamma \cup \{\psi\} \models \lfloor \psi \rfloor$  does not imply  $\Gamma \models \psi \rightarrow \lfloor \psi \rfloor$ . To better understand it, let us compare the following three statements: (a)  $\Gamma \cup \{\psi\} \models \varphi$ ; (b)  $\Gamma \models \lfloor \psi \rfloor \rightarrow \varphi$ ; and (c)  $\Gamma \models \psi \rightarrow \varphi$ , where we assume that  $\psi$  is closed for simplicity. By definition, (a) means that for all models  $M$  such that  $M \models \Gamma$  and  $M \models \psi$ , we have  $M \models \varphi$ . Here,  $M \models \psi$  means that  $|\psi|_{M,\rho} = M$  for all  $\rho$ . Statement (b) means that for all models  $M$  such that  $M \models \Gamma$ , we have  $M \models \lfloor \psi \rfloor \rightarrow \varphi$ . Compared with (a), (b) checks more models. It checks not only models where  $\psi$  holds but also those where  $\psi$  does not hold. For models  $M$  where  $\psi$  hold, we can easily conclude that  $M \models \lfloor \psi \rfloor \rightarrow \varphi$  because by (a), we have the stronger result  $M \models \varphi$ . For those models  $M$  where  $\psi$  does not hold, we have that  $|\psi|_{M,\rho} \neq M$  for all  $\rho$ . This means that  $|\lfloor \psi \rfloor|_{M,\rho} = \emptyset$  for all  $\rho$ , and thus  $|\lfloor \psi \rfloor \rightarrow \varphi|_{M,\rho} = M$  no matter what  $\varphi$  is. This way, (a) implies (b), even if (b) checks more models than (a). The above reasoning fails for (c) because we cannot conclude anything on models where  $\psi$  does not hold.

Next, we prove Theorem 3.7.

*Proof of Theorem 3.7.* We do mathematical induction on the length of the proof  $\Gamma \cup \{\psi\} \vdash_{\mathcal{H}} \varphi$ .

(Base Case). Suppose the proof length is 1. In this case,  $\varphi$  is an axiom of  $\mathcal{H}$  or  $\varphi \in \Gamma \cup \{\psi\}$ . We have  $\Gamma \vdash_{\mathcal{H}} \lfloor \psi \rfloor \rightarrow \varphi$  (noticing Corollary 3.1 if  $\varphi$  is  $\psi$ ).

(Induction Step). Suppose the proof  $\Gamma \cup \{\psi\} \vdash_{\mathcal{H}} \varphi$  has  $n + 1$  steps:

$$\varphi_1, \dots, \varphi_n, \varphi.$$

We now do a case analysis on how  $\varphi$  is proved.

Suppose  $\varphi$  is an axiom in  $\mathcal{H}$  or  $\varphi \in \Gamma \cup \{\psi\}$ . We have  $\Gamma \vdash_{\mathcal{H}} [\psi] \rightarrow \varphi$  for the same reason as (Base Case).

Suppose  $\varphi$  is proved by applying (MODUS PONENS) on  $\varphi_i$  and  $\varphi_j$  for some  $1 \leq i, j \leq n$ , where  $\varphi_j$  has the form  $\varphi_i \rightarrow \varphi$ . By the induction hypotheses,  $\Gamma \vdash_{\mathcal{H}} [\psi] \rightarrow \varphi_i$  and  $\Gamma \vdash_{\mathcal{H}} [\psi] \rightarrow (\varphi_i \rightarrow \varphi)$ . By FOL reasoning,  $\Gamma \vdash_{\mathcal{H}} [\psi] \rightarrow \varphi$ .

Suppose  $\varphi$  is proved by applying ( $\exists$ -GENERALIZATION) on  $\varphi_1 \rightarrow \varphi_2$ , and  $\varphi$  has the form  $(\exists x. \varphi_1) \rightarrow \varphi_2$ , where  $x \notin \text{freeVar}(\varphi_2)$ . By the induction hypothesis,  $\Gamma \vdash_{\mathcal{H}} [\psi] \rightarrow (\varphi_1 \rightarrow \varphi_2)$ . Therefore,  $\Gamma \vdash_{\mathcal{H}} \varphi_1 \rightarrow ([\psi] \rightarrow \varphi_2)$ . By assumption, the proof of  $\varphi$  does not apply ( $\exists$ -GENERALIZATION) on any free variable of  $\psi$ . Therefore,  $x \notin \text{freeVar}(\psi)$ , and we have  $\Gamma \vdash_{\mathcal{H}} (\exists x. \varphi_1) \rightarrow ([\psi] \rightarrow \varphi_2)$  by ( $\exists$ -GENERALIZATION). Finally, we have  $\Gamma \vdash_{\mathcal{H}} [\psi] \rightarrow ((\exists x. \varphi_1) \rightarrow \varphi_2)$  by FOL reasoning.

Suppose  $\varphi$  is proved by applying (FRAMING) on  $\varphi_i$  for some  $1 \leq i \leq n$ , then  $\varphi_i$  must have the form  $\varphi'_i \rightarrow \varphi''_i$ , and  $\varphi$  must have the form  $C_{\sigma}[\varphi'_i] \rightarrow C_{\sigma}[\varphi''_i]$  for some  $\sigma$ . By the induction hypothesis,  $\Gamma \vdash_{\mathcal{H}} [\psi] \rightarrow (\varphi'_i \rightarrow \varphi''_i)$ . We can prove  $\Gamma \vdash_{\mathcal{H}} [\psi] \rightarrow (C_{\sigma}[\varphi'_i] \rightarrow C_{\sigma}[\varphi''_i])$  as follows:

1	$[\psi] \rightarrow (\varphi'_i \rightarrow \varphi''_i)$	hypothesis
2	$\varphi'_i \rightarrow \varphi''_i \vee [\neg\psi]$	by 1, FOL reasoning
3	$C_{\sigma}[[\neg\psi]] \rightarrow [\neg\psi]$	Corollary 3.1
4	$C_{\sigma}[\varphi'_i] \rightarrow C_{\sigma}[\varphi''_i \vee [\neg\psi]]$	by 2, (FRAMING)
5	$C_{\sigma}[\varphi'_i] \rightarrow C_{\sigma}[\varphi''_i] \vee C_{\sigma}[[\neg\psi]]$	by 4, Proposition 3.4
6	$C_{\sigma}[\varphi''_i] \vee C_{\sigma}[[\neg\psi]] \rightarrow C_{\sigma}[\varphi''_i] \vee [\neg\psi]$	by 3, FOL reasoning
7	$C_{\sigma}[\varphi'_i] \rightarrow C_{\sigma}[\varphi''_i] \vee [\neg\psi]$	by 5, 6, FOL reasoning
8	$[\psi] \rightarrow (C_{\sigma}[\varphi'_i] \rightarrow C_{\sigma}[\varphi''_i])$	by 7, FOL reasoning

Therefore, the conclusion holds by induction.

QED.

Next, we continue to prove the proof rules of  $\mathcal{P}$ .

**Lemma 3.15.** (EQUALITY ELIMINATION) can be proved in  $\mathcal{H}$ .

*Proof.* Recall the definition of equality  $(\varphi_1 = \varphi_2) \equiv [\varphi_1 \leftrightarrow \varphi_2]$ . Theorem 3.7 together with Proposition 3.5 give us a nice way to deal with equality premises. To prove  $\vdash_{\mathcal{H}} (\varphi_1 = \varphi_2) \rightarrow (\psi[\varphi_1/x] \rightarrow \psi[\varphi_2/x])$ , we apply Theorem 3.7 and prove  $\{\varphi_1 \leftrightarrow \varphi_2\} \vdash_{\mathcal{H}} \psi[\varphi_1/x] \rightarrow \psi[\varphi_2/x]$ , which is proved by Proposition 3.5. Note that the (formal) proof given in Proposition 3.5 does not use ( $\exists$ -GENERALIZATION) at all, so the conditions of Theorem 3.7 are satisfied. QED.

**Lemma 3.16.** (FUNCTIONAL SUBSTITUTION) can be proved in  $\mathcal{H}$ .

*Proof.* Let  $z$  be a fresh variable that does not occur free in  $\varphi$  and  $\varphi'$ , and is distinct from  $x$ . Notice the side condition that  $y$  does not occur free in  $\varphi'$ .

1	$\varphi' = z \leftrightarrow z = \varphi'$	definition
2	$z = \varphi' \rightarrow (\varphi[z/x] \rightarrow \varphi[\varphi'/x])$	Lemma 3.15
3	$(\forall x. \varphi) \rightarrow \varphi[z/x]$	by axiom
4	$\varphi' = z \rightarrow ((\forall x. \varphi) \rightarrow \varphi[z/x])$	FOL reasoning
5	$\varphi' = z \rightarrow (\varphi[z/x] \rightarrow \varphi[\varphi'/x])$	FOL reasoning
6	$\varphi' = z \rightarrow ((\forall x. \varphi) \rightarrow \varphi[\varphi'/x])$	FOL reasoning
7	$\forall z. (\varphi' = z \rightarrow ((\forall x. \varphi) \rightarrow \varphi[\varphi'/x]))$	by 6
8	$(\exists z. \varphi' = z) \rightarrow ((\forall x. \varphi) \rightarrow \varphi[\varphi'/x])$	FOL reasoning
9	$(\forall x. \varphi) \wedge (\exists z. \varphi' = z) \rightarrow \varphi[\varphi'/x]$	FOL reasoning
10	$(\forall x. \varphi) \wedge (\exists y. \varphi' = y) \rightarrow \varphi[\varphi'/x]$	FOL reasoning

QED.

**Lemma 3.17.**  $\vdash_{\mathcal{H}} C_{\sigma}[\varphi_1 \wedge (x \in \varphi_2)] = C_{\sigma}[\varphi_1] \wedge (x \in \varphi_2)$ .

*Proof.* We first prove  $\vdash_{\mathcal{H}} C_{\sigma}[\varphi_1 \wedge (x \in \varphi_2)] \rightarrow C_{\sigma}[\varphi_1] \wedge (x \in \varphi_2)$ . By FOL reasoning, it suffices to show both  $\vdash_{\mathcal{H}} C_{\sigma}[\varphi_1 \wedge (x \in \varphi_2)] \rightarrow C_{\sigma}[\varphi_1]$  and  $\vdash_{\mathcal{H}} C_{\sigma}[\varphi_1 \wedge (x \in \varphi_2)] \rightarrow (x \in \varphi_2)$ . The first follows immediately by (FRAMING) and FOL reasoning. The second can be proved as:

1	$[x]$
2	$[(x \wedge \neg \varphi_2) \vee (x \wedge \varphi_2)]$
3	$[x \wedge \neg \varphi_2] \vee [x \wedge \varphi_2]$
4	$\neg[x \wedge \neg \varphi_2] \rightarrow [x \wedge \varphi_2]$
5	$C_{\sigma}[[x \wedge \varphi_2]] \rightarrow \neg[x \wedge \neg \varphi_2]$
6	$C_{\sigma}[[x \wedge \varphi_2]] \rightarrow [x \wedge \varphi_2]$
7	$C_{\sigma}[\varphi_1 \wedge [x \wedge \varphi_2]] \rightarrow C_{\sigma}[[x \wedge \varphi_2]]$
8	$C_{\sigma}[\varphi_1 \wedge [x \wedge \varphi_2]] \rightarrow [x \wedge \varphi_2]$
9	$C_{\sigma}[\varphi_1 \wedge (x \in \varphi_2)] \rightarrow (x \in \varphi_2)$

QED.

**Lemma 3.18.**  $\vdash_{\mathcal{H}} \exists y. ((x = y) \wedge \varphi) = \varphi[x/y]$  if  $x$  and  $y$  are distinct.

*Proof.* The proof is by structural induction on  $\varphi$  and Lemma 3.17.

QED.

**Lemma 3.19.**  $\vdash_{\mathcal{H}} \varphi = \exists y. ([y \wedge \varphi] \wedge y)$  if  $y \notin \text{free Var}(\varphi)$ .

*Proof.* We first prove  $\vdash_{\mathcal{H}} \exists y. ([y \wedge \varphi] \wedge y) \rightarrow \varphi$ .

1	$\neg([y \wedge \varphi] \wedge (y \wedge \neg\varphi))$	(SINGLETON VARIABLE)
2	$[y \wedge \varphi] \wedge y \rightarrow \varphi$	by 1, FOL reasoning
3	$\forall y. ([y \wedge \varphi] \wedge y \rightarrow \varphi)$	by 2, axiom
4	$\exists y. ([y \wedge \varphi] \wedge y) \rightarrow \varphi$	by 3, FOL reasoning

We then prove  $\vdash_{\mathcal{H}} \varphi \rightarrow \exists y. ([y \wedge \varphi] \wedge y)$ . Let  $x$  be a fresh variable distinct from  $y$ .

1	$x \in \varphi \rightarrow x \in \varphi$
2	$x \in \varphi \rightarrow [x \wedge \varphi]$
3	$x \in \varphi \rightarrow [x \wedge [x \wedge \varphi]]$
4	$x \in \varphi \rightarrow x \in [x \wedge \varphi]$
5	$x \in \varphi \rightarrow \exists y. (x = y \wedge x \in [y \wedge \varphi])$
6	$x \in \varphi \rightarrow \exists y. (x \in y \wedge x \in [y \wedge \varphi])$
7	$x \in \varphi \rightarrow \exists y. (x \in (y \wedge [y \wedge \varphi]))$
8	$x \in \varphi \rightarrow x \in \exists y. (y \wedge [y \wedge \varphi])$
9	$x \in (\varphi \rightarrow \exists y. (y \wedge [y \wedge \varphi]))$
10	$\forall x. (x \in (\varphi \rightarrow \exists y. (y \wedge [y \wedge \varphi])))$
11	$\varphi \rightarrow \exists y. (y \wedge [y \wedge \varphi])$

QED.

**Lemma 3.20.** (MEMBERSHIP SYMBOL) is provable in  $\mathcal{H}$ .

*Proof.* We first prove  $\vdash_{\mathcal{H}} x \in C_{\sigma}[\varphi] \rightarrow \exists y. (y \in \varphi \wedge x \in C_{\sigma}[y])$ . Let  $\Psi \equiv \exists y. (y \in \varphi \wedge x \in C_{\sigma}[y])$ .

1	$\exists y. (y \in \varphi \wedge x \in C_{\sigma}[y]) \rightarrow \Psi$
2	$\exists y. ([y \wedge \varphi] \wedge x \in C_{\sigma}[y]) \rightarrow \Psi$
3	$\exists y. ([x \wedge [y \wedge \varphi]] \wedge x \in C_{\sigma}[y]) \rightarrow \Psi$
4	$\exists y. (x \in [y \wedge \varphi] \wedge x \in C_{\sigma}[y]) \rightarrow \Psi$
5	$\exists y. (x \in ([y \wedge \varphi] \wedge C_{\sigma}[y])) \rightarrow \Psi$
6	$x \in \exists y. ([y \wedge \varphi] \wedge C_{\sigma}[y]) \rightarrow \Psi$
7	$x \in \exists y. C_{\sigma}[[y \wedge \varphi] \wedge y] \rightarrow \Psi$
8	$x \in C_{\sigma}[\exists y. [y \wedge \varphi] \wedge y] \rightarrow \Psi$
9	$x \in C_{\sigma}[\varphi] \rightarrow \Psi$

We then prove  $\vdash_{\mathcal{H}} \exists y. (y \in \varphi \wedge x \in C[y]) \rightarrow x \in C[\varphi]$ . In fact, we just need to apply the same derivation as above on  $\vdash_{\mathcal{H}} \Psi \rightarrow \exists y. (y \in \varphi \wedge x \in C[y])$ . QED.

So far, we have proved all the proof rules of  $\mathcal{P}$  using  $\mathcal{H}$  and the definedness axioms. Therefore, we have Lemma 3.21.

**Lemma 3.21.** Let  $\Gamma$  be a theory that contains the definedness symbols and axioms. For every pattern  $\varphi$ ,  $\Gamma \vdash_{\mathcal{P}} \varphi$  implies  $\Gamma \vdash_{\mathcal{H}} \varphi$ .

Therefore,  $\mathcal{H}$  is complete for theories containing definedness.

**Theorem 3.8.** Let  $\Gamma$  be a theory that contains the definedness symbols and axioms. For every pattern  $\varphi$ ,  $\Gamma \models \varphi$  implies  $\Gamma \vdash_{\mathcal{H}} \varphi$ .

*Proof.* Use Lemma 3.21 and the completeness of  $\mathcal{P}$  (Theorem 2.19). QED.

### 3.3 LOCAL COMPLETENESS

We will present and prove local completeness for  $\mathcal{H}$ . Local completeness states the equivalence between the local validity relation  $\models^{loc}$  and the local provability relation  $\vdash_{\mathcal{H}}^{loc}$ . Both relations are stronger than their (global) counterparts  $\vdash_{\mathcal{H}}$  and  $\models$ . The relation among these four relations has been shown in Figure 3.1.

Let us start by defining the two local relations.

**Definition 3.3.** Let  $\Gamma$  be a theory and  $\varphi$  be a pattern. The *local provability relation*  $\Gamma \vdash_{\mathcal{H}}^{loc} \varphi$  holds iff there exists a finite subset  $\Delta \subseteq \Gamma$  such that  $\emptyset \vdash_{\mathcal{H}} \bigwedge \Delta \rightarrow \varphi$ , where  $\bigwedge \Delta$  is the conjunction of all patterns in  $\Delta$ . We let  $\bigwedge \emptyset$  be  $\top$ . The *local validity relation*  $\Gamma \models^{loc} \varphi$  holds iff for any model  $M$ , any valuation  $\rho$ , and any element  $a \in M$ ,  $a \in |\psi|_{M,\rho}$  for all  $\psi \in \Gamma$  implies  $a \in |\varphi|_{M,\rho}$ .

The local relations are stronger than their global counterparts. In addition, if  $\Gamma = \emptyset$ , the local relations are equivalent to their global counterparts.

**Proposition 3.9.** *For any  $\Gamma$  and  $\varphi$ , the following hold:*

1.  $\Gamma \vdash_{\mathcal{H}}^{loc} \varphi$  implies  $\Gamma \vdash_{\mathcal{H}} \varphi$ ;
2.  $\Gamma \models^{loc} \varphi$  implies  $\Gamma \models \varphi$ ;
3.  $\emptyset \vdash_{\mathcal{H}}^{loc} \varphi$  iff  $\emptyset \vdash_{\mathcal{H}} \varphi$ ;
4.  $\emptyset \models^{loc} \varphi$  iff  $\emptyset \models \varphi$ .

*Proof.* (1). By definition, there exists a finite subset  $\Delta \subseteq \Gamma$  such that  $\emptyset \vdash_{\mathcal{H}} \bigwedge \Delta \rightarrow \varphi$ . Note that  $\Gamma \vdash_{\mathcal{H}} \bigwedge \Delta$ , so by (MODUS PONENS),  $\Gamma \vdash_{\mathcal{H}} \varphi$ .

(2). Let  $M$  be a model such that  $M \models \Gamma$ . Let  $\rho$  be any valuation and  $a \in M$  be any element. Since  $M \models \Gamma$ , we have  $M \models \psi$  for all  $\psi \in \Gamma$ . Therefore,  $|\psi|_{M,\rho} = M$  for all  $\psi \in \Gamma$ ,



and thus  $a \in |\psi|_{M,\rho}$  for all  $\psi \in \Gamma$ . By the definition of  $\Gamma \models^{loc} \varphi$ ,  $a \in |\varphi|_{M,\rho}$ . Because  $a$  is arbitrarily chosen,  $|\varphi|_{M,\rho} = M$ . Because  $\rho$  is also arbitrarily chosen,  $M \models \varphi$ .

(3) and (4). By definition.

QED.

We point out that the other directions of (1) and (2) in Proposition 3.9 do not hold in general. Consider  $\Gamma = \{\neg x\}$ . We will show that  $\Gamma \not\models_{\mathcal{H}}^{loc} \perp$  but  $\Gamma \vdash_{\mathcal{H}} \perp$ . To prove  $\Gamma \not\models_{\mathcal{H}}^{loc} \perp$ , assume the opposite, that is,  $\emptyset \vdash_{\mathcal{H}} \neg x \rightarrow \perp$ . By the soundness of  $\mathcal{H}$  (Theorem 3.1),  $\emptyset \models \neg x \rightarrow \perp$ . To show the contradiction, let us construct a model  $M$  whose carrier set is  $\{0, 1\}$ . Let  $\rho$  be a valuation such that  $\rho(x) = 0$ . Then we have  $|\neg x \rightarrow \perp|_{M,\rho} = \{0\}$ , which is not  $\{0, 1\}$ . This contradiction shows that  $\Gamma \not\models_{\mathcal{H}}^{loc} \perp$ . On the other hand, we can prove  $\Gamma \vdash_{\mathcal{H}} \perp$  as follows. Firstly, we have  $\Gamma \vdash_{\mathcal{H}} \neg x$ , which is equivalent to  $\Gamma \vdash_{\mathcal{H}} x \rightarrow \perp$ . By ( $\exists$ -GENERALIZATION), we have  $\Gamma \vdash_{\mathcal{H}} (\exists x. x) \rightarrow \perp$ . By (EXISTENCE) and (MODUS PONENS), we have  $\Gamma \vdash_{\mathcal{H}} \perp$ . Therefore, the other directions of (1) and (2) in Proposition 3.9 do not hold in general.

Now, to establish Figure 3.1, we only need to prove local completeness, stated in Theorem 3.14. The proof presented here is inspired by the completeness proofs for polyadic modal logic [37] and hybrid logic [40], with novel techniques to handle sorts, many-sorted symbols, and quantifiers.

We start by defining consistent sets.

**Definition 3.4.** A theory  $\Gamma$  is *consistent*, if  $\Gamma \not\models_{\mathcal{H}}^{loc} \perp$ . In addition,  $\Gamma$  is a *maximal consistent set* (MCS) if for every  $\Gamma' \supsetneq \Gamma$ ,  $\Gamma'$  is inconsistent.

Intuitively, a consistent set gives a consistent “view” of elements in the underlying carrier set. Recall that a pattern is matched by certain elements. If  $\Gamma$  is consistent, all patterns in  $\Gamma$  can be matched by at least one common element. In other words, the infinite conjunction “pattern”  $\bigwedge \Gamma$  is not  $\perp$ . The larger  $\Gamma$  is, the smaller  $\bigwedge \Gamma$  becomes. An MCS is thus a maximal  $\Gamma$ , without making  $\bigwedge \Gamma$  to be  $\perp$ . Note that the smallest patterns except  $\perp$  are singleton patterns, which are matched by one element. Therefore, we can think of MCSs as representations of individual elements. This useful intuition motivates the definition of *canonical models* whose elements are MCSs (Definition 3.6) as well as the Truth Lemma (Lemma 3.26), which states that “Matching = Membership in MCSs”. Truth Lemma is the key result that connects proofs and semantics.

We prove some properties about MCSs.

**Proposition 3.10.** *Let  $\Gamma$  be an MCS. The following propositions hold:*

1.  $\varphi \in \Gamma$  iff  $\Gamma \models_{\mathcal{H}}^{loc} \varphi$ ; In particular, if  $\vdash_{\mathcal{H}} \varphi$  then  $\varphi \in \Gamma$ ;

2.  $\neg\varphi \in \Gamma$  iff  $\varphi \notin \Gamma$ ;
3.  $\varphi_1 \wedge \varphi_2 \in \Gamma$  iff  $\varphi_1 \in \Gamma$  and  $\varphi_2 \in \Gamma$ ; In general, for any finite pattern set  $\Delta$ ,  $\bigwedge \Delta \in \Gamma$  iff  $\Delta \subseteq \Gamma$ ;
4.  $\varphi_1 \vee \varphi_2 \in \Gamma$  iff  $\varphi_1 \in \Gamma$  or  $\varphi_2 \in \Gamma$ ; In general, for any finite pattern set  $\Delta$ ,  $\bigvee \Delta \in \Gamma$  iff  $\Delta \cap \Gamma \neq \emptyset$ ; As a convention, when  $\Delta = \emptyset$ ,  $\bigvee \Delta$  is  $\perp$ ;
5.  $\varphi_1, \varphi_1 \rightarrow \varphi_2 \in \Gamma$  implies  $\varphi_2 \in \Gamma$ ; In particular, if  $\vdash_{\mathcal{H}} \varphi_1 \rightarrow \varphi_2$ , then  $\varphi_1 \in \Gamma$  implies  $\varphi_2 \in \Gamma$ .

*Proof.* By propositional reasoning.

QED.

**Definition 3.5.** For an MCS  $\Gamma$ , we say that  $\Gamma$  is a *witnessed MCS*, if for every  $\exists x. \varphi \in \Gamma$ , there exists  $y$  such that  $(\exists x. \varphi) \rightarrow \varphi[y/x] \in \Gamma$ .

In the following, we show any consistent set  $\Gamma$  can be extended to a witnessed MCS  $\Gamma^+$ . The extension, however, requires an extension of the set of variables. To see why such an extension is needed, consider the following example. Let  $\Sigma = (S, V, \Sigma)$  be a signature and  $\Gamma = \{\neg x \mid x \in V\}$  be a pattern set containing all variable negations. We leave it for the readers to show that  $\Gamma$  is consistent. Here, we claim the consistent set  $\Gamma$  cannot be extended to a witnessed MCS  $\Gamma^+$  in the signature  $\Sigma$ . The proof is by contradiction. Assume  $\Gamma^+$  exists. By Proposition 3.10 and (EXISTENCE),  $\exists x. x \in \Gamma^+$ . Because  $\Gamma^+$  is a witnessed MCS, there is a variable  $y$  such that  $(\exists x. x) \rightarrow y \in \Gamma^+$ , and by Proposition 3.10,  $y \in \Gamma^+$ . On the other hand,  $\neg y \in \Gamma \subseteq \Gamma^+$ . This contradicts the consistency of  $\Gamma^+$ .

**Lemma 3.22.** Let  $\Sigma = (S, V, \Sigma)$  be a signature and  $\Gamma$  be a consistent set. Extend the variable set  $V$  to  $V^+$  with countably infinitely many new variables, and denote the extended signature as  $\Sigma^+ = (V^+, S, \Sigma)$ . There exists a pattern set  $\Gamma^+$  in the extended signature  $\Sigma^+$  such that  $\Gamma \subseteq \Gamma^+$  and  $\Gamma^+$  is a witnessed MCS.

*Proof.* We use  $\text{MLPATTERN}$  and  $\text{MLPATTERN}^+$  denote the set of all patterns in the original and extended signatures, respectively. Enumerate all patterns  $\varphi_1, \varphi_2, \dots \in \text{MLPATTERN}^+$  and all variables  $x_1, x_2, \dots$  in  $V^+ \setminus V$ . We will construct a non-decreasing sequence of pattern sets  $\Gamma_0 \subseteq \Gamma_1 \subseteq \Gamma_2 \dots \subseteq \text{MLPATTERN}_s^+$ , with  $\Gamma_0 = \Gamma$ . Notice that  $\Gamma_0$  contains variables only in  $V$ . Eventually, we will let  $\Gamma^+ = \bigcup_{i \geq 0} \Gamma_i$  to be the witnessed MCS.

For every  $n \geq 1$ , we define  $\Gamma_n$  as follows. If  $\Gamma_{n-1} \cup \{\varphi_n\}$  is inconsistent, then let  $\Gamma_n = \Gamma_{n-1}$ . Otherwise,

if  $\varphi_n$  is not of the form  $\exists x. \psi$ :

$$\Gamma_n = \Gamma_{n-1} \cup \{\varphi_n\}$$

if  $\varphi_n \equiv \exists x. \psi$  and  $x_i$  is the first variable in  $V^+ \setminus V$

that does not occur free in  $\Gamma_{n-1}$  and  $\psi$ :

$$\Gamma_n = \Gamma_{n-1} \cup \{\varphi_n\} \cup \{\psi[x_i/x]\}$$

Notice that in the second case, we can always pick a variable  $x_i$  that satisfies the conditions because by construction,  $\Gamma_{n-1} \cup \{\varphi_n\}$  uses at most finitely many variables in  $V^+ \setminus V$ .

We show that  $\Gamma_n$  is consistent for every  $n \geq 0$  by induction. The base case is to show  $\Gamma_0$  is consistent in the extended signature. Assume it is not. Then there exists a finite subset  $\Delta_0 \subseteq_{\text{fin}} \Gamma_0$  such that  $\vdash_{\mathcal{H}} \bigwedge \Delta_0 \rightarrow \perp$ . The proof of  $\bigwedge \Delta_0 \rightarrow \perp$  is a finite sequence of patterns in  $\text{MLPATTERN}^+$ . We can replace every occurrence of the variable  $y \in V^+ \setminus V$  ( $y$  can have any sort) with a variable  $y \in V$  that has the same sort as  $y$  and does not occur (no matter bound or free) in the proof. By induction on the length of the proof, the resulting sequence is also a proof of  $\bigwedge \Delta_0 \rightarrow \perp$ , and it consists of only patterns in  $\text{PATTERN}$ . This contradicts the consistency of  $\Gamma_0$  as a subset of  $\text{PATTERN}$ , and this contradiction finishes our proof of the base case.

Now assume  $\Gamma_{n-1}$  is consistent for  $n \geq 1$ . We will show  $\Gamma_n$  is also consistent. If  $\Gamma_{n-1} \cup \{\varphi_n\}$  is inconsistent or  $\varphi_n$  does not have the form  $\exists x. \psi$ ,  $\Gamma_n$  is consistent by construction. Assume  $\Gamma_{n-1} \cup \{\varphi_n\}$  is consistent,  $\varphi_n \equiv \exists x. \psi$ , but  $\Gamma_n = \Gamma_{n-1} \cup \{\varphi_n\} \cup \{\psi[x_i/x]\}$  is not consistent. Then there exists a finite subset  $\Delta \subseteq_{\text{fin}} \Gamma_{n-1} \cup \{\varphi_n\}$  such that  $\vdash_{\mathcal{H}} \bigwedge \Delta \rightarrow \neg\psi[x_i/x]$ . By (UNIVERSAL GENERALIZATION),  $\vdash_{\mathcal{H}} \forall x_i. (\bigwedge \Delta \rightarrow \neg\psi[x_i/x])$ . Notice that  $x_i \notin \text{free Var}(\bigwedge \Delta)$  by construction, so by FOL reasoning  $\vdash_{\mathcal{H}} \bigwedge \Delta \rightarrow \neg\exists x_i. (\psi[x_i/x])$ . Since  $x_i \notin \text{free Var}(\psi)$ , by  $\alpha$ -renaming,  $\exists x_i. (\psi[x_i/x]) \equiv \exists x. \psi \equiv \varphi_n$ , and thus  $\vdash_{\mathcal{H}} \bigwedge \Delta \rightarrow \neg\varphi_n$ . This contradicts the assumption that  $\Gamma_{n-1} \cup \{\varphi_n\}$  is consistent.

Since  $\Gamma_n$  is consistent for any  $n \geq 0$ ,  $\Gamma^+ = \bigcup_n \Gamma_n$  is also consistent. This is because the derivation that shows inconsistency would use only finitely many patterns in  $\Gamma^+$ . In addition, we know  $\Gamma^+$  is maximal and witnessed by construction. QED.

We will prove that for every witnessed MCS  $\Gamma = \{\Gamma_s\}_{s \in S}$ , there exists a model  $M$  and a valuation  $\rho$  such that for every  $\varphi \in \Gamma_s$ ,  $|\varphi|_{M, \rho} \neq \emptyset$ . The next definition defines the canonical model which contains all witnessed MCSs as its elements. We will construct our intended model  $M$  as a submodel of the canonical model.

**Definition 3.6.** Given a signature  $\Sigma = (S, \Sigma)$ . The canonical model  $W = (\{W_s\}_{s \in S}, \_W)$  consists of

- a carrier set  $W_s = \{\Gamma \mid \Gamma \text{ is a witnessed MCS of sort } s\}$  for every sort  $s \in S$ ;

- an interpretation  $\sigma_W: W_{s_1} \times \cdots \times W_{s_n} \rightarrow \mathcal{P}(W_s)$  for every symbol  $\sigma \in \Sigma_{s_1 \dots s_n, s}$ , defined as  $\Gamma \in \sigma_W(\Gamma_1, \dots, \Gamma_n)$  if and only if for any  $\varphi_i \in \Gamma_i$ ,  $1 \leq i \leq n$ ,  $\sigma(\varphi_1, \dots, \varphi_n) \in \Gamma$ ; In particular, the interpretation for a constant symbol  $\sigma \in \Sigma_{\lambda, s}$  is  $\sigma_W = \{\Gamma \in W_s \mid \sigma \in \Gamma\}$ .

The carrier set  $W$  is not empty, thanks to Lemma 3.22.

The canonical model has a nontrivial property stated as the next lemma. The proof of the lemma is difficult, so we leave it to the end of the subsection.

**Theorem 3.11.** Let  $\Sigma = (S, \Sigma)$  be a signature and  $\Gamma$  be a witnessed MCS of sort  $s \in S$ . Given a symbol  $\sigma \in \Sigma_{s_1 \dots s_n, s}$  and patterns  $\varphi_1, \dots, \varphi_n$  of appropriate sorts. If  $\sigma(\varphi_1, \dots, \varphi_n) \in \Gamma$ , then there exist  $n$  witnessed MCSs  $\Gamma_1, \dots, \Gamma_n$  of appropriate sorts such that  $\varphi_i \in \Gamma_i$  for every  $1 \leq i \leq n$ , and  $\Gamma \in \sigma_W(\Gamma_1, \dots, \Gamma_n)$ .

**Definition 3.7.** Let  $\Sigma = (S, \Sigma)$  be a signature and  $W = (\{W_s\}_{s \in S}, \_W)$  be the canonical model. Given a witnessed MCS  $\Gamma = \{\Gamma_s\}_{s \in S}$ . Define  $Y = \{Y_s\}_{s \in S}$  be the smallest sets such that  $Y_s \subseteq W_s$  for every sort  $s$ , and the following inductive properties are satisfied:

- $\Gamma_s \in Y_s$  for every sort  $s$ ;
- If  $\Delta \in Y_s$  and there exist a symbol  $\sigma \in \Sigma_{s_1 \dots s_n, s}$  and witnessed MCSs  $\Delta_1, \dots, \Delta_n$  of appropriate sorts such that  $\Delta \in \sigma_W(\Delta_1, \dots, \Delta_n)$ , then  $\Delta_1 \in Y_{s_1}, \dots, \Delta_n \in Y_{s_n}$ .

Let  $Y = (Y, \_Y)$  be the model generated from  $\Gamma$ , where

$$\sigma_Y(\Delta_1, \dots, \Delta_n) = Y_s \cap \sigma_W(\Delta_1, \dots, \Delta_n) \quad \text{for every } \sigma \in \Sigma_{s_1 \dots s_n, s} \text{ and } \Delta_1 \in Y_{s_1}, \dots, \Delta_n \in Y_{s_n}.$$

We give some intuition about the generated model  $Y = (Y, \_Y)$ . The interpretation  $\sigma_Y$  is just the restriction of the interpretation  $\sigma_M$  on  $Y$ . The carrier set  $Y$  is defined inductively. Firstly,  $Y$  contains  $\Gamma$ . Given a set  $\Delta \in Y$ . If sets  $\Delta_1, \dots, \Delta_n$  are “generated” from  $\Delta$  by a symbol  $\sigma$ , meaning that  $\Delta \in \sigma_W(\Delta_1, \dots, \Delta_n)$ , then they are also in  $Y$ . Of course, a set  $\Delta$  is in  $Y$  maybe because it is generated from a set  $\Delta'$  by a symbol  $\sigma'$ , while  $\Delta'$  is generated from a set  $\Delta''$  by a symbol  $\sigma''$ , and so on. This generating path keeps going and eventually ends at  $\Gamma$  in finite number of steps. By definition, every member of  $Y$  has at least one such generating path, which we formally define as follows.

**Definition 3.8.** Let  $\Gamma = \{\Gamma_s\}_{s \in S}$  be a witnessed MCS and  $Y$  be the model generated from  $\Gamma$ . A *generating path*  $\pi$  is either the empty path  $\epsilon$ , or a sequence of pairs  $\langle (\sigma_1, p_1), \dots, (\sigma_k, p_k) \rangle$  where  $\sigma_1, \dots, \sigma_k$  are symbols (not necessarily distinct) and  $p_1, \dots, p_k$  are natural numbers representing positions. The *generating path relation*, denoted as  $GP$ , is a binary relation between witnessed MCSs in  $Y$  and generating paths, defined as the smallest relation that satisfies the following conditions:

- $GP(\Gamma_s, \epsilon)$  holds for every sort  $s$ ;
- If  $GP(\Delta, \pi)$  holds for a set  $\Delta \in Y_s$  and a generating path  $\pi$ , and there exist a symbol  $\sigma \in \Sigma_{s_1 \dots s_n, s}$  and witnessed MCSs  $\Delta_1, \dots, \Delta_n$  such that  $\Delta \in \sigma_W(\Delta_1, \dots, \Delta_n)$ , then  $GP(\Delta_i, \langle \pi, (\sigma, i) \rangle)$  holds for every  $1 \leq i \leq n$ .

We say that  $\Delta$  has a generating path  $\pi$  in the generated model if  $GP(\Delta, \pi)$  holds. It is easy to see that every witnessed MCS in  $Y$  has at least one generating path, and if a witnessed MCS of sort  $s$  has the empty path  $\epsilon$  as its generating path, it must be  $\Gamma_s$  itself.

**Definition 3.9.** Given a generating path  $\pi$ . Define the application context  $C_\pi$  inductively as follows. If  $\pi = \epsilon$ , then  $C_\pi$  is the identity context  $\square$ . If  $\pi = \langle \pi_0, (\sigma, i) \rangle$  where  $\sigma \in \Sigma_{s_1 \dots s_n, s}$  and  $1 \leq i \leq n$ , then  $C_\pi = C_{\pi_0}[\sigma(\top_{s_1}, \dots, \top_{s_{i-1}}, \square, \top_{s_{i+1}}, \dots, \top_{s_n})]$ .

A good intuition about Definition 3.9 is given as the next lemma.

**Lemma 3.23.** Let  $\Gamma$  be a witnessed MCS and  $Y$  be the model generated from  $\Gamma$ . Let  $\Delta \in Y$ . If  $\Delta$  has a generating path  $\pi$ , then  $C_\pi[\varphi] \in \Gamma$  for any pattern  $\varphi \in \Delta$ .

*Proof.* The proof is by induction on the length of the generating path  $\pi$ . If  $\pi$  is the empty path  $\epsilon$ , then  $\Delta$  must be  $\Gamma$  and  $C_\pi$  is the identity context, and  $C_\pi[\varphi] = \varphi \in \Gamma$  for any  $\varphi \in \Delta$ . Now assume  $\Delta$  has a generating path  $\pi = \langle \pi_0, (\sigma, i) \rangle$  with  $\sigma \in \Sigma_{s_1 \dots s_n, s}$ . By Definition 3.8, there exist witnessed MCSs  $\Delta_{s_1}, \dots, \Delta_{s_n}, \Delta_s \in Y$  and  $1 \leq i \leq n$  such that  $\Delta = \Delta_{s_i}$ ,  $\Delta_s \in \sigma_W(\Delta_{s_1}, \dots, \Delta_{s_n})$ , and  $\Delta_s$  has  $\pi_0$  as its generating path. For every  $\varphi \in \Delta = \Delta_i$ , since  $\top_{s_j} \in \Delta_{s_j}$  for any  $j \neq i$ , by Definition 3.6,  $\sigma(\top_{s_1}, \dots, \top_{s_{i-1}}, \varphi, \top_{s_{i+1}}, \dots, \top_{s_n}) \in \Delta_s$ . By induction hypothesis,  $C_{\pi_0}[\sigma(\top_{s_1}, \dots, \top_{s_{i-1}}, \varphi, \top_{s_{i+1}}, \dots, \top_{s_n})] \in \Gamma$ , while the latter is exactly  $C_\pi[\varphi]$ . QED.

**Lemma 3.24.** Let  $\Gamma$  be a witnessed MCS and  $Y$  be the model generated from  $\Gamma$ . For every  $\Gamma_1, \Gamma_2 \in Y$  of the same sort and every variable  $x$ , if  $x \in \Gamma_1 \cap \Gamma_2$  then  $\Gamma_1 = \Gamma_2$ .

*Proof.* Let  $\pi_i$  be a generating path of  $\Gamma_i$  for  $i = 1, 2$ . Assume  $\Gamma_1 \neq \Gamma_2$ . Then there exists a pattern  $\varphi$  such that  $\varphi \in \Gamma_1$  and  $\neg\varphi \in \Gamma_2$ . Because  $x \in \Gamma_1 \cap \Gamma_2$ , we know  $x \wedge \varphi \in \Gamma_1$  and  $x \wedge \neg\varphi \in \Gamma_2$ . By Lemma 3.23,  $C_{\pi_1}[x \wedge \varphi], C_{\pi_2}[x \wedge \neg\varphi] \in \Gamma$ , and thus  $C_{\pi_1}[x \wedge \varphi] \wedge C_{\pi_2}[x \wedge \neg\varphi] \in \Gamma$ . On the other hand,  $\neg(C_{\pi_1}[x \wedge \varphi] \wedge C_{\pi_2}[x \wedge \neg\varphi])$  is an instance of (SINGLETON VARIABLE) and thus it is included in  $\Gamma$ . This contradicts the consistency of  $\Gamma$ . QED.

We will establish an important result about generated models in Lemma 3.26 (the Truth Lemma), which links the semantics and syntax and is essential to the completeness result. Roughly speaking, the lemma says that for any generated model  $Y$  and any witnessed MCS

$\Delta \in Y$ , a pattern  $\varphi$  is in  $\Delta$  if and only if the interpretation of  $\varphi$  in  $Y$  contains  $\Delta$ . To prove the lemma, it is important to show that every variable is interpreted to a singleton. Lemma 3.24 ensures that every variable belongs to at most one witnessed MCS. To make sure it is interpreted to exactly one MCS, we complete our model by adding a dummy element  $\star$  to the carrier set, and interpreting all variables which are interpreted to none of the MCSs to the dummy element. This motivates the next definition.

**Definition 3.10.** Let  $\Gamma = \{\Gamma_s\}_{s \in S}$  be a witnessed MCS and  $Y$  be the  $\Gamma$ -generated model.  $\Gamma$ -completed model, denoted as  $M = (\{M_s\}_{s \in S}, \_M)$ , is inductively defined as follows for all sorts  $s \in S$ :

- $M_s = Y_s$ , if every  $x : s$  belongs at least one MCS in  $Y_s$ ;
- $M_s = Y_s \cup \{\star_s\}$ , otherwise.

We assume  $\star_s$  is an entity that is different from any MCSs, and  $\star_{s_1} \neq \star_{s_2}$  if  $s_1 \neq s_2$ . For every  $\sigma \in \Sigma_{s_1 \dots s_n, s}$ , define its interpretation

$$\sigma_M(\Delta_1, \dots, \Delta_n) = \begin{cases} \emptyset & \text{if some } \Delta_i = \star_{s_i} \\ \sigma_Y(\Delta_1, \dots, \Delta_n) \cup \{\star_s\} & \text{if all } \Delta_j \neq \star_{s_j} \text{ and some } \Delta_i = \Gamma_{s_i} \\ \sigma_{Y_{T_0}}(\Delta_1, \dots, \Delta_n) & \text{otherwise} \end{cases}$$

The completed valuation  $\rho : V \rightarrow M$  is defined as

$$\rho(x : s) = \begin{cases} \Delta & \text{if } x : s \in \Delta \\ \star_s & \text{otherwise} \end{cases}$$

The valuation  $\rho$  is a well-defined function, because by Lemma 3.24, if there are two witnessed MCSs  $\Delta_1$  and  $\Delta_2$  such that  $x \in \Delta_1$  and  $x \in \Delta_2$ , then  $\Delta_1 = \Delta_2$ .

Now we come back to prove Lemma 3.11. We need the following technical lemma.

**Lemma 3.25.** Let  $\sigma \in \Sigma_{s_1 \dots s_n, s}$  be a symbol,  $\Phi_1, \dots, \Phi_n, \phi$  be patterns of appropriate sorts, and  $y_1, \dots, y_n, x$  be variables of appropriate sorts such that  $y_1, \dots, y_n$  are distinct, and

$$y_1, \dots, y_n \notin \text{freeVar}(\phi) \cup \bigcup_{1 \leq i \leq n} \text{freeVar}(\Phi_i)$$

Then we have

$$\vdash \sigma(\Phi_1, \dots, \Phi_n) \rightarrow \exists y_1 \dots \exists y_n. \sigma(\Phi_1 \wedge (\exists x. \phi \rightarrow \phi[y_1/x]), \dots, \Phi_n \wedge (\exists x. \phi \rightarrow \phi[y_n/x]))$$

*Proof.* Notice that for every  $1 \leq i \leq n$ ,

$$\vdash_{\mathcal{H}} \exists x. \phi \rightarrow \exists y_i. (\phi[y_i/x]).$$

By easy matching logic reasoning,

$$\vdash \sigma(\Phi_1, \dots, \Phi_n) \rightarrow \sigma(\Phi_1 \wedge (\exists x. \phi \rightarrow \exists y_1. (\phi[y_1/x])), \dots, \Phi_n \wedge (\exists x. \phi \rightarrow \exists y_n. (\phi[y_n/x])))$$

Then use Proposition 3.4 to move the quantifiers  $\exists y_1, \dots, \exists y_n$  to the top. QED.

Now we are ready to prove Lemma 3.11.

*Proof of Lemma 3.11.* Recall that  $\Gamma \in \sigma_W(\Gamma_1, \dots, \Gamma_n)$  means for every  $\phi_i \in \Gamma_i$ ,  $1 \leq i \leq n$ ,  $\sigma(\phi_1, \dots, \phi_n) \in \Gamma$ . The main technique that we will be using here is similar to Lemma 3.22. We start with the singleton sets  $\{\varphi_i\}$  for every  $1 \leq i \leq n$  and extend them to witnessed MCSs  $\Gamma_i$ , while this time we also need to make sure the results  $\Gamma_1, \dots, \Gamma_n$  satisfy the desired property  $\Gamma \in \sigma_W(\Gamma_1, \dots, \Gamma_n)$ . Another difference compared to Lemma 3.22 is that this time we do not extend our set of variables, because our starting point,  $\{\varphi_i\}$ , contains just one pattern and uses only finitely many variables. Readers will see how these conditions play a role in the upcoming proof.

Enumerate all patterns of sorts  $s_1, \dots, s_n$  as follows  $\psi_0, \psi_1, \psi_2, \dots \in \bigcup_{1 \leq i \leq n} \text{PATTERN}_{s_i}$ . Notice that  $s_1, \dots, s_n$  do not need to be all distinct. To ease our notation, we define a “choice” operator, denoted as  $[\varphi_s]_{s'}$ , as follows

$$[\varphi_s]_{s'} = \begin{cases} \varphi_s & \text{if } s = s' \\ \text{nothing} & \text{otherwise} \end{cases}$$

For example,  $\varphi_s \wedge [\psi]_s$  means  $\varphi_s \wedge \psi$  if  $\psi$  also has sort  $s$ . Otherwise, it means  $\varphi_s$ . The choice operator propagates with all logic connectives in the natural way. For example,  $[\neg\psi]_s = \neg[\psi]_s$ .

In the following, we will define a non-decreasing sequence of pattern sets  $\Gamma_i^{(0)} \subseteq \Gamma_i^{(1)} \subseteq \Gamma_i^{(2)} \subseteq \dots \subseteq \text{PATTERN}_{s_i}$  for each  $1 \leq i \leq n$ , such that the following conditions are true for all  $1 \leq i \leq n$  and  $k \geq 0$ :

1. If  $\psi_k$  has sort  $s_i$ , then either  $\psi_k$  or  $\neg\psi_k$  belongs to  $\Gamma_i^{(k+1)}$ .
2. If  $\psi_k$  has the form  $\exists x. \phi_k$  and it belongs to  $\Gamma_i^{(k+1)}$ , then there exists a variable  $z$  such that  $(\exists x. \phi_k) \rightarrow \phi_k[z/x]$  also belongs to  $\Gamma_i^{(k+1)}$ .
3.  $\Gamma_i^{(k)}$  is finite.

4. Let  $\pi_i^{(k)} = \bigwedge \Gamma_i^{(k)}$  for every  $1 \leq i \leq n$ . Then  $\sigma(\pi_1^{(k)}, \dots, \pi_n^{(k)}) \in \Gamma$ .
5.  $\Gamma_i^{(k)}$  is consistent.

Among the above five conditions, condition (2)–(5) are like “safety” properties while condition (1) is like a “liveness” properties. We will eventually let  $\Gamma_i = \bigcup_{k \geq 0} \Gamma_i^{(k)}$  and prove that  $\Gamma_i$  has the desired property. Before we present the actual construction, we give some hints on how to prove these conditions hold. Conditions (1)–(3) will be satisfied directly by construction, although we will put a notable effort in satisfying condition (2). Condition (4) will be proved hold by induction on  $k$ . Condition (5) is in fact a consequence of condition (4) as shown below. Assume condition (4) holds but condition (5) fails. This means that  $\Gamma_i^{(k)}$  is not consistent for some  $1 \leq i \leq n$ , so  $\vdash_{\mathcal{H}} \pi_i^{(k)} \rightarrow \perp$ . By (FRAMING)

$$\vdash_{\mathcal{H}} \sigma(\pi_1^{(k)}, \dots, \pi_i^{(k)}, \dots, \pi_n^{(k)}) \rightarrow \sigma(\pi_1^{(k)}, \dots, \perp, \dots, \pi_n^{(k)})$$

Then by Proposition 3.4 and FOL reasoning,

$$\vdash_{\mathcal{H}} \sigma(\pi_1^{(k)}, \dots, \pi_i^{(k)}, \dots, \pi_n^{(k)}) \rightarrow \perp$$

Since  $\sigma(\pi_1^{(k)}, \dots, \pi_i^{(k)}, \dots, \pi_n^{(k)}) \in \Gamma$  by condition (4), we know  $\perp \in \Gamma$  by Proposition 3.10. And this contradicts the fact that  $\Gamma$  is consistent.

Now we are ready to construct the sequence  $\Gamma_i^{(0)} \subseteq \Gamma_i^{(1)} \subseteq \Gamma_i^{(2)} \subseteq \dots$  for  $1 \leq i \leq n$ . Let  $\Gamma_i^{(0)} = \{\varphi_i\}$  for  $1 \leq i \leq n$ . Obviously,  $\Gamma_i^{(0)}$  satisfies conditions (3) and (4). Condition (5) follows as a consequence of condition (4). Conditions (1) and (2) are not applicable.

Suppose we have already constructed sets  $\Gamma_i^{(k)}$  for every  $1 \leq i \leq n$  and  $k \geq 0$ , which satisfy the conditions (1)–(5). We show how to construct  $\Gamma_i^{(k+1)}$ . In order to satisfy condition (1), we should add either  $\psi_k$  or  $\neg\psi_k$  to  $\Gamma_i^{(k)}$ , if  $\Gamma_i^{(k)}$  has the same sort as  $\psi_k$ . Otherwise, we simply let  $\Gamma_i^{(k+1)}$  be the same as  $\Gamma_i^{(k)}$ . The question here is: if  $\Gamma_i^{(k)}$  has the same sort as  $\psi_k$ , which pattern should we add to  $\Gamma_i^{(k)}$ ,  $\psi_k$  or  $\neg\psi_k$ ? Obviously, condition (3) will still hold no matter which one we choose to add, so we just need to make sure that we do not break conditions (2) and (4).

Let us start by satisfying condition (4). Consider pattern  $\sigma(\pi_1^{(k)}, \dots, \pi_n^{(k)})$ , which, by condition (4), is in  $\Gamma$ . This tells us that the pattern

$$\sigma(\pi_1^{(k)} \wedge [\psi_k \vee \neg\psi_k]_{s_1}, \dots, \pi_n^{(k)} \wedge [\psi_k \vee \neg\psi_k]_{s_n})$$

is also in  $\Gamma$ . Recall that  $[\_]_s$  is the choice operator, so if  $\psi_k$  has sort  $s_i$ , then  $\pi_i^{(k)} \wedge [\psi_k \vee \neg\psi_k]_{s_i}$  is  $\pi_i^{(k)} \wedge (\psi_k \vee \neg\psi_k)$ . Otherwise, it is  $\pi_i^{(k)}$ . Use Proposition 3.4 and FOL reasoning, and notice



that the choice operator propagates with the disjunction  $\vee$  and the negation  $\neg$ , we get

$$\sigma((\pi_1^{(k)} \wedge [\psi_k]_{s_1}) \vee (\pi_1^{(k)} \wedge \neg[\psi_k]_{s_1}), \dots, (\pi_n^{(k)} \wedge [\psi_k]_{s_n}) \vee (\pi_n^{(k)} \wedge \neg[\psi_k]_{s_n})) \in \Gamma$$

Then we use Proposition 3.4 again and move all the disjunctions to the top, and we end up with a disjunction of  $2^n$  patterns:

$$\bigvee \sigma(\pi_1^{(k)} \wedge [\neg]_1^{(k)}[\psi_k]_{s_1}, \dots, \pi_n^{(k)} \wedge [\neg]_n^{(k)}[\psi_k]_{s_n}) \in \Gamma$$

where  $[\neg]$  means either nothing or  $\neg$ . Notice that some  $[\psi_k]_{s_i}$ 's might be nothing, so some of these  $2^n$  patterns may be the same.

Notice that  $\Gamma$  is an MCS. By proposition 3.10, among these  $2^n$  patterns there must exists one pattern that is in  $\Gamma$ . We denote *that* pattern as

$$\sigma(\pi_1^{(k)} \wedge [\neg]_1^{(k)}[\psi_k]_{s_1}, \dots, \pi_n^{(k)} \wedge [\neg]_n^{(k)}[\psi_k]_{s_n})$$

For any  $1 \leq i \leq n$ , if  $[\neg]_i^{(k)}[\psi_k]_{s_i}$  does not have the form  $\exists x. \phi$ , we simply define  $\Gamma_i^{(k+1)} = \Gamma_i^{(k)} \cup \{[\neg]_i^{(k)}[\psi_k]_{s_i}\}$ . If  $[\neg]_i^{(k)}[\psi_k]_{s_i}$  does have the form  $\exists x. \phi$ , we need special effort to satisfy condition (2). Without loss of generality and to ease our notation, let us assume that *for every*  $1 \leq i \leq n$ , the pattern  $[\neg]_i^{(k)}[\psi_k]_{s_i}$  has the same form  $\exists x. \phi$ . We are going to find for each index  $i$  a variable  $z_i$  such that

$$\sigma(\pi_1^{(k)} \wedge \exists x. \phi \wedge (\exists x. \phi \rightarrow \phi[z_1/x]), \dots, \pi_n^{(k)} \wedge \exists x. \phi \wedge (\exists x. \phi \rightarrow \phi[z_n/x])) \in \Gamma$$

This will allow us to define  $\Gamma_i^{(k+1)} = \Gamma_i^{(k)} \cup \{\exists x. \phi\} \cup \{\exists x. \phi \rightarrow \phi[z_i/x]\}$  which satisfies conditions (2) and (4).

We find these variables  $z_i$ 's by Lemma 3.25 and the fact that  $\Gamma$  is a witnessed set. Let  $\Phi_i \equiv \pi_i^{(k)} \wedge \exists x. \phi$  for  $1 \leq i \leq n$ . By construction,  $\sigma(\Phi_1, \dots, \Phi_n) \in \Gamma$ . Hence, by Lemma 3.25 and Proposition 3.10, for any distinct variables  $y_1, \dots, y_n \notin \text{free Var}(\phi) \cup \bigcup_{1 \leq i \leq n} \text{free Var}(\Phi_i)$ ,

$$\exists y_1 \dots \exists y_n. \sigma(\Phi_1 \wedge (\exists x. \phi \rightarrow \phi[y_1/x]), \dots, \Phi_n \wedge (\exists x. \phi \rightarrow \phi[y_n/x])) \in \Gamma$$

The set  $\Gamma$  is a witnessed set, so there exist variables  $z_1, \dots, z_n$  such that

$$\sigma(\Phi_1 \wedge (\exists x. \phi \rightarrow \phi[z_1/x]), \dots, \Phi_n \wedge (\exists x. \phi \rightarrow \phi[z_n/x])) \in \Gamma$$

This justifies our construction  $\Gamma_i^{(k+1)} = \Gamma_i^{(k)} \cup \{\exists x. \phi\} \cup \{\exists x. \phi \rightarrow \phi[z_i/x]\}$ .

So far we have proved our construction of the sequences  $\Gamma_i^{(0)} \subseteq \Gamma_i^{(1)} \subseteq \Gamma_i^{(2)} \subseteq \dots$  for

$1 \leq i \leq n$  satisfy the conditions (1)–(5). Let  $\Gamma_i = \bigcup_{k \geq 0} \Gamma_i^{(k)}$  for  $1 \leq i \leq n$ . By construction,  $\Gamma_i$  is a witnessed MCS. It remains to prove that  $\Gamma \in \sigma_W(\Gamma_1, \dots, \Gamma_n)$ . To prove it, assume  $\phi_i \in \Gamma_i$  for  $1 \leq i \leq n$ . By construction, there exists  $K > 0$  such that  $\phi_i \in \Gamma_i^{(K)}$  for all  $1 \leq i \leq n$ . Therefore,  $\vdash_{\mathcal{H}} \pi_i^{(K)} \rightarrow \phi_i$ . By condition (4),  $\sigma(\pi_1^{(K)}, \dots, \pi_n^{(K)}) \in \Gamma$ , and thus by (FRAMING) and Proposition 3.10,  $\sigma(\phi_1, \dots, \phi_n) \in \Gamma$ . QED.

**Lemma 3.26** (Truth Lemma). Let  $\Gamma$  be a witnessed MCS,  $M$  be its completed model, and  $\rho$  be the completed valuation. For any witnessed MCS  $\Delta \in M$  and any pattern  $\varphi$  such that  $\Delta$  and  $\varphi$  have the same sort,

$$\varphi \in \Delta \quad \text{if and only if} \quad \Delta \in |\varphi|_{M,\rho}$$

*Proof.* The proof is by induction on the structure of  $\varphi$ . If  $\varphi$  is a variable the conclusion follows by Definition 3.6. If  $\varphi$  has the form  $\psi_1 \wedge \psi_2$  or  $\neg\psi_1$ , the conclusion follows from Proposition 3.10. If  $\varphi$  has the form  $\sigma(\varphi_1, \dots, \varphi_n)$ , the conclusion from left to right is given by Lemma 3.11. The conclusion from right to left follows from Definition 3.6.

Now assume  $\varphi$  has the form  $\exists x. \psi$ . If  $\exists x. \psi \in \Delta$ , since  $\Delta$  is a witnessed set, there is a variable  $y$  such that  $\exists x. \psi \rightarrow \psi[y/x] \in \Delta$ , and thus  $\psi[y/x] \in \Delta$ . By induction hypothesis,  $\Delta \in |\psi[y/x]|_{M,\rho}$ , and thus  $\Delta \in |\exists x. \psi|_{M,\rho}$ .

Consider the other direction. Assume  $\Delta \in |\exists x. \psi|_{M,\rho}$ . By definition there exists a witnessed set  $\Delta' \in M$  such that  $\Delta \in |\psi|_{M,\rho[\Delta'/x]}$ . By Definition 3.10, every element in  $M$  (no matter if it is an MCS or  $\star$ ) has a variable that is assigned to it by the completed valuation  $\rho$ . Let us assume that variable  $y$  is assigned to  $\Delta'$ , i.e.,  $\rho(y) = \Delta'$ . By Lemma 3.1,  $\Delta \in |\psi|_{M,\rho'} = |\psi[y/x]|_{M,\rho}$ . By induction hypothesis,  $\psi[y/x] \in \Delta$ . Finally notice that  $\vdash_{\mathcal{H}} \psi[y/x] \rightarrow \exists y. \psi[y/x]$ . By Proposition 3.10,  $\exists y. \psi[y/x] \in \Delta$ , i.e.,  $\exists x. \psi \in \Delta$ . QED.

**Theorem 3.12.** For any consistent set  $\Gamma$ , there is a model  $M$  and a valuation  $\rho$  such that for all patterns  $\varphi \in \Gamma$ ,  $|\varphi|_{M,\rho} \neq \emptyset$ .

*Proof.* Use Lemma 3.22 and extend  $\Gamma$  to a witnessed MCS  $\Gamma^+$ . Let  $M$  and  $\rho$  be the completed model and valuation generated by  $\Gamma^+$  respectively. By Lemma 3.26, for all patterns  $\varphi \in \Gamma \subseteq \Gamma^+$ , we have  $\Gamma^+ \in |\varphi|_{M,\rho}$ , so  $|\varphi|_{M,\rho} \neq \emptyset$ . QED.

Now we are ready to prove local completeness of  $\mathcal{H}$ .

**Theorem 3.13.** For any  $\Gamma$  and  $\varphi$ ,  $\Gamma \models^{loc} \varphi$  implies  $\Gamma \vdash_{\mathcal{H}}^{loc} \varphi$ .

*Proof.* Assume the opposite that  $\Gamma \not\vdash_{\mathcal{H}}^{loc} \varphi$ , which implies that  $\Gamma \cup \{\neg\varphi\}$  is consistent. Extend it to a witnessed MCS  $\Gamma^+$  and let  $M, \rho$  be the completed model and completed valuation generated by  $\Gamma^+$ . By Lemma 3.26,  $\Gamma^+ \in |\psi|_{M,\rho}$  for all  $\psi \in \Gamma$ , and  $\Gamma^+ \in |\neg\varphi|_{M,\rho}$ , i.e.,  $\Gamma^+ \notin |\varphi|_{M,\rho}$ . This contradicts with  $\Gamma \models^{loc} \varphi$ . QED.

**Theorem 3.14.** For any  $\varphi$ ,  $\emptyset \models \varphi$  implies  $\emptyset \vdash_{\mathcal{H}} \varphi$ .

*Proof.* By Proposition 3.9.

QED.

In the literature, both Theorem 3.13 and Theorem 3.14 are called local completeness. To distinguish them, Theorem 3.13 is called strong local completeness while Theorem 3.14 is called weak local completeness.

## Chapter 4: FROM MATCHING LOGIC TO MATCHING $\mu$ -LOGIC

Fixpoints are ubiquitous in computer science. They are given different names when appearing in different contexts. Inductive datatypes are an example of fixpoints. The datatype of cons-lists  $\text{list} ::= \text{Nil} \mid \text{Cons}(\text{element}, \text{list})$  is the smallest set that is closed under  $\text{Nil}$  and  $\text{Cons}$ . Many temporal operators are fixpoints;  $\Box\varphi$  (“always”) can be defined as a greatest fixpoint based on  $\circ\varphi$  (“next”); see Section 5.8. The reachability relation  $\varphi_1 \Rightarrow \varphi_2$  in reachability logic (RL) (Section 2.14) is also a fixpoint; see Section 5.11. Furthermore, these fixpoints are studied and reasoned about using different methods. For inductive datatypes, we often use structural induction to prove recursive properties about them. For temporal operators, we can use the specialized proof rules of temporal logics to reason about them. For example,  $\Box(\varphi \rightarrow \circ\varphi) \rightarrow (\varphi \rightarrow \Box\varphi)$  is the (IND) proof rule of infinite-trace LTL (Figure 2.5) that captures the inductive nature of  $\Box$ . For RL, the (CIRCULARITY) proof rule in Figure 2.12 captures the co-inductive nature of reachability reasoning.

On the other hand, the Knaster-Tarski fixpoint theorem (Theorem 2.1) governs everything we need to know about the existence and construction of fixpoints. For any monotone function  $f: \mathcal{P}(A) \rightarrow \mathcal{P}(A)$ ,  $f$  has fixpoints, and the least/greatest fixpoints are given as follows:

$$\mathbf{lfp} f = \bigcap \{A_0 \subseteq A \mid f(A_0) \subseteq A_0\} \quad \mathbf{gfp} f = \bigcup \{A_0 \subseteq A \mid A_0 \subseteq f(A_0)\}$$

Let us look at the  $\mathbf{lfp} f$  as the discussion for  $\mathbf{gfp} f$  is similar. From the construction above, we know two things about  $\mathbf{lfp} f$ . Firstly, it is a fixpoint, so  $\mathbf{lfp} f = f(\mathbf{lfp} f)$ . Secondly, for every  $A_0$  such that  $f(A_0) \subseteq A_0$  (i.e.,  $A_0$  is a pre-fixpoint of  $f$ ),  $\mathbf{lfp} f \subseteq A_0$ .

Our goal is to incorporate Theorem 2.1 into matching logic so we can obtain a unifying foundation for specifying and reasoning fixpoints that can handle all instances and examples of fixpoints, including inductive datatypes, temporal operators, reachability rules, and so on. Luckily, matching logic patterns fit nicely with the setting of Theorem 2.1 because for any pattern  $\varphi$  and a free variable  $x$  in it, we can regard  $\varphi$  (w.r.t.  $x$ ) as a function over the powerset domain in the following sense:  $\psi \mapsto \varphi[\psi/x]$  for every  $\psi$ . Here,  $\psi$  is a pattern (so semantically, a set) and  $\varphi[\psi/x]$  is the result of “applying”  $\varphi$  to  $\psi$ . If  $\varphi$  is positive in  $x$ , then the corresponding function  $\psi \mapsto \varphi[\psi/x]$  is monotone and thus has fixpoints. We denote the least and the greatest fixpoints as  $\mu x . \varphi$  and  $\nu x . \varphi$ , respectively. We also know two things about  $\mu x . \varphi$  (and similarly for  $\nu x . \varphi$ ). Firstly, it is a fixpoint, so  $\vdash (\mu x . \varphi) \leftrightarrow \varphi[(\mu x . \varphi)/x]$ . Secondly, it is smaller than any pre-fixpoint, so  $\vdash \varphi[\psi/x] \rightarrow \psi$  implies  $\vdash (\mu x . \varphi) \rightarrow \psi$ . Similarly for  $\nu x . \varphi$ , we know that it is a fixpoint, so  $\vdash (\nu x . \varphi) \leftrightarrow \varphi[(\nu x . \varphi)/x]$ . Secondly, it

is larger than any post-fixpoint, so  $\vdash \psi \rightarrow \varphi[\psi/x]$  implies  $\vdash \psi \rightarrow \nu x . \varphi$ .

If we could define  $\mu x . \varphi$  and  $\nu x . \varphi$  as notation in matching logic, just like how  $\forall x . \varphi \equiv \neg \exists x . \neg \varphi$  is defined, we would have a directly logical incarnation of Theorem 2.1 in matching logic and obtain a unifying logical foundation to specify and reason about any types of fixpoints. Unfortunately, it turns out that  $\mu x . \varphi$  and  $\nu x . \varphi$  cannot be defined as notation in matching logic. We have to extend matching logic with a new set of *set variables*, denoted by  $X, Y$ , etc., and introduce an explicit  $\mu$  operator to build least fixpoints. Greatest fixpoints can be built by the dual  $\nu$  operator. We call the extended logic matching  $\mu$ -logic to emphasize that it has an explicit  $\mu$  operator. The purpose of this chapter is to formally present matching  $\mu$ -logic and introduce its extended syntax, semantics, and proof rules.

#### 4.1 NECESSITY OF EXTENSION

We explain why matching logic must be extended to support fixpoints. Let us assume that we find a way to define fixpoints in matching logic as notation. That means that  $\mu x . \varphi$  is a matching logic pattern, where  $x$  is a regular variable of matching logic. Then we show that the following rule fails to hold:

$$\text{(EQUIVALENCE CONGRUENCE)} \quad \frac{\varphi_1 \leftrightarrow \varphi_2}{(\mu x . \varphi_1) \leftrightarrow (\mu x . \varphi_2)}$$

Note that (EQUIVALENCE CONGRUENCE) is a highly desired property that is expected to hold in any reasonable formal system. Its failure is thus strong evidence that  $\mu x . \varphi$  must not, or at least, should not be defined as notation. It is more natural and reasonable to extend matching logic to matching  $\mu$ -logic in order to support fixpoints.

To see why (EQUIVALENCE CONGRUENCE) fails, let us first note that  $\mu x . x$  should be equivalent to  $\perp$ . This is because  $\mu x . x$  represents the identity function, whose least fixpoint is the empty set, i.e.,  $\perp$ . Let us also note that  $\mu x . c$  should be equivalent to  $c$  for a constant symbol  $c$ . This is because  $\mu x . c$  represents a constant function that returns  $c$  for all inputs. So its only fixpoint is  $c$  itself.

We now build a counterexample of (EQUIVALENCE CONGRUENCE). We define a matching logic theory  $\Gamma = \{x, c\}$ . The axiom  $x$  enforces the underlying carrier set to be a singleton set, say  $\{\star\}$ . The axiom  $c$  enforces the interpretation of  $c$  to be  $\{\star\}$ , too. Thus we have  $\Gamma \models x \leftrightarrow c$ . However,  $\Gamma \not\models (\mu x . x) \leftrightarrow (\mu x . c)$ , because the left-hand side should be equivalent to  $\perp$  while the right-hand side should be equivalent to  $c$ , and  $\Gamma \not\models \perp \leftrightarrow c$ . Thus, (EQUIVALENCE CONGRUENCE) fails to hold.

It means that if we were to define fixpoints in matching logic as notation, we either need to drop the highly desired property (EQUIVALENCE CONGRUENCE) or live in a weird world where  $\mu x . x$  is not equivalent to  $\perp$  (or  $\mu x . c$  is not equivalent to  $c$ ). We want neither of the above. Thus, we conclude that the proper way to add fixpoint support to in matching logic is to extend it to matching  $\mu$ -logic, which we present in Section 4.2.

As a side remark, (EQUIVALENCE CONGRUENCE) does hold in matching  $\mu$ -logic; see Lemma 4.4.

## 4.2 MATCHING $\mu$ -LOGIC SYNTAX, SEMANTICS, AND PROOF SYSTEM

We define matching  $\mu$ -logic syntax, semantics, and proof system and present some basic results about its formal reasoning.

### 4.2.1 Matching $\mu$ -logic syntax and semantics

**Definition 4.1.** A *matching  $\mu$ -logic signature* or simply a *signature*  $(S, \Sigma)$  is the same as a matching logic signature in Definition 2.45. Given a matching  $\mu$ -logic signature  $(S, \Sigma)$ , an  $S$ -indexed set  $EV = \{EV_s\}_{s \in S}$  of *element variables* denoted by  $x:s$ ,  $y:s$ , etc., and an  $S$ -indexed set  $SV = \{SV_s\}_{s \in S}$  of *set variables* denoted by  $X:s$ ,  $Y:s$ , etc., the syntax of *matching  $\mu$ -logic patterns* or simply *patterns* is given by extending the syntax of matching logic with the following grammar rules:

$$\begin{array}{l} \text{matching } \mu\text{-logic patterns} \quad \varphi_s ::= (\text{syntax of matching logic}) \\ \quad \quad \quad | X:s \\ \quad \quad \quad | \mu X:s . \varphi_s \end{array}$$

where  $\mu X:s . \varphi_s$  requires that  $\varphi_s$  is *positive* in  $X:s$ , i.e.,  $X:s$  does not occur in an odd number of  $\neg$ 's.

We feel free to drop the sorts when they are understood or not important. The notion of free variables  $\text{freeVar}(\varphi)$  and capture-avoiding substitution  $\varphi[\psi/x]$  and  $\varphi[\psi/X]$  are defined in the usual way. We define  $\nu X . \varphi$  as follows

$$\nu X . \varphi \equiv \neg \mu X . \neg \varphi[\neg X/X]$$

Note that there are three  $\neg$ 's, not two. Also note that  $\neg \varphi[\neg X/X]$  is positive in  $X$  whenever  $\varphi$  is positive in  $X$ .

**Definition 4.2.** Given a signature  $(S, \Sigma)$ , a *matching  $\mu$ -logic  $(S, \Sigma)$ -model* or simply  $(S, \Sigma)$ -*model* is the same as a matching logic model in Definition 2.46. Given an  $(S, \Sigma)$ -model  $M = (\{M_s\}_{s \in S}, \{\sigma_M\}_{\sigma \in \Sigma})$ , a *matching  $\mu$ -logic  $M$ -valuation* or simply  $M$ -*valuation* is a pair  $\rho = (\rho_{EV}, \rho_{SV})$  where  $\rho_{EV} : EV \rightarrow M$  is a matching logic  $M$ -valuation and  $\rho_{SV} : SV \rightarrow \mathcal{P}(M)$ .

**Definition 4.3.** Given a signature  $(S, \Sigma)$  and an  $(S, \Sigma)$ -model  $M = (\{M_s\}_{s \in S}, \{\sigma_M\}_{\sigma \in \Sigma})$ , the matching  $\mu$ -logic interpretation function  $|\varphi|_{M, \rho}$  is inductively defined for all  $\varphi$  and  $\rho$  as follows:

1.  $|X : s|_{M, \rho} = \rho_{SV}(X : s)$  for  $X : s \in SV$ ;
2.  $|\mu X : s . \varphi_s|_{M, \rho} = \mathbf{lfp}(A \mapsto |\varphi|_{M, \rho[A/X : s]})$ ;
3. The rest rules are the same as Definition 2.47.

Here,  $\mathbf{lfp}(A \mapsto |\varphi|_{M, \rho[A/X : s]})$  is the least fixpoint of the function that maps  $A$  to  $|\varphi|_{M, \rho[A/X : s]}$  for  $A \subseteq M$ .

The derived semantics of  $\mu X : s . \varphi_s$  and  $\nu X : s . \varphi_s$  are as follows:

$$\begin{aligned} |\mu X : s . \varphi_s|_{M, \rho} &= \mathbf{lfp}(A \mapsto |\varphi|_{M, \rho[A/X : s]}) = \bigcap \{A \subseteq M_s \mid |\varphi|_{M, \rho[A/X : s]} \subseteq A\} \\ |\nu X : s . \varphi_s|_{M, \rho} &= \mathbf{gfp}(A \mapsto |\varphi|_{M, \rho[A/X : s]}) = \bigcup \{A \subseteq M_s \mid A \subseteq |\varphi|_{M, \rho[A/X : s]}\} \end{aligned}$$

A *matching  $\mu$ -logic theory* or simply *theory*  $\Gamma$  is a set of patterns/axioms. We define  $M \models \varphi$ ,  $M \models \Gamma$ , and  $\Gamma \models \varphi$  in the usual way. Note that if  $\psi \in \Gamma$  has free set variables, then those set variables are effectively universally quantified. This way, matching  $\mu$ -logic allows to write axioms that features monadic universal second-order quantification at the top of axioms. This fact is useful for defining powersets (Section 5.7.1) and SOL (Section 5.7) as matching  $\mu$ -logic theories.

#### 4.2.2 Matching $\mu$ -logic proof system $\mathcal{H}_\mu$

The matching  $\mu$ -logic proof system  $\mathcal{H}_\mu$ , as shown in Figure 4.1, extends the matching logic proof system  $\mathcal{H}$  in Figure 3.2 with three proof rules: (SUBSTITUTION), (PRE-FIXPOINT), and (KNASTER TARSKI). The latter two have been discussed in Section 4.1. They are a direct logical incarnation of the Knaster-Tarski fixpoint theorem (Theorem 2.1) into matching  $\mu$ -logic. The (SUBSTITUTION) rule captures the semantic effect that a free set variable in a matching  $\mu$ -logic axiom is universally quantified. So if  $\varphi$  is provable and  $X \in \text{freeVar}(\varphi)$ , then  $\varphi[\psi/X]$  is still provable for any  $\psi$ . We use  $\vdash_{\mathcal{H}_\mu}$  or simply  $\vdash$  to denote the corresponding

(SYSTEM $\mathcal{H}$ )	all proof rules of $\mathcal{H}$ in Figure 3.2
(SUBSTITUTION)	$\frac{\varphi}{\varphi[\psi/X]}$
(PRE-FIXPOINT)	$\varphi[\mu X . \varphi/X] \rightarrow \mu X . \varphi$
(KNASTER TARSKI)	$\frac{\varphi[\psi/X] \rightarrow \psi}{\mu X . \varphi \rightarrow \psi}$

Figure 4.1: Matching  $\mu$ -Logic Proof System  $\mathcal{H}_\mu$ 

provability relation of  $\mathcal{H}_\mu$ . Since  $\mathcal{H}_\mu$  extends  $\mathcal{H}$ , we have that  $\Gamma \vdash_{\mathcal{H}} \varphi$  implies  $\Gamma \vdash \varphi$  for any matching logic theory  $\Gamma$  and pattern  $\varphi$ .

**Theorem 4.1.**  $\mathcal{H}_\mu$  is sound, i.e., for any  $\Gamma$  and  $\varphi$ ,  $\Gamma \vdash \varphi$  implies  $\Gamma \models \varphi$ .

*Proof.* We only need to verify the soundness of (SUBSTITUTION), (PRE-FIXPOINT), and (KNASTER TARSKI).

For (SUBSTITUTION), let us assume any  $M \models \varphi$ . By definition,  $|\varphi|_{M,\rho} = M$  for all  $\rho$ . Our goal is to show  $M \models \varphi[\psi/X]$ . Let  $\rho$  be any valuation. We have  $|\varphi[\psi/X]|_{M,\rho} = |\varphi|_{M,\rho[\psi|_{M,\rho}/X]}$ . Note that  $\rho[\psi|_{M,\rho}/X]$  is just another valuation, so  $|\varphi|_{M,\rho[\psi|_{M,\rho}/X]} = M$  by assumption.

For (PRE-FIXPOINT), let  $\rho$  be any valuation. Our goal is to prove  $|\varphi[\mu X . \varphi/X] \rightarrow \mu X . \varphi|_{M,\rho} = M$ . By definition,  $|\varphi[\mu X . \varphi/X]|_{M,\rho} = |\varphi|_{M,\rho[\mu X . \varphi|_{M,\rho}/X]}$ , and  $|\mu X . \varphi|_{M,\rho} = \bigcap \{A \mid |\varphi|_{M,\rho[A/X]} \subseteq A\}$ . By Theorem 2.1,  $|\mu X . \varphi|_{M,\rho}$  itself is a fixpoint of  $|\varphi|_{M,\rho[A/X]} = A$ . Therefore,  $|\varphi|_{M,\rho[\mu X . \varphi|_{M,\rho}/X]} = |\mu X . \varphi|_{M,\rho}$ .

For (KNASTER TARSKI), let  $M \models \varphi[\psi/X] \rightarrow \psi$ . Our goal is to prove  $M \models \mu X . \varphi \rightarrow \psi$ . Let  $\rho$  be any valuation. We need to prove  $|\mu X . \varphi|_{M,\rho} \subseteq |\psi|_{M,\rho}$ . Note that  $|\mu X . \varphi|_{M,\rho}$  is defined as the least fixpoint of  $|\varphi|_{M,\rho[A/X]} = A$ . By Theorem 2.1, it suffices to prove  $|\psi|_{M,\rho}$  is a pre-fixpoint, i.e.,  $|\varphi|_{M,\rho[\psi|_{M,\rho}/X]} \subseteq |\psi|_{M,\rho}$ . This is given by our assumption,  $M \models \varphi[\psi/X] \rightarrow \psi$ . This implies that  $|\varphi[\psi/X]|_{M,\rho} \subseteq |\psi|_{M,\rho}$ , i.e.,  $|\varphi|_{M,\rho[\psi|_{M,\rho}/X]} \subseteq |\psi|_{M,\rho}$ . QED.

#### 4.2.3 Basic properties about $\mathcal{H}_\mu$

We prove some basic properties about  $\mathcal{H}_\mu$ . Firstly, we generalized Lemma 3.1 matching  $\mu$ -logic with set variables and the  $\mu$  operator.

**Lemma 4.1.**  $|\varphi[\psi/X]|_{M,\rho} = |\varphi|_{M,\rho[\rho(\psi)/X]}$  for all  $X \in SV$ .



*Proof.* The proof is the same as Lemma 3.1. The only interesting case is when  $\varphi \equiv \mu Z . \varphi_1$ . By  $\alpha$ -renaming, we can safely assume  $Z \notin \text{free Var}(\psi)$ . We have:

$$\begin{aligned}
 |(\mu Z . \varphi_1)[\psi/X]|_{M,\rho} &= |\mu Z . (\varphi_1[\psi/X])|_{M,\rho} \\
 &= \bigcap \{A \mid |\varphi_1[\psi/X]|_{M,\rho[A/Z]} \subseteq A\} \\
 &= \bigcap \{A \mid |\varphi_1|_{M,\rho[A/Z]}[|\psi|_{M,\rho[A/Z]/X}] \subseteq A\} \\
 &= \bigcap \{A \mid |\varphi_1|_{M,\rho[A/Z]}[|\psi|_{M,\rho/X}] \subseteq A\} \\
 &= \bigcap \{A \mid |\varphi_1|_{M,\rho[|\psi|_{M,\rho/X}][A/Z]} \subseteq A\} \\
 &= |\mu Z . \varphi_1|_{M,\rho[|\psi|_{M,\rho/X}]} \\
 &= |\varphi|_{M,\rho[|\psi|_{M,\rho/X}]}.
 \end{aligned}$$

QED.

Then we show that  $\mu X . \varphi$  is indeed a fixpoint in  $\mathcal{H}_\mu$ .

**Lemma 4.2.**  $\vdash \mu X . \varphi \leftrightarrow \varphi[\mu X . \varphi/X]$ .

*Proof.* We prove both directions.

(Case “ $\rightarrow$ ”). Apply (KNASTER TARSKI), and we prove  $\vdash \varphi[(\varphi[\mu X . \varphi/X])/X] \rightarrow \varphi[\mu X . \varphi/X]$ . By Lemma 4.6, and the fact that  $\varphi$  is positive in  $X$ , we just need to prove  $\vdash \varphi[\mu X . \varphi/X] \rightarrow \varphi$ , which is proved by (PRE-FIXPOINT).

(Case “ $\leftarrow$ ”) is exactly (PRE-FIXPOINT).

QED.

The proof system  $\mathcal{H}_\mu$  only defines (PRE-FIXPOINT) and (KNASTER TARSKI) for  $\mu X . \varphi$ . Here we show that their dual versions also hold for  $\nu X . \varphi$ .

**Lemma 4.3.** The following propositions hold:

- (PRE-FIXPOINT):  $\vdash \nu X . \varphi \rightarrow \varphi[\nu X . \varphi/X]$ ;
- (KNASTER TARSKI):  $\vdash \psi \rightarrow \varphi[\psi/X]$  implies  $\vdash \psi \rightarrow \nu X . \varphi$ .

*Proof.* Simply unfold  $\nu X . \varphi$  to  $\neg \mu X . \neg(\varphi[\neg X/X])$  and use the version of (PRE-FIXPOINT) and (KNASTER TARSKI) for the least fixpoints.

QED.

We verify that (EQUIVALENCE CONGRUENCE) in Section 4.1 is indeed derivable in  $\mathcal{H}_\mu$ .

**Lemma 4.4.**  $\Gamma \vdash \varphi_1 \rightarrow \varphi_2$  implies  $\Gamma \vdash \mu X . \varphi_1 \rightarrow \mu X . \varphi_2$ .

*Proof.* Use (KNASTER TARSKI), and then (SUBSTITUTION).

QED.

**Lemma 4.5.** For any context  $C$ , we have  $\Gamma \vdash \varphi_1 \leftrightarrow \varphi_2$  iff  $\Gamma \vdash C[\varphi_1] \leftrightarrow C[\varphi_2]$ .

*Proof.* Carry out induction on the structure of  $C$ . Except the case  $C \equiv \mu X . C_1$ , all other cases have been proved in Proposition 3.5. While the  $\mu$ -case is proved by Lemma 4.4. QED.

Lemma 4.5 allows us to in-place unfold a fixpoint pattern in any context. That is, we can freely replace  $\mu X . \varphi$  (or  $\nu X . \varphi$ ) with  $\varphi[(\mu X . \varphi)/X]$  (or  $\varphi[(\nu X . \varphi)/X]$ ) in any context.

**Lemma 4.6.** A context  $C$  is positive if it is positive in  $\Box$ ; otherwise, it is negative. Let  $\Gamma \vdash \varphi_1 \rightarrow \varphi_2$ . We have

$$\begin{array}{ll} \Gamma \vdash C[\varphi_1] \rightarrow C[\varphi_2] & \text{if } C \text{ is positive,} \\ \Gamma \vdash C[\varphi_2] \rightarrow C[\varphi_1] & \text{if } C \text{ is negative.} \end{array}$$

*Proof.* Carry out induction on the structure of  $C$ . The cases when  $C$  is a propositional/FOL context are trivial. The case when  $C$  is a symbol application is proved by (FRAMING). The case when  $C$  is a  $\mu$ -binder is proved by Lemma 4.4. QED.

**Lemma 4.7.** Let  $\psi$  be a predicate pattern, i.e.,  $\vdash \psi = \top \vee \psi = \perp$ , and  $C$  be a context where  $\Box$  is not under any  $\mu$ 's. We have  $\vdash \psi \wedge C[\varphi] \leftrightarrow \psi \wedge C[\psi \wedge \varphi]$  for all  $\varphi$ .

*Proof.* Carry out induction on the structure of  $C$ . The cases when  $C$  is a propositional/FOL context are trivial. The case when  $C$  is a symbol application is proved using the fact that predicate patterns propagate through symbols. Since  $\Box$  does not occur under any  $\mu$ 's, we have considered all the cases. QED.

**Lemma 4.8.** Let  $\psi$  be a predicate pattern and  $\varphi$  be a pattern. Let  $X$  be a set variable that does not occur under any  $\mu$ 's in  $\varphi$ , and  $X \notin \text{free Var}(\psi)$ . We have  $\vdash \psi \wedge \mu X . \varphi \leftrightarrow \mu X . (\psi \wedge \varphi)$ .

*Proof.* Note that “ $\leftarrow$ ” is proved by Lemma 4.4. We only need to prove “ $\rightarrow$ ”. By propositional reasoning, the goal becomes  $\vdash \mu X . \varphi \rightarrow \psi \rightarrow \mu X . (\psi \wedge \varphi)$  and we apply (KNASTER TARSKI). We obtain  $\vdash \psi \wedge \varphi[\psi \rightarrow \mu X . (\psi \wedge \varphi)/X] \rightarrow \mu X . (\psi \wedge \varphi)$ . By (PRE-FIXPOINT), we just need to prove  $\vdash \psi \wedge \varphi[\psi \rightarrow \mu X . (\psi \wedge \varphi)/X] \rightarrow \psi \wedge \varphi[\mu X . (\psi \wedge \varphi)/X]$ . By Lemma 4.8, we just need to prove  $\vdash \psi \wedge \varphi[\psi \wedge (\psi \rightarrow \mu X . (\psi \wedge \varphi))/X] \rightarrow \psi \wedge \varphi[\mu X . (\psi \wedge \varphi)/X]$ , which then by Lemma 4.6 becomes  $\vdash \psi \wedge \varphi[\psi \wedge (\mu X . (\psi \wedge \varphi))/X] \rightarrow \psi \wedge \varphi[\mu X . (\psi \wedge \varphi)/X]$ , which then follows by Lemma 4.8. QED.

We present a deduction theorem for  $\mathcal{H}_\mu$ .

**Theorem 4.2.** Let  $\Gamma$  be a theory that includes the definedness symbols and axioms, and  $\varphi, \psi$  be two patterns. If  $\Gamma \cup \{\psi\} \vdash \varphi$  and the proof (1) does not use (UNIVERSAL GENERALIZATION) on free element variables in  $\psi$ ; (2) does not use (KNASTER TARSKI), *unless* (KNASTER TARSKI) set variable  $X$  does not occur under any  $\mu$ -binder in  $\varphi$  and  $X \notin \text{freeVar}(\psi)$ ; (3) does not use (SUBSTITUTION) on free set variables in  $\psi$ , then  $\Gamma \vdash [\psi] \rightarrow \varphi$ .

*Proof.* Carry out induction on the length of the proof  $\Gamma \cup \{\psi\} \vdash \varphi$ . (Base Case) and (Induction Case) for (MODUS PONENS) and (UNIVERSAL GENERALIZATION) are proved as in Theorem 4.2. We only need to prove (Induction Case) for (KNASTER TARSKI) and (SUBSTITUTION).

(Knaster-Tarski). Suppose  $\varphi \equiv \mu X. \varphi_1 \rightarrow \varphi_2$ . We should prove that  $\Gamma \vdash [\psi] \rightarrow (\mu X. \varphi_1 \rightarrow \varphi_2)$ , i.e.,  $\Gamma \vdash [\psi] \wedge \mu X. \varphi_1 \rightarrow \varphi_2$ . Note that  $[\psi]$  is a predicate pattern. By Lemma 4.8, our goal becomes  $\Gamma \vdash \mu X. ([\psi] \wedge \varphi_1) \rightarrow \varphi_2$ . By (KNASTER TARSKI), we need to prove  $\Gamma \vdash ([\psi] \wedge \varphi_1)[\varphi_2/X] \rightarrow \varphi_2$ . Note that  $X \notin \text{freeVar}([\psi])$ , so the above becomes  $\Gamma \vdash [\psi] \wedge \varphi_1[\varphi_2/X] \rightarrow \varphi_2$ , i.e.,  $\Gamma \vdash [\psi] \rightarrow \varphi_1[\varphi_2/X] \rightarrow \varphi_2$ , which is our induction hypothesis.

(SUBSTITUTION). Trivial. Note that  $X \notin \text{freeVar}(\psi)$ .

QED.

### 4.3 REDUCTION TO MONADIC SECOND-ORDER LOGIC

It is shown in [2] that matching logic patterns can be translated into equivalent formulas in pure predicate logic with equality (i.e., FOL that is extended with equality and has no function symbols). The idea is to define for every pattern  $\varphi$  a corresponding formula written  $PL_2(\varphi, r)$  where  $r$  is a fresh variable, with the intuition that  $r$  matches  $\varphi$  iff  $PL_2(\varphi, r)$  holds. In other words, we reduce the powerset semantics of patterns to the classical FOL semantics by defining the membership relation. This way, a pattern  $\varphi$  is valid (i.e., is matched by every element) iff  $PL(\varphi) \equiv \forall r : PL_2(\varphi, r)$  holds.

Matching  $\mu$ -logic extends matching  $\mu$ -logic with set variables and the  $\mu$  operator, and thus goes beyond the expressive power of FOL with equality. However, as we will show later, there is a reduction from matching  $\mu$ -logic to SOL, or more precisely, to monadic SOL (abbreviated as MSOL). The intuition is the same. For any matching  $\mu$ -logic pattern  $\varphi$ , we define a corresponding MSOL formula  $MSOL_2(\varphi, r)$  with a fresh variable  $r$ , such that  $r$  matches  $\varphi$  iff  $MSOL_2(\varphi, r)$  holds. Then,  $\varphi$  is valid iff  $MSOL(\varphi) \equiv \forall r. MSOL_2(\varphi, r)$  holds.

The reduction from matching  $\mu$ -logic to MSOL is given as follows. Given a matching  $\mu$ -logic signature  $(S, \Sigma)$  and the two sets  $EV = \{EV_s\}_{s \in S}$  and  $SV = \{SV_s\}_{s \in S}$  of element and set variables, we define a MSOL signature  $(S^{MSOL}, C^{MSOL}, \Pi^{MSOL})$  by letting  $S^{MSOL} = S$ ,

$C^{MSOL} = \emptyset$ , and  $\Pi^{MSOL} = \{\pi_\sigma : s_1 \times \dots \times s_n \times s \mid \sigma \in \Sigma_{s_1 \dots s_n, s}\}$ . All element variables in  $EV$  are included as MSOL element variables. For every set variable  $X : s \in SV_s$ , we add it as a unary predicate variable over sort  $s$ . We define the translation from  $(S, \Sigma)$ -patterns to  $(S^{MSOL}, C^{MSOL}, \Pi^{MSOL})$ -formulas as follows:<sup>1</sup>

$$\begin{aligned}
 MSOL(\varphi) &= \forall r . MSOL_2(\varphi, r) \\
 MSOL_2(x, r) &= x = r \\
 MSOL_2(\sigma(\varphi_1, \dots, \varphi_n), r) &= \exists r_1 \dots \exists r_n . MSOL_2(\varphi_i, r_i) \wedge \pi_\sigma(r_1, \dots, r_n, r) \\
 MSOL_2(\neg \varphi, r) &= \neg MSOL_2(\varphi, r) \\
 MSOL_2(\varphi_1 \wedge \varphi_2, r) &= MSOL_2(\varphi_1, r) \wedge MSOL_2(\varphi_2, r) \\
 MSOL_2(\exists x . \varphi, r) &= \exists x . MSOL_2(\varphi, r) \\
 MSOL_2(X, r) &= X(r) \\
 MSOL_2(\mu X . \varphi, r) &= \forall X . (\forall r' . MSOL_2(\varphi, r') \rightarrow X(r')) \rightarrow X(r) \\
 MSOL(\Gamma) &= \{MSOL(\psi) \mid \psi \in \Gamma\}
 \end{aligned}$$

As said,  $MSOL_2(\varphi, r)$  captures the intuition that  $r$  matches  $\varphi$ . The top translation  $MSOL(\varphi)$  captures the intuition that  $\varphi$  is valid iff it is matched by all  $r$ . Therefore, we have the following theorem.

**Theorem 4.3.** For any  $\Gamma$  and  $\varphi$ ,  $\Gamma \models \varphi$  iff  $MSOL(\Gamma) \models_{\text{SOL}} MSOL(\varphi)$ .

*Proof.* It suffices to show that there exists a bijection between  $(S, \Sigma)$ -models  $M$  and  $(S^{MSOL}, C^{MSOL}, \Pi^{MSOL})$ -models  $M'$  such that  $M \models \varphi$  iff  $M' \models_{\text{SOL}} MSOL(\varphi)$ . The bijection is defined as follows:

1.  $M'_s = M_s$  for all  $s \in S$ ;
2.  $\pi_{\sigma M'} = \{(a_1, \dots, a_n, b) \mid b \in \sigma_M(a_1, \dots, a_n)\}$ .

To show that  $M \models \varphi$  iff  $M' \models_{\text{SOL}} MSOL(\varphi)$ , it suffices to show  $a \in |\varphi|_{M, \rho}$  iff  $M', \rho[a/r] \models_{\text{SOL}} MSOL_2(\varphi)$ , which follows by structural induction on  $\varphi$ . We only need to prove the cases for  $MSOL_2(X)$  and  $MSOL_2(\mu X . \varphi)$  because the other cases are the same as the translation from matching logic to predicate logic in [2, Section 10].

(Case  $MSOL_2(X, r)$ ). We have  $a \in |X|_{M, \rho}$  iff  $a \in \rho(X)$  iff  $M', \rho[a/r] \models_{\text{SOL}} X(r)$ .

(Case  $MSOL_2(\mu X . \varphi, r)$ ). We have  $a \in |\mu X . \varphi|_{M, \rho}$  iff  $a \in \mathbf{lfp}(A \mapsto |\varphi|_{M, \rho[A/X]})$  iff  $a \in \bigcap \{A \mid |\varphi|_{M, \rho[A/X]} \subseteq A\}$  iff for every  $A$ ,  $|\varphi|_{M, \rho[A/X]} \subseteq A$  implies  $a \in A$ . Note that

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<sup>1</sup>The unsorted version of the translation of  $\mu X . \varphi$  was initially proposed by Adam Fiedler.

for any fixed  $A$ ,  $|\varphi|_{M,\rho[A/X]} \subseteq A$  iff for every  $a$ ,  $a \in |\varphi|_{M,\rho[A/X]}$  implies  $a \in A$ , iff (by the induction hypotheses)  $M', \rho[a/r, A/X] \models_{\text{SOL}} \forall r'. MSOL_2(\varphi, r') \rightarrow X(r')$ . Therefore, we have that “for every  $A$ ,  $|\varphi|_{M,\rho[A/X]} \subseteq A$  implies  $a \in A$ ” is equivalent to “for every  $A$ ,  $M', \rho[a/r, A/X] \models_{\text{SOL}} \forall r'. MSOL_2(\varphi, r') \rightarrow X(r')$ ”. The latter is equivalent to  $M', \rho[a/r] \models_{\text{SOL}} \forall X. (\forall r'. MSOL_2(\varphi, r') \rightarrow X(r')) \rightarrow X(r)$ . QED.

As a closing remark, we point out that translating patterns to MSOL introduces new complexity to not only specifying properties but also reasoning about them. Such complexity comes from the fact that during the translation new quantifiers are introduced, such as in  $MSOL_2(\sigma(\varphi_1, \dots, \varphi), r)$  and  $MSOL_2(\mu X. \varphi, r)$ . Therefore, it is more difficult to reason about the MSOL translations than to directly reason about matching  $\mu$ -logic patterns using  $\mathcal{H}_\mu$ .

## Chapter 5: EXPRESSIVE POWER

In this chapter we study the expressive power of matching  $\mu$ -logic. We will consider various logics, calculi, and foundations of computation and show how to define them in matching  $\mu$ -logic as theories.

### 5.1 DEFINING RECURSIVE SYMBOLS

We know that in matching  $\mu$ -logic, we can use  $\mu X . \varphi$  to specify a recursive set that satisfies the equation  $X = \varphi$ , where the interesting case is when  $X$  has free occurrences in  $\varphi$ . For example,  $\mu X . 3 \vee plus(X, X)$  specifies the set of all nonzero multiples of 3, which is the smallest set that includes 3 and is closed under *plus*. Intuitively,  $\mu X . 3 \vee plus(X, X)$  defines a constant symbol  $\mathbf{m3} \in \Sigma_{\lambda, \mathbf{Nat}}$  by the following recursive definition:

$$\mathbf{m3} \in \Sigma_{\lambda, \mathbf{Nat}} \qquad \mathbf{m3} =_{\mathbf{Ifp}} 3 \vee plus(\mathbf{m3}, \mathbf{m3}).$$

Our goal is to generalize the above and define recursive symbols of any arities. For example, we would like to define a unary symbol  $collatz \in \Sigma_{\mathbf{Nat}, \mathbf{Nat}}$  by the following recursive definition:

$$collatz(n) =_{\mathbf{Ifp}} n \vee (even(n) \wedge collatz(n/2)) \vee (odd(n) \wedge collatz(3n + 1))$$

Intuitively,  $collatz(n)$  captures the set of all numbers in the Collatz sequence starting from  $n$ , where a Collatz sequence is obtained by repeating the following procedure: if the current number is even then the next number is  $n/2$ ; otherwise, the next number is  $3n + 1$ . However, the  $\mu$  operator in matching  $\mu$ -logic can only be applied to set variables, not symbols, so the following attempt is syntactically wrong:

$$collatz(n) = \mu \sigma(n) . n \vee (even(n) \wedge \sigma(n/2)) \vee (odd(n) \wedge \sigma(3n + 1))$$

One possible solution is to extend matching  $\mu$ -logic with recursive symbols and allow  $\mu$  to bind symbol variables, and not just set variables. We need to extend the syntax, semantics, and proof system accordingly, similarly to how LFP extends FOL with predicate variables. The other approach, which will be presented in this section, is to define recursive symbols using axioms. After all, the proof rules (PRE-FIXPOINT) and (KNASTER TARSKI) in Fig. 4.1 are a direct incarnation of the Knaster-Tarski fixpoint theorem (Theorem 2.1). The latter has been repeatedly demonstrated to serve as a solid if not the main foundation for recursion.

Therefore, matching  $\mu$ -logic should be sufficient in practice for defining one's desired approach to recursion/induction/fixpoints as theories, just like how equality, membership, and functions are defined as theories, as shown in Section 2.13.2.

Our definition of recursive symbols is based on the principle of currying-uncurrying [41, 42], which is used in various settings (e.g., simply-typed lambda calculus [43]) as a means to reduce the study of multiary functions to unary functions. The principle of currying-uncurrying gives us a one-to-one correspondence between an  $n$ -ary symbol  $\sigma \in \Sigma_{s_1 \dots s_n, s}$  with a set variable  $\sigma : s_1 \otimes \dots \otimes s_n \otimes s$ . Here  $s_1 \otimes \dots \otimes s_n \otimes s$  is a sort whose carrier set is axiomatically defined to be the product of the carrier sets of  $s_1, \dots, s_n, s$ , i.e.,  $M_{s_1 \otimes \dots \otimes s_n \otimes s} = M_{s_1} \times \dots \times M_{s_n} \times M_s$ . Then, any recursive symbol from  $s_1, \dots, s_n$  to  $s$  can be defined using the  $\mu$  operator and the set variable  $\sigma : s_1 \otimes \dots \otimes s_n \otimes s$ .

**Definition 5.1.** For sets  $M_{s_1}, \dots, M_{s_n}, M_s$ , the *principle of currying-uncurrying* means the following isomorphism:

$$\mathcal{P}(M_{s_1} \times \dots \times M_{s_n} \times M_s) \xrightleftharpoons[\text{uncurry}]{\text{curry}} [M_{s_1} \times \dots \times M_{s_n} \rightarrow \mathcal{P}(M_s)]$$

given by

$$\begin{aligned} \text{curry}(\alpha)(a_1, \dots, a_n) &= \{b \in M_s \mid (a_1, \dots, a_n, b) \in \alpha\} \\ \text{uncurry}(f) &= \{(a_1, \dots, a_n, b) \mid b \in f(a_1, \dots, a_n)\}. \end{aligned}$$

for all  $\alpha \subseteq M_{s_1} \times \dots \times M_{s_n} \times M_s$ ,  $a_i \in M_{s_i}$ ,  $1 \leq i \leq n$ , and  $f : M_{s_1} \times \dots \times M_{s_n} \rightarrow \mathcal{P}(M_s)$ .

To use the principle of currying-uncurrying in matching  $\mu$ -logic, we first need to define product sets as theories.

**Definition 5.2.** For sorts  $s_1, \dots, s_n$ , we define the *product sort*  $s_1 \otimes \dots \otimes s_n$  with a symbol set  $\Sigma^{\text{product}}$  and an axiom set  $\Gamma^{\text{product}}$ , where

$$\Sigma^{\text{product}} = \{\langle \_, \dots, \_ \rangle \in \Sigma_{s_1 \dots s_n, s_1 \otimes \dots \otimes s_n}^{\text{product}}\} \cup \{\text{proj}_i \in \Sigma_{s_1 \otimes \dots \otimes s_n, s_i}^{\text{product}} \mid 1 \leq i \leq n\}$$

and  $\Gamma^{\text{product}}$  includes the following axioms:

(FUNCTION)	$\langle \_, \dots, \_ \rangle : s_1 \times \dots \times s_n \rightarrow s_1 \otimes \dots \otimes s_n$
(FUNCTION)	$\text{proj}_i : s_1 \otimes \dots \otimes s_n \rightarrow s_i \quad \text{with } 1 \leq i \leq n$
(INJECTIVITY)	$\langle x_1, \dots, x_n \rangle = \langle y_1, \dots, y_n \rangle = x_1 = y_1 \wedge \dots \wedge x_n = y_n$
(PROJECTION)	$\text{proj}_i(\langle x_1, \dots, x_n \rangle) = x_i \quad \text{with } 1 \leq i \leq n$

$$(\text{PRODUCT}) \quad \exists x_1 \dots \exists x_n. \langle x_1, \dots, x_n \rangle$$

**Proposition 5.1.** For  $M \models \Gamma^{\text{product}}$ , there is an isomorphism  $M_{s_1 \otimes \dots \otimes s_n} \xrightarrow[j]{i} M_{s_1} \times \dots \times M_{s_n}$ .

*Proof.* For  $a_i \in M_{s_i}$ ,  $1 \leq i \leq n$ , we define  $\langle a_1, \dots, a_n \rangle_M$  by

$$\{\langle a_1, \dots, a_n \rangle_M\} = (\langle \_, \dots, \_ \rangle)_M(a_1, \dots, a_n)$$

where  $(\langle \_, \dots, \_ \rangle)_M: M_{s_1} \times \dots \times M_{s_n} \rightarrow \mathcal{P}(M_{s_1 \otimes \dots \otimes s_n})$  is the interpretation of  $\langle \_, \dots, \_ \rangle$  in  $M$ . This is well-defined because of (FUNCTION), which states that  $\langle \_, \dots, \_ \rangle$  is a function and returns thus only singleton sets. Let  $j: M_{s_1} \times \dots \times M_{s_n} \rightarrow M_{s_1 \otimes \dots \otimes s_n}$  be a function defined by  $j(a_1, \dots, a_n) = \langle a_1, \dots, a_n \rangle_M$  for all  $a_i \in M_{s_i}$ ,  $1 \leq i \leq n$ . Then  $j$  is surjective because of (PRODUCT). Furthermore,  $j$  is injective because of (INJECTIVITY). Therefore,  $j$  is bijective and has an inverse  $i$ , given by  $i(\langle a_1, \dots, a_n \rangle_M) = (a_1, \dots, a_n)$ , for all  $a_i \in M_{s_i}$ ,  $1 \leq i \leq n$ . Thanks to this isomorphism, we feel free to write  $\langle a_1, \dots, a_n \rangle_M$  just as  $(a_1, \dots, a_n)$ . QED.

To define recursive symbols, we often consider the product sort  $s_1 \otimes \dots \otimes s_n \otimes s$ , where  $s_1, \dots, s_n$  are the argument sorts and  $s$  is the return sort. It is often convenient to add a new *application symbol*  $\_(\_, \dots, \_) \in \Sigma_{(s_1 \otimes \dots \otimes s_n \otimes s) s_1 \dots s_n, s}^{\text{product}}$  and include the following axioms in  $\Gamma^{\text{product}}$ :

$$\begin{aligned} (\text{FUNCTION}) \quad & \_(\_, \dots, \_): (s_1 \otimes \dots \otimes s_n \otimes s) \times s_1 \times \dots \times s_n \rightarrow s \\ (\text{APPLICATION}) \quad & p(x_1, \dots, x_n) = \exists y. y \wedge \langle x_1, \dots, x_n, y \rangle \in p \end{aligned}$$

Intuitively, (APPLICATION) states that  $p(x_1, \dots, x_n)$  includes all  $y$ 's such that  $\langle x_1, \dots, x_n, y \rangle$  matches  $p$ . By tacitly using the same syntax  $\_(\_, \dots, \_)$  for the application symbol given above and the application operator in the matching  $\mu$ -logic syntax, we blur their distinction. In particular, if  $\sigma: s_1 \otimes \dots \otimes s_n \otimes s$  is a set variable and  $\varphi_1, \dots, \varphi_n$  have sorts  $s_1, \dots, s_n$ , respectively, then  $\sigma(\varphi_1, \dots, \varphi_n)$  is a well-formed pattern of sort  $s$ .

Now we are ready to define recursive symbols as recursive sets, which are definable using the  $\mu$  operator.

**Definition 5.3.** For a symbol  $\sigma \in \Sigma_{s_1 \dots s_n, s}$ , we write  $\sigma(x_1, \dots, x_n) =_{\text{IfP}} \varphi$  to mean the following axiom:

$$\sigma(x_1, \dots, x_n) = (\mu\sigma: s_1 \otimes \dots \otimes s_n \otimes s. \exists x_1 \dots \exists x_n. \langle x_1, \dots, x_n, \varphi \rangle)(x_1, \dots, x_n)$$

where the symbol  $\sigma \in \Sigma_{s_1 \dots s_n, s}$  in  $\varphi$  is tacitly regarded as the set variable  $\sigma: s_1 \otimes \dots \otimes s_n \otimes s$ ,



which is then bound by  $\mu$ . We call  $\sigma \in \Sigma_{s_1 \dots s_n, s}$  a *recursive symbol* and  $\sigma(x_1, \dots, x_n) =_{\text{Ifp}} \varphi$  its *recursive definition*.

Recursive symbols can be used to define various inductive data structures and relations. For example, in Sections 5.2 and 5.3, we show how to define LFP formulas and SL recursive symbols using matching  $\mu$ -logic recursive symbols. As for formal reasoning, Definition 5.3 is not the most convenient because it involves a lot of detail related to the construction of the product sort. To reason about recursive symbols more easily, we generalize the (PRE-FIXPOINT) and (KNASTER TARSKI) proof rules and prove that they are derivable in matching  $\mu$ -logic, so we can reason about recursive symbols in the same way as the basic least fixpoints  $\mu X. \varphi$ .

**Theorem 5.2.** Let  $\Gamma$  be a theory with a recursive symbol  $\sigma \in \Sigma_{s_1 \dots s_n, s}$  defined by

$$\sigma(x_1, \dots, x_n) =_{\text{Ifp}} \varphi$$

For any  $\psi$  such that

$$\begin{aligned} \Gamma \vdash (\exists z_1 \dots \exists z_n. z_1 \in \varphi_1 \wedge \dots \wedge z_n \in \varphi_n \wedge \psi[z_1/x_1, \dots, z_n/x_n]) \\ \rightarrow \psi[\varphi_1/x_1, \dots, \varphi_n/x_n] \end{aligned} \quad (5.1)$$

for all  $\varphi_1, \dots, \varphi_n$ , the following hold:

- (PRE-FIXPOINT):  $\Gamma \vdash \varphi \rightarrow \sigma(x_1, \dots, x_n)$ ;
- (KNASTER TARSKI):  $\Gamma \vdash \varphi[\psi/\sigma] \rightarrow \psi$  implies  $\Gamma \vdash \sigma(x_1, \dots, x_n) \rightarrow \psi$ , where  $\varphi[\psi/\sigma]$  is the result of substituting all sub-patterns of the form  $\sigma(\varphi_1, \dots, \varphi_n)$  in  $\varphi$  for  $\psi[\varphi_1/x_1, \dots, \varphi_n/x_n]$ .

*Proof.* See Section 5.14.1

QED.

## 5.2 DEFINING FOL WITH LEAST FIXPOINTS

Recall that FOL with least fixpoint (abbreviated LFP) extends FOL with predicate variables in  $PV = \{PV_{s_1 \dots s_n}\}_{s_1, \dots, s_n \in S}$  and the following grammar rules (see Definition 2.8):

$$\begin{array}{ll} \text{LFP formulas} & \varphi ::= (\text{syntax of FOL formulas}) \\ & | P(t_{s_1}, \dots, t_{s_n}) \quad \text{with } P \in PV_{s_1, \dots, s_n} \\ & | [\text{Ifp}_{P, x_1 : s_1, \dots, x_n : s_n} \varphi](t_{s_1}, \dots, t_{s_n}) \quad \text{with } P \in PV_{s_1, \dots, s_n} \end{array}$$

We can define LFP in matching  $\mu$ -logic by extending the theory  $\Gamma^{\text{FOL}}$  for FOL in Definition 2.51 with the definitions and notation for recursive symbols in Section 5.1. Furthermore, we add every predicate variable  $P \in PV_{s_1 \dots s_n}$  as a set variable  $P: s_1 \otimes \dots \otimes s_n \otimes \text{Formula}$  and define the following notation:

$$\begin{aligned} & [\text{Ifp}_{P, x_1 : s_1 \dots, x_n : s_n} \varphi](t_{s_1}, \dots, t_{s_n}) \\ & \equiv (\mu P: s_1 \otimes \dots \otimes s_n \otimes \text{Formula} . \exists x_1 : s_1 \dots \exists x_n : s_n . \langle x_1, \dots, x_n, \varphi \rangle)(t_{s_1}, \dots, t_{s_n}) \end{aligned}$$

Let us use  $\Gamma^{\text{LFP}}$  to denote resulting theory. Note that  $\Gamma^{\text{LFP}}$  only extends  $\Gamma^{\text{FOL}}$  with the generic definitions for recursive symbols and does not include any LFP-specific axioms.

**Theorem 5.3.** For any LFP formula  $\varphi$ ,  $\models_{\text{LFP}} \varphi$  iff  $\Gamma^{\text{LFP}} \models \varphi$ .

*Proof.* See Section 5.14.2.

QED.

### 5.3 DEFINING SEPARATION LOGIC WITH RECURSIVE SYMBOLS

Separation logic (abbreviated SL) recursive symbols are a special instance of matching  $\mu$ -logic recursive symbols. More precisely, SL recursive symbols are matching  $\mu$ -logic recursive symbols whose return sort is **Map**—the sort of maps—defined in Definition 2.52. For example, the following recursive definition of singly-linked lists

$$\text{list}(x) =_{\text{Ifp}} (x = \text{nil}) \wedge \text{emp} \vee \exists y . (x \neq \text{nil}) \wedge x \mapsto y * \text{list}(y)$$

is a verbatim definition of a matching  $\mu$ -logic recursive symbol  $\text{list} \in \Sigma_{\text{Nat}, \text{Map}}^{\text{Map}}$ , without any translation or encoding, thanks to the notation in Section 5.1.

**Theorem 5.4.** Let  $M^{\text{Map}}$  be the standard map model in Definition 2.52. For every SL recursive symbol  $P$  defined by  $P(x_1, \dots, x_n) =_{\text{Ifp}} \psi$ , we add  $P \in \Sigma_{\text{Nat} \dots \text{Nat}, \text{Map}}^{\text{Map}}$  as a matching  $\mu$ -logic recursive symbol defined by the same equation  $P(x_1, \dots, x_n) =_{\text{Ifp}} \psi$ . Let us still use  $\Gamma^{\text{SOL}}$  to denote the extended theory. Then for any SL formula  $\varphi$  with recursive symbols,  $\models_{\text{SOL}} \varphi$  iff  $\Gamma^{\text{SOL}} \models \varphi$ .

*Proof.* See Section 5.14.3.

QED.

### 5.4 DEFINING EQUATIONAL SPECIFICATIONS

Given that we can define equality and functions in matching logic even without the fixpoint extension (see Section 2.13.2), it is not surprising that we can define equational specifications

as theories. Therefore, the point of this section is to formally state and prove an expected result, and more importantly, to prepare for the definitions of term algebras and initial algebras in Section 5.6.

Given an equational specification  $(S, F, E)$ , we extend the theory  $\Gamma^{\text{FOL}}$  for FOL in Definition 2.51 with the following notation:

$$\forall V . t_s = t'_s \quad \equiv \quad \forall x_1 : s_1 \dots \forall x_n : s_n . t_s =_s^{\text{Formula}} t'_s$$

where  $V = \{x_1 : s_1, \dots, x_n : s_n\}$ . Then, all  $(S, F)$ -terms are patterns of the corresponding sorts and all  $(S, F)$ -equations are patterns of sort **Formula**. Let  $\Gamma^{\text{EqSpec}} = \Gamma^{\text{FOL}} \cup E$  be the resulting theory for the equational specification  $(S, F, E)$ .

**Theorem 5.5.** Under the above notation, for any equation  $e$ , the following are equivalent:

- (1)  $\Gamma^{\text{EqSpec}} \vdash e$ ; (2)  $\Gamma^{\text{EqSpec}} \models e$ ; (3)  $E \models_{\text{EQ}} e$ ; (4)  $E \vdash_{\text{EQ}} e$ .

*Proof.* We prove that  $(1) \implies (2) \implies (3) \implies (4) \implies (1)$ . Note that  $(1) \implies (2)$  is the soundness of equational reasoning;  $(3) \implies (4)$  is the completeness of equational deduction; and  $(4) \implies (1)$  holds because matching  $\mu$ -logic supports equational reasoning. Thus, we only need to prove  $(2) \implies (3)$ .

We show that for any  $(S, F, E)$ -algebra  $A$ , there exists a corresponding matching  $\mu$ -logic model  $M$  such that for any equation  $e$ ,  $A \models_{\text{EQ}} e$  iff  $M \models e$ . We define the model  $M$  as follows:

1. the carrier set  $M_s = A_s$  for all  $s \in S$ , and  $M_{\text{Formula}} = \{\star\}$ , where  $\star$  is a distinguished dummy element;
2.  $f_M(a_1, \dots, a_n) = \{f_A(a_1, \dots, a_n)\}$  for all  $a_i \in M_{s_i}$ ,  $1 \leq i \leq n$ ;
3.  $[a]_s^{s'} = M_{s'}$  for all  $a \in M_s$  and  $s, s' \in S$ .

Note that the above is the same model for FOL, which is known to yield the same semantics as FOL for terms [2]. Furthermore, equality has the intended semantics in matching  $\mu$ -logic (Section 2.13.2). Therefore,  $A \models_{\text{EQ}} e$  iff  $E \models e$ , and we proved Theorem 5.5. QED.

## 5.5 DEFINING TERM ALGEBRAS

Term algebras are a typical example of initial algebras when the equation set  $E = \emptyset$ . In matching  $\mu$ -logic, *initiality* is axiomatized by two axioms: (NO CONFUSION) and (NO JUNK).

No-confusion is defined for any  $E$  in Theorem 2.6. When  $E = \emptyset$ , it takes the following simpler form:

**Lemma 5.1.** Let  $(F, \emptyset)$  be an equational specification with no equations. An  $F$ -algebra  $A$  satisfies no-confusion iff (1)  $A_f$  is injective for all  $f \in F$ , and (2) the ranges/codomains of  $A_f$  and  $A_{f'}$  are disjoint for any distinct  $f, f' \in F$ .

We can directly translate Lemma 5.1 into the following axioms:

$$\begin{aligned} \text{(NO CONFUSION I)} \quad & f(x_1, \dots, x_n) = f(x'_1, \dots, x'_n) \rightarrow x_1 = x'_1 \wedge \dots \wedge x_n = x'_n \\ \text{(NO CONFUSION II)} \quad & f(x_1, \dots, x_n) \neq g(y_1, \dots, y_m) \quad \text{if } f \text{ and } g \text{ are distinct} \end{aligned}$$

An algebra  $A$  satisfies no-junk if its carrier sets are the *smallest sets* closed under operations, so we need to use the least fixpoint operator  $\mu$  to axiomatize no-junk. Let us first look at some simple examples.

**Example 5.6.** Consider  $(S, F)$  where  $S = \{\mathbf{Nat}\}$  and  $F = \{\mathbf{zero} \in F_{\epsilon, \mathbf{Nat}}, \mathbf{succ} \in F_{\mathbf{Nat}, \mathbf{Nat}}\}$ . We define the carrier set of  $\mathbf{Nat}$  as follows:

$$\top_{\mathbf{Nat}} = \mu D . \mathbf{zero} \vee \mathbf{succ}(D)$$

Example 5.6 states that  $\top_{\mathbf{Nat}}$  is the smallest set  $D$  that contains  $\mathbf{zero}$  and is closed under  $\mathbf{succ}$ . Since there is only one sort  $\mathbf{Nat}$ , it is called *single recursion* or *direct recursion* in the literatures. In general,  $(S, F)$  may include many sorts and operations, which causes *mutual recursion*. We convert mutual recursion to single recursion:

**Example 5.7.** Let  $S = \{s_1, s_2\}$  and  $F = \{a_1 \in F_{\epsilon, s_1}, a_2 \in F_{\epsilon, s_2}, f \in F_{s_1 s_2, s_2}, g \in F_{s_1 s_2, s_1}\}$ . Conceptually, we would like to use the fixpoint operator  $\mu$  to capture mutual recursion:

$$\langle \top_{s_1}, \top_{s_2} \rangle = \mu \langle D_1, D_2 \rangle . \langle a_1 \vee g(D_1, D_2), a_2 \vee f(D_1, D_2) \rangle$$

However,  $\mu$  can only bind a set variable, and not a structure such as  $\langle D_1, D_2 \rangle$ . To correct it, we replace  $\langle D_1, D_2 \rangle$  by a set variable  $D$  over the pair sort  $s_1 \otimes s_2$  and use the projection operation  $\mathbf{proj}$  to restore  $D_1$  and  $D_2$ . The corrected definition is:

$$\langle \top_{s_1}, \top_{s_2} \rangle = \mu D . \langle a_1 \vee g(D_1, D_2), a_2 \vee f(D_1, D_2) \rangle$$

where  $D_1 \equiv \mathbf{proj}_1(D)$  and  $D_2 \equiv \mathbf{proj}_2(D)$ . Here,  $\langle \top_{s_1}, \top_{s_2} \rangle = \Phi$  is an abbreviation of two axioms:  $\top_{s_1} = \mathbf{proj}_1(\Phi)$  and  $\top_{s_2} = \mathbf{proj}_2(\Phi)$ .

Our last example is about *void sorts*. A sort  $s$  is *void* if it has no ground terms, i.e.,  $T_{F,s} = \emptyset$ . The following example shows why void sorts require special treatment:

**Example 5.8.** Let  $S = \{s_3, s_4\}$  and  $F = \{b \in F_{\epsilon, s_3}\}$ . Then  $s_3$  is a non-void sort and  $s_4$  is a void sort. If we follow Example 5.7, we get the following axiom:

$$\langle \top_{s_3}, \top_{s_4} \rangle = \mu D . \langle b, \perp \rangle \quad // \text{ incorrect}$$

The above is incorrect because  $\langle b, \perp \rangle$  equals to  $\perp$ , due to the semantics of patterns. Since  $\perp$  can propagate through applications, an undefined fragment can make the entire structure undefined. The void-ness of  $s_4$  propagates to  $s_3$  and make the latter also a void sort, which is not intended. To avoid such issue, we only consider non-void sorts. This leads us to the following definition of no-junk.

**Definition 5.4.** Let  $(S, F)$  be a many-sorted signature where all sorts in  $S$  are non-void sorts. Let us define the following no-junk axioms:

$$\text{(NO JUNK)} \quad \langle \top_{s_1}, \dots, \top_{s_n} \rangle = \mu D . \underbrace{\left\langle \bigvee_{f \in F_{w, s_1}, w \in S^*} f D_w, \dots, \bigvee_{f \in F_{w, s_n}, w \in S^*} f D_w \right\rangle}_{\text{denoted by } F(D)}$$

Thus, term algebras can be precisely captured in matching  $\mu$ -logic by the (NO CONFUSION II), (NO CONFUSION I), and (NO JUNK) axioms.

It is known that term algebras have a complete FOL axiomatization, such that for any FOL sentence  $\varphi$ , either  $\varphi$  or  $\neg\varphi$  can be proved [44, 45]. Together with the completeness of FOL deduction, we know that it is *decidable* to determine whether a given FOL sentence holds in a term algebra.

This complete FOL axiomatization of term algebras is a beautiful result but understandably weaker than our result. Firstly, it does not capture precisely term algebras, but only up to *elementary equivalence*. Indeed, it allows arbitrarily large models due to the Löwenheim-Skolem theorem [46]. In addition, it is not extensible. The FOL axiomatization requires all operations to be free constructors, so one cannot, for example, take the complete axiomatization of **zero** and **succ**, extend it with **plus**, **mult**, and their Peano axioms, and hope to get a complete FOL axiomatization of natural numbers with addition and multiplication—the completeness is lost in the extension. In contrast, our matching  $\mu$ -logic theory defines precisely term algebras, and its *extensibility* is what allows us to define equational axioms and to capture initial  $E$ -algebras for an equational specification  $E$ , as discussed in Section 5.6.

## 5.6 DEFINING INITIAL ALGEBRAS

In Section 5.5, we defined terms algebras in matching  $\mu$ -logic. Term algebras are special cases of initial algebras when  $E = \emptyset$ . In this section, we extend the result to defining initial  $E$ -algebras for any equational specification  $E$ .

### 5.6.1 Basic definitions

Recall that  $\simeq_E$  is the *smallest relation* that includes the identity relation and all instances of the equations in  $E$ , and is closed under converse, composition, and congruence w.r.t. all operations in  $F$  (Proposition 2.4). Thus,  $\simeq_E$  can be axiomatized by the least fixpoint operator  $\mu$ . We use a symbol  $\mathbf{Eq} \in \Sigma$  to define  $\simeq_E$ , where  $\mathbf{Eq}$  is axiomatized to represent the set of pairs of  $\simeq_E$ -equivalent terms.

Let us define the common operations and notations for binary relations as follows:

$$\begin{aligned}
\varphi_1 \subsetneq \varphi_2 &\equiv \forall x_1 . x_1 \in \varphi_1 \rightarrow \exists x_2 . x_2 \in \varphi_2 . \langle x_1, x_2 \rangle \in \mathbf{Eq} \\
\varphi_1 \simeq \varphi_2 &\equiv \varphi_1 \subsetneq \varphi_2 \wedge \varphi_2 \subsetneq \varphi_1 \\
R^{-1} &\equiv \text{converseRel } R \\
R_1 \circ R_2 &\equiv \text{composeRel } R_1 \ R_2 \\
(\text{IDENTITY}) \quad \text{idRel} &= \bigvee_{s \in S} \exists x : s . \langle x, x \rangle \\
(\text{CONVERSE}) \quad R^{-1} &= \exists x . \exists y . \langle y, x \rangle \wedge (\langle x, y \rangle \in R) \\
(\text{COMPOSITION}) \quad R_1 \circ R_2 &= \exists x . \exists y . \exists z . \langle x, z \rangle \wedge (\langle x, y \rangle \in R_1 \wedge \langle y, z \rangle \in R_2) \\
(\text{CONGRUENCE}) \quad \text{congRel } R &= \bigvee_{f \in F_{s_1 \dots s_n, s}} \exists x_1, y_1 : s_1 \dots \exists x_n, y_n : s_n . \\
&\quad \langle f(x_1, \dots, x_n), f(y_1, \dots, y_n) \rangle \wedge \bigwedge_{1 \leq i \leq n} \langle x_i, y_i \rangle \in R \\
(\text{EQUIVALENCE}) \quad \mathbf{Eq} &= \mu R . \text{idRel} \vee R^{-1} \vee (R \circ R) \vee (\text{congRel } R) \vee \bigvee_{(\forall V . t=t') \in E} \exists V . \langle t, t' \rangle
\end{aligned}$$

For example,  $\text{idRel}$  is the identity relation;  $R^{-1}$  is the converse relation of  $R$ ;  $R_1 \circ R_2$  is composition of  $R_1$  and  $R_2$ ; and  $\text{congRel } R$  is the relation obtained by propagating  $R$  through all operations in  $F$ . The axiom (EQUIVALENCE) states that  $\mathbf{Eq}$  is the smallest relation that includes  $\text{idRel}$  and all equations in  $E$ , and is closed under converse, composition, and congruence. We write  $\varphi_1 \subsetneq \varphi_2$  to mean that  $\varphi_1$  is contained in  $\varphi_2$  modulo  $\mathbf{Eq}$ , and  $\varphi_1 \simeq \varphi_2$  to mean that  $\varphi_1$  and  $\varphi_2$  are equal modulo  $\mathbf{Eq}$ .

Thus, initial  $E$ -algebras are precisely captured by the above axioms up to isomorphism. Note that we distinguish two different equalities: one is the pure syntactic equality (written  $t = t'$ ) and the other is  $\simeq_E$ -equivalence (written  $t \simeq t'$ ). The latter corresponds to the equality in the quotient term algebra  $T_{F/E}$ .

### 5.6.2 Induction principles

*Initial algebra semantics reasoning* asks what truths about initial algebras can be formally derived, and how. Theorem 2.3 states that valid ground equations in an initial algebra can be proved by equational deduction, but this result does not hold for non-ground equations (??).

Initial algebra reasoning is a synonym for *induction*. Various inductive techniques in formal program verification flourished in the 1960s [47, 48, 49, 50, 51, 52, 53]. Later, it was discovered that initiality, or more precisely, the *no-junk* property is what powers induction and the induction-based proof techniques in initial algebras [23, Proposition 16]. If an algebra  $A$  satisfies no-junk, all elements of  $A$  can be represented by some terms, and thus the (unique) morphism  $f_A: T_{F/E} \rightarrow A$  is surjective. Inductive principles on  $T_{F/E}$  are then “mapped” to  $A$  through  $f_A$ , whose surjectivity guarantees that all elements in  $A$  are covered in the inductive reasoning. Various induction principles have been adopted as proof-theoretical alternatives to initiality (see, e.g., [23, Section 4.4]) and have led to practical tools [35].

Recall that in Section 5.5, we defined the no-junk property using one fixpoint axiom (NO JUNK). The matching  $\mu$ -logic proof system also has one proof rule—(KNASTER-TARSKI)—which is dedicated to fixpoint reasoning

$$\vdash \varphi[\psi/X] \rightarrow \psi \quad \text{implies} \quad \vdash \mu X . \varphi \rightarrow \psi$$

It is shown that by combining (NO JUNK) and (KNASTER-TARSKI), we obtain inductive reasoning. Induction *is* matching  $\mu$ -logic reasoning within the theory of initial algebras:

$$\boxed{\text{Induction}} = \boxed{\text{Axiom (NO-JUNK)}} + \boxed{\text{Rule (KNASTER-TARSKI)}}$$

Let us consider natural numbers built from **zero** and **succ**. We have two Peano axioms:  $E^{\text{Nat}} = \{\forall x: \text{Nat} . \text{plus}(x, \text{zero}) \simeq x \text{ and } \forall x, y: \text{Nat} . \text{plus}(x, \text{succ}(y)) \simeq \text{succ}(\text{plus}(x, y))\}$ . A typical example is the proof of the following property:

$$\forall y: \text{Nat} . \text{plus}(\text{zero}, y) \simeq y \tag{\dagger}$$

Note that  $(\dagger)$  does *not* hold in all algebras that validate the Peano axioms. Consider, for

example, an algebra with only two elements  $\{0, \star\}$ , where **zero** is interpreted as 0, **succ** is interpreted as the identity function on  $\{0, \star\}$ , and **plus** is interpreted as the binary function that returns its first argument. In this algebra, both axioms hold, but not  $(\dagger)$ .

Property  $(\dagger)$  holds if we consider the initial algebra of  $E^{\text{Nat}}$ . However, by default, initial algebra semantics does not distinguish *constructors* and *defined functions*. Instead, it treats them equally as operations. It results in unnecessarily tedious inductive proofs, because induction cases are created for all operators, even for defined functions (see, e.g., [54, Section 2.4]). In other words, when we prove  $(\dagger)$  and apply inductive reasoning on  $y$ , we have *three* cases rather than two, where the extra one is for **plus**. This is not expected. To carry out the usual inductive reasoning with only cases for **zero** and **succ**, we need to *prove* that **plus** is a defined function, i.e., it does not effectively create new (ground) terms (Step 1). After that, we apply structural induction on  $(\dagger)$  w.r.t. **zero** and **succ** only (Step 2), and prove all the sub-goals (Step 3).

In many initial algebra semantics papers [55, 56, 57, 58, 59, 60] and tools [35], Step 1 is proved by noting that the two Peano equations, when oriented from left to right, become *rewrite rules* that reduce the size of the sub-terms whose top-level operation is **plus**. Any ground term that contains **plus** can be rewritten to a canonical term without **plus**. Therefore, **plus** is a defined function. This technique, called *sufficient completeness*, goes back to [61] and is further developed and implemented in the above-mentioned works.

In practice, inductive equational theorem provers allow users to explicitly declare constructors and defined functions, following one (or both) of the following aesthetically different but ultimately equivalent approaches:

1. To declare a sub-signature of constructors (supported by Maude [35] and proof assistants such as Coq).
2. To define a sub-specification of constructors and import it in a “protected” mode (supported by OBJ [62], CafeOBJ [63], and Maude [35]).

Either way, initiality is defined only for constructors. Defined functions must be proved well-defined. Both approaches are *extensions* to the vanilla equational specifications in Section 2.5: (1) adds constructor signatures and (2) adds a module system.

What is *not an extension* is the following axiomatic matching  $\mu$ -logic approach, where the statement “**plus** is a defined function” is expressed and derived *within* matching  $\mu$ -logic:<sup>1</sup>

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<sup>1</sup>A similar result holds for the general case, when  $E$  includes equations for constructors and  $E' \supseteq E$  further includes those for defined functions. The well-definedness of the defined functions can be proved by showing that  $E$ -equality is preserved, i.e.,  $\simeq_E$  equals to  $\simeq_{E'}$ , where  $\simeq_E$  and  $\simeq_{E'}$  are least fixpoint patterns as the right- and left-hand sides of Theorem 5.9.



**Theorem 5.9.**  $\vdash \underbrace{(\mu D . \text{zero} \vee \text{succ}(D) \vee \text{plus}(D, D))}_{\text{equals to } \top_{\text{Nat}} \text{ by axiom (No JUNK)}} \simeq (\mu D . \text{zero} \vee \text{succ}(D))$

That is, the smallest set generated by  $\{\text{zero}, \text{succ}, \text{plus}\}$  equals to the one generated by  $\{\text{zero}, \text{succ}\}$ , modulo  $E^{\text{Nat}}$ . Theorem 5.9, which accomplishes Step 1, is a theorem that is formally derivable using the existing proof system, requiring no reasoning *outside the logic*, which is in sharp contrast to the classical initial algebra semantics approaches and tools.

An advantage of specifying defined functions by theorems (such as Theorem 5.9) is that we can reason about ADTs using different but equivalent constructor sets. For example, lists can be defined using *nil* and *cons*, or using *nil*, one-element lists, and concatenation. Such flexibility is not possible in frameworks that enforce explicit specification of the constructors for each ADT.

After Step 1, we carry out Step 2, which is to apply structural induction on  $y$  in  $(\dagger)$ . It generates two sub-goals:

$$\text{plus}(\text{zero}, \text{zero}) \simeq \text{zero} \quad (\dagger_1)$$

$$\forall z : \text{Nat} . \text{plus}(\text{zero}, z) \simeq z \rightarrow \text{plus}(\text{zero}, \text{succ}(z)) \simeq \text{succ}(z) \quad (\dagger_2)$$

where  $(\dagger_1)$  is the base case and  $(\dagger_2)$  is the induction case. In matching  $\mu$ -logic, the above inductive proof is carried out, *within the logic*, using (KNASTER-TARSKI). Specifically, let

$$\Psi \equiv \exists y : \text{Nat} . y \wedge (\text{plus}(\text{zero}, y) \simeq y) \quad (5.2)$$

be the pattern that is matched by all  $y$  that satisfy  $(\dagger)$ .<sup>2</sup> Then,

$$\begin{array}{ll} \vdash \forall y : \text{Nat} . \text{plus}(\text{zero}, y) \simeq y & \text{iff} \\ \vdash \top_{\text{Nat}} \rightarrow \Psi & \text{iff} \\ \vdash (\mu D . \text{zero} \vee \text{succ}(D)) \rightarrow \Psi & \text{if } // \text{ by (KNASTER-TARSKI)} \\ \vdash \text{zero} \rightarrow \Psi \text{ and } \vdash \text{succ}(\Psi) \rightarrow \Psi & \text{iff} \\ \vdash \text{plus}(\text{zero}, \text{zero}) \simeq \text{zero} \text{ and} & \\ \vdash \forall y : \text{Nat} . \text{plus}(\text{zero}, y) \simeq y \rightarrow \text{plus}(\text{zero}, \text{succ}(y)) \simeq \text{succ}(y) & \end{array} \quad (\ddagger)$$

Finally, we carry out Step 3 and prove  $(\dagger_1)$  and  $(\dagger_2)$  by equational reasoning, which is a special instance of matching logic reasoning. Therefore, we formally derived  $(\dagger)$  using only the matching  $\mu$ -logic proof system.

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<sup>2</sup>This pattern is similar to set comprehension in set theory.

### 5.6.3 Deriving induction principles as theorems

In the above example, the inductive step ( $\dagger$ ) is carried out using the matching  $\mu$ -logic proof system. In other words, Peano induction is derivable using the matching  $\mu$ -logic proof system:

**Theorem 5.10.** Consider the notations in Section 5.6.2. For any pattern  $\Psi$  of sort **Nat**,

$$\frac{\text{zero} \rightarrow \Psi \quad \text{succ}(\Psi) \rightarrow \Psi}{\top_{\text{Nat}} \rightarrow \Psi}$$

Theorem 5.10 is the logical incarnation of Peano induction in matching  $\mu$ -logic, where  $\Psi$  is any property that we want to (inductively) prove for natural numbers. The first premise,  $\vdash \text{zero} \rightarrow \Psi$ , states that **zero** satisfies property  $\Psi$ . The second premise states that the following induction case holds:

**Lemma 5.2.** Under the notations of Theorem 5.10,

$$\vdash \text{succ}(\Psi) \rightarrow \Psi \quad \text{iff} \quad \vdash \forall x : \text{Nat} . (x \in \Psi \rightarrow \text{succ}(x) \in \Psi).$$

where the right-hand side is exactly the induction case of Peano induction.

Intuitively, the right-hand side states that  $\Psi$  is closed under **succ**; that is, if we start with any  $x$  that satisfies  $\Psi$  and apply **succ** to it, **succ**( $x$ ) still satisfies  $\Psi$ . Hence, if we apply **succ** to the *pattern*  $\Psi$ , which, by definition, is matched by all the numbers that satisfy  $\Psi$ , the result **succ**( $\Psi$ ) is still included by  $\Psi$ . And that is exactly the left-hand side.

It is not a coincidence that the proof rule (KNASTER-TARSKI) has such a close connection to induction principles. In our view, induction principles are instances of the Knaster-Tarski fixpoint theorem [21], of which (KNASTER-TARSKI) is a logical encoding. It is particularly interesting to see that such a connection can be so elegantly expressed within matching  $\mu$ -logic as formal proofs, by the following theorem.

**Theorem 5.11.** Consider the notations in Section 5.5. For any pattern  $\Psi$  of sort  $s_1 \otimes \dots \otimes s_n$ , we have

$$\frac{F \Psi \rightarrow \Psi}{\langle \top_{s_1}, \dots, \top_{s_n} \rangle \rightarrow \Psi} \tag{5.3}$$

The above is equivalent to the following more verbose form:

$$\frac{\bigvee_{f \in F_{w,s_1}, w \in S^*} f \Psi_w \rightarrow \Psi_{s_1} \quad \dots \quad \bigvee_{f \in F_{w,s_n}, w \in S^*} f \Psi_w \rightarrow \Psi_{s_n}}{\top_{s_1} \rightarrow \Psi_{s_1} \text{ and } \top_{s_2} \rightarrow \Psi_{s_2} \text{ and } \dots \text{ and } \top_{s_n} \rightarrow \Psi_{s_n}}$$

where  $\Psi_{s_i} \equiv \text{proj}_i(\Psi)$  is the  $i$ -th projection of  $\Psi$  and  $f \Psi_w \equiv (f \Psi_{s'_1} \cdots \Psi_{s'_m})$  for  $m = s'_1 \cdots s'_m$ .

*Proof.* By (KNASTER-TARSKI) and (NO JUNK). QED.

In conclusion, matching  $\mu$ -logic captures inductive reasoning, including structural induction (Theorems 5.10 and 5.11) and constructor analysis (Theorem 5.9), within the logic itself. Matching  $\mu$ -logic captures initial algebra semantics both proof- and model-theoretically.

## 5.7 DEFINING SECOND-ORDER LOGIC

We have seen the syntax and semantics of SOL in Section 2.4. Here we show that SOL can be defined in matching  $\mu$ -logic. The key component is the definition of powersets. More specifically, for every sort  $s$  we can define a new sort  $2^s$ , called the power sort of  $s$ , such that the carrier set of  $2^s$  is isomorphic to the carrier set of  $s$ . Then, we can use first-order quantification over  $2^s$  to mimic (monadic) second-order quantification over  $s$ . Similarly, we can use first-order quantification over  $2^{s_1 \otimes \cdots \otimes s_n}$  to mimic second-order quantification over the  $n$ -relations of  $s_1 \times \cdots \times s_n$ .

We start by defining powersets in Section 5.7.1. Then we define monadic SOL in Section 5.7.2. Finally, we define (full) SOL in Section 5.7.3.

### 5.7.1 Defining powersets

Note that matching  $\mu$ -logic has set variables that range over the subsets of the underlying carrier set(s). If an axiom  $\varphi$  has free set variables, then for any model  $M \models \varphi$ ,  $|\varphi|_{M,\rho} = M$  for all  $\rho$ . In other words, all the free variables in  $\varphi$ , including those free set variables, are, semantically speaking, universally quantified. This way, we can write matching  $\mu$ -logic axioms that have the same expressive power as monadic SOL where all predicate variables are universally quantified at the top. We can use this feature to define powersets.

**Definition 5.5.** Let  $s$  be a sort. We define a new sort  $2^s$  called the *power sort of  $s$*  and a symbol  $\text{extension} \in \Sigma_{2^s,s}$  called the *extension symbol*. We use  $\alpha, \beta, \dots$  to denote element variables of  $2^s$ . We define the following axioms:

$$\begin{aligned} (\text{POWERSET}) \quad & \exists \alpha : 2^s . \text{extension}(\alpha) = X : s \\ (\text{EXTENSIONALITY}) \quad & \forall \alpha : 2^s . \forall \beta : 2^s . \text{extension}(\alpha) = \text{extension}(\beta) \rightarrow \alpha = \beta \end{aligned}$$

Note that  $X : s$  is a free set variable in (POWERSET).

To understand Definition 5.5, let us consider an intended model  $M$  where  $M_{2^s} = \mathcal{P}(M_s)$ . In (ARITY),  $\alpha$  is an element variable of  $2^s$ , so let us assume that it evaluates to some  $A \in M_{2^s}$ . Then,  $\text{extension}(\alpha)$  is matched by all the elements in  $A$ . Here, note the difference between  $\alpha$  and  $\text{extension}(\alpha)$ . The former is an element variable and is matched by one element, which is  $A$ . The latter is a pattern and is matched by all the elements in  $A$ . In other words,  $A$  is regarded as an individual element in the sort/universe  $2^s$  but a “set” in the sort/universe  $s$ . The symbol “**extension**” takes  $A$  as an element and returns  $A$  itself as a set. This has a similar meaning to the term “extension” in logic and philosophy—an extension of a concept consists of the things to which it applies. Here, we see the element  $A \in \mathcal{P}(M_s)$  as an intensional concept and the set  $A \subseteq M_s$  as its extension.

With the above intuition, the axioms in Definition 5.5 are self-explanatory. (POWERSET) states that any subset  $X$  of  $M_s$  has a corresponding element  $\alpha$  in  $M_{2^s}$ , such that the extension of  $\alpha$  is  $X$ . Therefore, (POWERSET) enforces that  $M_{2^s}$  to be at least as large as  $\mathcal{P}(M_s)$ . On the other hand, (EXTENSIONALITY) states that  $\alpha = \beta$  whenever their extensions are equal, so  $M_{2^s}$  is at most as large as  $\mathcal{P}(M_s)$ . Putting the arguments together,  $M_{2^s}$  is exactly  $\mathcal{P}(M_s)$  up to isomorphism.

Let us also define the reverse of **extension**, called *intension*, as follows:

$$\text{intension}(\varphi) \equiv \exists \alpha : 2^s . \alpha \wedge (\text{extension}(\alpha) = \varphi)$$

If  $\varphi$  has sort  $s$ ,  $\text{intension}(\varphi)$  is a singleton pattern of sort  $2^s$  such that  $\text{extension}(\text{intension}(\varphi)) = \varphi$ . The singleton-ness is guaranteed by (EXTENSIONALITY).

We have shown that powersets can be completely, finitely axiomatized in matching logic. This result is known to not hold in FOL, because by the Löwenheim-Skolem theorem [46], if a FOL theory has infinite models, then it has a countable model. However, using powersets, we can enforce uncountable models by first enforcing an infinite model and considering its powerset. As an example, we define natural numbers **Nat** using *zero* and *suc*, and define the standard injectivity axioms  $\text{zero} \neq \text{suc}(x)$  and  $\text{suc}(x) = \text{suc}(y) \rightarrow x = y$  to enforce **Nat** to be infinite, as it must contain *zero*, *suc(zero)*, *suc(suc(zero))*, etc., which are all distinct. If powersets could have been completely axiomatizable in FOL, then we could define the powerset of natural numbers  $2^{\text{Nat}}$  that is uncountable, contradicting the Löwenheim-Skolem theorem.

## 5.7.2 Defining monadic SOL

Recall that monadic SOL, abbreviated as MSOL, is the instance of SOL with only unary/-monadic predicate variables. Let us fix a MSOL signature  $(S, C, \Pi)$  where  $S$  is a set of sorts,  $C$  is an  $S$ -indexed set of constant symbols, and  $\Pi$  is an  $S^*$ -indexed set of predicate symbols. Let  $EV = \{EV_s\}_{s \in S}$  be an  $S$ -indexed set of element variables and  $PV = \{PV_s\}_{s \in S}$  be an  $S$ -indexed set of unary predicate variables.

We now define the corresponding matching  $\mu$ -logic signature  $(S^{\text{MSOL}}, \Sigma^{\text{MSOL}})$  and theory  $\Gamma^{\text{MSOL}}$  for MSOL. Let

$$\begin{aligned} S^{\text{MSOL}} &= S \cup \{2^s \mid s \in S\} \cup \{\text{Formula}\} \\ \Sigma^{\text{MSOL}} &= \Sigma^{\text{powersort}} \cup C \cup \Pi \\ \Gamma^{\text{MSOL}} &= \Gamma^{\text{powersort}} \cup \Gamma^{\text{function}(C)} \cup \Gamma^{\text{predicate}(\Pi)} \end{aligned}$$

That is,  $S^{\text{MSOL}}$  includes all the sorts in  $S$  and their corresponding power sorts, plus a distinguished sort **Formula** for MSOL formulas. The symbol set  $\Sigma^{\text{MSOL}}$  includes the necessary symbols for powersorts and all the constant and predicate symbols of MSOL. The theory  $\Gamma^{\text{MSOL}}$  includes the necessary axioms for powersorts, the function axioms for the constant symbols in  $C$ , and the predicate axioms for the predicate symbols in  $\Pi$ .

Next, we show that MSOL formulas are patterns of sort **Formula**. We include all unary predicate variables in  $PV$  as matching  $\mu$ -logic element variables of the corresponding power sorts. More specifically, for every  $R \in PV_s$ , we add  $R:2^s$  or simply  $R$  as a matching  $\mu$ -logic element variable of sort  $2^s$ . Then, we define the following notations:

$$\begin{aligned} R(t_s) &\equiv t_s \in_s^{\text{Formula}} \text{extension}(R) && \text{with } R \in PV_s \text{ and } t_s \text{ is a term} \\ \exists R. \varphi &\equiv \exists R:2^s. \varphi && \text{with } R \in PV_s \end{aligned}$$

Under the above notations and the notations for FOL (Section 2.13.2), all MSOL formulas are patterns of sort **Formula**.

**Theorem 5.12.** Under the above notations,  $\models_{\text{SOL}} \varphi$  iff  $\Gamma^{\text{MSOL}} \models \varphi$  for any MSOL formula  $\varphi$ .

*Proof.* Note that there is a one-to-one correspondence between the SOL models and the matching  $\mu$ -logic  $\Gamma^{\text{MSOL}}$ -models. For any SOL model  $M = (\{M_s\}_{s \in S}, \{c_M\}_{c \in C}, \{\pi_M\}_{\pi \in \Pi})$ , we define the corresponding matching  $\mu$ -logic model  $M' = (\{M'_s\}_{s \in S^{\text{MSOL}}}, \{\sigma_{M'}\}_{\sigma \in \Sigma^{\text{MSOL}}})$  where

1.  $M'_s = M_s$  for  $s \in S$ ;
2.  $M'_{2^s} = \mathcal{P}(M_s)$ ;

3.  $M'_{\text{Formula}} = \{\star\}$ ;
4.  $c_{M'} = \{c_M\}$  for  $c \in C$ ;
5.  $\pi_{M'}(a_{s_1}, \dots, a_{s_n}) = \{\star\}$  iff  $\pi_M(a_{s_1}, \dots, a_{s_n})$  holds, for  $\pi \in \Pi_{s_1 \dots s_n}$  and  $a_{s_i} \in M_{s_i}$  for  $1 \leq i \leq n$ .
6.  $\text{extension}_{M'}(A_s) = A_s$  for  $A_s \subseteq M_s$ .

Note that (2) and (6) are enforced by the axioms for power sorts. By structural induction, we can show that  $M, \rho \models_{\text{SOL}} \varphi$  iff  $|\varphi|_{M', \rho} = \{\star\}$ , for any MSOL formula  $\varphi$ . Note that the FOL cases have been considered in Section 2.13.2, so here we only need to consider two cases:  $\varphi$  has the form  $R(t_s)$  or  $\exists R. \varphi_1$ . If  $\varphi$  has the form  $R(t_s)$ ,  $M, \rho \models_{\text{SOL}} R(t_s)$  iff  $\rho(R)(\bar{\rho}(t_s))$  holds, iff  $|t_s|_{M', \rho} \in \rho(R)$ , iff  $|t_s| \in \text{extension}(R)|_{M', \rho} = \{\star\}$  by the semantics of  $\in$ , iff  $|R(t_s)|_{M', \rho} = \{\star\}$ . If  $\varphi$  has the form  $\exists R. \varphi_1$ ,  $M, \rho \models_{\text{SOL}} \exists R. \varphi_1$  iff there exists  $\alpha_R \subseteq M_s$  such that  $M, \rho[\alpha_R/R] \models_{\text{SOL}} \varphi_1$ , iff there exists  $\alpha_R \in M'_{2^s}$  such that  $|\varphi_1|_{M', \rho[\alpha_R/R]} = \{\star\}$  by the induction hypothesis, iff  $|\exists R. \varphi_1|_{M', \rho} = \{\star\}$ . Since  $M$  and  $\rho$  are arbitrarily chosen,  $\models_{\text{SOL}} \varphi$  iff  $\Gamma^{\text{MSOL}} \models \varphi$  for any MSOL formula  $\varphi$ . QED.

### 5.7.3 Defining full SOL

We extend Theorem 5.12 to full SOL, by allowing predicate variables of any arities. The idea is similar. For any predicate variable  $R$  of sort  $s_1 \times \dots \times s_n$ , we add it as an element variable of sort  $2^{s_1 \otimes \dots \otimes s_n}$ . Then, second-order quantification over  $R$  of sort  $s_1 \times \dots \times s_n$  becomes first-order quantification over (element variable)  $R$  of the power sort  $2^{s_1 \otimes \dots \otimes s_n}$ . This is because for any matching  $\mu$ -logic model  $M$ ,  $M_{2^{s_1 \otimes \dots \otimes s_n}}$  is isomorphic to  $\mathcal{P}(M_{s_1} \times \dots \times M_{s_n})$ , which is exactly the set of relations over  $M_{s_1}, \dots, M_{s_n}$ .

Let us fix a SOL signature  $(S, C, \Pi)$ . Let  $EV = \{EV_s\}_{s \in S}$  be an  $S$ -indexed set of element variables and  $PV = \{PV_s\}_{s \in S}$  be an  $S^+$ -indexed set of predicate variables. The corresponding matching  $\mu$ -logic signature  $(S^{\text{SOL}}, \Sigma^{\text{SOL}})$  and theory  $\Gamma^{\text{SOL}}$  for SOL are defined as follows:

$$\begin{aligned}
 S^{\text{MSOL}} &= S \cup \{2^{s_1 \otimes \dots \otimes s_n} \mid s_1, \dots, s_n \in S, n \geq 1\} \cup \{\text{Formula}\} \\
 \Sigma^{\text{SOL}} &= \Sigma^{\text{powersort}} \cup C \cup \Pi \\
 \Gamma^{\text{SOL}} &= \Gamma^{\text{powersort}} \cup \Gamma^{\text{function}(C)} \cup \Gamma^{\text{predicate}(\Pi)}
 \end{aligned}$$

Furthermore, for every  $R \in PV_{s_1 \dots s_n}$ , we add it as an matching  $\mu$ -logic element variable

$R:2^{s_1 \otimes \dots \otimes s_n}$  or simply  $R$ , and define the following notations:

$$\begin{aligned} R(t_{s_1}, \dots, t_{s_n}) &\equiv \langle t_{s_1}, \dots, t_{s_n} \rangle \in_{s_1 \otimes \dots \otimes s_n}^{\text{Formula}} \text{extension}(R) \\ \exists R. \varphi &\equiv \exists R:2^{s_1 \otimes \dots \otimes s_n}. \varphi \end{aligned}$$

Then all SOL formulas are patterns of sort **Formula**.

**Theorem 5.13.** Under the above notations,  $\models_{\text{SOL}} \varphi$  iff  $\Gamma^{\text{SOL}} \models \varphi$  for any SOL formula  $\varphi$ .

*Proof.* The proof is similar to Theorem 5.12. We show that  $M, \rho \models_{\text{SOL}} \varphi$  iff  $|\varphi|_{M', \rho} = \{\star\}$ , where  $M'$  is the corresponding matching  $\mu$ -logic model of a given SOL model  $M$ . The proof is also by structural induction on  $\varphi$ , and we only need need to consider one more case, which is when  $\varphi$  has the form  $R(t_{s_1}, \dots, t_{s_n})$  for  $R \in PV_{s_1 \dots s_n}$ . In this case,  $M, \rho \models_{\text{SOL}} R(t_{s_1}, \dots, t_{s_n})$  iff  $\rho(R)(\bar{\rho}(t_{s_1}), \dots, \bar{\rho}(t_{s_n}))$  holds, iff  $|\langle t_{s_1}, \dots, t_{s_n} \rangle|_{M', \rho} \in |R|_{M', \rho}$ , iff  $|R(t_{s_1}, \dots, t_{s_n})|_{M', \rho} = \{\star\}$ . All the other cases are the same as Theorem 5.12. QED.

## 5.8 DEFINING MODAL $\mu$ -CALCULUS

We have seen the syntax and semantics of modal  $\mu$ -calculus in Section 2.8. Recall that modal  $\mu$ -calculus formulas are interpreted over transition systems. Therefore, we first show how to specify transition systems in matching  $\mu$ -logic. Recall that a transition system  $T = (S, \xrightarrow{T})$  consists of a set  $S$  of states and a binary relation  $\xrightarrow{T} \subseteq S \times S$ , called the transition relation.

Let us define a signature for transition systems  $\Sigma^{\text{TS}} = (\{\text{State}\}, \{\bullet \in \Sigma_{\text{State}, \text{State}}^{\text{TS}}\})$  where **State** is a sort of states and “ $\bullet$ ” is called *one-path next*. Intuitively,  $\bullet s$  is matched by all states  $s'$  such that  $s' \xrightarrow{T} s$ . In other words,  $\bullet s$  is matched by all the  $R$ -predecessors of  $s$  because for them,  $s$  holds next, on one path. Another way to look at this is to consider a series of state transitions and the corresponding patterns:

$$\begin{array}{ccccccc} \dots & s & \xrightarrow{T} & s' & \xrightarrow{T} & s'' & \dots & // \text{ states} \\ & \bullet \bullet \varphi & & \bullet \varphi & & \varphi & & // \text{ patterns} \end{array}$$

Suppose  $\varphi$  holds in  $s''$ , then  $\bullet \varphi$  (i.e., “next  $\varphi$ ”) holds in  $s'$ , and  $\bullet \bullet \varphi$  (i.e., “next next  $\varphi$ ”) holds in  $s$ . Therefore,  $\bullet \varphi$  is matched by states that *have at least one next state* that satisfies  $\varphi$ , conforming to the intuition. We define the dual of  $\bullet$  as  $\circ \varphi \equiv \neg \bullet \neg \varphi$ , called *all-path next*. The difference is that  $\circ \varphi$  is matched by  $s$  if *for all* states  $t$  such that  $s \xrightarrow{T} t$ , we have  $t$  matches  $\varphi$ . In particular, if  $s$  has no successor, then  $s$  matches  $\circ \varphi$  for any  $\varphi$ .

An important observation here is that the  $\Sigma^{\text{TS}}$ -models are *exactly* transition systems, where  $\bullet \in \Sigma_{\text{State}, \text{State}}^{\text{TS}}$  captures the transition relation  $\xrightarrow{T}$ . Specifically, for any transition

system  $T = (S, \xrightarrow{T})$ , we can regard  $T$  as a matching  $\mu$ -logic  $\Sigma^{\text{TS}}$ -model where  $T_{\text{State}} = S$  and  $\bullet_T(t) = \{s \in S \mid s \xrightarrow{T} t\}$ . That is to say, by building patterns using  $\bullet$ , we can specify and reason about many interesting properties about transition systems. To begin with, let us define the following notations:

$$\begin{aligned} \text{“eventually” } \diamond \varphi &\equiv \mu X . \varphi \vee \bullet X \\ \text{“always” } \Box \varphi &\equiv \nu X . \varphi \wedge \circ X \\ \text{“(strong) until” } \varphi_1 U \varphi_2 &\equiv \mu X . \varphi_2 \vee (\varphi_1 \wedge \bullet X) \\ \text{“well-founded” } \text{WF} &\equiv \mu X . \circ X \quad // \text{ no infinite paths} \end{aligned}$$

Proposition 5.14 shows that the above notations have the intended semantics.

**Proposition 5.14.** *Let  $T = (S, \xrightarrow{T})$  be a transition system regarded as a  $\Sigma^{\text{TS}}$ -model, and let  $\rho$  be any valuation and  $s \in S$ . Then:*

- $s \in |\bullet \varphi|_{T,\rho}$  if there exists  $t \in S$  such that  $s \xrightarrow{T} t$ ,  $t \in |\varphi|_{T,\rho}$ ; in particular,  $s \in |\bullet \top|_{T,\rho}$  if  $s$  has an  $R$ -successor;
- $s \in |\circ \varphi|_{T,\rho}$  if for all  $t \in S$  such that  $s \xrightarrow{T} t$ ,  $t \in |\varphi|_{T,\rho}$ ; in particular,  $s \in |\circ \perp|_{T,\rho}$  if  $s$  has no  $R$ -successor;
- $s \in |\diamond \varphi|_{T,\rho}$  if there exists  $t \in S$  such that  $s \xrightarrow{T}^* t$ ,  $t \in |\varphi|_{T,\rho}$ ;
- $s \in |\Box \varphi|_{T,\rho}$  if for all  $t \in S$  such that  $s \xrightarrow{T}^* t$ ,  $t \in |\varphi|_{T,\rho}$ ;
- $s \in |\varphi_1 U \varphi_2|_{T,\rho}$  if there exists  $n \geq 0$  and  $t_1, \dots, t_n \in S$  such that  $s \xrightarrow{T} t_1 \xrightarrow{T} \dots \xrightarrow{T} t_n$ ,  $t_n \in |\varphi_2|_{T,\rho}$ , and  $s, t_1, \dots, t_{n-1} \in |\varphi_1|_{T,\rho}$ ;
- $s \in |\text{WF}|_{T,\rho}$  if  $s$  is  $R$ -well-founded, meaning that there is no infinite sequence  $t_1, t_2, \dots \in S$  with  $s \xrightarrow{T} t_1 \xrightarrow{T} t_2 \xrightarrow{T} \dots$ ;

where  $R^* = \bigcup_{i \geq 0} R^i$  is the reflexive transitive closure of  $R$ .

Now, we are ready to define modal  $\mu$ -calculus in matching  $\mu$ -logic. Let us regard all the propositional variables of modal  $\mu$ -calculus as matching  $\mu$ -logic set variables. Then all modal  $\mu$ -calculus formulas  $\varphi$  are matching  $\mu$ -logic  $\Sigma^{\text{TS}}$ -patterns of sort **State**. Let  $\Gamma^\mu$  be the empty  $\Sigma^{\text{TS}}$ -theory. Theorem 5.15 shows that  $\Gamma^\mu$  correctly captures the semantics and formal proofs of modal  $\mu$ -calculus. The proof of Theorem 5.15 is interesting because the same idea can be used to prove similar results for other logics.



**Theorem 5.15.** The following properties are equivalent for all modal  $\mu$ -calculus formulas  $\varphi$ : (1)  $\models_{\mu} \varphi$ ; (2)  $\vdash_{\mu} \varphi$ ; (3)  $\Gamma^{\mu} \vdash \varphi$ ; (4)  $\Gamma^{\mu} \models \varphi$ ; (5)  $M \models \varphi$  for all  $\Sigma^{\text{TS}}$ -models  $M$  such that  $M \models \Gamma^{\mu}$ ; (6)  $T \models_{\mu} \varphi$  for all transition systems  $T$ .

*Proof sketch.* See Section 5.14.4 for full details.

We only need to prove “(2)  $\Rightarrow$  (3)” and “(5)  $\Rightarrow$  (6)”, as the rest are already proved/known. “(1)  $\Rightarrow$  (2)” follows by the completeness of modal  $\mu$ -logic, which is nontrivial but known [64]. “(2)  $\Rightarrow$  (3)” follows by proving all modal  $\mu$ -calculus proof rules as theorems in matching  $\mu$ -logic (Proposition 3.6). “(3)  $\Rightarrow$  (4)” follows by the soundness of matching  $\mu$ -logic. “(4)  $\Rightarrow$  (5)” follows by definition. “(5)  $\Rightarrow$  (6)” follows by proving “ $\not\models_{\mu} \varphi$  implies  $\Gamma^{\mu} \not\models \varphi$ ”. We take a transition system  $T = (S, \xrightarrow{T})$  and a valuation  $\rho$  such that  $|\varphi|_{T,\rho}^{L\mu} \neq S$ . Then we show that  $T \models \Gamma^{\mu}$  and  $|\varphi|_{T,V} \neq S$ . This means that  $\Gamma^{\mu} \not\models \varphi$ . Finally, “(6)  $\Rightarrow$  (1)” follows by definition. QED.

Therefore, modal  $\mu$ -calculus can be regarded as an empty matching  $\mu$ -logic theory without quantifiers, over a signature containing only one sort and only one unary symbol. It is worth mentioning that variants of modal  $\mu$ -calculus with more modal modalities have been proposed (see [65] for a survey). At our knowledge, however, all such variants consider only unary modal modalities and they are only required to obey the usual (K) and (N) proof rules of modal logic. In contrast, matching  $\mu$ -logic allows polyadic and even many-sorted symbols while still obeying the desired (K) and (N) rules (see Proposition 3.6), allows arbitrary further constraining axioms in theories, and also quantification over element variables and many-sorted universes. It thus suggests that matching  $\mu$ -logic may offer a unifying playground to specify and reason about transition systems, by means of  $\Sigma^{\text{TS}}$ -theories/models. We can define various temporal/dynamic operations and modalities as *notations* from the basic “one-path next” symbol “ $\bullet$ ” and the  $\mu$ -binder, without a need to extend the logic. We can restrict the underlying transition systems using axioms, without a need to modify or extend the proof system. In what follows, we show that by adding proper axioms and introducing good notations, we can capture a variety of logics for specifying and reasoning about dynamic behaviors of programs and computing systems. In particular, we will consider linear temporal logic (LTL, both the infinite- and finite-trace variants), computation tree logic (CTL), dynamic logic (DL), and reachability logic (RL).

## 5.9 DEFINING TEMPORAL LOGICS

Since matching  $\mu$ -logic can define modal  $\mu$ -calculus, it is not surprising that it can also define various temporal logics such as LTL and CTL as theories whose axioms constrain the

underlying transition relations. What is interesting, in our view, is that the resulting theories are simple, intuitive, and faithfully capture both the semantics and formal proofs of these temporal logics.

### 5.9.1 Defining infinite-trace LTL

We have seen the syntax and semantics of infinite-trace LTL in Section 2.9.1. Note that the infinite-trace LTL syntax, namely the following

$$\varphi ::= p \in AP \mid \varphi \wedge \varphi \mid \neg\varphi \mid \circ\varphi \mid \varphi U \varphi$$

has already been subsumed by matching  $\mu$ -logic. As for models, infinite-trace LTL requires infinite traces, so the underlying transition relations are *linear* (i.e.,  $s \xrightarrow{T} s'$  and  $s \xrightarrow{T} s''$  implies  $s' = s''$ ) and *infinite* (i.e. “non-stopping”, for every  $s$  there is  $s'$  such that  $s \xrightarrow{T} s'$ ). To capture these two characteristics, we add two axioms:

$$(\text{INF}) \quad \bullet \top \qquad (\text{LIN}) \quad \bullet X \rightarrow \circ X$$

and denote the resulting  $\Sigma^{\text{TS}}$ -theory as  $\Gamma^{\text{infLTL}}$ . Note that by (SUBSTITUTION) in Figure 4.1 we can prove from axiom (LIN) that  $\bullet\varphi \rightarrow \circ\varphi$  for all patterns  $\varphi$ . Intuitively, (INF) forces all states  $s$  to have at least one successor, and thus all traces can be extended to an infinite trace, and (LIN) forces all states  $s$  to have only a linear future.

Theorem 5.16 shows that  $\Gamma^{\text{infLTL}}$  captures the semantics and formal proofs of infinite-trace LTL.

**Theorem 5.16.** The following properties are equivalent for all infinite-trace LTL formulas  $\varphi$ : (1)  $\vdash_{\text{infLTL}} \varphi$ ; (2)  $\models_{\text{infLTL}} \varphi$ ; (3)  $\Gamma^{\text{infLTL}} \vdash \varphi$ ; (4)  $\Gamma^{\text{infLTL}} \models \varphi$ .

*Proof.* See Section 5.14.6.

QED.

Therefore, infinite-trace LTL can be regarded as a theory containing two axioms, (INF) and (LIN), over the same signature  $\Sigma^{\text{TS}}$  for transition systems.

### 5.9.2 Defining finite-trace LTL

We have seen the syntax and semantics of finite-trace LTL in Section 2.9.2. Note that the finite-trace LTL syntax, namely the following

$$\varphi ::= p \in AP \mid \varphi \wedge \varphi \mid \neg\varphi \mid \circ\varphi \mid \varphi W \varphi$$

can be defined in matching  $\mu$ -logic by introducing the following notation for  $W$ :

$$\text{“weak until”} \quad \varphi_1 W \varphi_2 \equiv \mu X . \varphi_2 \vee (\varphi_1 \wedge \circ X).$$

As for models, finite-trace LTL requires finite traces, so the underlying transition relations are linear and finite (i.e., there is no infinite trace). To capture both characteristics we add two axioms:

$$(\text{FIN}) \quad \text{WF} \equiv \mu X . \circ X \quad (\text{LIN}) \quad \bullet X \rightarrow \circ X$$

and call the resulting  $\Sigma^{\text{TS}}$ -theory  $\Gamma^{\text{finLTL}}$ . Intuitively, (FIN) forces all states to be well-founded, meaning that there is no infinite execution trace in the underlying transition systems.

**Theorem 5.17.** The following properties are equivalent for all finite-trace LTL formula  $\varphi$ :

- (1)  $\vdash_{\text{finLTL}} \varphi$ ; (2)  $\models_{\text{finLTL}} \varphi$ ; (3)  $\Gamma^{\text{finLTL}} \vdash \varphi$ ; (4)  $\Gamma^{\text{finLTL}} \models \varphi$ .

*Proof.* See Section 5.14.7.

QED.

Therefore, finite-trace LTL can be regarded as a theory containing two axioms, (FIN) and (LIN), over the same signature  $\Sigma^{\text{TS}}$  for transition systems.

### 5.9.3 Defining CTL

We have seen the syntax and semantics of CTL in Section 2.9.3. Note that the CTL syntax, namely the following

$$\varphi ::= p \in AP \mid \varphi \wedge \varphi \mid \neg \varphi \mid \text{AX} \varphi \mid \text{EX} \varphi \mid \varphi \text{ AU } \varphi \mid \varphi \text{ EU } \varphi$$

can be subsumed in matching  $\mu$ -logic by introducing the following notations:

$$\begin{aligned} \text{AX} \varphi &\equiv \circ \varphi & \varphi_1 \text{ AU } \varphi_2 &\equiv \mu X . \varphi_2 \vee (\varphi_1 \wedge \circ X) \\ \text{EX} \varphi &\equiv \bullet \varphi & \varphi_1 \text{ EU } \varphi_2 &\equiv \mu X . \varphi_2 \vee (\varphi_1 \wedge \bullet X) \end{aligned}$$

As for models, CTL requires infinite computation trees, so let us add the axiom (INF) and call the resulting  $\Sigma^{\text{TS}}$ -theory  $\Gamma^{\text{CTL}}$ .

**Theorem 5.18.** For all CTL formulas  $\varphi$ , the following are equivalent: (1)  $\vdash_{\text{CTL}} \varphi$ ; (2)  $\models_{\text{CTL}} \varphi$ ; (3)  $\Gamma^{\text{CTL}} \vdash \varphi$ ; (4)  $\Gamma^{\text{CTL}} \models \varphi$ .

Therefore, CTL can be regarded as a theory with one axiom, over the same signature  $\Sigma^{\text{TS}}$  for transition systems.

It may be insightful to look at all three temporal logics discussed in this section (namely infinite-trace LTL, finite-trace LTL, and CTL) through the lenses of matching  $\mu$ -logic, as theories over a unary symbol signature. Modal  $\mu$ -calculus is the empty theory and thus the least constrained one. CTL comes immediately next with only one axiom, (INF), to enforce infinite computation traces; Infinite-trace LTL further constrains with the linearity axiom (LIN). Finally, finite-trace LTL replaces (INF) with (FIN). We believe that matching  $\mu$ -logic can serve as a convenient and uniform framework to define and study temporal logics. For example, finite-trace CTL can be trivially obtained as the theory containing only the axiom (FIN). LTL with both finite and infinite traces is the theory containing only the axiom (LIN). And CTL with unrestricted (finite or infinite branch) models is the empty theory (i.e., modal  $\mu$ -calculus).

## 5.10 DEFINING DYNAMIC LOGIC

We have seen the syntax of dynamic logic (DL) in Section 2.10. It is known that DL can be embedded in the variant of modal  $\mu$ -calculus with multiple modalities (see, e.g., [65]). The idea is to define a modality  $[a]$  for every atomic program  $a \in APgm$ , and then to define the four program constructs as least/greatest fixpoints. We can easily adopt the same approach and associate an empty matching  $\mu$ -logic theory to DL, over a signature containing as many unary symbols as atomic programs. However, matching  $\mu$ -logic allows us to propose a better embedding, unrestricted by the limitations of modal  $\mu$ -calculus. Indeed, the embedding in [65] suffers from at least two limitations that we can avoid with matching  $\mu$ -logic. First, sometimes transitions are not just labeled with discrete programs, such as in *hybrid systems* [66] and *cyber-physical systems* [67] where the labels are *continuous values* such as elapsing time. We cannot introduce for every time  $t \in \mathbb{R}_{\geq 0}$  a modality  $[t]$ , as only countably many modalities are allowed. Instead, we may want to axiomatize the domains of such possibly continuous values and treat them as any other data. Second, we may want to quantify over such values, be they discrete or continuous, and we would not be able to do so (even in matching  $\mu$ -logic) if they are encoded as modalities/symbols.

Let us instead define a signature of *labeled transition systems*

$$\Sigma^{\text{LTS}} = (\{\text{State}, \text{Pgm}\}, \Sigma_{\lambda, \text{Pgm}}^{\text{LTS}} \cup \{\bullet \in \Sigma_{\text{Pgm State, State}}^{\text{LTS}}\})$$

where the “one-path next  $\bullet$ ” is a *binary symbol* taking an additional  $\text{Pgm}$  argument, and

for all atomic programs  $a \in APgm$  we add a constant symbol  $a \in \Sigma_{\lambda, Pgm}^{LTS}$ . Just like how all  $\Sigma^{TS}$ -models are transition systems (Section 5.8), we have that all  $\Sigma^{LTS}$ -models are labeled transition systems. We define compound programs in DL as the following notations:

$$\begin{array}{ll}
 \langle \alpha \rangle \varphi \equiv \bullet(\alpha, \varphi) & [\alpha] \varphi \equiv \neg \langle \alpha \rangle \neg \varphi \\
 (SEQ) \quad [\alpha ; \beta] \varphi \equiv [\alpha][\beta] \varphi & (CHOICE) \quad [\alpha \cup \beta] \varphi \equiv [\alpha] \varphi \wedge [\beta] \varphi \\
 (TEST) \quad [\psi?] \varphi \equiv (\psi \rightarrow \varphi) & (ITER) \quad [\alpha^*] \varphi \equiv \nu X. (\varphi \wedge [\alpha] X)
 \end{array}$$

Let  $\Gamma^{DL}$  denote the empty  $\Sigma^{LTS}$ -theory.

**Theorem 5.19.** For all DL formulas  $\varphi$ , the following are equivalent: (1)  $\vdash_{DL} \varphi$ ; (2)  $\models_{DL} \varphi$ ; (3)  $\Gamma^{DL} \vdash \varphi$ ; (4)  $\Gamma^{DL} \models \varphi$ .

*Proof.* See Section 5.14.9.

QED.

We point out that the iterative operator  $[\alpha^*] \varphi$  is axiomatized with *two* axioms in the proof system of DL (see Figure 2.8):

$$\begin{array}{ll}
 (DL_6) & \varphi \wedge [\alpha][\alpha^*] \varphi \leftrightarrow [\alpha^*] \varphi \\
 (DL_7) & \varphi \wedge [\alpha^*](\varphi \rightarrow [\alpha] \varphi) \rightarrow [\alpha^*] \varphi
 \end{array}$$

while we just regard it as syntactic sugar, via (ITER). One may argue that (ITER) desugars to the  $\nu$ -binder, though, which obeys the proof rules (PRE-FIXPOINT) and (KNASTER TARSKI) that essentially have the same effect as (DL<sub>6</sub>) and (DL<sub>7</sub>). We agree. And that is exactly why we think that we should have *one uniform and fixed logic*, such as matching  $\mu$ -logic, where general fixpoint axioms are given to specify and reason about *any fixpoint properties* of *any domains* and to develop general-purpose automatic tools and provers. When it comes to specific domains and special-purpose logics, we can define them as theories/notations in MmL, as what we have done in this section for modal  $\mu$ -calculus and all its fragment logics. Often, these special-purpose logics are simpler than matching  $\mu$ -logic and more computationally efficient. In particular, modal  $\mu$ -calculus and all its fragment logics shown in this section are not only *complete* but also *decidable* [68], while matching  $\mu$ -logic does not have any complete proof system and thus its validity is not semi-decidable. Therefore, the existing decision procedures and completeness results of these special-purpose logics give decision procedures and completeness results (such as Theorem 5.15) for the corresponding matching  $\mu$ -logic theories.

## 5.11 DEFINING REACHABILITY LOGIC

We have seen the syntax and semantics of reachability logic (RL) in Section 2.14. To define the syntax of RL, we define the extended signature  $\Sigma^{\text{RL}} = \Sigma^{\text{Cfg}} \cup \{\bullet \in \Sigma_{\text{Cfg}, \text{Cfg}}\}$  where “ $\bullet$ ” is a unary symbol called *one-path next*. To define reachability rules  $\varphi_1 \Rightarrow \varphi_2$ , we define:

“weak eventually”  $\diamond_w \varphi \equiv \nu X. \varphi \vee \bullet X$  // equal to  $\neg \text{WF} \vee \diamond \varphi$

“reaching star”  $\varphi_1 \Rightarrow^* \varphi_2 \equiv \varphi_1 \rightarrow \diamond_w \varphi_2$

“reaching plus”  $\varphi_1 \Rightarrow^+ \varphi_2 \equiv \varphi_1 \rightarrow \bullet \diamond_w \varphi_2$

Notice that the “weak eventually”  $\diamond_w \varphi$  is defined similarly to the “eventually”  $\diamond \varphi \equiv \mu X. \varphi \vee \bullet X$ , but instead of using least fixpoint  $\mu$ -binder, we define it as a greatest fixpoint. One can prove that  $\diamond_w \varphi = \neg \text{WF} \vee \diamond \varphi$ , that is, a configuration  $\gamma$  satisfies  $\diamond_w \varphi$  if either it satisfies  $\diamond \varphi$ , or it is not well-founded, meaning that there exists an infinite execution path from  $\gamma$ . Also notice that “reaching plus”  $\varphi_1 \Rightarrow^+ \varphi_2$  is a stronger version of “reaching star”, requiring that  $\diamond_w \varphi_2$  should hold *after at least one step*. This *progressive condition* is crucial to the soundness of RL reasoning: as shown in (TRANSITIVITY) in Figure 2.12, circularities are flushed into the axiom set only *after one reachability step is established*. This leads us to the following translation from RL sequents to matching  $\mu$ -logic patterns.

**Definition 5.6.** Given a rule  $\varphi_1 \Rightarrow \varphi_2$ , define the matching  $\mu$ -logic pattern  $\Box(\varphi_1 \Rightarrow \varphi_2) \equiv \Box(\varphi_1 \Rightarrow^+ \varphi_2)$  and extend it to a rule set  $A$  as follows:  $\Box A \equiv \bigwedge_{\varphi_1 \Rightarrow \varphi_2 \in A} \Box(\varphi_1 \Rightarrow \varphi_2)$ . Define the translation RL2MmL from RL sequents to matching  $\mu$ -logic patterns as follows:

$$\text{RL2MmL}(A \vdash_C \varphi_1 \Rightarrow \varphi_2) = (\forall \Box A) \wedge (\forall \circ \Box C) \rightarrow (\varphi_1 \Rightarrow^\star \varphi_2)$$

where  $\star = *$  if  $C = \emptyset$  and  $\star = +$  otherwise. We use  $\forall \varphi$  as a shorthand for  $\forall \vec{x}. \varphi$  where  $\vec{x} = \text{freeVar}(\varphi)$ . Recall that the “ $\circ$ ” in  $\forall \circ \Box C$  is “all-path next”.

Hence, the translation of  $A \vdash_C \varphi_1 \Rightarrow \varphi_2$  depends on whether  $C$  is empty or not. When  $C$  is nonempty, the RL sequent is *stronger* in that it requires *at least one step* being made in  $\varphi_1 \Rightarrow \varphi_2$ . Axioms (in  $A$ ) are also *stronger* than circularities (in  $C$ ) in that axioms *always* hold, while circularities only hold *after at least one step* because of the leading all-path next “ $\circ$ ”; and since the “next” is an “all-path” one, it does not matter which step is actually made, as circularities hold on *all* next states.

**Theorem 5.20.** Let  $\Gamma^{\text{RL}} = \{\varphi \in \text{MLPATTERN}_{\text{Cfg}} \mid M^{\text{Cfg}} \models \varphi\}$  be the set patterns (without  $\mu$ ) of sort Cfg that hold in  $M^{\text{Cfg}}$ . For all RL systems  $S$  and rules  $\varphi_1 \Rightarrow \varphi_2$  satisfying the

same technical assumptions in [12], the following are equivalent: (1)  $S \vdash_{\text{RL}} \varphi_1 \Rightarrow \varphi_2$ ; (2)  $S \models_{\text{RL}} \varphi_1 \Rightarrow \varphi_2$ ; (3)  $\Gamma^{\text{RL}} \vdash \text{RL2MmL}(S \vdash_{\emptyset} \varphi_1 \Rightarrow \varphi_2)$ ; (4)  $\Gamma^{\text{RL}} \models \text{RL2MmL}(S \vdash_{\emptyset} \varphi_1 \Rightarrow \varphi_2)$ .

*Proof.* See Section 5.14.10.

QED.

Therefore, provided that an oracle for validity of all the configuration patterns in  $M^{\text{Cfg}}$  is available, the matching  $\mu$ -logic proof system is capable of deriving any valid reachability rules. This way, matching  $\mu$ -logic serves as an even more fundamental logic foundation than RL for the  $\mathbb{K}$  framework (Section 2.15) and thus for programming language specification and verification, because matching  $\mu$ -logic can express significantly more properties than partial correctness reachability.

## 5.12 DEFINING $\lambda$ -CALCULUS

We have seen the syntax of  $\lambda$ -calculus and its concrete ccc models in Section 2.11. We will define a theory  $\Gamma^\lambda$  that captures the syntax, semantics, and formal proofs of  $\lambda$ -calculus. To do that, we need to address two challenges.

The first challenge is to handle the binding behavior of  $\lambda$ , that is, to define  $\lambda x.e$  as a notation in matching  $\mu$ -logic such that it satisfies the (meta-level) properties regarding free variables,  $\alpha$ -equivalence, and capture-free substitution. The second challenge is to prove the following equivalent result, called the conservative extension theorem:

$$\Gamma^\lambda \vdash e_1 = e_2 \quad \xleftrightarrow[\text{extensiveness}]{\text{conservativeness}} \quad \vdash_\lambda e_1 = e_2 \text{ for all } e_1, e_2 \in \Lambda \quad (5.4)$$

The conservativeness direction is the difficult part. Indeed, matching  $\mu$ -logic has a richer syntax and a more complex proof system than  $\lambda$ -calculus. We need to show that this more complex infrastructure cannot be used to prove more equations between  $\lambda$ -expressions.

To solve the first challenge, we make an important observation that  $\lambda$  plays two important roles: (i) it builds a *term*  $\lambda x.e$ , and (ii) it builds a *binding* of  $x$  into  $e$ . We will separate these two roles when defining  $\lambda x.e$  as a notation in matching  $\mu$ -logic, where we build terms using symbols and creating the binding behavior using the built-in binder  $\exists$ .

To solve the second challenge, We give two different proofs for the conservativeness of  $\Gamma^\lambda$ , each providing a unique insight about the construction of  $\Gamma^\lambda$ . The first proof is a model-theoretic proof, discussed in Section 5.12.2. It considers the concrete cc models for  $\lambda$ -calculus, which are known to be complete with respect to  $\lambda$ -calculus reasoning (Section 2.11). This model-theoretic proof is easier to understand and is what inspired our encoding of the  $\lambda$  binder but it does not generalize to binders used in other formal systems, such as  $\pi$ -calculus

or type systems. Therefore, we give another proof-theoretic proof, based purely on the syntax and formal proofs of  $\lambda$ -calculus, instead of its models. The proof-theoretic is easier to be generalized to the binders in other formal systems.

### 5.12.1 Defining the $\lambda$ binder

Our definition of the  $\lambda$  binder in matching  $\mu$ -logic is inspired by the concrete ccc models in Section 2.11. The key ingredient is the retraction function  $\mathcal{G}$  that encodes representable functions into elements, so let us first define representable functions and the retraction function.

Recall that  $f_{e,x}^\rho$  is the representable function in Section 2.11, which corresponds to the interpretation of  $\lambda x . e$  under  $\rho$  in the concrete ccc model. The graph of  $f_{e,x}^\rho$ ,

$$\text{graph}(f_{e,x}^\rho) = \{(a, |e|_{\rho[a/x]}^\lambda) \mid \text{for all } a \text{ in the concrete ccc model } A\} \quad (5.5)$$

contains all the argument-value pairs of  $f_{e,x}^\rho$ . Note that this graph is an element in  $\mathcal{P}(A \times A)$ , the powerset of  $A \times A$ , but not every element in  $\mathcal{P}(A \times A)$  is the graph of a representable function. Therefore, the retraction function  $\mathcal{G}$  is captured as a *partial function* from  $\mathcal{P}(A \times A)$  to  $A$  which is defined only on the graphs of representable functions, and undefined elsewhere.

Now we start to define  $\Gamma^\lambda$  following the above intuition. Firstly, we include all  $\lambda$ -calculus variables in  $V$  as element (and not set) variables in matching  $\mu$ -logic. Then, we define three sorts: **Exp** as the sort of  $\lambda$ -expressions; *Pair* as the *product sort* of  $V$  and **Exp** (Definition 5.2); and  $2^{Pair}$  as its *power sort* (Definition 5.5). Intuitively,  $2^{Pair}$  is the sort of all binary relations, including non-functions, over  $V$  and **Exp**, because the inhabitant of  $2^{Pair}$  is the powerset of the Cartesian product of the inhabitants of  $V$  and **Exp**, by Proposition 5.1 and ??.

Next, we define a partial function **lambda**:  $2^{Pair} \multimap \text{Exp}$ , to represent the retraction function  $\mathcal{G}$  in Section 2.11. We define **app**:  $\text{Exp} \times \text{Exp} \rightarrow \text{Exp}$  to be the application function and write  $e_1 e_2 \equiv \text{app}(e_1, e_2)$ . Abstraction  $\lambda x . e$  is defined as the following syntactic sugar, where we extract the general *binding notation*  $[x : V] e$  for clarity and because it can be used to define any other binders, not only  $\lambda$ :

$$[x : V] e \equiv \text{intension}(\exists x : V . \langle x, e \rangle) \quad // \text{ the binding notation} \quad (5.6)$$

$$\lambda x . e \equiv \text{lambda}([x : V] e) \quad // \lambda\text{-abstraction} \quad (5.7)$$

Equation (5.7) is a logical incarnation of the semantics of  $\lambda x . e$  in the concrete ccc models into matching  $\mu$ -logic. In a concrete ccc model,  $|\lambda x . e|_\rho^\lambda = \mathcal{G}(f_{e,x}^\rho)$ , where  $f_{e,x}^\rho(a) = |e|_{\rho[a/x]}^\lambda$ .



In matching  $\mu$ -logic,  $\exists x : V . \langle x, e \rangle$  denotes the union set  $\bigcup_x \{(x, e)\}$ , namely the graph of  $f_{e,x}^\rho$ . Note that  $\forall x : V . \langle x, e \rangle$  also yields the correct binding behavior, but it does not have the right semantic meaning of a graph. The binding notation  $[x : V] e$  takes this graph as a *set* of pairs and *packs* them into one object in the power sort  $2^{Pair}$ . Then, this packed object is passed to **lambda**, which decodes/retracts it into the intended interpretation of  $\lambda x . e$ . For now, we do not know any property about **lambda**, except that it is a partial function from  $2^{Pair}$  to **Exp**. Its intended behavior will be axiomatized by the axiom schema  $(\beta)$ —the axiom schema that characterizes  $\lambda$ -abstraction and the semantics of  $\lambda$ .

Under the above notations, all  $\lambda$ -expressions are patterns. Particularly, the notation  $\lambda x . e$  yields the right binding behaviors about  $\lambda$  via the built-in binder  $\exists$ . Let  $\Gamma^\lambda$  be the theory for  $\lambda$ -calculus that includes all the above definitions and notations and all instances of the  $(\beta)$  axiom schema:

$$\forall x_1 : V . \dots \forall x_n : V . (\lambda x . e) e' = e[e'/x]$$

where  $x_1, \dots, x_n$  are all the free variables in  $freeVar((\lambda x . e) e')$ . Note that the axioms are needed to specify the semantics of  $\lambda$  in matching  $\mu$ -logic, not its binding behavior. The latter is directly inherited from that of the built-in binder  $\exists$ .

We emphasize that the encoding of  $\lambda x . e$  in Equations (5.6) and (5.7) is only possible because matching  $\mu$ -logic treats terms and formulas uniformly as patterns, and it allows (FOL-style) quantification to be built on terms. A similar definition will immediately fail in FOL, because FOL enforces a clear distinction between terms and formulas at the syntax level and quantification only applies to formulas.

We finish this section by proving the extensiveness theorem for  $\lambda$ -calculus.

**Theorem 5.21.**  $\vdash_\lambda e_1 = e_2$  implies  $\Gamma^\lambda \vdash e_1 = e_2$ , for all  $e_1, e_2 \in \Lambda$ .

*Proof.* Note that  $\Gamma^\lambda$  contains all instances of  $(\beta)$  and equational reasoning is available in matching  $\mu$ -logic. QED.

### 5.12.2 Model-theoretic conservativeness proof

Here we prove the conservativeness of  $\Gamma^\lambda$  using concrete ccc models of  $\lambda$ -calculus. The main proof steps are summarized in Fig. 5.1. The only nontrivial one is Step 3, which requires to show that  $M \models e_1 = e_2$  for all  $M \models \Gamma^\lambda$  implies  $A \models_\lambda e_1 = e_2$  for all  $A$ . The following is the key lemma that establishes the connection between concrete ccc models and  $\Gamma^\lambda$ -models.

**Lemma 5.3.** For any concrete ccc model  $A$  and any valuation  $\rho$ , there exists a  $\Gamma^\lambda$ -model  $M^A$  and a corresponding valuation  $\rho^A$  such that  $|e|_{\rho^A} = \{|e|_\rho^\lambda\}$  for every  $e \in \Lambda$ .

$$\begin{array}{c}
 \Gamma^\lambda \vdash e_1 = e_2 \implies_1 \Gamma^\lambda \models e_1 = e_2 \implies_2 M \models e_1 = e_2 \text{ for } \Sigma^\lambda\text{-models } M \\
 \Downarrow_3 \\
 \vdash_\lambda e_1 = e_2 \iff_5 \models_\lambda e_1 = e_2 \iff_4 A \models_\lambda e_1 = e_2 \text{ for all concrete ccc models } A
 \end{array}$$

Figure 5.1: Main Steps in the Model-Theoretic Conservativeness Proof

*Proof.* Let us fix a concrete ccc model  $(A, \_ \bullet\_A, \mathcal{G})$ , where  $R(A)$  is its set of representable functions and  $\mathcal{G}: R(A) \rightarrow A$  is its retraction function. Let  $M_{\text{Exp}}^A = A$ . By Proposition 5.1 and ??,  $M_{\text{Pair}}^A = A \times A$  and  $M_{2\text{Pair}}^A = \mathcal{P}(A \times A)$ . We define  $\text{lambda}_{M^A}$  accordingly to the retraction function  $\mathcal{G}$ ; i.e.,  $\text{lambda}_{M^A}(P) = \{\mathcal{G}(f)\}$  whenever  $P = \text{graph}(f)$  and  $f \in R(A)$ , and  $\text{lambda}_{M^A}(P) = \emptyset$ , otherwise.

We define the corresponding  $\rho^A$  as  $\rho^A(x) = \rho(x)$  for every  $x \in V$ . We prove that  $|e|_{\rho^A} = \{|e|_\rho^\lambda\}$  for every  $e \in \Lambda$  by structural induction on  $e$ . The only nontrivial case is when  $e$  is  $\lambda x . e_1$ . In this case, we have

$$\begin{aligned}
 |\lambda x . e_1|_{\rho^A} &= |\text{lambda}(\text{intension}(\exists x : V . \langle x, e_1 \rangle))|_{\rho^A} \\
 &= \text{lambda}_{M^A}(|\text{intension}(\exists x : V . \langle x, e_1 \rangle)|_{\rho^A}) \\
 &= \text{lambda}_{M^A}(|\exists x : V . \langle x, e_1 \rangle|_{\rho^A}) \\
 &= \text{lambda}_{M^A}\left(\bigcup_{a \in A} \{(a, |e_1|_{\rho^A[a/x]})\}\right) \\
 &= \text{lambda}_{M^A}\left(\bigcup_{a \in A} \{(a, |e_1|_\rho^\lambda[a/x])\}\right) \\
 &= \text{lambda}_{M^A}(\text{graph}(f_{e_1, x}^\rho)) \\
 &= \{\mathcal{G}(f_{e_1, x}^\rho)\} \\
 &= \{|\lambda x . e_1|_\rho^\lambda\}
 \end{aligned}$$

Finally, we need to verify that  $M^A \models (\beta)$ . It is straightforward. Using the above result, for any  $x \in V$ ,  $e, e' \in \Lambda$ , and  $\rho$ , we have that  $|(\lambda x . e)e'|_\rho^\lambda = |e[e'/x]|_\rho^\lambda$  in  $A$  implies  $|(\lambda x . e)e'|_{\rho^A} = |e[e'/x]|_{\rho^A}$  in  $M^A$ . Noting that  $\rho^A$  is arbitrary (as  $\rho$  is arbitrary),  $M^A \models (\beta)$ . QED.

The operations `intension` and `lambda` are crucial in the proof of Lemma 5.3. Without them,  $\exists x : V . \langle x, e \rangle$  is merely the graph set, not a singleton pattern, and thus cannot be directly used to interpret  $\lambda x . e$ .

Using Lemma 5.3, we can immediately prove Step 3 in Fig. 5.1:

**Lemma 5.4.** If  $M \models e_1 = e_2$  for every  $\Gamma^\lambda$ -model  $M$ , then  $A \models_\lambda e_1 = e_2$  for every concrete ccc models  $A$ .

*Proof.* Let  $A$  be any concrete ccc model and  $\rho$  be any valuation. By Lemma 5.3, there exists a  $\Gamma^\lambda$ -model  $M^A$  and a valuation  $\rho^A$  such that  $|e|_{\rho^A} = \{|e|_\rho^\lambda\}$  for any  $e \in \Lambda$ . Since  $M^A \models e_1 = e_2$ , we have  $|e_1|_{\rho^A} = |e_2|_{\rho^A}$ , and thus  $|e_1|_\rho^\lambda = |e_2|_\rho^\lambda$ . Since  $\rho$  is arbitrary,  $A \models_\lambda e_1 = e_2$ . QED.

Now we finish the model-theoretic conservativeness proof in Figure 5.1.

**Theorem 5.22.**  $\Gamma^\lambda \vdash e_1 = e_2$  implies  $\vdash_\lambda e_1 = e_2$ , for all  $e_1, e_2 \in \Lambda$ .

*Proof.* See Fig. 5.1, where Step 1 is proved by Theorem 4.1; Step 2 is proved by definition; Step 3 is proved by Lemma 5.4; Step 4 is proved by definition; and Step 5 is proved by Theorem 2.13. QED.

Theorem 5.22 together with Theorem 5.21 show that  $\Gamma^\lambda$  is a conservative extension of  $\lambda$ -calculus.

**Theorem 5.23.** For every  $e_1, e_2 \in \Lambda$ , these are equivalent: (1)  $\Gamma^\lambda \vdash e_1 = e_2$ ; (2)  $\Gamma^\lambda \models e_1 = e_2$ ; (3)  $\models_\lambda e_1 = e_2$ ; (4)  $\vdash_\lambda e_1 = e_2$ .

*Proof.* (1)  $\implies$  (2) is by Theorem 4.1. (2)  $\implies$  (3) is by Lemma 5.4. (3)  $\implies$  (4) is by Lemma 2.13. (4)  $\implies$  (1) is by Theorem 5.22. QED.

The equivalence (1)  $\iff$  (4) is called the conservative extension theorem for  $\Gamma^\lambda$ . The equivalence (2)  $\iff$  (4) is called the (deductive) completeness of matching  $\mu$ -logic with respect to  $\Gamma^\lambda$ . By defining  $\lambda$ -calculus in matching  $\mu$ -logic, we automatically obtain a model of theory for  $\lambda$ -calculus via the matching  $\mu$ -logic  $\Gamma^\lambda$ -models.

### 5.12.3 Proof-theoretic conservativeness proof

The model-theoretic conservativeness proof is intuitive because it is based on the models of  $\lambda$ -calculus. It also has a clear limitation, which is that it requires a model theory for  $\lambda$ -calculus. In Section 5.12.2, we use the concrete ccc models for  $\lambda$ -calculus and especially the completeness result in Theorem 2.13. Therefore, the model-theoretic proof in Section 5.12.2 is specific to  $\lambda$ -calculus and the concrete ccc models. It is not easy to generalize to the binders in other formal systems, especially those do not have an accessible model theory.

Therefore, we give an alternative, proof-theoretic conservativeness proof that is entirely based on the syntactic structure of  $\lambda$ -calculus. As a result, the proof-theoretic proof is easier to generalize to other logical systems and binders.

We build a special  $\Gamma^\lambda$ -model  $T$ , which we call the *term model* of  $\lambda$ -calculus,<sup>3</sup> and follow the term algebra technique [69, 70, 71]:  $T$  has as elements the equivalence classes of  $\lambda$ -expressions modulo  $\alpha\beta$ -equivalence, and each  $e \in \Lambda$  is interpreted in  $T$  as the equivalence class containing itself, denoted by  $[e]$ . Formally, we will prove this:

**Theorem 5.24.** Let  $[e] = \{e' \in \Lambda \mid \vdash_\lambda e = e'\}$  be the equivalence class of  $e$  modulo  $\alpha\beta$ -equivalence. Let  $[\Lambda] = \{[e] \mid e \in \Lambda\}$  be the set of all these classes. Then, there is a  $\Gamma^\lambda$ -model  $T$ , called *term model*, and a valuation  $\rho_T$ , called *term valuation*, such that  $|e|_{T, \rho_T} = \{[e]\}$  for all  $e \in \Lambda$ . Since  $T$  is a fixed model, we abbreviate  $|e|_{T, \rho_T}$  as  $|e|_{\rho_T}$ .

Note that for distinct variables  $x, y \in V$ , we have  $[x] \neq [y]$  [32, Fact 2.1.37]. Clearly,  $x \in [x]$ , but  $[x]$  also includes infinitely many expressions:  $(\lambda y . y)x$ ,  $(\lambda y . y)((\lambda y . y)x)$ , etc.

We will show the construction of  $T$  shortly after. For now, let us first prove Theorem 5.22 from Theorem 5.24.

*Another Proof of Theorem 5.22.* Suppose  $\Gamma^\lambda \vdash e_1 = e_2$ . We have

$$\begin{array}{llll}
 \Gamma^\lambda \vdash e_1 = e_2 & \Rightarrow & \Gamma^\lambda \models e_1 = e_2 & \text{by Theorem 4.1} \\
 & \Rightarrow & T \models e_1 = e_2 & \text{by definition} \\
 & \Rightarrow & |e_1|_{\rho_T} = |e_2|_{\rho_T} & \text{by Proposition 2.16} \\
 & \Rightarrow & [e_1] = [e_2] & \text{by Theorem 5.24} \\
 & \Rightarrow & \vdash_\lambda e_1 = e_2 & \text{by the definition of } [e] \text{ in Theorem 5.24}
 \end{array}$$

QED.

Now we construct  $T$  and show that  $T \models \Gamma^\lambda$ . We define  $T_{\text{Exp}} = [\Lambda]$ , which is the set of equivalence classes of  $\lambda$ -expressions. By Proposition 5.1 and ??,  $T_{\text{Pair}} = [\Lambda] \times [\Lambda]$  and  $T_{2\text{Pair}} = \mathcal{P}([\Lambda] \times [\Lambda])$ . We define  $\mathbf{app}_T([e_1], [e_2]) = [e_1 e_2]$  for  $e_1, e_2 \in \Lambda$ . Note that this definition is well-defined, because  $\vdash_\lambda e_1 e_2 = e'_1 e'_2$  whenever  $\vdash_\lambda e_1 = e'_1$  and  $\vdash_\lambda e_2 = e'_2$ . Finally, we define

$$\mathbf{lambda}_T \left( \bigcup_{z \in V} ([z], [e[z/x]]) \right) = \{[\lambda x . e]\}, \quad \text{for any } x \in V \text{ and } e \in \Lambda. \quad (5.8)$$

and  $\mathbf{lambda}_T(P) = \emptyset$ , if  $P$  is not a graph of the above form.

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<sup>3</sup>In the literature on  $\lambda$ -calculus, term models have a different meaning. For example, in [32], term models are special  $\lambda$ -calculus models constructed based on the combinatory algebra semantics; see Section 5.12.4 for a comparison.

The construction of  $T$ , especially Equation (5.8), is critically depending on the notation definition  $\lambda x . e \equiv \text{lambda}(\text{intension } \exists x : V . \langle x, e \rangle)$ . The  $\alpha$ -equivalence  $\lambda x . e \equiv \lambda z . (e[z/x])$  is captured, both syntactically and semantically, by collecting all the pairs  $\langle z, e[z/x] \rangle$  for all  $z$ , using the pattern  $\exists x : V . \langle x, e \rangle$ . Therefore,  $\exists x : V . \langle x, e \rangle$  encapsulates all the information about  $[\lambda x . e]$ , which is *packed* by **intension** and passed to **lambda**, and then *retracted* to restore the original expression  $\lambda x . e$ . Proposition 5.25 shows that the condition in Equation (5.8) on  $\text{lambda}_T$  is consistent.

**Proposition 5.25.**  $[\lambda x . e] = [\lambda x' . e']$ , whenever

$$\bigcup_{z \in V} ([z], [e[z/x]]) = \bigcup_{z \in V} ([z], [e'[z/x']]) \quad (5.9)$$

*Proof.* Assume the opposite, i.e.,  $[\lambda x . e] \neq [\lambda x' . e']$ . Let  $z^* \in V$  be a fresh variable that does not occur in  $\lambda x . e$  or  $\lambda x' . e'$ . Then we have  $\lambda x . e \equiv \lambda z^* . e[z^*/x]$  and  $\lambda x' . e' \equiv \lambda z^* . e'[z^*/x']$ . By the assumption, we have  $[\lambda z^* . e[z^*/x]] \neq [\lambda z^* . e'[z^*/x']]$ , and thus  $[e[z^*/x]] \neq [e'[z^*/x']]$ . Noting that  $[z_1] = [z_2]$  iff  $z_1 = z_2$ , for every  $z_1, z_2 \in V$ . Thus,  $([z^*], [e[z^*/x]])$  is in the left-hand side of Equation (5.9) but not the right-hand side. This is a contradiction. QED.

So far, we have constructed the term model  $T$ . We now define the term valuation  $\rho_T$  as  $\rho_T(x) = [x]$  for every  $x \in V$ .

**Proposition 5.26.**  $|e|_{\rho_T} = \{[e]\}$ , and  $|e|_{\rho[\rho(z)/x]} = |e[z/x]|_{\rho}$  for all  $\rho$ .

*Proof.* We prove both properties simultaneously by induction on the  $\lambda$ -depth  $d(e)$  of  $e$ , which is the maximum number of nested  $\lambda$ 's in  $e$ . If  $d(e) = 0$  then  $e$  is a variable or is built purely using applications (i.e., **app**) and has no  $\lambda$  abstraction. In this case, both properties can be proved by another structural induction on  $e$ . If  $d(e) \geq 1$  then  $e$  has either the form  $e_1 e_2$  where  $d(e_1), d(e_2) \leq d(e)$ , or the form  $\lambda x . e_1$  where  $d(e_1) \leq d(e) - 1$ . Then another structural induction on  $e$  proves both properties. QED.

**Proposition 5.27.** If  $\vdash_{\lambda} e = e'$ , then  $|e|_{\rho} = |e'|_{\rho}$  for any  $\rho \in \text{VarVal}$ .

*Proof.* Note that the interpretation of a  $\lambda$ -expression relies on its free variables. Suppose  $\text{freeVar}(e) \cup \text{freeVar}(e') = \{x_1, \dots, x_n\}$  and  $\rho(x_i) = [y_i]$  for  $i \in \{1, \dots, n\}$ . Then,  $y_i$  is the only variable that is in  $[y_i]$ . Since  $\rho$  equals to  $\rho_T[[y_1]/x_1] \cdots [[y_n]/x_n]$  restricted on  $x_1, \dots, x_n$ , we have  $|e|_{\rho} = |e|_{\rho_T[[y_1]/x_1] \cdots [[y_n]/x_n]}$ . By Proposition 5.26,  $|e|_{\rho_T[[y_1]/x_1] \cdots [[y_n]/x_n]} = |e[y_1/x_1] \cdots [y_n/x_n]|_{\rho_T} = \{[e[y_1/x_1] \cdots [y_n/x_n]]\}$ . Similarly  $|e'|_{\rho} = \{[e'[y_1/x_1] \cdots [y_n/x_n]]\}$ . Then,  $\vdash_{\lambda} e[y_1/x_1] \cdots [y_n/x_n] = e'[y_1/x_1] \cdots [y_n/x_n]$ . Then we have

$$[e[y_1/x_1] \cdots [y_n/x_n]] = [e'[y_1/x_1] \cdots [y_n/x_n]]$$

Hence,  $|e|_\rho = |e'|_\rho$ .

QED.

Now we only need to prove Theorem 5.24.

*Proof of Theorem 5.24.* We have shown that  $|e|_{\rho_T} = \{|e|\}$  for every  $e \in \Lambda$ , in Proposition 5.26. It remains to show that  $T$  validates  $(\beta)$ , i.e.,  $|(\lambda x . e) e'|_\rho = |e[e'/x]|_\rho$  for all  $\rho$ . The latter follows immediately from Proposition 5.27. QED.

#### 5.12.4 Discussion

We first compare our term model  $T$  for  $\lambda$ -calculus to the other classic notion of term models. In  $\lambda$ -calculus, a *term model* [32, Definition 5.2.11] is a special  $\lambda$ -model, which is an algebra with  $[\Lambda]$  being the underlying carrier set. The operations include a binary application function given by  $[e_1][e_2] = [e_1 e_2]$  for  $e_1, e_2 \in \Lambda$  as well as two constants:  $k = [\lambda x . \lambda y . x]$  and  $s = [\lambda x . \lambda y . \lambda z . (xz)(yz)]$ . We denote the above model by  $A$  and call it a *classical term model*, to not confuse it with our term model  $T$ . Clearly,  $T$  and  $A$  follow different approaches to capture  $\lambda$ -expressions. While  $A$  uses the name-free, combinators approach, where  $\lambda$  is handled by abstraction elimination, our term model  $T$  gives an explicit and constructive interpretation to  $\lambda$ , as shown in Equation (5.8).

Next, we discuss the *representability problem* [72, pp. 8], which is a long-standing, concerning and open problem in the study of  $\lambda$ -calculus. The problem asks if a given class of  $\lambda$ -calculus models is *representationally complete*, in the sense that there exists a model in the given class such that any two expressions  $e_1$  and  $e_2$  are provably equal if and only if they are interpreted as the same element/value in that model. Representability completeness indicates that a class of  $\lambda$ -calculus models is sufficient in capturing the formal reasoning in  $\lambda$ -calculus, so one may *reduce* the study of formal reasoning in  $\lambda$ -calculus to the study of models, where more mathematical tools and techniques can be applied. Hence, *reduction* is the main motivation.

$\lambda$ -calculus models are broadly divided into *syntactic models* and *non-syntactic models* [73, pp. 13], depending on whether their construction is based on the syntax and provability of  $\lambda$ -calculus or not. All the classical term models in  $\lambda$ -calculus, as well as our particular term model in Section 5.12.3, are syntactic models. Syntactic models are often representationally complete, but studying them tends to be as hard as studying the syntax and formal reasoning directly, and thus the reduction to syntactic models usually does not help simplify the study of  $\lambda$ -calculus. Thus, for decades researchers have been searching for and studying sub-classes of non-syntactic concrete ccc models, hoping they are also representationally complete. So far, three main such sub-classes have been identified, known as the *main semantics* of  $\lambda$ -calculus: Scott's continuous semantics [74], Berry's stable semantics [75, 76], and Bucciarelli-Ehrhard

strongly stable semantics [77]. The representability problem for the main semantics (and their sub-classes) has remained largely open as of today, except for some negative results proved for some sub-classes (e.g., graph models [78]).

Theorem 5.24 shows that the class of  $\Gamma^\lambda$ -models of is representationally complete, positively answering the representability problem for our matching  $\mu$ -logic semantics of  $\lambda$ -calculus. Our proof does not rely on any known results about the representational completeness of any existing semantics; instead, it is entirely based on the model theory of matching  $\mu$ -logic, which is not specific to  $\lambda$ -calculus but which allows for an appropriate axiomatization of  $\lambda$ -calculus as a theory that is hereby endowed with the desired representationally complete models automatically. We can push Theorem 5.24 even further to any equational extensions of  $\lambda$ -calculus, known as  $\lambda$ -theories. Indeed, the definition of the equivalence class  $[e]$  as the set of  $\alpha\beta$ -equivalent expressions of  $e$ , has not been critical in the proof of Theorem 5.24, and the conclusion still holds if we consider any equivalence class  $[e]$  that includes the basic  $\alpha\beta$ -equivalence. Therefore, we conclude that the matching  $\mu$ -logic definition of  $\lambda$ -calculus is representationally complete for *all*  $\lambda$ -theories.

Although we do not solve any of the existing open problems, our work suggests the matching  $\mu$ -logic can be a viable alternative to the existing  $\lambda$ -calculus models within the main semantics. The matching  $\mu$ -logic models are as good as the existing models for  $\lambda$ -calculus in terms of theoretical properties w.r.t. formal reasoning and semantics, yet unlike the existing models, they are general in the sense that they are not crafted specifically for  $\lambda$ -calculus, but are obtained from the matching  $\mu$ -logic theory  $\Gamma^\lambda$ . We give a general solution for all the binders, which for  $\lambda$ -calculus is as good as the state of the art, considering *both* the proof-theoretic *and* the model-theoretic aspects.

### 5.13 DEFINING TERM GENERIC LOGIC

We have shown how to define the  $\lambda$  binder as the following notation (Eqs. (5.6)-(5.7)):

$$\lambda x . e \equiv \text{lambda } [x : V] e \quad (5.10)$$

In this section we show that our approach is not specific to  $\lambda$ -calculus. We provide evidence that matching  $\mu$ -logic can serve as a general approach to dealing with binders. We will show how to use patterns similar to Eq. (5.10) to define the binders in a variety of logical systems, including System F [79, 80], pure type systems [81],  $\pi$ -calculus [82], and more, and prove a corresponding conservative extension theorem for each of them. To do that, several challenges need to be solved.

Binders	Behaviors	Meaning	Systems
$\lambda x . e$	binding $x$ into $e$	function abstraction	$\lambda$ -calculus
$\lambda x : e_1 . e_2$	binding $x$ into $e_2$	function abstraction	System F
$\lambda t . e$	binding $t$ into $e$	type abstraction	System F
$\Pi t . e$	binding $t$ into $e$	$\Pi$ -type constructor	System F
$\lambda x : e_1 . e_2$	binding $x$ into $e_2$	function abstraction	Pure type system
$\pi x : e_1 . e_2$	binding $x$ into $e_2$	type abstraction	Pure type system
$\text{Inp}(x, y, e)$	binding $y$ into $e$	input process	$\pi$ -calculus
$\nu y . e$	binding $y$ into $e$	new process name creation	$\pi$ -calculus
$\text{Bout}(e_1, x, y, e_2)$	binding $y$ into $e_2$	bound output transition	$\pi$ -calculus
$\text{Inp}(e_1, x, y, e_2)$	binding $y$ into $e_2$	input transition	$\pi$ -calculus

Figure 5.2: Example Binders and Their Behavior in Logical Systems

The first challenge is that binders can have more complex binding behavior than in  $\lambda$ -calculus; see Fig. 5.2. For example,  $\lambda x : e_1 . e_2$  in System F binds  $x$  within  $e_2$ , but not in  $e_1$ ;  $\text{Inp}(x, y, e)$  in  $\pi$ -calculus has the binding variable in the second position (i.e.,  $y$ ), and not the first position. We deal with this binding behavior by desugaring to binders whose binding variable is their first argument and is bound within the second argument only; that is, we desugar an arbitrary binder to a binder of the form  $b(x, e_1, \dots, e_n)$ , where  $x$  is bound in  $e_1$  but not in  $e_2, \dots, e_n$ . Clearly, this desugaring process is just a sequence of argument swappings. Then, we further desugar  $b(x, e_1, \dots, e_n)$  to  $b'(b''(x, e_1), e_2, \dots, e_n)$ , where  $b'$  is a (binding-free) symbol and  $b''$  is a binder that binds  $x$  to  $e_1$ , just like  $\lambda$  in  $\lambda$ -calculus. Finally, we define  $b''(x, e_1)$  as the following syntactic sugar:

$$b''(x, e) \equiv \text{retraction}_b [x : V] e \quad (5.11)$$

in the same way as in Eq. (5.10), except that here we use a new retraction symbol  $\text{retraction}_b$  that is specific to the binder  $b$ . Each binder has its own retraction symbol, but the other infrastructure symbols, such as products, powersets, and the binding notation  $[x : V] e$ , are the same. From now on, we will only consider binders  $b(x, e)$  that bind  $x$  within  $e$ , for technical convenience.

The second challenge is that logical systems featuring bindings are very different from each other, in terms of the kinds of *logical reasoning* that is carried out in them. For example, System F derives *typing judgments*  $\Gamma \triangleright e_1 : e_2$  to mean that  $e_1$  has type  $e_2$  under typing environment  $\Gamma$ ;  $\pi$ -calculus derives *transitions*  $e_1 \xrightarrow{\text{act}} e_2$  to mean that process  $e_1$  transits by action  $\text{act}$  to process  $e_2$ . It is tedious and non-systematic to consider these logical systems *separately*, because we would need to capture their specific logical reasoning and prove the conservative extension theorem for each of them, more or less similarly to the syntax-based



proof in Section 5.12.3.

To capture the various logical systems featuring bindings more systematically, we employ a parametric framework for binders, called *term-generic logic* [33] (TGL). TGL is a parametric variant of FOL, whose syntax is parametric on a set of (generic) terms that are not constructed from constants and functions, but defined axiomatically. When we instantiate TGL with the term syntax of a given system (e.g.,  $\lambda$ -calculus, System F,  $\pi$ -calculus, etc), it becomes a (first-order) *meta-logic* of that system and can be used to specify and reason about its meta-properties. Using TGL, we give a systematic treatment of binders in the various logical systems. We will capture TGL in matching  $\mu$ -logic and prove a conservative extension theorem for TGL, from which the conservative extension theorems for the other logical systems follow as corollaries.

Why not use TGL directly then, but instead use matching  $\mu$ -logic? There are two reasons. Firstly, TGL in its full generality is not implementable, because it does not deal with any concrete syntax of binders. Its notion of (generic) terms is given axiomatically and needs to be instantiated, which is what we have done in Section 2.12, where we instantiate TGL to bridge matching  $\mu$ -logic and other logical systems with binders. The second reason is that TGL is a logic specifically designed for binders, while matching  $\mu$ -logic serves as the unifying logical foundation for the  $\mathbb{K}$  framework, as discussed in Section 2.15. Therefore, matching  $\mu$ -logic supports reasoning in many mathematical domains other than binders, and thus it is more practical than TGL.

We define a theory  $\Gamma^{\text{TGL}}$  and introduce notations such that all TGL terms and formulas are well-formed patterns. We show that  $\Gamma^{\text{TGL}}$  is a conservative extension of TGL, by proving the following equivalence theorem.

**Theorem 5.28.** Under the notations in Theorem 2.14, the following are equivalent: (1)  $(\Gamma^{\text{TGL}} \cup E) \vdash \bigwedge \Delta_1 \rightarrow \bigvee \Delta_2$ . (2)  $(\Gamma^{\text{TGL}} \cup E) \models \bigwedge \Delta_1 \rightarrow \bigvee \Delta_2$ ; (3)  $E \models_{\text{TGL}} \Delta_1 \triangleright \Delta_2$ ; (4)  $E \vdash_{\text{TGL}} \Delta_1 \triangleright \Delta_2$ ; Here,  $\bigwedge \Delta_1$  is the conjunction of patterns in  $\Delta_1$  and  $\bigvee \Delta_2$  is the disjunction of patterns in  $\Delta_2$ .

The many-sorted binder syntax and TGL terms are captured by defining sorts and many-sorted functions, and defining binders as in Eq. (5.11). TGL formulas, except  $\pi(e_1, \dots, e_n)$ , are captured by matching  $\mu$ -logic's derived connectives and equality. Predicate  $\pi(e_1, \dots, e_n)$  for  $\pi \in \Pi_{s_1 \dots s_n}$ , is captured by defining a symbol  $\pi$  and the following axiom:

$$(\text{PREDICATE}) \quad \forall x_1 : s_1 \dots \forall x_n : s_n. (\pi x_1 \dots x_n = \top) \vee (\pi x_1 \dots x_n = \perp) \quad (5.12)$$

which specifies that  $\pi$  returns either  $\top$  or  $\perp$ , i.e., it indeed builds predicate patterns. Without

such axioms,  $\pi x_1 \cdots x_n$  could be any subset. Let  $\Gamma^{\text{TGL}}$  contain all the above definitions and notations.

Under the above notations and axioms, all TGL terms are functional patterns and all TGL formulas are predicate patterns.

Theorem 5.28 is proved using a model-based approach similar to Fig. 5.1. Here we explain the only nontrivial proof step, which is (2)  $\implies$  (3). This is proved by constructing a matching  $\mu$ -logic model  $M^A$  from any given TGL model  $A$ , such that all TGL terms and formulas are interpreted the same in  $M^A$  and  $A$ , i.e.,  $|e|_\rho = \{A_e(\rho)\}$  for every  $e \in \text{TGLTERM}$ ;  $|\varphi|_\rho = M^A$  whenever  $\rho \in A_\varphi$ , and  $|\varphi|_\rho = \emptyset$ , whenever  $\rho \notin A_\varphi$ , for every  $\varphi \in \text{TGLFORM}$ .

Using TGL and Theorem 5.28, we obtain a systematic proof of the conservative extension theorems and deductive completeness theorems for all logical systems that have been defined in TGL and studied in [33, Section 4] and [83, Section 4], including System F [79, 80] (both the typing and reduction versions),  $\lambda$ -calculus (including the untyped [31], sub-typed [84], illative [32], and linear versions [85, 86]), pure type systems [81], and  $\pi$ -calculus [82]. The systematic proof works as follows. For each logical system  $L$ , its set of terms  $\text{Term}_L$  can be captured by a binder syntax using the desugaring discussed at the beginning of Section 2.12. The proof/type system of  $L$  that derives sequents of the form  $\vdash_L \Phi$  is captured by a set of TGL axioms  $E^L$ , where each axiom corresponds to one type/proof rule of  $L$  [33]. An *adequacy theorem* is also proved there for each  $L$ , stating that  $\vdash_L \Phi$  iff  $E^L \vdash_{\text{TGL}} \Phi^{\text{TGL}}$ , where  $\Phi^{\text{TGL}}$  (of the form  $\Delta_1^\Phi \triangleright \Delta_2^\Phi$ ) is the corresponding TGL encoding of the  $L$ -sequent  $\Phi$ . Let  $\Gamma^L = \Gamma^{\text{TGL}} \cup E^L$  be the theory that captures  $L$ , and  $\Phi^{\text{ML}} = \bigwedge \Delta_1^\Phi \rightarrow \bigvee \Delta_2^\Phi$  be the encoding of  $\Phi$ . By Theorem 2.14, we have that  $\vdash_L \Phi$  in  $L$ , iff  $E^L \vdash_{\text{TGL}} \Phi^{\text{TGL}}$  in TGL, iff  $\Gamma^L \vdash \Phi^{\text{ML}}$  in matching  $\mu$ -logic, iff  $\Gamma^L \models \Phi^{\text{ML}}$  in matching  $\mu$ -logic. Hence,  $\Gamma^L$  is a conservative extension of  $L$  and the class of matching logic models of  $\Gamma^L$  is complete with respect to  $L$ .

Note that the term “consistency” has different meanings in different contexts. In type systems, inconsistency means the ability to prove any typing judgments  $t : \tau$ . Similarly, in  $\lambda$ -calculus or other equational logic theories, inconsistency means the ability to prove any equations  $e_1 = e_2$ . However, in matching  $\mu$ -logic (and also FOL), inconsistency means the ability to prove logical false  $\perp$ . Thus, inconsistency for classical logics such as matching  $\mu$ -logic is stricter than that for type systems and  $\lambda$ -calculus. For example, if  $T$  is a PTS that contains the typing axiom  $\text{Type} : \text{Type}$ , then  $T$  is inconsistent [87], but  $\Gamma^T$  is still a consistent matching  $\mu$ -logic theory and has a model that interprets the typing relation  $\_ : \_$  as the total relation on all PTS terms.

## 5.14 PROOFS

## 5.14.1 Proof of Theorem 5.2

Even though we tacitly blur the distinction between constant symbol  $\sigma \in \Sigma_{\lambda, s_1 \otimes \dots \otimes s_n \otimes s}$  and  $n$ -ary symbol  $\sigma \in \Sigma_{s_1 \dots s_n, s}$ , doing so will cause us a lot of trouble in this section, when our aim is to prove such a blur of syntax actually works. Therefore, within this section, we introduce and use a more distinct syntax that distinguishes the two, as follows

$\sigma \in \Sigma_{s_1, \dots, s_n, s}$	an $n$ -ary symbol
$\alpha_\sigma \in \Sigma_{\lambda, s_1 \otimes \dots \otimes s_n \otimes s}$	the corresponding constant symbol
$\sigma(\varphi_1, \dots, \varphi_n)$	symbol application
$\alpha_\sigma[\varphi_1, \dots, \varphi_n]$	projections
$\sigma(x_1, \dots, x_n) = \alpha_\sigma[x_1, \dots, x_n]$	recursive symbol
$\alpha_\sigma = \mu\alpha. \exists \vec{x} \langle \vec{x}, \varphi[\alpha/\sigma] \rangle$	definition of $\alpha_\sigma$

Before we prove Theorem 5.2, we introduce a useful lemma that allows us to prove properties about least fixpoint patterns. Recall that rule (KNASTER TARSKI) allows us to prove theorems of the form  $\Gamma \vdash \mu X. \varphi \rightarrow \psi$ . However, in practice, the least fixpoint pattern  $\mu X. \varphi$  is not always the only components on the left hand side, but rather stay *within some contexts*. The following lemma is particularly useful in practice, as it allows us to “plug out” the least fixpoint pattern from its context, so that we can apply (KNASTER TARSKI). After that, we may “plug it back” into the context.

**Lemma 5.5.** Let  $C[\square]$  be a context such that  $\square$  does not occur under any  $\mu$ -binder, and

- $C[\varphi \wedge \psi] = C[\varphi] \wedge \psi$ , for all patterns  $\varphi$  and all predicate patterns  $\psi$ ;
- $C[\exists x. \varphi] = \exists x. C[\varphi]$ , for all  $\varphi$  and  $x \notin \text{freeVar}(C[\square])$ .

Then we have that  $\Gamma \vdash C[\varphi] \rightarrow \psi$  if and only if  $\Gamma \vdash \varphi \rightarrow \exists x. x \wedge [C[x] \rightarrow \psi]$ .

*Proof.* We prove both directions simultaneously. Note that it is easy to prove that  $\Gamma \vdash \varphi = \exists x. (x \wedge (x \in \varphi))$  using rules (MEMBERSHIP) in the proof system  $\mathcal{P}$  (see Fig. 2.11).

We start with  $\Gamma \vdash C[\varphi] \rightarrow \psi$ . By the mentioned equality, we get  $\Gamma \vdash C[\exists x. (x \wedge (x \in \varphi))] \rightarrow \psi$ . By the properties of  $C$ , it becomes  $\Gamma \vdash (\exists x. C[x] \wedge x \in \varphi) \rightarrow \psi$ , which, by FOL reasoning, becomes  $\Gamma \vdash x \in \varphi \rightarrow (C[x] \rightarrow \psi)$ . Note that  $x \in \varphi$  is a predicate pattern, so the goal is equivalent to  $\Gamma \vdash x \in \varphi \rightarrow [C[x] \rightarrow \psi]$ .

Now we are almost done. To show the “if” part, we apply (MEMBERSHIP INTRODUCTION) on  $\Gamma \vdash \varphi \rightarrow \exists x. x \wedge [C[x] \rightarrow \psi]$  and obtain  $\Gamma \vdash y \in \varphi \rightarrow \exists x. (y \in x) \wedge [C[x] \rightarrow \psi]$ . Note that  $y$  is a fresh variable and  $y \notin \text{free Var}(C[x]) \cup \text{free Var}(\psi)$ , so  $y \in [C[x] \rightarrow \psi] = [C[y] \rightarrow \psi]$ . Notice that  $y \in x = (y = x)$ . And we obtain  $\Gamma \vdash y \in \varphi \rightarrow [C[y] \rightarrow \psi]$ . Done.

To show the “only if” part, we apply some simple FOL reasoning on  $\Gamma \vdash x \in \varphi \rightarrow [C[x] \rightarrow \psi]$  and conclude that  $\Gamma \vdash (\exists x. (x \wedge x \in \varphi)) \rightarrow \exists x. (x \wedge [C[x] \rightarrow \psi])$ . Then by the equality  $\varphi = \exists x. (x \wedge x \in \varphi)$ , we are done. QED.

Note the conditions about the context  $C$  in Lemma 5.5 are important. Many contexts that arise in practice satisfy the conditions. In particular, (nested) symbol contexts satisfy the conditions automatically.

Under the above new notation and the lemma, we are ready to prove Theorem 5.2.

*Proof of Theorem 5.2.* (PRE-FIXPOINT). This is proved by simply unfolding  $\alpha_\sigma$  following its definition.

(KNASTER TARSKI). We give the following proof that goes backward from conclusion to their sufficient conditions.

$$\begin{aligned}
 & \sigma(x_1, \dots, x_n) \rightarrow \psi \\
 \Leftarrow & \alpha_\sigma[x_1, \dots, x_n] \rightarrow \psi \\
 \Leftarrow & \alpha \rightarrow \exists \alpha. (\alpha \wedge [\alpha[x_1, \dots, x_n] \rightarrow \psi]) \\
 \Leftarrow & \alpha_\sigma \rightarrow \underbrace{\forall \vec{x}. \exists \alpha. (\alpha \wedge [\alpha[x_1, \dots, x_n] \rightarrow \psi])}_{\alpha_0} \\
 \Leftarrow & \exists \vec{x}. \langle \vec{x}, \varphi[\forall \vec{x}. \alpha_0/\sigma] \rangle \rightarrow \forall \vec{x}. \alpha_0 \\
 \Leftarrow & \langle \vec{x}, \varphi[\forall \vec{x}. \alpha_0/\sigma] \rangle \rightarrow \alpha_0[z_1/x_1 \dots z_n/x_n] \\
 \Leftarrow & \langle \vec{x}, \varphi[\forall \vec{x}. \alpha_0/\sigma] \rangle \\
 & \rightarrow \exists \alpha. (\alpha \wedge [\alpha[z_1, \dots, z_n] \rightarrow \psi[z_1/x_1 \dots z_n/x_n]]) \\
 \Leftarrow & \langle \vec{x}, \varphi[\forall \vec{x}. \alpha_0/\sigma] \rangle[x_1, \dots, x_n] \rightarrow \psi \\
 \Leftarrow & \varphi[\forall \vec{x}. \alpha_0/\sigma] \rightarrow \psi \\
 \Leftarrow & \varphi[\forall \vec{x}. \alpha_0/\sigma] \rightarrow \varphi[\psi/\sigma]
 \end{aligned}$$

Notice that the last step is by  $\Gamma \vdash \varphi[\psi/\sigma] \rightarrow \psi$ .

By the positiveness of  $\varphi$  in  $\sigma$ , we just need to prove that for all  $\varphi_1, \dots, \varphi_n$ :

$$\Gamma \vdash (\forall \vec{x}. \alpha_0)[\varphi_1, \dots, \varphi_n] \rightarrow \psi[\varphi_1/x_1 \dots \varphi_n/x_n]$$

By (KEY-VALUE) and definition of  $\alpha_0$ , the above becomes

$$\Gamma \vdash z_1 \in \varphi_1 \wedge \dots \wedge z_n \in \varphi_n \wedge \psi[z_1/x_1 \dots z_n/x_n] \rightarrow \psi[\varphi_1/x_1 \dots \varphi_n/x_n],$$

which holds by assumption. QED.

What is interesting in the above proof is that we used only (KEY-VALUE) and did not use (INJECTIVITY) and (PRODUCT DOMAIN). The last two axioms are used in the proof of Theorem 5.3, where we need to establish an isomorphism between *models* of LFP and MmL. In there, the two axioms are needed to constrain matching  $\mu$ -logic models.

#### 5.14.2 Proof of Theorem 5.3

We first show that the theory of products  $\Gamma^{\text{product}}$  in Definition 5.2 captures precisely the product set  $M_s \times M_t$ .

Now, we are ready to prove Theorem 5.3.

*Proof of Theorem 5.3.* The proof is mainly based on the isomorphism between LFP models and matching  $\mu$ -logic  $\Gamma^{\text{LFP}}$ -models. Notice that the (FUNCTION) axioms forces symbols in all  $\Gamma^{\text{LFP}}$ -models are functions. In addition, we use the axiom  $\forall x : \text{Formula} \forall y : \text{Formula} . x = y$  to force that the carrier set of **Formula** must be a singleton set, say,  $\{\star\}$ .

(The “if” direction). We follow the same idea as we prove that matching logic captures faithfully FOL (see [2]), we construct from an LFP model  $(\{M_s^{\text{LFP}}\}_{s \in S}, \Sigma^{\text{LFP}}, \Pi^{\text{LFP}})$  a corresponding matching  $\mu$ -logic  $\Gamma^{\text{LFP}}$  model, denoted  $(\{M_s^{\text{MmL}}\}_{s \in S} \cup \{M_{\text{Formula}}^{\text{MmL}}\}, \Sigma^{\text{MmL}})$  with  $M_s^{\text{MmL}} = M_s^{\text{LFP}}$ ,  $M_{\text{Formula}}^{\text{MmL}} = \{\star\}$ , and  $\Sigma^{\text{MmL}}$  consisting of symbols that are all functions. An LFP valuation  $\rho^{\text{LFP}}$  derives a corresponding matching  $\mu$ -logic valuation  $\rho^{\text{MmL}}$  such that  $\rho^{\text{MmL}}(x) = \rho^{\text{LFP}}(x)$  for all LFP (element) variables  $x$  and  $\rho^{\text{MmL}}(R) = \rho^{\text{LFP}}(R) \times \{\star\}$ . Our goal is to prove that for all LFP formulas  $\varphi$ , we have  $M^{\text{LFP}}, \rho^{\text{LFP}} \models_{\text{LFP}} \varphi$  if and only if  $|\varphi|_{M, \rho^{\text{MmL}}} = \{\star\}$ .

Firstly, notice that as shown in [2],  $|t|_{M, \rho^{\text{MmL}}} = \{\rho^{\text{LFP}}(t)\}$  for all terms  $t$ . Therefore, to simplify our notation we uniformly use  $\rho(t)$  in LFP and matching  $\mu$ -logic to mean the evaluation of  $t$ . Carry out induction on the structure of  $\varphi$ . The only additional cases (compared with FOL) are  $\varphi \equiv R(t_1, \dots, t_n)$  and  $\varphi \equiv [\text{Ifp}_{R, x_1, \dots, x_n} \psi](t_1, \dots, t_n)$ . The first case is easy, as shown in the following reasoning:  $M^{\text{LFP}}, \rho^{\text{LFP}} \models R(t_1, \dots, t_n)$  iff  $(\rho(t_1), \dots, \rho(t_n)) \in \rho^{\text{LFP}}(R)$  iff  $(\rho(t_1), \dots, \rho(t_n), \star) \in |R|_{M, \rho^{\text{MmL}}}$  iff  $|R(t_1, \dots, t_n)|_{M, \rho^{\text{MmL}}} = \{\star\}$ . The second case when  $\varphi \equiv [\text{Ifp}_{R, x_1, \dots, x_n} \psi](t_1, \dots, t_n)$  is shown as the following reasoning:

$$M^{\text{LFP}}, \rho^{\text{LFP}} \models_{\text{LFP}} [\text{Ifp}_{R, x_1, \dots, x_n} \psi](t_1, \dots, t_n)$$

$$\begin{aligned}
& \text{iff } (\rho(t_1), \dots, \rho(t_n)) \in \\
& \quad \bigcap \{ \alpha \subseteq M_{s_1}^{\text{LFP}} \times \dots \times M_{s_n}^{\text{LFP}} \mid \text{for all } a_i \in M_{s_i}^{\text{LFP}}, 1 \leq i \leq n, \\
& \quad M^{\text{LFP}}, \rho^{\text{LFP}}[\alpha/R, \vec{a}/\vec{x}] \models_{\text{LFP}} \psi \text{ implies } (a_1, \dots, a_n) \in \alpha \} \\
& \text{iff (by induction hypothesis)} \\
& \quad (\rho(t_1), \dots, \rho(t_n)) \in \\
& \quad \bigcap \{ \alpha \subseteq M_{s_1}^{\text{MmL}} \times \dots \times M_{s_n}^{\text{MmL}} \mid \text{for all } a_i \in M_{s_i}^{\text{MmL}}, 1 \leq i \leq n, \\
& \quad |\psi|_{M, (\rho[\alpha/R, \vec{a}/\vec{x}])^{\text{MmL}}} = \{\star\} \text{ implies } (a_1, \dots, a_n) \in \alpha \} \\
& \text{iff (by definition of } (\rho[\alpha/R, \vec{a}/\vec{x}])^{\text{MmL}}) \\
& \quad (\rho(t_1), \dots, \rho(t_n)) \in \\
& \quad \bigcap \{ \alpha^+ \subseteq M_{s_1}^{\text{MmL}} \times \dots \times M_{s_n}^{\text{MmL}} \times \{\star\} \mid \\
& \quad \text{for all } a_i \in M_{s_i}^{\text{MmL}}, 1 \leq i \leq n, \\
& \quad |\psi|_{M, \rho^{\text{MmL}}[\alpha^+/R, \vec{a}/\vec{x}]} = \{\star\} \text{ implies } (a_1, \dots, a_n, \star) \in \alpha^+ \} \\
& \text{iff (by reasoning about sets)} \\
& \quad (\rho(t_1), \dots, \rho(t_n)) \in \\
& \quad \bigcap \{ \alpha^+ \subseteq M_{s_1}^{\text{MmL}} \times \dots \times M_{s_n}^{\text{MmL}} \times \{\star\} \mid \\
& \quad \bigcup_{a_i \in M_{s_i}^{\text{MmL}}} (a_1, \dots, a_n, |\psi|_{M, \rho^{\text{MmL}}[\alpha^+/R, \vec{a}/\vec{x}]}) \subseteq \alpha^+ \} \\
& \text{iff (by matching } \mu\text{-logic semantics)} \\
& \quad (\rho(t_1), \dots, \rho(t_n)) \in \\
& \quad |\mu R: s_1 \otimes \dots \otimes s_n \otimes \text{Formula} . \exists x_1 \dots \exists x_n . \langle x_1, \dots, x_n, \psi \rangle|_{M, \rho^{\text{MmL}}},
\end{aligned}$$

and the last statement is equal to  $|\text{lf}_p_{R, x_1, \dots, x_n} \psi|(t_1, \dots, t_n)|_{M, \rho^{\text{MmL}}}$ .

And now we conclude that  $\Gamma^{\text{LFP}} \models \varphi$  implies  $\models_{\text{LFP}} \varphi$ . Otherwise, there exists an LFP model  $M^{\text{LFP}}$  and valuation  $\rho^{\text{LFP}}$  such that  $M^{\text{LFP}}, \rho^{\text{LFP}} \not\models_{\text{LFP}} \varphi$ , and this implies that in the  $\Gamma^{\text{LFP}}$ -model  $M^{\text{MmL}}$ , we have  $|\varphi|_{M, \rho^{\text{MmL}}} \neq \{\star\}$ , meaning that  $\Gamma^{\text{LFP}} \not\models \varphi$ .

(The “only if” part). Notice the axiom  $\forall x : \text{Formula} \forall y : \text{Formula} . x = y$  forces that  $M_{\text{Formula}} = \{\star\}$  must be a singleton set, which ensures that the above translation from an LFP model  $M^{\text{LFP}}$  to a matching  $\mu$ -logic model  $M^{\text{MmL}}$  can go *backward*. Specifically, for every matching  $\mu$ -logic function symbol  $f \in \Sigma_{s_1 \dots s_n, s}^{\text{MmL}}$ , we construct from its interpretation  $f_{M^{\text{MmL}}} : M_{s_1} \times \dots \times M_{s_n} \rightarrow \mathcal{P}(M_s)$ , the corresponding LFP function  $f_{M^{\text{LFP}}} : M_{s_1} \times \dots \times M_{s_n} \rightarrow M_s$  such that  $f_{M^{\text{MmL}}}(a_1, \dots, a_n) = \{f_{M^{\text{LFP}}}(a_1, \dots, a_n)\}$ . Similarly, for every matching  $\mu$ -logic function symbol  $\pi \in \Sigma_{s_1 \dots s_n, \text{Formula}}^{\text{MmL}}$ , we construct from its interpretation  $\pi_{M^{\text{MmL}}} : M_{s_1} \times \dots \times M_{s_n} \rightarrow$

$\{\emptyset, \{\star\}\}$ , the corresponding LFP predicate  $\pi_{M^{\text{LFP}}} \subseteq M_{s_1} \times \cdots \times M_{s_n}$ , such that  $\pi_{M^{\text{LFP}}} \subseteq M_{s_1} \times \cdots \times M_{s_n} = \{(a_1, \dots, a_n) \mid \pi_{M^{\text{MML}}}(a_1, \dots, a_n) = \{\star\}\}$ . Then we carry out the same reasoning as in the “if” part. QED.

#### 5.14.3 Proof of Theorem 5.4

*Proof.* We only need to prove that for every  $s$  and  $h$ ,  $s, h \models_{\text{SL}} p(x_1, \dots, x_n)$  iff  $h \in |p(x_1, \dots, x_n)|_{\text{Map}, \rho_s}$ , where  $\rho_s(x) = s(x)$  for all  $x$ . We conduct structural induction on  $\varphi$ . The case when  $\varphi \equiv p(\varphi_1, \dots, \varphi_n)$  where  $p$  is a recursive predicate is proved directly by the definition of the canonical model **Map**. The other cases have been proved in [2, Proposition 9.2]. QED.

#### 5.14.4 Proof of Theorem 5.15

Theorem 5.15 shows that our definition of modal  $\mu$ -logic in matching  $\mu$ -logic is faithful. We have shown a proof sketch in the main paper. We give the complete detailed proof in this subsection. The main purpose is to give an example, as the proofs of the corresponding theorems for LTL/CTL/DL have similar forms.

**Lemma 5.6.**  $\vdash_{\mu} \varphi$  implies  $\Gamma^{\mu} \vdash \varphi$ .

*Proof.* We need to prove that all modal  $\mu$ -calculus proof rules are provable in matching  $\mu$ -logic. Recall that modal  $\mu$ -calculus contains all propositional tautologies and (MODUS PONENS), plus the following four rules:

$$\begin{array}{ll} \text{(K)} & \circ(\varphi_1 \rightarrow \varphi_2) \rightarrow (\circ\varphi_1 \rightarrow \circ\varphi_2) \\ & \frac{\varphi}{\circ\varphi} \\ \text{(N)} & \circ\varphi \\ (\mu_1) & \varphi[(\mu X.\varphi)/X] \rightarrow \mu X.\varphi \\ & \frac{\varphi[\psi/X] \rightarrow \psi}{\mu X.\varphi \rightarrow \psi} \\ (\mu_2) & \mu X.\varphi \rightarrow \psi \end{array}$$

Notice that (K) and (N) are proved by Proposition 3.6, and  $(\mu_1)$  and  $(\mu_2)$  are exactly (PRE-FIXPOINT) and (KNASTER TARSKI). QED.

**Lemma 5.7.** For all  $T = (S, R)$  and all valuations  $V: AP \rightarrow \mathcal{P}(S)$ , we have  $s \in \llbracket \varphi \rrbracket_V^T$  if and only if  $s \in |\varphi|_{T,V}$ .

*Proof.* Carry out structural induction on  $\varphi$ .

(Case  $\varphi \equiv X$ ). We have  $\llbracket X \rrbracket_V^T = V(X) = |X|_{T,V}$ .

(Case  $\varphi \equiv \varphi_1 \wedge \varphi_2$ ). We have  $\llbracket \varphi_1 \wedge \varphi_2 \rrbracket_V^T = \llbracket \varphi_1 \rrbracket_V^T \cap \llbracket \varphi_2 \rrbracket_V^T = |\varphi_1|_{T,V} \wedge |\varphi_2|_{T,V} = |\varphi_1 \wedge \varphi_2|_{T,V}$ .

(Case  $\varphi \equiv \neg\varphi_1$ ). We have  $\llbracket \neg\varphi_1 \rrbracket_V^T = S \setminus \llbracket \varphi_1 \rrbracket_V^T = S \setminus |\varphi_1|_{T,V} = S \setminus (S \setminus |\neg\varphi_1|_{T,V}) = |\neg\varphi_1|_{T,V}$ .

(Case  $\varphi \equiv \circ\varphi_1$ ). By Proposition 5.14, we have  $\llbracket \circ\varphi_1 \rrbracket_V^T = \{s \in S \mid s R t \text{ implies } t \in \llbracket \varphi_1 \rrbracket_V^T \text{ for all } t \in S\} = \{s \in S \mid s \in |\circ\varphi_1|_{T,V}\} = |\circ\varphi_1|_{T,V}$ .

(Case  $\varphi \equiv \mu X . \varphi_1$ ). We have  $\llbracket \mu X . \varphi_1 \rrbracket_V^T = \bigcap \{A \subseteq S \mid \llbracket \varphi_1 \rrbracket_{V[A/X]}^T \subseteq A\} = |\mu X . \varphi_1|_{T,V}$ .  
QED.

**Corollary 5.1.**  $\Gamma^\mu \models \varphi$  implies  $\models_\mu \varphi$ .

*Proof.* Assume the opposite. Then there exist  $T = (S, R)$ ,  $\rho: AP \rightarrow \mathcal{P}(S)$ , and  $s \in S$  such that  $s \notin \llbracket \varphi \rrbracket_V^T$ . By Lemma 5.7,  $s \notin |\varphi|_{T,V}$ . Since  $T \models \Gamma^\mu$ , we have  $\Gamma^\mu \not\models \varphi$ . QED.

Now we have completed the proof of Theorem 5.15, where (2)  $\implies$  (3) is given by Lemma 5.6, and (5)  $\implies$  (6) is given by Corollary 5.1.

#### 5.14.5 Proof of Proposition 5.14

*Proof of Proposition 5.14.* We simply apply definitions. Recall that  $s \in \bullet_T(t)$  iff  $s R t$ .

(Case “ $\bullet$ ”).  $s \in |\bullet\varphi|_{T,\rho}$  iff there exists  $t \in |\varphi|_{T,\rho}$  such that  $s \in \bullet_T(t)$  iff there exists  $t$  such that  $s R t$  and  $t \in |\varphi|_{T,\rho}$ .

(Case “ $\circ$ ”).  $s \in |\circ\varphi|_{T,\rho}$  iff  $s \in |\neg\bullet\neg\varphi|_{T,\rho}$  iff  $s \notin |\bullet\neg\varphi|_{T,\rho}$  iff (use (Case “ $\bullet$ ”)) for all  $t$ ,  $t \in |\neg\varphi|_{T,\rho}$  implies  $s \notin \bullet_T(t)$  iff for all  $t$ ,  $s \in \bullet_T(t)$  implies  $t \in |\varphi|_{T,\rho}$  iff for all  $t$ ,  $s R t$  implies  $t \in |\varphi|_{T,\rho}$ .

(Case “ $\diamond$ ”). Note that  $|\diamond\varphi|_{T,\rho} = \bigcap \{A \subseteq S \mid |\varphi \vee \bullet X|_{T,\rho[A/X]} \subseteq A\} = \bigcap \{A \subseteq S \mid |\varphi|_{T,\rho} \cup \bullet_T(A) \subseteq A\}$ . On the other hand,  $\{s \in S \mid \exists t \in S \text{ such that } t \in |\varphi|_{T,\rho} \text{ and } s R^* t\} = \{s \in S \mid \exists t \in S, \exists n \geq 0 \text{ such that } t \in |\varphi|_{T,\rho} \text{ and } s R^n t\} = \{s \in S \mid \exists n \geq 0 \text{ such that } s \in \bullet_T^n(|\varphi|_{T,\rho})\} = \bigcup_{n \geq 0} \bullet_T^n(|\varphi|_{T,\rho})$ . Therefore, we just need to prove the two sets:

$$\begin{aligned} (\eta) &\equiv \bigcap \{A \subseteq S \mid |\varphi|_{T,\rho} \cup \bullet_T(A) \subseteq A\} \\ (\xi) &\equiv \bigcup_{n \geq 0} \bullet_T^n(|\varphi|_{T,\rho}) \end{aligned}$$

are equal. This can be directly proved by Knaster-Tarski theorem.

(Case “ $\square$ ”). Similar to (Case “ $\diamond$ ”).

(Case “ $\varphi_1 U \varphi_2$ ”). As in (Case “ $\diamond$ ”), we define two sets:

$$\begin{aligned} (\eta) &\equiv |\varphi_1 U \varphi_2|_{T,\rho} = \bigcap \{A \subseteq S \mid |\varphi_2|_{T,\rho} \cup (|\varphi_1 \cap \bullet_T(A)|_{T,\rho}) \subseteq A\} \\ (\xi) &\equiv \{s \in S \mid \text{exist } n \geq 0 \text{ and } t_1, \dots, t_n \in S \text{ such that} \\ &\quad s R t_1 R \dots R t_n, \text{ and } s, t_1, \dots, t_{n-1} \in |\varphi_1|_{T,\rho}, t_n \in |\varphi_2|_{T,\rho}\} \end{aligned}$$



and then use Knaster-Tarski theorem to prove them equal.

(Case “WF”). Again, we define two sets:

$$\begin{aligned}(\eta) &\equiv |\mu X . \circ X|_{T,\rho} = \bigcap \{A \subseteq S \mid (S \setminus A) \subseteq \bullet_T(S \setminus A)\} \\(\xi) &\equiv \{s \in S \mid s \text{ has no infinite path}\}\end{aligned}$$

and then use Knaster-Tarski theorem to prove them equal.

QED.

#### 5.14.6 Proof of Theorem 5.16

Let us first review some characteristic subclasses of transition systems.

**Definition 5.7.** A transition system  $T = (S, R)$  is:

- *well-founded* if for all  $s \in S$ , there is no infinite path from  $s$ ;
- *non-terminating*, if for all  $s \in S$  there is  $t \in S$  such that  $s R t$ .
- *linear*, if for all  $s \in S$  and  $t_1, t_2 \in S$  such that  $s R t_1$  and  $s R t_2$ , then  $t_1 = t_2$ .

**Lemma 5.8.**  $\vdash_{\text{infLTL}} \varphi$  implies  $\Gamma^{\text{infLTL}} \vdash \varphi$ .

*Proof.* We just need to prove that all proof rules in Fig. 2.5 can be proved in  $\Gamma^{\text{infLTL}}$ .

(TAUT) and (MP). Trivial.

(K<sub>o</sub>) and (N<sub>o</sub>). By Proposition 3.6.

(K<sub>□</sub>) and (N<sub>□</sub>). Proved by applying (KNASTER TARSKI) first, followed by simple propositional and modal logic reasoning.

(FUN, “ $\rightarrow$ ”). Proved from axiom (INF)  $\bullet \top$  and simple modal logic reasoning.

(FUN, “ $\leftarrow$ ”). Exactly axiom (LIN).

(U<sub>1</sub>). By (KNASTER TARSKI) followed by propositional reasoning.

(U<sub>2</sub>). By definition of  $\varphi_1 U \varphi_2$  as a least fixpoint and (FUN).

(IND). By (KNASTER TARSKI).

QED.

**Lemma 5.9.**  $s \models_{\text{infLTL}} \varphi$  if and only if  $s \in |\varphi|_{T,V}$ .

*Proof.* We make two interesting observations. Firstly, it suffices to prove merely the “only if” part. The “if” part follows by considering the “only if” part on  $\neg\varphi$ .

Secondly, the definition of “ $s \models_{\text{infLTL}} \varphi$ ” is an *inductive* one, meaning that “ $\models_{\text{infLTL}}$ ” is the least relation that satisfies the five conditions in Definition ???. To show that “ $s \models_{\text{infLTL}} \varphi$ ”

implies  $s \in |\varphi|_{T,V}$ , it suffices to show that  $s \in |\varphi|_{T,V}$  satisfies the same conditions. This is easily followed by Proposition 5.14.

Note how interesting that this lemma is proved by applying Knaster-Tarski theorem in the meta-level. QED.

**Corollary 5.2.**  $\Gamma^{\text{infLTL}} \models \varphi$  implies  $\models_{\text{infLTL}} \varphi$ .

*Proof.* Assume the opposite and there exists a transition system  $T = (S, R)$  that is linear and non-terminating, a valuation  $V$ , and a state  $s \in S$  such that  $s \not\models_{\text{infLTL}} \varphi$ . By Lemma 5.9,  $s \notin |\varphi|_{T,V}$ , meaning that  $T \not\models \varphi$ . Since  $T$  is non-terminating and linear, the axioms (INF) and (LIN) hold in  $T$ , and thus  $\Gamma^{\text{infLTL}} \not\models \varphi$ . Contradiction. QED.

Now we are ready to prove Theorem 5.16.

*Proof of Theorem 5.16.* Use Lemma 5.8 and Corollary 5.2, as well as the soundness of MmL proof system and the completeness of infinite-trace LTL proof system. QED.

#### 5.14.7 Proof of Theorem 5.17

**Lemma 5.10.**  $\vdash_{\text{finLTL}} \varphi$  implies  $\Gamma^{\text{finLTL}} \vdash \varphi$ .

*Proof.* We just need to prove all proof rules in Fig. 2.6 can be proved by axioms (FIN) and (LIN) in MmL. We skip the ones that have shown in Lemma 5.8.

( $\neg\circ$ ). Proved by axiom (LIN).

(COIND). Use axiom (FIN)  $\mu X . \circ X$  and to prove  $\Gamma^{\text{finLTL}} \vdash \mu X . \circ X \rightarrow \varphi$  by (KNASTER TARSKI).

(FIX). By definition of  $\varphi_1 W \varphi_2$  as a least fixpoint. QED.

**Lemma 5.11.**  $s \models_{\text{finLTL}} \varphi$  if and only if  $s \in |\varphi|_{T,V}$ .

*Proof.* As in Lemma 5.9, we just need to prove the “only if” part, by showing that  $s \in |\varphi|_{T,V}$  satisfies the five conditions in Definition ???. This is easily followed by Proposition 5.14. The case  $\varphi_1 W \varphi_2$  shall be proved by directly applying matching  $\mu$ -logic semantics. QED.

**Corollary 5.3.**  $\Gamma^{\text{finLTL}} \models \varphi$  implies  $\models_{\text{finLTL}} \varphi$ .

*Proof.* Assume the opposite and use Lemma 5.11. QED.

Now we can prove Theorem 5.17.

*Proof of Theorem 5.17.* Use Lemma 5.10 and Corollary 5.3, as well as the soundness of the matching  $\mu$ -logic proof system and the completeness of finite-trace LTL proof system. QED.

### 5.14.8 Proof of Theorem 5.18

**Lemma 5.12.**  $\vdash_{\text{CTL}} \varphi$  implies  $\Gamma^{\text{CTL}} \vdash \varphi$ .

*Proof.* We just need to prove all CTL rules from the axiom (INF) in matching  $\mu$ -logic. We skip the first 7 rules as they are simple. The rest 3 rules can be proved by applying (KNASTER TARSKI) and use properties in Properties 5.29. QED.

**Lemma 5.13.**  $s \models_{\text{CTL}} \varphi$  if and only if  $s \in |\varphi|_{T,V}$ .

*Proof.* As in Lemma 5.9, we just need to prove the “only if” part by showing that  $s \in |\varphi|_{T,V}$  satisfies all 7 conditions in Definition ???. The first 5 of them are simple. We show the last two ones about “EU” and “AU”.

(Case EU). Assume there exists a path  $s_0 s_1 \dots$  and  $k \geq 0$  such that  $s_k \in |\varphi_2|_{T,V}$  and  $s_0, \dots, s_{k-1} \in |\varphi_1|_{T,V}$ . Our goal is to show  $s_0 \in |\varphi_1 \text{ EU } \varphi_2|_{T,V}$ . By semantics of matching  $\mu$ -logic,  $|\varphi_1 \text{ EU } \varphi_2|_{T,V} = |\mu X. \varphi_2 \vee (\varphi_1 \wedge \bullet X)|_{T,V} = \bigcap \{A \subseteq S \mid |\varphi_2|_{T,V} \cup (|\varphi_1|_{T,V} \cap \bullet_T(A)) \subseteq A\}$ . Therefore, it suffices to prove that  $s_0 \in A$  for all  $A \subseteq S$  such that  $|\varphi_2|_{T,V} \subseteq A$  and  $|\varphi_1|_{T,V} \cap \bullet_T(A) \subseteq A$ . This is easy,  $s_k \in |\varphi_2|_{T,V} \subseteq A$  implies  $s_{k-1} \in \bullet_T(s_k)$ . Also,  $s_{k-1} \in |\varphi_1|_{T,V}$  by assumption. Then  $s_{k-1} \in |\varphi_1|_{T,V} \cap \bullet_T(s_k) \subseteq A$ . Repeat this procedure for  $k$  times and we obtain  $s_0 \in A$ . Done.

(Case AU). Let us denote  $\circ_T(A) = \{s \in S \mid \text{for all } t \in S \text{ such that } s R t, t \in A\}$  to be the “interpretation” of “all-path next  $\circ$ ” in  $T$ . Prove by contradiction. Assume the opposite statement that  $s_0 \notin |\varphi_1 \text{ AU } \varphi_2|_{T,V} = |\mu X. \varphi_2 \vee (\varphi_1 \wedge \circ X)|_{T,V} = \bigcap \{A \subseteq S \mid |\varphi_2|_{T,V} \cup (|\varphi_1|_{T,V} \cap \circ_T(A)) \subseteq A\}$ . This means that there exists  $A \subseteq S$  such that  $|\varphi_2|_{T,V} \subseteq A$  and  $|\varphi_1|_{T,V} \cap \circ_T(A) \subseteq A$ , and  $s_0 \notin A$ . This is going to cause contradiction. Firstly by  $|\varphi_2|_{T,V} \subseteq A$ ,  $s_0 \notin |\varphi_2|_{T,V}$ , which implies that  $s_0 \in \neg \varphi_2|_{T,V}$ . Secondly by  $|\varphi_1|_{T,V} \cap \circ_T(A) \subseteq A$ , we know that  $(S \setminus A) \subseteq \neg \varphi_1|_{T,V} \cup \bullet_T(S \setminus A)$ . Since  $s_0 \notin A$ , we know either  $s_0 \in \neg \varphi_1|_{T,V}$  or  $s_0 \in \bullet_T(S \setminus A)$ . If it is the first case, then we have a contradiction as any path starting from  $s_0$  contradicts with the condition. If it is the second case, then there exists a state, say  $s_1$ , such that  $s_0 R s_1$  and  $s_1 \notin A$ , which also implies  $s_1 \notin |\varphi_2|_{T,V}$ . Repeat this process and obtain a sequence of state  $s_0 s_1 \dots$ . If the sequence is finite, say  $s_0 s_1 \dots s_n$ , then by construction  $s_0, \dots, s_n \notin |\varphi_2|_{T,V}$  and  $s_n \in \neg \varphi_1|_{T,V}$ , which is a contradiction to the condition. If the sequence is infinite, then by construction  $s_0 s_1 \dots$  satisfies that  $s_0, s_1, \dots \notin |\varphi_2|_{T,V}$ , which also contradicts to the condition. QED.

**Corollary 5.4.**  $\Gamma^{\text{CTL}} \models \varphi$  implies  $\models_{\text{CTL}} \varphi$ .

*Proof.* Use Lemma 5.13 and prove by contradiction. Note that it is easy to verify that  $T \models \Gamma^{\text{CTL}}$  if  $T$  is non-terminating. QED.

Now we are ready to prove Theorem 5.18.

*Proof of Theorem 5.18.* Use Lemma 5.12 and Corollary 5.4, as well as soundness of MmL and completeness of CTL. QED.

#### 5.14.9 Proof of Theorem 5.19

**Lemma 5.14.**  $\vdash_{DL} \varphi$  implies  $\Gamma^{DL} \vdash \varphi$ .

*Proof.* We just need to prove that all proof rules in Fig. ?? can be proved in  $\Gamma^{DL}$ . First of all, rules (DL<sub>3</sub>) to (DL<sub>6</sub>) follow from (syntactic sugar) definitions. Rules (TAUT) and (MP) are trivial. We only prove (DL<sub>1</sub>), (DL<sub>2</sub>), (DL<sub>7</sub>), and (GEN).

Notice that  $[\alpha]\varphi$  is defined a syntactic sugar based on the structure of  $\alpha$ . Therefore, we carry out structure induction on  $\alpha$ . We should be careful to prevent circular reasoning. Our proving strategy is to prove (GEN) first, and then prove (DL<sub>1</sub>) and (DL<sub>2</sub>) simultaneously, and finally prove (DL<sub>7</sub>).

(GEN). Carry out induction on  $\alpha$ . All cases are trivial. Notice the case when  $\alpha \equiv \beta^*$  is proved by proving  $\Gamma^{DL} \vdash \varphi \rightarrow [\alpha^*]\varphi$  using (KNASTER TARSKI). After simplification, the goal becomes  $\Gamma^{DL} \vdash \varphi \rightarrow [\beta]\varphi$ . This is proved by applying induction hypothesis, which shows  $\Gamma^{DL} \vdash [\beta]\varphi$ .

(DL<sub>1</sub>) and (DL<sub>2</sub>). We prove both rules simultaneously by induction on  $\alpha$ . We discuss only interesting cases and skip the trivial ones. (DL<sub>1</sub>,  $\alpha \equiv \beta_1 ; \beta_2$ ) is proved from induction hypothesis, by applying (GEN) on  $[\beta_1]$ . (DL<sub>1</sub>,  $\alpha \equiv \beta^*$ ) is proved by applying (KNASTER TARSKI), following by applying (DL<sub>2</sub>, “ $\rightarrow$ ”) on  $[\beta]$ . (DL<sub>2</sub>,  $\alpha \equiv \beta^*$ , “ $\rightarrow$ ”) is proved by (KNASTER TARSKI). (DL<sub>2</sub>,  $\alpha \equiv \beta^*$ , “ $\leftarrow$ ”) is proved by (KNASTER TARSKI), followed by (DL<sub>2</sub>) on  $[\beta]$ .

(DL<sub>7</sub>) is proved directly by (KNASTER TARSKI), followed by (DL<sub>2</sub>, “ $\leftarrow$ ”) on  $[\alpha]$ .

QED.

We now connect the semantics of DL with the semantics of MmL. First of all, we show that the transition system  $T = (S, \{R_a\}_{a \in APgm})$  can be regarded as a  $\Sigma^{LTS}$ -model, where  $S$  is the carrier set of **State** and  $APgm$  (the set of atomic programs) is the carrier set of **Pgm**. The “one-path next  $\bullet \in \Sigma_{Pgm\ State, State}$ ” is interpreted *according to DL semantics* for all  $t \in S$  and  $a \in APgm$ :

$$\bullet_T(a, t) = \{s \in S \mid (s, t) \in R_a\}.$$

In addition, valuation  $V: AP \rightarrow \mathcal{P}(S)$  can be regarded as a matching  $\mu$ -logic valuation (where we safely ignore the valuations of element variables because they do not appear in DL syntax).

**Lemma 5.15.** Under the above notations,  $\llbracket \varphi \rrbracket_V^T = |\varphi|_{T,V}$ .

*Proof.* As in Lemma 5.9, we just need to prove that  $\llbracket \varphi \rrbracket_V^T \subseteq |\varphi|_{T,V}$  by showing that  $|\varphi|_{T,V}$  satisfies the conditions in Definition ???. The only interesting case is to show  $|\llbracket \alpha \rrbracket_V^T|_{T,V} = \{s \in S \mid \text{for all } t \in S, (s, t) \in \llbracket \alpha \rrbracket_V^T \text{ implies } t \in |\varphi|_{T,V}\}$ . We prove it by carrying out structural induction on the DL program formula  $\alpha$ . The case when  $\alpha \equiv a$  for  $a \in APgm$  is easy. The cases when  $\alpha \equiv \beta_1 ; \beta_2$ ,  $\alpha \equiv \beta_1 \cup \beta_2$ , and  $\alpha \equiv \psi?$  follows directly by basic analysis about sets and using definition of the semantics of DL program formulas. The interesting case is when  $\alpha \equiv \beta^*$ . In this case we should prove  $|\llbracket \beta^* \rrbracket_V^T|_{T,V} = |\nu X. \varphi \wedge [\beta]X|_{T,V} = \bigcup \{A \mid A \subseteq |\varphi|_{T,V} \cap |[\beta]X|_{T,V[A/X]}\} = \bigcup \{A \mid A \subseteq |\varphi|_{T,V} \cap \{s \mid \text{for all } t, (s, t) \in \llbracket \beta \rrbracket_V^T \text{ implies } t \in S\}\} \stackrel{?}{=} \{s \mid \text{for all } t, (s, t) \in \llbracket \beta^* \rrbracket_V^T \text{ implies } t \in |\varphi|_{T,V}\}$ . We denote the left-hand side of “ $\stackrel{?}{=}$ ” as  $(\eta)$  and the right-hand side as  $(\xi)$ .

To prove  $(\eta) = (\xi)$ , we prove containment from both directions.

(Case  $(\eta) \subseteq (\xi)$ ). This is proved by considering an  $s \in (\eta)$  and show  $s \in (\xi)$ . By construction of  $(\eta)$ , there exists  $A \subseteq S$  such that  $A \subseteq |\varphi|_{T,V} \cap \{s \mid \text{for all } t, (s, t) \in \llbracket \beta \rrbracket_V^T \text{ implies } t \in A\}$ , and that  $s \in A$ . In order to prove  $s \in (\xi)$ , we assume  $t \in S$  such that  $(s, t) \in (\llbracket \beta \rrbracket_V^T)^*$  and try to prove  $t \in |\varphi|_{T,V}$ . By definition, there exists  $k \geq 0$  and  $s_0, \dots, s_k$  such that  $s = s_0$ ,  $t = s_k$ , and  $(s_i, s_{i+1}) \in \llbracket \beta \rrbracket_V^T$  for all  $0 \leq i < k$ . By induction and the property of  $A$ , and that  $s_0 \in A$ , we can prove that  $s_0, s_1, \dots, s_k \in |\varphi|_{T,V}$ , and thus  $t \in |\varphi|_{T,V}$ . Done.

(Case  $(\xi) \subseteq (\eta)$ ). Notice that the set  $\eta$  is defined as a greatest fixpoint, so it suffices to show that  $(\xi)$  satisfies the condition, i.e., to prove that  $(\xi) \subseteq |\varphi|_{T,V} \cap \{s \mid \text{for all } t, (s, t) \in \llbracket \beta \rrbracket_V^T \text{ implies } t \in (\xi)\}$ . This can be easily proved by the definition of  $(\xi)$ . Done. QED.

**Corollary 5.5.**  $\Gamma^{\text{DL}} \models \varphi$  implies  $\models_{\text{DL}} \varphi$ .

*Proof.* Use Lemma 5.15, and for the sake of contradiction, assume the opposite. Suppose there exists  $T = (S, \{R_a\}_{a \in APgm})$  and a valuation  $V$  and a state  $s$  such that  $s \notin \llbracket \varphi \rrbracket_V^T$ . We then know  $s \notin |\varphi|_{T,V}$ , which implies that  $T \not\models \varphi$ . Obviously  $T \models \Gamma^{\text{DL}}$  as the theory  $\Gamma^{\text{DL}}$  contains no addition axioms. This means that  $\Gamma^{\text{DL}} \not\models \varphi$ . QED.

We are ready to prove Theorem 5.19.

*Proof of Theorem 5.19.* Use Lemma 5.14 and Corollary 5.5, as well as soundness of MmL and completeness of DL. QED.

### 5.14.10 Proof of Theorem 5.20

As a review, we have defined the following notations:

“one-path next”	$\bullet \varphi$ , where $\bullet \in \Sigma_{\text{Cfg}, \text{Cfg}}$
“all-path next”	$\circ \varphi \equiv \neg \bullet \neg \varphi$
“eventually”	$\diamond \varphi \equiv \mu X . \varphi \vee \bullet X$
“always”	$\Box \varphi \equiv \nu X . \varphi \wedge \circ X$
“well-founded”	$\text{WF} \equiv \mu X . \circ X$
“weak eventually”	$\diamond_w \varphi \equiv \nu X . \varphi \vee \bullet X$

**Proposition 5.29.** *The following propositions hold:*

1.  $\vdash \bullet \perp \leftrightarrow \perp$
2.  $\vdash \bullet(\varphi_1 \vee \varphi_2) \leftrightarrow \bullet \varphi_1 \vee \bullet \varphi_2$
3.  $\vdash \bullet(\exists x . \varphi) \leftrightarrow \exists x . \bullet \varphi$
4.  $\vdash \circ \top \leftrightarrow \top$
5.  $\vdash \circ(\varphi_1 \wedge \varphi_2) \leftrightarrow \circ \varphi_1 \wedge \circ \varphi_2$
6.  $\vdash \circ(\forall x . \varphi) \leftrightarrow \forall x . \circ \varphi$
7.  $\vdash \varphi \rightarrow \diamond \varphi$  and  $\vdash \bullet \diamond \varphi \rightarrow \diamond \varphi$
8.  $\vdash \Box \varphi \rightarrow \varphi$  and  $\vdash \Box \varphi \rightarrow \circ \Box \varphi$
9.  $\vdash \varphi \rightarrow \diamond_w \varphi$  and  $\vdash \bullet \diamond_w \varphi \rightarrow \diamond_w \varphi$
10.  $\Gamma \vdash \varphi_1 \rightarrow \varphi_2$  implies  $\Gamma \vdash \star \varphi_1 \rightarrow \star \varphi_2$  where  $\star \in \{\bullet, \circ, \diamond, \Box, \diamond_w\}$
11.  $\vdash \diamond \perp \leftrightarrow \perp$
12.  $\vdash \diamond(\varphi_1 \vee \varphi_2) \leftrightarrow \diamond \varphi_1 \vee \diamond \varphi_2$
13.  $\vdash \diamond(\exists x . \varphi) \leftrightarrow \exists x . \diamond \varphi$
14.  $\vdash \Box \top \leftrightarrow \top$
15.  $\vdash \Box(\varphi_1 \wedge \varphi_2) \leftrightarrow \Box \varphi_1 \wedge \Box \varphi_2$

16.  $\vdash \Box(\forall x. \varphi) \leftrightarrow \forall x. \Box\varphi$
17.  $\vdash \Box\varphi \leftrightarrow \neg \Diamond \neg\varphi$
18.  $\vdash \circ\varphi_1 \wedge \bullet\varphi_2 \rightarrow \bullet(\varphi_1 \wedge \varphi_2)$
19.  $\vdash \circ(\varphi_1 \rightarrow \varphi_2) \wedge \bullet\varphi_1 \rightarrow \bullet\varphi_2$
20.  $\vdash \Diamond_w\varphi \leftrightarrow (\mathbf{WF} \rightarrow \Diamond\varphi)$
21.  $\vdash \Diamond_w(\varphi_1 \vee \varphi_2) \leftrightarrow \Diamond_w\varphi_1 \vee \Diamond_w\varphi_2$
22.  $\vdash \Diamond_w(\exists x. \varphi) \leftrightarrow \exists x. \Diamond_w\varphi$
23.  $\vdash \star\star\varphi \leftrightarrow \star\varphi$  where  $\star \in \{\Diamond, \Box, \Diamond_w\}$
24.  $\vdash \mathbf{WF} \leftrightarrow \mu X. \circ^k X$  when  $k \geq 1$
25.  $\vdash \mathbf{WF} \leftrightarrow \mu X. \circ \Box X$
26.  $\vdash \Box\varphi_1 \wedge \Diamond_w\varphi_2 \rightarrow \Diamond_w(\varphi_1 \wedge \varphi_2)$
27.  $\vdash \Box(\varphi_1 \rightarrow \varphi_2) \wedge \varphi_1 \rightarrow \varphi_2$

*Proof.* We prove them in order.

(1–3) follows from (PROPAGATION), and (FRAMING).

(4–6) are proved from (1–3) and that  $\circ\varphi \equiv \neg \bullet \neg\varphi$ .

(7) is proved by (PRE-FIXPOINT) that  $\vdash \varphi \vee \bullet \Diamond\varphi \rightarrow \Diamond\varphi$ .

(8) is proved by (PRE-FIXPOINT) that  $\vdash \Box\varphi \rightarrow \varphi \wedge \bullet \Box\varphi$ .

(9) is proved by (KNASTER TARSKI) that  $\vdash \varphi \vee \bullet \Diamond_w\varphi \rightarrow \Diamond_w\varphi$ .

(10, when  $\star$  is  $\bullet$ ) is exactly (FRAMING).

(10, when  $\star$  is  $\circ$ ) is exactly Proposition 3.6.

(10, when  $\star$  is  $\Diamond$ ) requires us to prove  $\Gamma \vdash \Diamond\varphi_1 \rightarrow \Diamond\varphi_2$ . By (KNASTER TARSKI), it suffices to prove  $\Gamma \vdash \varphi_1 \vee \bullet \Diamond\varphi_2 \rightarrow \Diamond\varphi_2$ , which is proved by (7).

(10, when  $\star$  is  $\Box$ ) requires us to prove  $\Gamma \vdash \Box\varphi_1 \rightarrow \Box\varphi_2$ . By (KNASTER TARSKI), it suffices to prove  $\Gamma \vdash \Box\varphi_1 \rightarrow \varphi_1 \wedge \bullet \Box\varphi_2$ , which is proved by (8).

(10, when  $\star$  is  $\Diamond_w$ ) requires us to prove  $\Gamma \vdash \Diamond_w\varphi_1 \rightarrow \Diamond_w\varphi_2$ . By (KNASTER TARSKI), it suffices to prove  $\Gamma \vdash \Diamond_w\varphi_1 \rightarrow \varphi_1 \vee \bullet \Diamond_w\varphi_2$ , which is proved by (PRE-FIXPOINT).

(11, “ $\rightarrow$ ”) is proved by (KNASTER TARSKI).

(11, “ $\leftarrow$ ”) is trivial.

(12, “ $\rightarrow$ ”) is proved by (KNASTER TARSKI), followed by (2) to propagate “ $\bullet$ ” through “ $\vee$ ”, and finished with (7).

(12, “ $\leftarrow$ ”) is prove by (10, when  $\star$  is  $\diamond$ ).

(13, “ $\rightarrow$ ”) is proved by (KNASTER TARSKI), followed by (3) to propagate “ $\bullet$ ” through “ $\exists$ ”, and finished with (7).

(13, “ $\leftarrow$ ”) is proved by (10, when  $\star$  is  $\diamond$ ).

(14–16) are proved similar to (11–13).

(17, both directions) are proved by (KNASTER TARSKI) followed by (PRE-FIXPOINT).

(18) is proved by  $\circ\varphi \equiv \neg \bullet \neg\varphi$  and (PROPAGATION).

(19) is proved by (18) followed by (10).

(20, “ $\rightarrow$ ”) is proved by proving  $\vdash \mathbf{WF} \rightarrow (\diamond_w\varphi \rightarrow \diamond\varphi)$ , which is proved by (KNASTER TARSKI) followed by (19).

(20, “ $\leftarrow$ ”) is proved by (KNASTER TARSKI), followed by (2) to propagate “ $\bullet$ ” through “ $\vee$ ”. After some additional propositional reasoning, we obtain two proof goals:  $\vdash \diamond\varphi \rightarrow \varphi \vee \bullet \diamond\varphi$  and  $\vdash \circ\mathbf{WF} \rightarrow \mathbf{WF}$ . The former is proved by (KNASTER TARSKI) and the latter is exactly (PRE-FIXPOINT).

(21, “ $\rightarrow$ ”) is proved by applying (20) everywhere followed by (12).

(21, “ $\leftarrow$ ”) is proved by (10, when  $\star$  is  $\diamond_w$ ).

(22, “ $\rightarrow$ ”) is proved by applying (20) everywhere followed by (13).

(22, “ $\leftarrow$ ”) is proved by (10, when  $\star$  is  $\diamond_w$ ).

(23, when  $\star$  is  $\diamond$ , “ $\rightarrow$ ”) is proved by (KNASTER TARSKI) followed by (7).

(23, when  $\star$  is  $\diamond$ , “ $\leftarrow$ ”) is proved by (7) and (10).

(23, when  $\star$  is  $\square$ , “ $\rightarrow$ ”) is proved by (8) and (10).

(23, when  $\star$  is  $\square$ , “ $\leftarrow$ ”) is proved by (KNASTER TARSKI) followed by (8).

(23, when  $\star$  is  $\diamond_w$ , “ $\rightarrow$ ”) is proved by applying (KNASTER TARSKI) first. Then we need to prove  $\vdash \diamond_w \diamond_w \varphi \rightarrow \varphi \vee \bullet \diamond_w \diamond_w \varphi$ . By (PRE-FIXPOINT), we know  $\vdash \diamond_w \diamond_w \varphi \rightarrow \diamond_w \varphi \vee \bullet \diamond_w \diamond_w \varphi$ . Thus, it suffices to prove  $\vdash \diamond_w \varphi \vee \bullet \diamond_w \diamond_w \varphi \rightarrow \varphi \vee \bullet \diamond_w \diamond_w \varphi$ . By propositional reasoning, we just need to prove  $\vdash \diamond_w \varphi \rightarrow \varphi \vee \bullet \diamond_w \diamond_w \varphi$ . By (KNASTER TARSKI), we know  $\vdash \diamond_w \varphi \rightarrow \varphi \vee \bullet \diamond_w \varphi$ , so it suffices to prove  $\vdash \varphi \vee \bullet \diamond_w \varphi \rightarrow \varphi \vee \bullet \diamond_w \diamond_w \varphi$ . Again by propositional reasoning, it suffices to prove  $\vdash \bullet \diamond_w \varphi \rightarrow \varphi \vee \bullet \diamond_w \diamond_w \varphi$ , which can be proved by proving  $\vdash \bullet \diamond_w \varphi \rightarrow \bullet \diamond_w \diamond_w \varphi$ , which is finally proved by (9) and (10).

(23, when  $\star$  is  $\diamond_w$ , “ $\leftarrow$ ”) is proved by (9) and (10).

Note it is sufficient to prove (24) only for the case  $k = 1$ .

(24, “ $\rightarrow$ ”) is proved by applying (KNASTER TARSKI) and (PRE-FIXPOINT) first. Then we need to prove  $\vdash \mu X . \circ \circ X \rightarrow \circ \mu X . \circ \circ X$ . Apply (KNASTER TARSKI) again, and finished by (PRE-FIXPOINT).



(24, “ $\leftarrow$ ”) is proved by applying (KNASTER TARSKI) followed by (PRE-FIXPOINT).

(25, “ $\rightarrow$ ”) is proved by applying (KNASTER TARSKI) followed by (PRE-FIXPOINT). Then we obtain  $\vdash \mu X . \circ \Box X \rightarrow \Box \mu X . \circ \Box X$ . Apply (KNASTER TARSKI) on  $\Box$ , and we obtain  $\vdash \mu X . \circ \Box X \rightarrow \circ \Box \mu X . \circ \Box X$ , finished by (PRE-FIXPOINT).

(25, “ $\leftarrow$ ”) is proved by (8), (10), and then apply Lemma 4.4.

(26) is proved by applying (KNASTER TARSKI) firstly. After propositional reasoning, we obtain two goals:  $\vdash \Box \varphi_1 \wedge \diamond_w \varphi_2 \rightarrow \varphi_1 \vee \bullet(\Box \varphi_1 \wedge \diamond_w \varphi_2)$  and  $\vdash \Box \varphi_1 \wedge \diamond_w \varphi_2 \rightarrow \varphi_2 \vee \bullet(\Box \varphi_1 \wedge \diamond_w \varphi_2)$ . The first goal is easily proved by (8). The second goal is by unfolding “ $\diamond_w \varphi_2$ ” and “ $\Box \varphi_1$ ”, and then use (18).

(27) is proved by (8).

QED.

**Lemma 5.16.**  $A \vdash_C \varphi_1 \Rightarrow \varphi_2$  implies  $\Gamma^{\text{RL}} \vdash \text{RL2MmL}(A \vdash_C \varphi_1 \Rightarrow \varphi_2)$ .

*Proof.* We need to prove that all reachability logic proof rules in Fig. 2.12 are provable in matching  $\mu$ -logic.

(AXIOM). We prove for the case when  $C \neq \emptyset$ . The case when  $C = \emptyset$  is the same. Our goal, after translation, is  $\Gamma^{\text{RL}} \vdash \forall \Box A \wedge \forall \Box C \rightarrow (\varphi_1 \rightarrow \bullet \diamond_w \varphi_2)$ . By assumption,  $\varphi_1 \Rightarrow \varphi_2 \in A$ , and thus we just need to prove  $\Gamma^{\text{RL}} \vdash \forall (\varphi_1 \rightarrow \bullet \diamond_w \varphi_2) \rightarrow (\varphi_1 \rightarrow \bullet \diamond_w \varphi_2)$ , which is trivial by FOL reasoning.

(REFLEXIVITY). Notice that  $C = \emptyset$  in this rule. Our goal, after translation, is  $\Gamma^{\text{RL}} \vdash \forall \Box A \rightarrow (\varphi \rightarrow \diamond_w \varphi)$ , which is true by Proposition 5.29.

(TRANSITIVITY,  $C = \emptyset$ ). Our goal, after translation, is  $\Gamma^{\text{RL}} \vdash \forall \Box A \rightarrow (\varphi_1 \rightarrow \diamond_w \varphi_3)$ . Our two assumptions are  $\Gamma^{\text{RL}} \vdash \forall \Box A \rightarrow (\varphi_1 \rightarrow \diamond_w \varphi_2)$  and  $\Gamma^{\text{RL}} \vdash \forall \Box A \rightarrow (\varphi_2 \rightarrow \diamond_w \varphi_3)$ . From the latter assumption and Proposition 5.29, we have  $\Gamma^{\text{RL}} \vdash \forall \Box A \rightarrow (\diamond_w \varphi_2 \rightarrow \diamond_w \diamond_w \varphi_3)$ , and then by propositional reasoning and the former assumption we have  $\Gamma^{\text{RL}} \vdash \forall \Box A \rightarrow (\varphi_1 \rightarrow \diamond_w \diamond_w \varphi_3)$ . Finally, by Proposition 5.29 we have  $\Gamma^{\text{RL}} \vdash \forall \Box A \rightarrow (\varphi_1 \rightarrow \diamond_w \varphi_3)$ , which is what we want to prove.

(TRANSITIVITY,  $C \neq \emptyset$ ). Our goal, after translation, is  $\Gamma^{\text{RL}} \vdash \forall \Box A \wedge \forall \circ \Box C \rightarrow (\varphi_1 \rightarrow \bullet \diamond_w \varphi_3)$ . Our two assumptions are  $\Gamma^{\text{RL}} \vdash \forall \Box A \wedge \forall \circ \Box C \rightarrow (\varphi_1 \rightarrow \bullet \diamond_w \varphi_2)$  and  $\Gamma^{\text{RL}} \vdash \forall \Box A \wedge \forall \circ \Box C \rightarrow (\varphi_2 \rightarrow \diamond_w \varphi_3)$ . From the first assumption, we have  $\Gamma^{\text{RL}} \vdash \forall \Box A \wedge \forall \circ \Box C \wedge \varphi_1 \rightarrow \forall \Box A \wedge \forall \circ \Box C \wedge \bullet \diamond_w \varphi_2$ , and thus by propositional reasoning, it suffices to prove that  $\Gamma^{\text{RL}} \vdash \forall \Box A \wedge \forall \circ \Box C \wedge \bullet \diamond_w \varphi_2 \rightarrow \bullet \diamond_w \varphi_3$ . From the second assumption and Proposition 5.29(10), we know that  $\Gamma^{\text{RL}} \vdash \bullet \diamond_w (\forall \Box A \wedge \forall \circ \Box C \wedge \varphi_2) \rightarrow \bullet \diamond_w \diamond_w \varphi_3$ , which by Proposition 5.29(23), implies  $\Gamma^{\text{RL}} \vdash \bullet \diamond_w (\forall \Box A \wedge \forall \circ \Box C \wedge \varphi_2) \rightarrow \bullet \diamond_w \varphi_3$ . Then, it suffices to prove  $\Gamma^{\text{RL}} \vdash \forall \Box A \wedge \forall \circ \Box C \wedge \bullet \diamond_w \varphi_2 \rightarrow \bullet \diamond_w (\forall \Box A \wedge \forall \circ \Box C \wedge \varphi_2)$ . The rest is easy, since by Proposition 5.29(8), we just need to prove  $\Gamma^{\text{RL}} \vdash \forall \circ \Box A \wedge \forall \circ \Box C \wedge \bullet \diamond_w \varphi_2 \rightarrow \bullet \diamond_w (\forall \Box A \wedge \forall \circ \Box C \wedge \varphi_2)$ , which then by Proposition 5.29(18) becomes  $\Gamma^{\text{RL}} \vdash \bullet (\forall \Box A \wedge \forall \circ \Box C \wedge \diamond_w \varphi_2) \rightarrow \bullet \diamond_w (\forall \Box A \wedge \forall \circ \Box C \wedge \varphi_2)$ , and

then by Proposition 5.29(10) becomes  $\Gamma^{\text{RL}} \vdash \forall \Box A \wedge \forall \Box C \wedge \diamond_w \varphi_2 \rightarrow \diamond_w (\forall \Box A \wedge \forall \Box C \wedge \varphi_2)$ , which is proved by Proposition 5.29(26).

(LOGIC FRAMING). We prove for the case when  $C \neq \emptyset$ . The case when  $C = \emptyset$  is the same. Our goal, after translation, is  $\Gamma^{\text{RL}} \vdash \forall \Box A \wedge \forall \circ \Box C \rightarrow (\varphi_1 \wedge \psi \rightarrow \bullet \diamond_w (\varphi_2 \wedge \psi))$ . Our assumption is  $\Gamma^{\text{RL}} \vdash \forall \Box A \wedge \forall \circ \Box C \rightarrow (\varphi_1 \rightarrow \bullet \diamond_w \varphi_2)$ . Notice that FOL formula  $\psi$  is a predicate pattern, so  $\vdash \bullet \diamond_w (\varphi_2 \wedge \psi) \leftrightarrow (\bullet \diamond_w \varphi_2) \wedge \psi$ , and the rest is by propositional reasoning. The condition that  $\psi$  is a FOL formula (and thus a predicate pattern) is crucial to propagate  $\psi$  throughout its context.

(CONSEQUENCE). This is the only rule where axioms in  $\Gamma^{\text{RL}}$  is actually used. Again, we prove for the case  $C \neq \emptyset$  as the case when  $C = \emptyset$  is the same. Our goal, after translation, is  $\Gamma^{\text{RL}} \vdash \forall \Box A \wedge \forall \circ \Box C \rightarrow (\varphi_1 \rightarrow \bullet \diamond_w \varphi_2)$ . Our three assumptions include  $M^{\text{Cfg}} \models \varphi_1 \rightarrow \varphi'_1$ ,  $M^{\text{Cfg}} \models \varphi'_2 \rightarrow \varphi_2$ , and  $\Gamma^{\text{RL}} \vdash \forall \Box A \wedge \forall \circ \Box C \rightarrow (\varphi'_1 \rightarrow \bullet \diamond_w \varphi'_2)$ . Notice that by definition of  $\Gamma^{\text{RL}}$ , we know immediately that  $\varphi_1 \rightarrow \varphi'_1 \in \Gamma^{\text{RL}}$  and  $\varphi'_2 \rightarrow \varphi_2 \in \Gamma^{\text{RL}}$ . The rest of the proof is simply by Proposition 5.29(10) and some propositional reasoning.

(CASE ANALYSIS). Simply by some propositional reasoning.

(ABSTRACTION). Simply by some FOL reasoning. Notice that  $\forall \Box A$  and  $\forall \Box C$  are closed patterns.

(CIRCULARITY). We prove for the case when  $C \neq \emptyset$ , as the case when  $C = \emptyset$  is the same. Our goal, after translation, is  $\Gamma^{\text{RL}} \vdash \forall \Box A \wedge \forall \circ \Box C \rightarrow (\varphi_1 \rightarrow \bullet \diamond_w \varphi_2)$ . By FOL reasoning and Proposition 5.29(20,2,25), the goal becomes  $\Gamma^{\text{RL}} \vdash \mu X. \circ \Box X \rightarrow \forall \Box A \wedge \forall \circ \Box C \rightarrow \forall (\varphi_1 \rightarrow \bullet \diamond_w \varphi_2)$ . By (KNASTER TARSKI) and some FOL reasoning, it suffices to prove  $\Gamma^{\text{RL}} \vdash \circ \Box (\forall \Box A \wedge \forall \circ \Box C \rightarrow \forall (\varphi_1 \rightarrow \bullet \diamond_w \varphi_2)) \wedge \forall \Box A \wedge \forall \circ \Box C \rightarrow (\varphi_1 \rightarrow \bullet \diamond_w \varphi_2)$ . Our assumption, after translation, is  $\Gamma^{\text{RL}} \vdash \forall \Box A \wedge \forall \circ \Box C \wedge \forall \circ (\varphi_1 \rightarrow \bullet \diamond_w \varphi_2) \rightarrow (\varphi_1 \rightarrow \bullet \diamond_w \varphi_2)$ , so it suffices to prove  $\Gamma^{\text{RL}} \vdash \circ \Box (\forall \Box A \wedge \forall \circ \Box C \rightarrow \forall (\varphi_1 \rightarrow \bullet \diamond_w \varphi_2)) \wedge \forall \Box A \wedge \forall \circ \Box C \rightarrow \forall \Box A \wedge \forall \circ \Box C \wedge \forall \circ (\varphi_1 \rightarrow \bullet \diamond_w \varphi_2)$ , which by some propositional reasoning becomes  $\Gamma^{\text{RL}} \vdash \circ \Box (\forall \Box A \wedge \forall \circ \Box C \rightarrow \forall (\varphi_1 \rightarrow \bullet \diamond_w \varphi_2)) \wedge \forall \Box A \wedge \forall \circ \Box C \rightarrow \forall \circ (\varphi_1 \rightarrow \bullet \diamond_w \varphi_2)$ . By Proposition 5.29(8), it becomes  $\Gamma^{\text{RL}} \vdash \circ \Box (\forall \Box A \wedge \forall \circ \Box C \rightarrow \forall (\varphi_1 \rightarrow \bullet \diamond_w \varphi_2)) \wedge \circ \forall \Box A \wedge \circ \forall \circ \Box C \rightarrow \forall \circ (\varphi_1 \rightarrow \bullet \diamond_w \varphi_2)$ , and by Proposition 5.29(5,6,10), it becomes  $\Gamma^{\text{RL}} \vdash \Box (\forall \Box A \wedge \forall \circ \Box C \rightarrow \forall (\varphi_1 \rightarrow \bullet \diamond_w \varphi_2)) \wedge \forall \Box A \wedge \forall \circ \Box C \rightarrow \forall (\varphi_1 \rightarrow \bullet \diamond_w \varphi_2)$ , which is proved by Proposition 5.29(27). QED.

**Corollary 5.6.**  $S \vdash_{\emptyset} \varphi_1 \Rightarrow \varphi_2$  implies  $\Gamma^{\text{RL}} \vdash \text{RL2MmL}(S \vdash_{\emptyset} \varphi_1 \Rightarrow \varphi_2)$ .

*Proof.* Let  $A = S$  and  $C = \emptyset$  in Lemma 5.16. QED.

**Lemma 5.17.**  $\Gamma^{\text{RL}} \models \text{RL2MmL}(S \vdash_{\emptyset} \varphi_1 \Rightarrow \varphi_2)$  implies  $S \models_{\text{RL}} \varphi_1 \Rightarrow \varphi_2$ .

*Proof.* Let  $T = (M_{\text{Cfg}}^{\text{Cfg}}, R)$  be the transition system that is yielded by  $S$ . We tactically use the same letter  $T$  to mean the extended  $\Sigma^{\text{RL}}$ -model  $M^{\text{Cfg}}$  with  $\bullet \in \Sigma_{\text{Cfg}, \text{Cfg}}$  be interested

as the transition relation  $R$ . Then  $T \models \Gamma^{\text{RL}}$ , because all axioms in  $\Gamma^{\text{RL}}$  are about only the configuration model  $M^{\text{Cfg}}$  and says nothing about the transition relation  $R$ . Since  $M^{\text{Cfg}} \models \Gamma^{\text{Cfg}}$  (by definition), then  $T \models \Gamma^{\text{Cfg}}$ . By condition of the lemma,  $T \models \text{RL2MmL}(S \vdash_{\emptyset} \varphi_1 \Rightarrow \varphi_2)$ , i.e.,  $T \models \forall \Box S \rightarrow \varphi_1 \rightarrow \Diamond_w \varphi_2$ . By construction of  $T$ , for all rules  $\psi_1 \Rightarrow \psi_2 \in S$ , we have  $T \models \psi_1 \rightarrow \bullet \psi_2$  (in MmL), which implies  $T \models \forall \Box (\psi_1 \rightarrow \Diamond_w \psi_2)$ , meaning that  $T \models \forall \Box S$ . Therefore,  $T \models \varphi_1 \rightarrow \Diamond_w \varphi_2$  (in MmL), which is exactly the same meaning as  $T \models_{\text{RL}} \varphi_1 \Rightarrow \varphi_2$  (in RL). QED.

Finally, we are ready to prove Theorem 5.20.

*Proof of Theorem 5.20.* Following the same roadmap as in the proof of Theorem 5.15, where (2)  $\Rightarrow$  (3) is given by Corollary 5.6 and (5)  $\Rightarrow$  (6) is given by Lemma 5.17. The rest is by the sound and (relative) completeness of RL. Notice that technical assumptions of [12] are needed for the completeness result of RL. QED.

## Chapter 6: REASONING ABOUT FIXPOINTS IN MATCHING $\mu$ -LOGIC

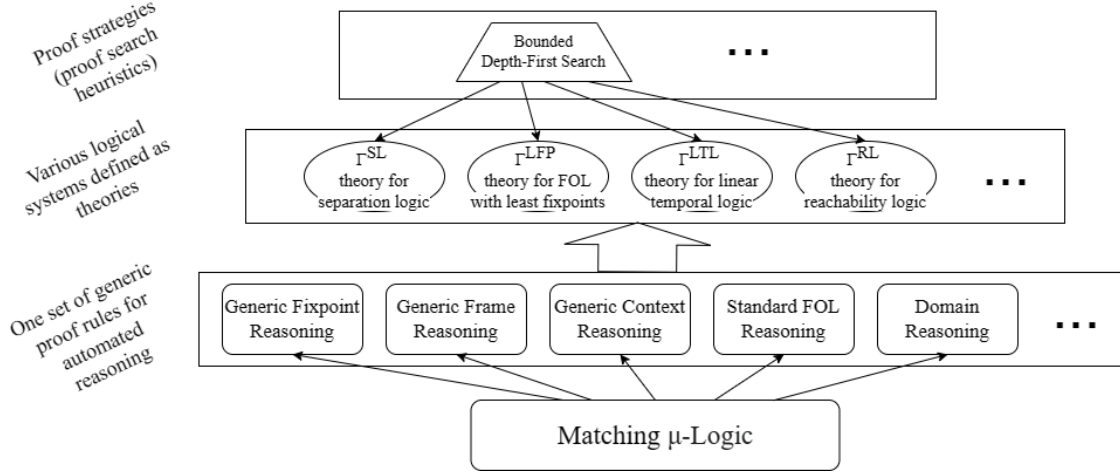
Automation of fixpoint reasoning has been extensively studied for various mathematical structures, logical formalisms, and computational domains, resulting in specialized fixpoint provers and proof techniques for heaps [26, 88, 89, 90, 91, 92], for streams [93], for term algebras [45], for temporal properties [94], for program reachability correctness [12], and for many other systems and inductive/coinductive properties. However, in spite of great theoretical and practical interest, there is no unifying framework for automated fixpoint reasoning.

Using matching  $\mu$ -logic, we envision a unifying automated proof framework for fixpoint reasoning, as shown in Figure 6.1. Proofs are done using a fixed set of proof rules that accomplish fixpoint reasoning, in addition to standard FOL reasoning, domain reasoning, frame reasoning, context reasoning, etc, for matching  $\mu$ -logic, independently of the underlying theory. This way, automated reasoning becomes proof search over the fixed set of proof rules, taking as input a logical theory  $\Gamma^L$  that defines/encodes a certain logical system or programming language  $L$  in matching  $\mu$ -logic. For efficiency, the framework implements various proof strategies as heuristics that guide the proof search, each strategy optimizing formal reasoning within a subset of logical theories.

We will present a prototype implementation of a unified proof framework for automating fixpoint reasoning based on matching  $\mu$ -logic. We have seen in Chapter 5 that matching  $\mu$ -logic has good expressive power and can serve as a foundation for a variety of logical systems, including LFP, modal logic, modal  $\mu$ -calculus, temporal logics (LTL, CTL, etc.), and separation logic. In addition, matching  $\mu$ -logic patterns admit compact syntax and convenient notations, which allow us to encode formulas in other logical systems almost verbatim.

Our unifying proof framework consists of three main reasoning modules: fixpoint, frame, and context (also illustrated in Fig. 6.1). The fixpoint reasoning module is the main one; the other two are to help fixpoint reasoning work properly. Note that these three modules are generic, that is, they work with all theories. Therefore, they accomplish fixpoint reasoning, frame reasoning, and context reasoning for *all* logical systems defined as theories in matching  $\mu$ -logic.

The main challenge behind developing such a unifying proof framework is that the Hilbert-style proof system  $\mathcal{H}_\mu$  of matching  $\mu$ -logic in Figure 4.1 is too fine-grained to be amenable for automation. For example, consider (MODUS PONENS), which says that “ $\vdash \varphi \rightarrow \psi$  and  $\vdash \varphi$  implies  $\vdash \psi$ ”. (MODUS PONENS) requires the prover to guess a premise  $\varphi$ , which does not

Figure 6.1: Unifying Proof Framework Based on Matching  $\mu$ -Logic

code well with automation. When it comes to fixpoint reasoning, the (KNASTER TARSKI) proof rule (also called Park induction [95]) for fixpoint reasoning

$$(\text{KNASTER TARSKI}) \quad \frac{\varphi[\psi/X] \rightarrow \psi}{(\mu X . \varphi) \rightarrow \psi}$$

is limited to handling the cases where the left-hand side of the proof goal is a standalone least fixpoint. It cannot be directly applied to proof goals in LFP or SL, such as  $ll(x, y) * list(y) \rightarrow list(x)$  (see Section 6.1.1), where the left-hand side  $C[ll(x, y)]$  contains the fixpoint  $ll(x, y)$  within a context  $C[\Box] \equiv \Box * list(y)$ . An indirect application is possible *in theory*, but it involves sophisticated, ad-hoc reasoning to eliminate the context  $C$  from the left-hand side, which cannot be efficiently automated.

Our fixpoint module addresses the above challenge by proposing a context-driven fixpoint proof rule, (KT), shown in Fig. 6.2. (KT) is a sequential composition of several proof rules that first (WRAP) context  $C$  within the right-hand side  $\psi$ , written  $C \multimap \psi$ , and eliminate it from the left-hand side, then apply inductive reasoning, and finally (UNWRAP)  $C$  and restore it on the left-hand side. The pattern  $C \multimap \psi$ , called *contextual implication*, is expressible in matching  $\mu$ -logic and intuitively defines all the elements which in context  $C$  satisfy  $\psi$ . The fixpoint module therefore makes contexts explicitly occur as conditions in proof goals. Sometimes the context conditions are needed to discharge a proof goal, other times not. The frame and context reasoning modules help to eliminate contexts from proof goals. Specifically, frame reasoning is used when the context is unnecessary: it reduces  $\vdash C[\varphi] \rightarrow C[\psi]$  to  $\vdash \varphi \rightarrow \psi$ . On the other hand, context reasoning is used when the context is needed in order to discharge the proof goal, by allowing us to derive  $\vdash C[C \multimap \psi] \rightarrow \psi$ . We shall discuss and analyze the frame and context reasoning in detail in Section 6.1.

We have not implemented any smart proof strategies or proof search heuristics, but only a naive bounded depth-first search (DFS) algorithm. Our evaluation on the SL-COMP benchmark shows that the naive bounded-DFS strategy can prove 90% of the properties without frame reasoning, and 95% with frame reasoning (Section 6.4). This was surprising, because it would place our generic proof framework in the third place in the SL-COMP competition, among dozens of specialized provers developed specifically for SL and heap reasoning. However, the remaining 5% properties appear to require complex, SL-specific reasoning, which is clearly beyond the ability of our generic framework. We have also considered only a small number of LFP and LTL proofs, which could all be done using the same simplistic bounded-DFS strategy; more powerful proof strategies will certainly be needed for more complex proofs and will be developed as part of future work.

To evaluate our unified proof framework and prototype implementation, we consider four representative logical systems for fixpoint reasoning: FOL with least fixpoints (LFP), separation logic (SL), linear temporal logic (LTL), and reachability logic (RL), all of which have been introduced in Chapters 2 and 5. We pick these four logics for their representativeness. LFP is the canonical logic for fixpoint reasoning in the first-order domain. SL is the representative logic for reasoning about data-manipulating programs with pointers. LTL is the temporal logic of choice for model checkers of infinite-trace systems, e.g., SPIN [94]. RL is a language-parametric generalization of Hoare logic where the programming language semantics is given as an input theory and partial correctness is specified and proved as a reachability rule  $\varphi_{pre} \Rightarrow \psi_{post}$ . These four logics therefore represent relevant instances of fixpoint reasoning across different and important domains. We believe that they form a good benchmark for evaluating a unified proof framework for fixpoint reasoning, so we set ourselves the long-term goal to support *all* of them. We will give special emphases to SL in this this, however, because it gathered much attention in recent years that resulted in several automated SL provers and its own international competition SL-COMP [96].

It would be unreasonable to hope at such an incipient stage that a generic automated prover can be superior to the state-of-the-art domain-specific provers and algorithmic decision procedures for all four logics, on all existing challenging benchmarks in their respective domains. Therefore, for each of the domains, we set ourselves a limited objective. For SL, the goal was to prove all the 280 benchmark properties collected by SL-COMP in the problem set `qf_shid_ent1` dedicated to inductive reasoning. For LTL, the goal was to prove the axioms about the modal operators “always”  $\Box\varphi$  and “until”  $\varphi_1 U \varphi_2$  in its complete proof system. For LFP and RL, our goal was to verify a simple imperative program `sum` that computes the total of 1 to input  $n$  using both the LFP and RL encodings, and show that it returns the correct sum  $n(n+1)/2$  on termination. We report what we have done in pushing towards

the above goals, and discuss the difficulties that we met, and the lessons we learned.

## 6.1 AUTOMATED PROOF FRAMEWORK FOR MATCHING $\mu$ -LOGIC

We will propose a new set of higher-level proof rules (as shown in Fig. 6.2) that aim at proof automation. The generic matching  $\mu$ -logic prover simply runs a simple bounded DFS algorithm over the higher-level proof rules. We will give an overview of the three key reasoning modules offered by the automated proof rules in Section 6.1.1 and then explain all proof rules in detail in Section 6.1.2. In Section 6.2, we use several examples to show how our proof framework can be applied to various logical theories.

### 6.1.1 Three reasoning modules

Our proof framework consists of three main reasoning modules: a fixpoint reasoning module, a context reasoning module, and a frame reasoning module. Recall that we write  $\Gamma \vdash \varphi$  to mean that  $\varphi$  can be proved from  $\Gamma$ . We write  $\vdash \varphi$  when  $\Gamma$  is not important.

**Fixpoint reasoning module and the core fixpoint rule (LFP)** As discussed above, the existing (KNASTER TARSKI) rule has several limitations due to its general nature, making it impractical for automation. Therefore, we consider two specialized proof rules, (LFP) and (GFP), explained below. Let  $p$  be a recursive symbol defined by

$$p(\tilde{x}) =_{\text{ifp}} \exists \tilde{x}_1 . \varphi_1(\tilde{x}, \tilde{x}_1) \vee \cdots \vee \exists \tilde{x}_m . \varphi_m(\tilde{x}, \tilde{x}_m)$$

where  $\tilde{x}, \tilde{x}_1, \dots, \tilde{x}_m$  are variable vectors. To prove  $\vdash p(\tilde{x}) \rightarrow \psi$  for some property  $\psi$ , the proof rule (LFP) firstly unfolds  $p(\tilde{x})$  according to its definition, and secondly *replaces* each recursive occurrence  $p(\tilde{y})$  (whose arguments  $\tilde{y}$  might be different from the original arguments  $\tilde{x}$ ) in  $\varphi_i$  by  $\psi[\tilde{y}/\tilde{x}]$ , i.e., the result of substituting in  $\psi$  the new arguments  $\tilde{y}$  for the original arguments  $\tilde{x}$ . Let us denote the result of substituting each  $\varphi_i$  as  $\varphi_i[\psi/p]$ . In summary, (LFP) is the following rule (also shown in Fig. 6.2):

$$(\text{LFP}) \quad \frac{\exists \tilde{x}_1 . \varphi_1[\psi/p] \rightarrow \psi \quad \cdots \quad \exists \tilde{x}_m . \varphi_m[\psi/p] \rightarrow \psi}{p(\tilde{x}) \rightarrow \psi} \quad (6.1)$$

Note that (LFP) generates  $m$  new sub-goals (above the bar), each corresponding to one case in the definition of  $p$ . All sub-goals have the same, original property  $\psi$  on the right-hand side. Intuitively, (LFP) is a logical incarnation of the *induction principle* that consists of case

analysis (according to the definition of  $p$ ) and inductive hypotheses (i.e., replacing  $p$  by the intended property  $\psi$  on the left-hand side).

**Context reasoning module and contextual implication** Although (LFP) is more syntax-driven than the original (KNASTER TARSKI) rule, it still has limitations. We illustrate them using a simple example in SL

$$\vdash ll(x, y) * list(y) \rightarrow list(x) \quad (6.2)$$

where  $ll$  and  $list$  are defined as recursive predicates:

$$\begin{aligned} ll(x, y) &=_{\text{ifp}} \text{emp} \wedge x = y \vee \exists z. x \mapsto z * ll(z, y) \\ list(x) &=_{\text{ifp}} \text{emp} \wedge x = 0 \vee \exists z. x \mapsto z * list(z) \end{aligned}$$

Intuitively,  $ll(x, y)$  states that there is a singly-linked list from  $x$  to  $y$  and  $list(x)$  equals to  $ll(x, 0)$ . Clearly, (LFP) cannot be applied directly to (6.2), because the left-hand side is not a recursive symbol, but a larger pattern  $ll(x, y) * list(y)$  in which the recursive pattern  $ll(x, y)$  occurs. In other words,  $ll(x, y)$  occurs *within a context* in the left-hand side. Let  $C[\Box] \equiv \Box * list(y)$  be the *context pattern* where  $\Box$  is a distinguished hole variable. We rewrite proof goal (6.2) to the following form using context  $C$ :

$$\vdash C[ll(x, y)] \rightarrow list(x) \quad (6.3)$$

Introducing contexts allows us to examine the limitations of rule (LFP) from a more structural point of view. Clearly, (LFP) can only be applied when  $C$  is the *identity context*, i.e.,  $C_{id}[\Box] \equiv \Box$ , but as we have seen above, in practice recursive patterns often occur within a non-identity context, so a major challenge in applying (LFP) in automated fixpoint reasoning is to handle such non-identity contexts in a systematic way.

**Contextual implications** To solve the above challenge, we propose an important concept called *contextual implication*. Recall that a *context*  $C$  is a pattern with a distinguished variable denoted  $h$ , called the hole variable. Note that we do not use the standard notation  $\Box$  to denote the hole variable, to not confuse it with the “always” operator  $\Box\varphi$  in LTL. We write  $C[\varphi]$  as the substitution  $C[\varphi/h]$ . We say that  $C$  is a *structure context* if  $C \equiv t \wedge \psi$  where  $t$  is an application context (Definition 3.1) and  $\psi$  is a predicate (i.e., patterns equivalent to  $\top$  or  $\perp$ ). For example,  $h * list(y) \wedge y > 1$  is a structure context (w.r.t.  $h$ ) because separating conjunction  $*$  is a symbol in matching  $\mu$ -logic (see Section 5.3). All contexts discussed here



are structure contexts.

A structure context  $C$  is *extensive* in the hole position, in the following sense. An element  $a$  matches  $C[\varphi]$  where  $C$  is a structure pattern and  $\varphi$  is any pattern plugged into the hole, if and only if there exists an element  $a_0$  that matches  $\varphi$  such that  $a$  equals  $C[a_0]$ . In other words, matching the entire structure  $C[\varphi]$  can be reduced to matching the local structure  $\varphi$  and the local reasoning we make about  $\varphi$  at the hole position can be lifted to the entire structure  $C[\varphi]$ . Therefore, structure contexts allows us to do contextual reasoning.

Let  $C[\square]$  be a structure context and  $\psi$  be a pattern. We define *contextual implication* w.r.t.  $C$  and  $\psi$  as the pattern whose matching those elements that satisfy  $\psi$  if plugged into  $C$ .

**Definition 6.1.** We define *contextual implication*  $C \multimap \psi \equiv \exists h. h \wedge (C[h] \subseteq \psi)$ .

Recall that in matching  $\mu$ -logic,  $\exists$  means set union. Thus,  $C \multimap \psi$  is the pattern matched by all  $h$  such that  $C[h] \subseteq \psi$  holds, i.e., when plugged in  $C$ , the result  $C[h]$  satisfies property  $\psi$ . The following is a useful result about contextual implications for structure contexts  $C$ :

$$\vdash C[\varphi] \rightarrow \psi \quad \xLeftrightarrow[(\text{UNWRAP}) \text{ context } C]{(\text{WRAP}) \text{ context } C} \quad \vdash \varphi \rightarrow (C \multimap \psi)$$

Note that  $C \multimap \psi$  is a regular pattern defined using the syntax of matching  $\mu$ -logic. It is *not an extension of matching  $\mu$ -logic*, but simply a convenient use of the existing expressiveness of patterns to simplify (and automate) formal reasoning by “pulling the target out of its context”.

Now, we revisit the SL example and look at proof goal (6.3). By wrapping the structure context  $C[\square] = h * \text{list}(y)$ , we transform it to the following equivalent goal, to which (LFP) can be applied:

$$\vdash ll(x, y) \rightarrow (C \multimap \text{list}(x)) \quad \text{where } C[\square] = h * \text{list}(y)$$

This way, contextual implication helps address the limitations of (LFP) by offering a *systematic and general* method to wrap/unwrap any contexts, making proof automation based on (LFP) possible.

We conclude the discussion on contextual implication with two remarks. Firstly, after context  $C$  is wrapped, the right-hand side becomes  $C \multimap \psi$ , which by (LFP) will be moved back to left-hand side and replace the recursive occurrences of the recursive pattern (see Equation (6.1), where  $\psi$  becomes  $C \multimap \psi$ ). Therefore, we need a set of proof rules to handle and *match* those contextual implications that occur on the left-hand side using pattern matching. This is explained in detail in Section 6.1.2.

The second remark is that our contextual implication generalizes separating implication  $\varphi \multimap \psi$  (the “magic wand”) in SL. Indeed, let context  $C_\varphi[\Box] = \Box * \varphi$ , then we have  $\varphi \multimap \psi = C_\varphi \multimap \psi$ . In other words, SL magic wand is a special instance of contextual implication, where the underlying theory is  $\Gamma^{\text{SL}}$  and context  $C[\Box]$  has the specific form  $\Box * \varphi$  where  $h$  occurs immediately below the top-level  $*$  operator, and the SL proof rule (ADJ) [25, pp. 5],  $\vdash \varphi_1 * \varphi \rightarrow \psi$  iff  $\vdash \varphi_1 \rightarrow (\varphi \multimap \psi)$ , is also a special instance of (WRAP) and (UNWRAP). However, contextual implications are more general, because they can be applied to any ML theories and any complex contexts  $C[\Box]$ , e.g., to entire program configurations (see Section 2.14) not only heaps.

**Frame reasoning within any contexts** Another advantage of having an explicit notion of context as shown above, is that *frame reasoning* can be generalized to all structure contexts  $C$ . In the following, we compare the frame reasoning in separation logic for heap contexts (left, also called (MONOTONE) in [25]) and the general frame reasoning in matching  $\mu$ -logic for any contexts  $C$  (right):

$$\text{(FRAME) rule in SL} \quad \frac{\varphi \rightarrow \psi}{\varphi * \varphi_{\text{rest}} \rightarrow \psi * \varphi_{\text{rest}}} \quad \text{Our (FRAME) rule} \quad \frac{\varphi \rightarrow \psi}{C[\varphi] \rightarrow C[\psi]}$$

Clearly, the SL (FRAME) rule is a special instance of our (FRAME) rule, where  $C[\Box] \equiv \Box * \varphi_{\text{rest}}$ . Our (FRAME) rule is more general and can be applied to any theories and complex contexts.

We conclude the discussion on frame reasoning with a remark about framing for Hoare-style program correctness using SL as an assertion logic, which has the following form:

$$\text{(FRAME ON PROGRAMS)} \quad \frac{\varphi \{ \text{code} \} \psi}{\varphi * \varphi_{\text{rest}} \{ \text{code} \} \psi * \varphi_{\text{rest}}} \quad \text{if } \text{code} \text{ does not modify } V_{\text{rest}}$$

where  $V_{\text{rest}} = \text{free Var}(\varphi_{\text{rest}})$ . If we instantiate **code** by the idle program **skip**, then (FRAME) in SL becomes an instance of (FRAME ON PROGRAMS). While (FRAME ON PROGRAMS) is certainly convenient in practice, we would like to point out that it is *language-specific* and generally *unsound*. Indeed, the rule and its side condition itself suggest that the language has a heap and **code** can modify pointers, which may not be the case for some functional, logic, or domain specific languages. Also, if the language has a construct **get\_memory()** that returns the total memory size, which we can find in most real languages, and **code** requires exactly say 8GB of memory space as specified by  $\psi$ , then  $\varphi * \varphi_{\text{rest}} \{ \text{code} \} \psi * \varphi_{\text{rest}}$  does not hold for any nonempty  $\varphi_{\text{rest}}$ , so the rule is unsound. In other words, the (FRAME ON PROGRAMS) proof rule is a privilege of certain toy programming languages, or abstractions

of real languages, whose soundness must be established for each language on a case by case basis. In contrast, (FRAME) in matching  $\mu$ -logic is universally sound for all logical theories and thus programming languages whose semantics are defined as matching  $\mu$ -logic theories. If one's particular language allows a proof rule like (FRAME ON PROGRAMS), then one can prove it as a separate lemma and then use it in proofs.

### 6.1.2 Framework description

Here we discuss our automated proof rules in Fig. 6.2, where the (KT) rule is a composition of (WRAP), (LFP), and (UNWRAP) as shown in Fig. 6.2. The generic proof framework is *parametric* in a theory  $\Gamma$ , and it proves implications, i.e.,  $\Gamma \vdash \varphi \rightarrow \psi$ .

A *proof rule* consists of several *premises* written above the bar and a *conclusion* written below the bar. Our prover takes the proposed proof rules and axioms in theory  $\Gamma$  and reduces the (given) proof goal by applying the rules *backward*, from conclusion to premises. New sub-goals will be generated during the proof. When all sub-goals are discharged, the prover stops with success. Therefore, our prover is essentially a simple *search algorithm* over the set of proof rules.

Before explaining the proof rules, we define some terminology. A *structure pattern* is a pattern built only from variables and symbols. A *conjunctive (resp. disjunctive) pattern* is a pattern of the form  $\varphi_1 \wedge \dots \wedge \varphi_n$  (resp.  $\varphi_1 \vee \dots \vee \varphi_n$ ), where  $\varphi_1, \dots, \varphi_n$  are structure patterns. In Fig. 6.2, we assume  $p$  is a recursive symbol defined by  $p(\tilde{x}) =_{\text{IFP}} \bigvee_i \varphi_i$  where each  $\varphi_i$  denotes one definition case.

(ELIM- $\exists$ ) is a standard FOL rule that simplifies the left-hand side by removing existential variables. Note that the side condition  $x \notin \text{freeVar}(\psi)$  is necessary for the soundness of the rule, but it can be easily satisfied by renaming the bound variables to some fresh ones. Therefore, by applying (ELIM- $\exists$ ) exhaustively, we can obtain a left-hand side that is quantifier-free at the top.

(SMT) does domain reasoning using SMT solvers such as Z3 [97] and CVC4 [98], where recursive symbols are treated as uninterpreted functions. Note that (SMT) is the only proof rule that *finishes* the proof, so it is always tried first. In practice, goals that can be proved by (SMT) are those about the common mathematical domains such as natural and integer numbers, using the underlying theory  $\Gamma$ . We write  $\models_{\text{SMT}} \varphi \rightarrow \psi$  to mean that  $\varphi \rightarrow \psi$  is proved by SMT solvers.

(PM) uses the pattern matching algorithm,  $\text{pm}$ , to instantiate the quantified variable(s)  $\tilde{y}$  on the right-hand side. The algorithm  $\text{pm}$  will be discussed in Section 6.3. The algorithm returns a match result as a substitution  $\theta$ , which tells us how to instantiate the variables  $\tilde{y}$ . If

---

(ELIM- $\exists$ )	$\frac{\varphi \rightarrow \psi}{(\exists x . \varphi) \rightarrow \psi} \text{ if } x \notin \text{free Var}(\psi)$
(SMT)	$\varphi \rightarrow \psi \quad \text{if } \models_{\text{SMT}} \varphi \rightarrow \psi$
(MATCH-CTX)	$\frac{C_{rest}[\varphi'\theta] \rightarrow \psi}{C_o[\forall \tilde{y} . (C' \multimap \varphi')] \rightarrow \psi} \text{ where } (C_{rest}, \theta) = \text{cm}(C_o, C', \tilde{y})$
(PM)	$\frac{\varphi \rightarrow \psi\theta}{\varphi \rightarrow \exists \tilde{y} . \psi} \text{ where } \theta \in \text{pm}(\varphi, \psi, \tilde{y}) \text{ matches } \varphi \text{ with } \psi$
(FRAME)	$\frac{\varphi \rightarrow \psi}{C[\varphi] \rightarrow C[\psi]}$
(UNFOLD-R)	$\frac{\varphi \rightarrow C[\varphi_i]}{\varphi \rightarrow C[p(\tilde{x})]} \text{ where } p(\tilde{x}) = \text{lfp } \bigvee_i \varphi_i$
(KT)	sequential compositions of the next 5 rules:

---

(WRAP)	$\frac{p(\tilde{x}) \rightarrow (C \multimap \psi)}{C[p(\tilde{x})] \rightarrow \psi}$
(INTRO- $\forall$ )	$\frac{p(\tilde{x}) \rightarrow \forall \tilde{y} . (C \multimap \psi)}{p(\tilde{x}) \rightarrow (C \multimap \psi)} \text{ where } \tilde{y} = \text{free Var}(\psi) \setminus \tilde{x}$
(LFP)	$\frac{\dots \varphi_i[\forall \tilde{y} . (C \multimap \psi)/p] \rightarrow \forall \tilde{y} . (C \multimap \psi)}{p(\tilde{x}) \rightarrow \forall \tilde{y} . (C \multimap \psi)}$
(ELIM- $\forall$ )	$\frac{\varphi \rightarrow (C \multimap \psi)}{\varphi \rightarrow \forall y . (C \multimap \psi)} \text{ if } y \notin \text{free Var}(\varphi)$
(UNWRAP)	$\frac{C[\varphi] \rightarrow \psi}{\varphi \rightarrow (C \multimap \psi)}$

---

Figure 6.2: Automatic Proof Rules for Fixpoint Reasoning

match succeeds, the instantiated proof goal  $\varphi \rightarrow \psi\theta$  should be immediately proved by (SMT).

Note that the soundness of our proof framework does not rely on the correctness of the matching algorithm, because (PM) is basically a standard FOL proof rule and holds for any substitution  $\theta$ . The matching algorithm is a heuristic to find a good  $\theta$ . We rely on the external SMT solver to check the correctness of the match result given by the matching algorithm, through rule (SMT).

The combination of (PM) (based on the **pattern matching** algorithm **pm**) and (SMT) (based on SMT solvers) gives us the ability to do *static reasoning* about structure patterns. In separation logic (SL), for example, structural patterns correspond to *spatial formulas* built from the heap constructors **emp**,  $\mapsto$ , and  $*$ , whose behaviors are axiomatized as

the algebraic specification given in Section 2.6 where  $*$  is associative and commutative and **emp** is its unit. If the matching algorithm **pm** does not support matching modulo associativity (A), commutativity (C), and unit elements (U), then it cannot effectively discharge (separation logic) goals that are provable. In general, matching modulo any (given) set of equations is undecidable [99], so in this paper, we implement a naive matching algorithm that supports matching modulo associativity (A-matching), and matching modulo associativity and commutativity (AC-matching), which turned out to be effective so far.

(UNFOLD-R) unfolds one recursive pattern  $p(\tilde{x})$  on the right-hand side within any context  $C$  (satisfying mild conditions for contextual implication in Section 6.1.1) following its definition  $p(\tilde{x}) =_{\text{def}} \bigvee_i \varphi_i$ . The technical conditions guarantee that disjunction distributes over the context, so  $C[\bigvee_i \varphi_i] = \bigvee_i C[\varphi_i]$ . Therefore, after applying (UNFOLD-R) we need to prove one of the new goals  $\varphi \rightarrow C[\varphi_i]$ .

(KT), named after the Knaster-Tarski fixpoint theorem [21], is a sequential composition of five proof rules shown in Fig. 6.2: (WRAP), (INTRO- $\forall$ ), (LFP), (ELIM- $\forall$ ), and (UNWRAP). We explained the core proof rule (LFP) in Section 6.1.1. We explained in Section 6.1.1 why we need (WRAP) and (UNWRAP) and showed how they help address the limitations of (LFP), so here we only present their formal forms. (INTRO- $\forall$ ) and (ELIM- $\forall$ ) are standard FOL rules. (INTRO- $\forall$ ) *strengthens* the right-hand side and thus makes the subsequent proofs easier, because the (strengthened) right-hand side will be moved to the left-hand side by (LFP). Then after (LFP), we apply (ELIM- $\forall$ ) to restore the right-hand side to the form right after (WRAP) is applied (note the premise of (WRAP) is the same as the premise of (ELIM- $\forall$ )).

There is a challenge raised by applying (LFP) on goals whose right-hand side are contextual implications, because those contextual implications are moved to the left-hand side by (LFP) and then block the proofs, because (so far) we have not defined any proof rules that can handle contextual implications on the left-hand side. This will be solved by (MATCH-CTX) which is explained below.

(MATCH-CTX) deals with the (quantified) contextual implication  $\forall \tilde{y}. (C' \multimap \psi')$  on the left-hand side introduced by (LFP) and is one of the most complicated proof rule in our proof system. Note that (LFP) does the substitution  $[\forall \tilde{y}. (C \multimap \psi)/p]$ , which means (see Section 6.1.1) to replace each recursive occurrence  $p(\tilde{x}')$  (where  $\tilde{x}'$  might be different from the original argument  $\tilde{x}$ ) by  $(\forall \tilde{y}. (C \multimap \psi))[\tilde{x}'/\tilde{x}]$ , whose result we denote as  $\forall \tilde{y}. (C' \multimap \psi')$ . The number of contextual implications on the left-hand side is the same as the number of recursive occurrences of  $p$  in its definition. (MATCH-CTX) eliminates one contextual implication at a time, through a context matching algorithm **cm**, which will be discussed in Section 6.3. Here, we give the key intuition behind it.

When can a contextual implication  $C' \multimap \psi'$  be eliminated? Recall Definition 6.1, which

defines  $C' \multimap \psi'$  to be the set of elements  $h$  such that  $C'[h]$  satisfies  $\psi'$ . Therefore, we have the following key property about contextual implications:

$$\vdash C'[C' \multimap \psi'] \rightarrow \psi' \quad (6.4)$$

This property is not unexpected. Indeed,  $C' \multimap \psi'$  is matched by any elements that imply  $\psi'$  when plugged in context  $C'$ . The above is a direct formalization of that intuition.

In principle, property (6.4) can be used to handle contextual implication on the left-hand side. If contextual implication  $C' \multimap \psi$  happens to occur within the same context  $C'$ , then we can replace  $C'[C' \multimap \psi]$  by  $\psi'$ , using property (6.4) and standard propositional reasoning. However, situations in practice are more complex. Firstly, contextual implication can be *quantified*, i.e.,  $\forall \tilde{y}. (C' \multimap \psi')$ , so we need to first instantiate it using a substitution  $\theta$ , to  $C'\theta \multimap \psi'\theta$ . Secondly, the out-most context  $C_o$  might contain more than needed to match with  $C'\theta$ . So after matching, the rest, unmatched context, denoted  $C_{rest}$ , stays in the proof goal. The **context matching** algorithm **cm** implements heuristics to find a suitable substitution  $\theta$  such that  $C'\theta$  matches with (a part of) the out-most context  $C_o$ , and when succeeding, it returns  $\theta$  and the remaining unmatched context  $C_{rest}$ .

(FRAME) is to support frame reasoning. In contrast to (MATCH-CTX), which uses the outer context to simply the contextual implication, i.e. it says the context does matter, (FRAME) is to remove the outer context, which does not matter.

We conclude by the soundness of the proof rules in Fig. 6.2.

**Theorem 6.1.** If  $\varphi$  is provable from  $\Gamma$  using the proof rules in Fig. 6.2 then  $\varphi$  is provable from  $\Gamma$  using the proof system  $\mathcal{H}_\mu$  in Figure 4.1 plus the proof rule (SMT).

*Proof.* (ELIM- $\exists$ ), (PM), (INTRO- $\forall$ ), (ELIM- $\forall$ ) can be proved by standard FOL reasoning, which are supported by the proof system  $\mathcal{H}_\mu$ . Rules (LFP) and (UNFOLD-R) can be proved by standard fixpoint reasoning, also supported by  $\mathcal{H}_\mu$ . Rules (FRAME), (MATCH-CTX), (WRAP), and (UNWRAP) rely on the properties of structure contexts. QED.

Combining Theorem 6.1 with Theorem 4.1, we conclude that our proof framework is sound, assuming that the SMT solvers used in the proof rule (SMT) are sound.

**Theorem 6.2.** If  $\varphi$  is provable from  $\Gamma$  using the proof rules in Fig. 6.2, then  $\Gamma \models \varphi$ , assuming the soundness of the SMT solvers used in the proof rule (SMT).

## 6.2 EXAMPLES

We have so far explained our proof rules. Next, we show how these rules are put into practice by using them to prove several example proof goals collected from the various logical systems. Our objective is to help the reader understand better our proof framework and some subtle technical details, to show that the proof rules in Fig. 6.2 are designed carefully to capture the essence of fixpoint reasoning, and to show that our proof method is general and can be used to reason about fixpoints that occur in various mathematical domains.

### 6.2.1 A basic SL example

We first prove  $\vdash ll(x, y) \rightarrow lr(x, y)$  where

$$\begin{aligned} ll(x, y) &=_{\text{ifp}} (x = y \wedge \text{emp}) \vee (x \neq y \wedge \exists t. x \mapsto t * ll(t, y)) \\ lr(x, y) &=_{\text{ifp}} (x = y \wedge \text{emp}) \vee (x \neq y \wedge \exists t. lr(x, t) * t \mapsto y) \end{aligned}$$

The proof tree is shown in Fig. 6.3. Since the left-hand side  $ll(x, y)$  is already a recursive pattern, the (WRAP) rule does not make any change. Therefore, we apply directly the (LFP) rule and get two new proof goals. One goal, shown below, corresponds to the base case of the definition of  $ll(x, y)$ :

$$\vdash (x = y \wedge \text{emp}) \rightarrow lr(x, y)$$

The other goal corresponds to the inductive case and is shown in the second last line in Fig. 6.3. For clarity, we breakdown the steps in calculating the substitution  $[lr(x, y)/ll]$  required by (LFP) below:

$$\begin{array}{ll} \vdash ll(x, y) \rightarrow lr(x, y) & \text{proof goal, before (LFP)} \\ \vdash (\exists z. x \mapsto z * ll(z, y) \wedge x \neq y) \rightarrow lr(x, y) & \text{phantom step 1: unfolding} \\ \vdash (\exists z. x \mapsto z * lr(z, y) \wedge x \neq y) \rightarrow lr(x, y) & \text{phantom step 2: substituting } lr \text{ for } ll \end{array}$$

Now, the base case goal can be proved by applying (UNFOLD-R) to unfold the right-hand side  $lr(x, y)$  to its base case and then calling SMT solvers. The inductive case (after eliminating  $\exists z$  from left-hand side),  $\vdash x \mapsto z * lr(z, y) \wedge x \neq y \rightarrow lr(x, y)$ , contains a recursive pattern  $lr(z, y)$  within a context  $C[h] = x \mapsto z * h \wedge x \neq y$ . Therefore, we (WRAP) the context and yield contextual implication  $C \multimap lr(x, y)$  on the right-hand side, and quantify it with  $\forall x$  by (INTRO- $\forall$ ). Then (LFP) is applied, yielding two sub-goals, one for the base case and one for the inductive case. We omit the base case and show the following breakdown steps for the

$$\begin{array}{c}
\text{SMT} \frac{\text{True}}{lr(x, w) * w \mapsto y \wedge z \neq y \wedge x \neq y \rightarrow x \neq y \wedge lr(x, w) * w \mapsto y} \\
\text{PM} \frac{}{lr(x, w) * w \mapsto y \wedge z \neq y \wedge x \neq y \rightarrow x \neq y \wedge \exists t. lr(x, t) * t \mapsto y} \\
\text{UNFOLD-R} \frac{}{lr(x, w) * w \mapsto y \wedge z \neq y \wedge x \neq y \rightarrow lr(x, y)} \\
\text{MATCH-CTX} \frac{}{x \mapsto z * (\forall x. (C' \multimap lr(x, w))) * w \mapsto y \wedge z \neq y \wedge x \neq y \rightarrow lr(x, y)} \quad (\dagger) \\
\text{ELIM-}\exists \frac{}{\exists w. x \mapsto z * (\forall x. (C' \multimap lr(x, w))) * w \mapsto y \wedge z \neq y \wedge x \neq y \rightarrow lr(x, y)} \\
\text{UNWRAP} \frac{}{\exists w. (\forall x. (C' \multimap lr(x, w))) * w \mapsto y \wedge z \neq y \rightarrow (C \multimap lr(x, y))} \\
\text{ELIM-}\forall \frac{}{\exists w. (\forall x. (C' \multimap lr(x, w))) * w \mapsto y \wedge z \neq y \rightarrow \forall x. (C \multimap lr(x, y))} \quad \dots \\
\text{LFP} \frac{}{ll(x, y) \rightarrow lr(x, y)}
\end{array}$$

$$\begin{array}{c}
\text{INTRO-}\forall \frac{lr(z, y) \rightarrow \forall x. (C \multimap lr(x, y))}{lr(z, y) \rightarrow (C \multimap lr(x, y))} \\
\text{WRAP} \frac{}{x \mapsto z * lr(z, y) \wedge x \neq y \rightarrow lr(x, y)} \\
\text{ELIM-}\exists \frac{}{\exists z. x \mapsto z * lr(z, y) \wedge x \neq y \rightarrow lr(x, y)} \quad \dots \\
\text{LFP} \frac{}{ll(x, y) \rightarrow lr(x, y)}
\end{array}$$

$$\begin{array}{l}
\text{where } C[\Box] \equiv x \mapsto z * \Box \wedge x \neq y \\
C'[\Box] \equiv x \mapsto z * h \wedge x \neq w
\end{array}$$

Figure 6.3: Proof Tree of  $\vdash ll(x, y) \rightarrow lr(x, y)$ 

inductive case, for clarity:

$$\begin{array}{ll}
\vdash lr(z, y) \rightarrow (C \multimap lr(x, y)) & \text{proof goal, before (LFP)} \\
\vdash (\exists w. lr(z, w) * w \mapsto y \wedge z \neq y) \rightarrow (C \multimap lr(x, y)) & \text{unfolding} \\
\vdash (\exists w. (\forall x. (C \multimap lr(x, y))) [w/y] * w \mapsto y \wedge z \neq y) \rightarrow (C \multimap lr(x, y)) & \text{substituting}
\end{array}$$

where  $(\forall x. (C \multimap lr(x, y))) [w/y] = \forall x. (C' \multimap lr(x, w))$  and  $C'[h] = x \mapsto z * h \wedge x \neq w$ .

Now the proof proceeds by (UNWRAP)-ping the context  $C$  on the right-hand side and moving it back to the left-hand side, and eliminating the quantifier  $\exists w$  by (ELIM- $\exists$ ). Then the proof goal becomes the following (formula  $(\dagger)$  in line 5, Fig. 6.3):

$$x \mapsto z * (\forall x. (C' \multimap lr(x, w))) * w \mapsto y \wedge z \neq y \wedge x \neq y \rightarrow lr(x, y)$$

At this point, the quantified contextual implication on the left-hand side is instantiated and matched by (MATCH-CTX), which calls the context matching algorithm **cm**, introduced in Section 6.3. Intuitively, the algorithm uses heuristics to produce an instantiation for  $\forall x$  (in this case, it happens that the algorithm instantiates  $\forall x$  to  $x$ ) and then checks if the out-most context  $C_o$  of  $(\dagger)$  implies the (instantiated) context  $C'$ , where  $C_o[h] \equiv x \mapsto z * h * w \mapsto y \wedge z \neq y \wedge x \neq y$ .

Note that context  $C'$  consists of a structure pattern  $x \mapsto z$  and a logical constraint  $x \neq w$ . The structure pattern is already matched in  $C_o$ . The logical constraint can be implied



$$\begin{array}{c}
\text{SMT} \frac{\text{True}}{lr(x, w, s_3) * \phi \rightarrow lr(x, w, s_3) * w \mapsto y \wedge s = s_3 \cup \{w\} \wedge x \neq y} \\
\text{PM} \frac{lr(x, w, s_3) * \phi \rightarrow \exists t \exists s_4. lr(x, t, s_4) * t \mapsto y \wedge s = s_4 \cup \{t\} \wedge x \neq y}{lr(x, w, s_3) * \phi \rightarrow \exists t \exists s_4. lr(x, t, s_4) * t \mapsto y \wedge s = s_4 \cup \{t\} \wedge x \neq y} \\
\text{UNFOLD-R} \frac{lr(x, w, s_3) * \phi \rightarrow lr(x, y, s)}{lr(x, w, s_3) * \phi \rightarrow lr(x, y, s)} \\
\text{MATCH-CTX} \frac{x \mapsto z * (\forall x \forall s. (C' \multimap lr(x, w, s))) * \phi \rightarrow lr(x, y, s)}{x \mapsto z * (\forall x \forall s. (C' \multimap lr(x, w, s))) * \phi \rightarrow lr(x, y, s)} \quad (\dagger) \\
\text{ELIM-}\exists \frac{x \mapsto z * (\exists w \exists s_2. (\forall x \forall s. (C' \multimap lr(x, w, s))) * \phi) \wedge s = s_1 \cup \{x\} \wedge x \neq y \rightarrow lr(x, y, s)}{x \mapsto z * (\exists w \exists s_2. (\forall x \forall s. (C' \multimap lr(x, w, s))) * \phi) \wedge s = s_1 \cup \{x\} \wedge x \neq y \rightarrow lr(x, y, s)} \\
\text{UNWRAP} \frac{\exists w \exists s_2. (\forall x \forall s. (C' \multimap lr(x, w, s))) * w \mapsto y \wedge s_1 = s_2 \cup \{w\} \wedge z \neq y \rightarrow (C \multimap lr(x, y, s))}{\exists w \exists s_2. (\forall x \forall s. (C' \multimap lr(x, w, s))) * w \mapsto y \wedge s_1 = s_2 \cup \{w\} \wedge z \neq y \rightarrow (C \multimap lr(x, y, s))} \\
\text{ELIM-}\forall \frac{\exists w \exists s_2. (\forall x \forall s. (C' \multimap lr(x, w, s))) * w \mapsto y \wedge s_1 = s_2 \cup \{w\} \wedge z \neq y \rightarrow \forall x \forall s. (C \multimap lr(x, y, s))}{\exists w \exists s_2. (\forall x \forall s. (C' \multimap lr(x, w, s))) * w \mapsto y \wedge s_1 = s_2 \cup \{w\} \wedge z \neq y \rightarrow \forall x \forall s. (C \multimap lr(x, y, s))} \quad \dots \\
\text{LFP} \frac{\text{INTRO-}\forall \frac{lr(z, y, s_1) \rightarrow \forall x \forall s. (C \multimap lr(x, y, s))}{lr(z, y, s_1) \rightarrow (C \multimap lr(x, y, s))}}{lr(z, y, s_1) \rightarrow (C \multimap lr(x, y, s))} \\
\text{WRAP} \frac{x \mapsto z * lr(z, y, s_1) \wedge s = s_1 \cup \{x\} \wedge x \neq y \rightarrow lr(x, y, s)}{x \mapsto z * lr(z, y, s_1) \wedge s = s_1 \cup \{x\} \wedge x \neq y \rightarrow lr(x, y, s)} \\
\text{ELIM-}\exists \frac{\exists z \exists s_1. x \mapsto z * lr(z, y, s_1) \wedge s = s_1 \cup \{x\} \wedge x \neq y \rightarrow lr(x, y, s)}{\exists z \exists s_1. x \mapsto z * lr(z, y, s_1) \wedge s = s_1 \cup \{x\} \wedge x \neq y \rightarrow lr(x, y, s)} \quad \dots \\
\text{LFP} \frac{\dots}{ll(x, y, s) \rightarrow lr(x, y, s)}
\end{array}$$

where  $C[\Box] \equiv x \mapsto z * \Box \wedge s = s_1 \cup \{x\} \wedge x \neq y$   
 $C'[\Box] \equiv C[\Box][w/y, s_2/s_1] = x \mapsto z * \Box \wedge s = s_2 \cup \{x\} \wedge x \neq w$   
 $\phi \equiv w \mapsto y \wedge s_1 = s_2 \cup \{w\} \wedge z \neq y \wedge s = s_1 \cup \{x\} \wedge x \neq y$

Figure 6.4: Proof Tree of  $\vdash ll(x, y, s) \rightarrow lr(x, y, s)$ 

from  $C_o$ , which has two structure patterns  $x \mapsto z$  and  $w \mapsto y$ , and using the basic SL axiom/property  $x_1 \mapsto y * x_2 \mapsto z \rightarrow x_1 \neq x_2$ . Therefore, (MATCH-CTX) is applied successfully, and the rest, unmatched context of  $C_o$  is left in the goal (line 4 of Fig. 6.3) and proved in the subsequent proofs.

### 6.2.2 A more complex SL example

The previous simple example does not illustrate the usage of (INTRO- $\forall$ ), because (MATCH-CTX) applied to goal  $(\dagger)$  in Fig. 6.3 decides to instantiate  $\forall x$  by  $x$ , which means that the proof could also work without (INTRO- $\forall$ ). In this section, we show a slightly more complex example that shows the necessity of (INTRO- $\forall$ ).

Consider the following slightly modified definitions of  $ll$  and  $lr$  that take a third argument  $s$  denoting the set of elements in the list segment:

$$\begin{aligned}
ll(x, y, s) &=_{\text{IFP}} (x = y \wedge \text{emp} \wedge s = \emptyset) \vee \exists x_1 \exists s_1. x \mapsto x_1 * ll(x_1, y, s_1) \wedge s = s_1 \cup \{x\} \wedge x \neq y \\
lr(x, y, s) &=_{\text{IFP}} (x = y \wedge \text{emp} \wedge s = \emptyset) \vee \exists y_1 \exists s_1. lr(x, y_1, s_1) * y_1 \mapsto y \wedge s = s_1 \cup \{y_1\} \wedge x \neq y
\end{aligned}$$

Its proof tree in Fig. 6.4 is similar to the one in Fig. 6.3, except that the use of rule (INTRO- $\forall$ ) is *necessary* for the proof to succeed, because we need to *instantiate* the quantifier  $\forall s$  of goal  $(\dagger)$  in Fig. 6.4, line 5, with a fresh variable  $s_3$  in the application of rule (MATCH-CTX).

$$\begin{array}{c}
\text{SMT} \frac{\text{True}}{x \mapsto t_1 * t_1 \mapsto t_2 * llE(t_2, z) \rightarrow x \mapsto t_1 * t_1 \mapsto t_2 * llE(t_2, z)} \\
\text{PM} \frac{x \mapsto t_1 * t_1 \mapsto t_2 * llE(t_2, z) \rightarrow \exists u_1 \exists u_2 . x \mapsto u_1 * u_1 \mapsto u_2 * llE(u_2, z)}{x \mapsto t_1 * t_1 \mapsto t_2 * llE(t_2, z) \rightarrow llE(x, z)} \\
\text{UNFOLD-R} \frac{x \mapsto t_1 * t_1 \mapsto t_2 * llE(t_2, z) \rightarrow llE(x, z)}{x \mapsto t_1 * t_1 \mapsto t_2 * (\forall z . (C \multimap llE(t_2, z))) * llO(y, z) \rightarrow llE(x, z)} \\
\text{MATCH-CTX} \frac{x \mapsto t_1 * t_1 \mapsto t_2 * (\forall z . (C \multimap llE(t_2, z))) * llO(y, z) \rightarrow llE(x, z)}{\exists t_1 \exists t_2 . x \mapsto t_1 * t_1 \mapsto t_2 * (\forall z . (C \multimap llE(t_2, z))) * llO(y, z) \rightarrow llE(x, z)} \\
\text{ELIM-}\exists \frac{\exists t_1 \exists t_2 . x \mapsto t_1 * t_1 \mapsto t_2 * (\forall z . (C \multimap llE(t_2, z))) * llO(y, z) \rightarrow llE(x, z)}{\exists t_1 \exists t_2 . x \mapsto t_1 * t_1 \mapsto t_2 * \forall z . (C \multimap llE(t_2, z)) \rightarrow (C \multimap llE(x, z))} \\
\text{UNWRAP} \frac{\exists t_1 \exists t_2 . x \mapsto t_1 * t_1 \mapsto t_2 * \forall z . (C \multimap llE(t_2, z)) \rightarrow (C \multimap llE(x, z))}{\exists t_1 \exists t_2 . x \mapsto t_1 * t_1 \mapsto t_2 * \forall z . (C \multimap llE(t_2, z)) \rightarrow (C \multimap llE(x, z))} \dots \\
\text{ELIM-}\forall \frac{\exists t_1 \exists t_2 . x \mapsto t_1 * t_1 \mapsto t_2 * \forall z . (C \multimap llE(t_2, z)) \rightarrow (C \multimap llE(x, z))}{llO(x, y) \rightarrow \forall z . (C \multimap llE(x, z))} \\
\text{LFP} \frac{llO(x, y) \rightarrow \forall z . (C \multimap llE(x, z))}{llO(x, y) \rightarrow (C \multimap llE(x, z))} \\
\text{INTRO-}\forall \frac{llO(x, y) \rightarrow (C \multimap llE(x, z))}{llO(x, y) * llO(y, z) \rightarrow llE(x, z)} \\
\text{WRAP} \frac{llO(x, y) * llO(y, z) \rightarrow llE(x, z)}{llO(x, y) * llO(y, z) \rightarrow llE(x, z)}
\end{array}$$

where  $C[\Box] \equiv \Box * llO(y, z)$

Figure 6.5: Proof Tree of  $\vdash llO(x, y) * llO(y, z) \rightarrow llE(x, z)$

Suppose there is no application of rule (INTRO- $\forall$ ). Then, we will have

$$x \mapsto z * (C' \multimap lr(x, w, s)) * w \mapsto y \wedge s_1 = s_2 \cup \{w\} \wedge z \neq y \wedge s = s_1 \cup \{x\} \wedge x \neq y \rightarrow lr(x, y, s)$$

where  $C'[\Box] = x \mapsto z * \Box \wedge s = s_2 \cup \{x\} \wedge x \neq w$ . So we cannot match  $s = s_1 \cup \{x\} \wedge s_1 = s_2 \cup \{w\}$  in the outer context with  $s = s_2 \cup \{x\}$  in the inner context. In other words, we cannot eliminate the inner context and the proof will get stuck.

### 6.2.3 A SL example featuring mutual recursion

Mutually recursive definitions are in general defined as:

$$\begin{cases}
p_1(\tilde{y}_1) = \text{lf}_{\mathbf{P}} \exists \tilde{x}_{11} . \varphi_{11}(\tilde{y}_1, \tilde{x}_{11}) \vee \dots \vee \exists \tilde{x}_{1m_1} . \varphi_{1m_1}(\tilde{y}_1, \tilde{x}_{1m_1}) \\
\dots \\
p_k(\tilde{y}_k) = \text{lf}_{\mathbf{P}} \exists \tilde{x}_{k1} . \varphi_{k1}(\tilde{y}_k, \tilde{x}_{k1}) \vee \dots \vee \exists \tilde{x}_{km_k} . \varphi_{km_k}(\tilde{y}_k, \tilde{x}_{km_k})
\end{cases}$$

which simultaneously define  $k$  recursive definitions  $p_1, \dots, p_k$  to be the least among those satisfy the equations. Our way of dealing with mutual recursion is to reduce it to several non-mutual, simple recursions. We use the following separation logic challenge test `qf_shid_ent1/10.tst.smt2` from the SL-COMP'19 competition [96] as an example. Con-

$$\begin{array}{c}
\text{SMT} \frac{\text{True}}{p \wedge (p \rightarrow \circ p) \wedge \circ \Box(p \rightarrow \circ p) \rightarrow \circ p \wedge \circ \Box(p \rightarrow \circ p)} \\
\text{UNFOLD-L} \frac{p \wedge (p \rightarrow \circ p) \wedge \circ \Box(p \rightarrow \circ p) \rightarrow \circ p \wedge \circ \Box(p \rightarrow \circ p)}{\circ \wedge \frac{p \wedge \Box(p \rightarrow \circ p) \rightarrow \circ p \wedge \Box(p \rightarrow \circ p)}{p \wedge \Box(p \rightarrow \circ p) \rightarrow \circ(p \wedge \Box(p \rightarrow \circ p))}} \\
\text{PM} \frac{p \wedge \Box(p \rightarrow \circ p) \rightarrow \circ(p \wedge \Box(p \rightarrow \circ p))}{p \wedge \Box(p \rightarrow \circ p) \rightarrow p \wedge \circ(p \wedge \Box(p \rightarrow \circ p))} \\
\text{GFP} \frac{p \wedge \Box(p \rightarrow \circ p) \rightarrow p \wedge \circ(p \wedge \Box(p \rightarrow \circ p))}{p \wedge \Box(p \rightarrow \circ p) \rightarrow \Box p}
\end{array}$$

Figure 6.6: Proof Tree of  $\vdash p \wedge \Box(p \rightarrow \circ p) \rightarrow \Box p$ 

sider the following definition of list segments of odd and even length:

$$\begin{cases}
llo(x, y) =_{\text{ifp}} x \mapsto y \vee \exists t. x \mapsto t * lle(t, y) \\
lle(x, y) =_{\text{ifp}} \exists t. x \mapsto t * llo(t, y)
\end{cases}$$

and the proof goal  $\vdash llo(x, y) * llo(y, z) \rightarrow lle(x, z)$ .

To proceed the proof, we first reduce the mutual recursion definition into the following two non-mutual, simple recursion definitions, which can be obtained systematically by unfolding the other recursive symbols to exhaustion.

$$\begin{aligned}
llo(x, y) &=_{\text{ifp}} x \mapsto y \vee \exists t_1 \exists t_2. x \mapsto t_1 * t_1 \mapsto t_2 * llo(t_2, y) \\
lle(x, y) &=_{\text{ifp}} \exists t_1 \exists t_2. x \mapsto t_1 * t_1 \mapsto t_2 * lle(t_2, y)
\end{aligned}$$

Then, the proof can be carried out in the normal way. We show the proof tree in Fig. 6.5.

#### 6.2.4 An LTL example

We demonstrate the generality of our proof method by showing how to prove the induction proof rule of the sound and complete proof system of LTL (Figure 2.5). Recall that LTL can be defined as a matching  $\mu$ -logic theory (Section 5.9.1).

Consider the following LTL rule for induction:  $\vdash p \wedge \Box(p \rightarrow \circ p) \rightarrow \Box p$ . Since the “always  $\Box$ ” operator is defined as a greatest fixpoint  $\Box \varphi =_{\text{gfp}} \varphi \wedge \circ \Box \varphi$ , we need a set of proof rules dual to those in Fig. 6.2, where the key rule, (GFP) (dual to (LFP)), is shown below:

$$(\text{GFP}) \quad \frac{\varphi \rightarrow \psi_i[\varphi/q]}{\varphi \rightarrow q(\tilde{y})} \quad q(\tilde{y}) =_{\text{gfp}} \bigvee \psi_i$$

(GFP) is used to discharge the right-hand side  $\Box p$  of the proof goal. We show the self-explanatory proof tree in Fig. 6.6. Note that during the proof we use the distributivity law

provided by the theory  $\Gamma^{\text{LTL}}$  in Section 5.9.1, denoted as proof step  $(\circ\wedge)$  in Fig. 6.6.

### 6.2.5 A verification example from RL

We have discussed RL and showed its matching  $\mu$ -logic theory in Section 5.11. Here, we use one example to illustrate how reachability reasoning, i.e. formal verification, can be handled uniformly by our proof framework. Before we dive into the technical details, let us remind readers that in RL, structure patterns are used to represent the program states, called *configurations* in RL, of the programming language. The reachability property  $\varphi_1 \Rightarrow \varphi_2$  then builds on top of the structure patterns and defines the transition relation among program configurations.

We use the following simple program `sum` to explain the core RL concepts.

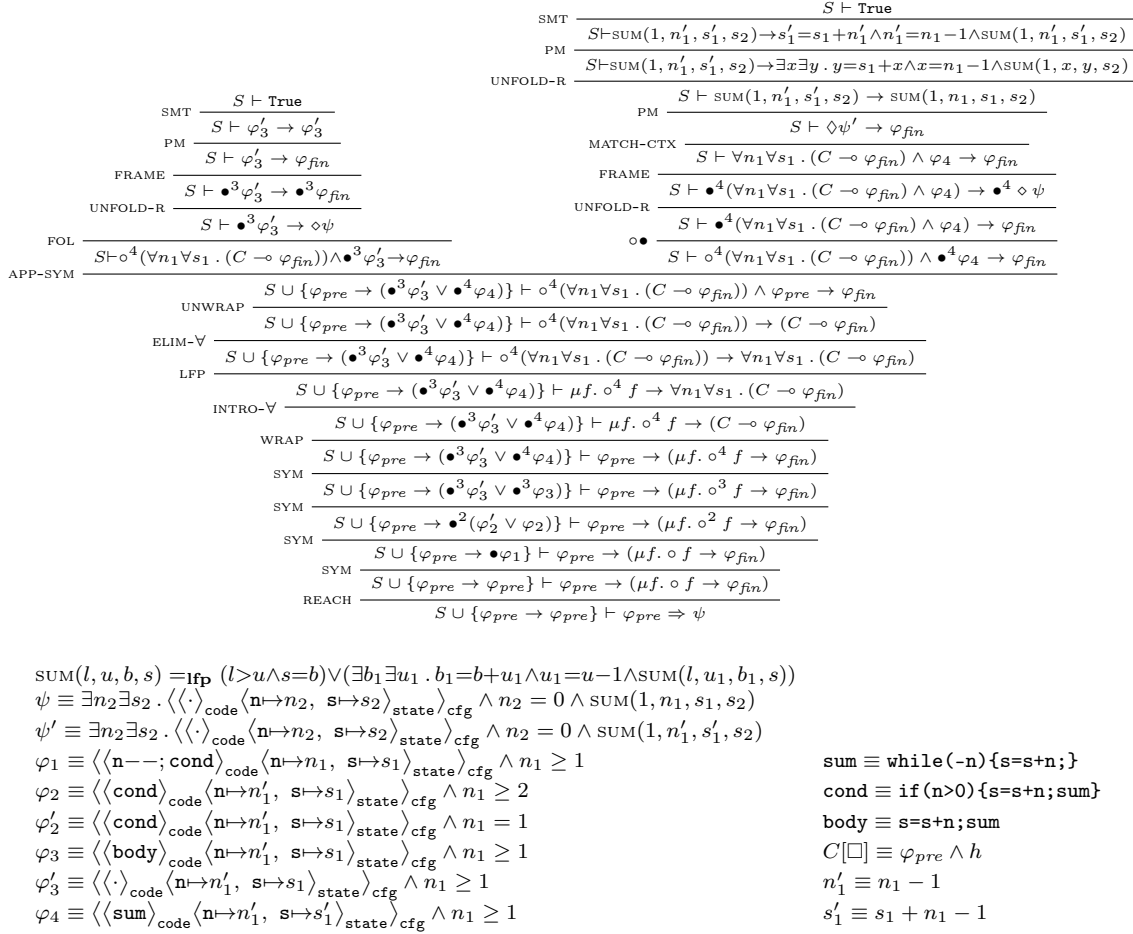
`sum  $\equiv$  while (--n) {s=s+n;}`

The program `sum` is written in a simple imperative language that has a C-like syntax. It calculates the total from 1 to  $n$  and adds it to the variable `s`. Its functional correctness means that when it terminates, the value of variable `s` should be  $s + n(n - 1)/2$ , where  $s$  and  $n$  are the initial values we give to the variables `s` and `n`, respectively.

In order to execute `sum`, we need to know the concrete values of `s` and `n`. This semantic information is organized as a mapping from variables to their values and we call the mapping a *state*. Knowing the program and the state where it is executed allows us to execute the program to termination. Thus, a program and a state forms a complete computation configuration for this simple imperative language and the configurations can be represented using structure patterns that hold all the semantic information needed for program execution. For example, let us write down the initial and final configurations of `sum` where we initialize `s` and `n` by the integer values  $s$  and  $n$ , respectively:

$$\begin{aligned}\varphi_{pre} &\equiv \langle \langle \text{sum} \rangle_{\text{code}} \langle \mathbf{n} \mapsto n, \mathbf{s} \mapsto s \rangle_{\text{state}} \rangle_{\text{cfg}} \wedge n \geq 1 \\ \varphi_{post} &\equiv \langle \langle \cdot \rangle_{\text{code}} \langle \mathbf{n} \mapsto 0, \mathbf{s} \mapsto s + n(n - 1)/2 \rangle_{\text{state}} \rangle_{\text{cfg}}\end{aligned}$$

Following RL convention, we write configurations in *cells* such as  $\langle \dots \rangle_{\text{code}}, \langle \dots \rangle_{\text{state}}$ ; from a logical point of view, these are simply structure patterns and are built from ML symbols in the same way how FOL terms are defined. The functional correctness of `sum` states the following: if we start from the initial configuration  $\varphi_{pre}$  and the program terminates, then the final configuration is  $\varphi_{post}$ , where there is nothing to be executed anymore (as denoted by the dot “ $\cdot$ ”, meaning “nothing”, in the  $\langle \dots \rangle_{\text{code}}$  cell), `n` is mapped to 0, and `s` is mapped to the

Figure 6.7: Verifying Functional Correctness of `sum` using Reachability Rules

correct total  $s + n(n-1)/2$ . This functional (partial) correctness property can be expressed by the reachability property  $\varphi_{pre} \Rightarrow \varphi_{post}$ . According to Section 5.11,  $\varphi_{pre} \Rightarrow \varphi_{post}$  is equal to  $\varphi_{pre} \rightarrow (\text{WF} \rightarrow \diamond \varphi_{post})$ , where  $\text{WF} = \mu X. \circ X$  is matched by all well-founded configurations (i.e., those without infinite execution traces) and  $\diamond \varphi_{post} = \mu X. \varphi_{post} \vee \bullet X$  is matched by all configurations that *eventually* reach  $\varphi_{post}$ , after at most finitely many execution steps. This encoding correctly captures the *partial* correctness.

We now prove that `sum` satisfies the correctness property  $\varphi_{pre} \Rightarrow \varphi_{post}$ . We put the proof tree in Fig. 6.7 and explain it at a higher-level below. Intuitively, the proof works by *symbolically executing* the program step by step and applying inductive reasoning to finish the proof as soon as repetitive configurations (i.e., those generated by the while-loop in `sum`) are identified during the proof. Each symbolic execution step corresponds to a reachability property that can be proved about `sum`. While we proceed with the proof and carry out symbolic execution, we collect the proved reachability properties so that they can be used

(by induction) to resolve the proof goal about the while-loop.

The proof goals have the form  $S \cup S_i \vdash \varphi_i \rightarrow \psi_i$  where  $S$  is a set of RL rules that include all the reachability rules axiomatizing the small-step style operational semantics of the language and  $S_i$  include those representing the results of  $i$ -step symbolic execution. Initially, the functional correctness proof goal is  $S \cup \{\varphi_{pre} \rightarrow \varphi_{pre}\} \vdash \varphi_{pre} \rightarrow \diamond_w \psi$ , where  $\psi$  is the final configuration  $\varphi_{post}$  rewritten using the recursive predicate  $\text{SUM}(l, u, b, s)$ , meaning the partial-sum relation:  $s = b + (u + (u - 1) + \dots + l)$ . Pattern  $\varphi_{pre} \rightarrow \varphi_{pre}$  corresponds to the symbolic execution reachability rule (i.e., lemma) that we can prove by executing the initial configuration  $\varphi_{pre}$  by 0 step. As the proof proceeds, more symbolic execution steps are carried out and more lemmas are proved. The following domain-specific rule is used to carry out symbolic execution and flush the newly-proved lemmas/rules that summarize the semantics of SUM into  $S_i$ :

$$(\text{SYM}) \frac{S \cup S_k \vdash \varphi \rightarrow (\mu f. \circ^j f \rightarrow \Diamond \psi)}{S \cup \{\varphi \rightarrow \varphi'\} \vdash \varphi \rightarrow (\mu f. \circ^i f \rightarrow \Diamond \psi)} \quad \text{if } S_k \neq \emptyset \wedge i \geq 1 \text{ where } (S_k, j) = \text{NEXT}(\varphi')$$

where NEXT takes the current symbolic configuration, executes it according to the semantics  $S$ , and outputs a rule that specifies the step (implemented similarly to [3]) and the number of steps taken. We stop execution when the code cell  $\langle \dots \rangle_{\text{code}}$  becomes empty (as in the case of  $\varphi'_3$ ) or contains the same code as that of  $\varphi_{pre}$  (as in the case of  $\varphi_4$ ). The collected rules (e.g.  $\{\varphi_{pre} \rightarrow (\bullet^3 \varphi'_3 \vee \bullet^4 \varphi_4)\}$ ) will be used to simplify  $\varphi_{pre}$  later (e.g. as in the application of (APP-SYM)).

## 6.3 ALGORITHMS

Our generic matching  $\mu$ -logic prover runs a simple DFS algorithm on top of the proof rules in Fig. 6.2. In this section, we show the top-level DFS algorithm in Fig. 6.8. We also show the pattern matching algorithms used by the proof rules (PM) and (MATCH-CTX) in Figure 6.9.

### 6.3.1 Top-level DFS proof search algorithm

The top-level proof search algorithm in Fig. 6.8 starts with procedure **Prove** on the goal  $\vdash \varphi \rightarrow \psi$ , which uses two counters  $c_{RU}, c_{KT}$ , both initialized to zero, to keep track of how many times (UNFOLD-R) and (KT) have been applied. Proof search terminates (unsuccessfully) if they exceed the preset search bounds, so the proof procedure is incomplete, which is expected. Specifically, the algorithm consists of two cases: (Base Case) and (Recursive Call).

```

function Prove( $\varphi \rightarrow \psi$ ,  $c_{RU}$ ,  $c_{KT}$ )
<1>   if (BasicProof( $\varphi \rightarrow \psi$ )) return true
<2>   let  $\{p_i\} := \text{rec\_sym}(\varphi)$ ,  $\{q_i\} := \text{rec\_sym}(\psi)$ ,  $OrSet := \emptyset$ 
<3>   foreach ( $\forall \tilde{y}. C' \multimap \varphi'$ )  $\in \varphi$  /* (MATCH-CTX) */
<4>      $C_0 := \varphi \setminus \{\forall \tilde{y}. C' \multimap \varphi'\}$ 
<5>      $(C_{rest}, \theta) := \text{cm}(C_0, C', \tilde{y})$  /*Section 6.3.2*/
<6>      $\varphi'' := C_{rest} \cup \varphi' \theta$ 
<7>      $ob := [\varphi'' \rightarrow \psi, c_{RU}, c_{KT}]$ ,  $OrSet \cup = \{\{ob\}\}$ 
<8>   foreach  $p_i \in \varphi, q_{i'} \in \psi$  /* (FRAME) */
<9>     if ( $\theta := \text{pm}([p_i], [q_{i'}], \text{freeVar}(\psi) \setminus \text{freeVar}(\varphi)) \neq \text{Failure}$ 
<10>        $\varphi' := \varphi \setminus \{p_i\}$ ,  $\psi' := (\psi \setminus \{q_{i'}\}) \theta$ 
<11>        $ob := [\varphi' \rightarrow \psi', c_{RU}, c_{KT}]$ ,  $OrSet \cup = \{\{ob\}\}$ 
<12>   if ( $c_{RU} < \text{MAXRIGHTBOUND}$ ) /* (UNFOLD-R) */
<13>     foreach ( $q_i \in \psi$ )
<14>       foreach ( $\psi_j \in (\{\psi_1 \dots \psi_k\} := \text{UNFOLD}(\psi, q_i))$ )
<15>          $ob := [\varphi \rightarrow \psi_j, c_{RU} + 1, c_{KT}]$ ,  $OrSet \cup = \{\{ob\}\}$ 
<16>   if ( $c_{KT} < \text{KTBOUND}$ ) /* (KT) */
<17>     foreach ( $p_i \in \varphi$ )
<18>       foreach ( $\varphi_j \in (\{\varphi_1, \varphi_2, \dots \varphi_l\} := \text{KT}(\varphi, p_i))$ )
<19>          $ob := [\varphi_j \rightarrow \psi, c_{RU}, c_{KT} + 1]$ 
<20>         if (trivially_true( $ob$ )) continue
<21>          $Obs := Obs \cup \{ob\}$ 
<22>          $OrSet \cup = \{Obs\}$ 
<23>   if ( $OrSet = \emptyset$ ) return false
<24>    $OrSet := \text{OrderByHeuristics}(OrSet)$ 
<25>   foreach ( $Obs \in OrSet$ )
<26>     if (ProveAll( $Obs$ )) return true
<27>   return false
endfunction

function ProveAll( $Obs$ )
<28>   foreach ( $[\varphi \rightarrow \psi, c_{RU}, c_{KT}] \in Obs$ )
<29>     if (not Prove( $\varphi \rightarrow \psi$ ,  $c_{RU}$ ,  $c_{KT}$ ))
<30>       return false;
<31>   return true
endfunction

```

Figure 6.8: Top-Level DFS Proof Search Algorithm

For (Base Case), procedure `BasicProof` is the exit point of the algorithm. For each proof goal, it firstly attempts a *basic proof*, i.e., to discharge by applying rule (PM) and then querying an SMT solver, where recursive symbols are treated as uninterpreted as in (SMT) proof rule. Intuitively, this step succeeds if the proof goal is simple enough such that a *proof by matching* can be achieved.

For (Recursive Call), when a basic proof fails, we collect all possible transformations of the proof goal, using (KT), (UNFOLD-R) rules, into a disjunction of conjunctions of sub-goals *OrSet* (i.e., a set of goal sets)—here, we only present the least fixpoint reasoning. The current proof goal can be successfully discharged if there is *one* set  $Obs \in OrSet$  whose goals can *all* be proved. The realization of the proof rules in our algorithm is straightforward, except for two noteworthy points:

1. (KT) applications will exhaustively search for all possible candidates.
2. When a proof goal has an unsatisfiable left-hand side, the proof goal is trivially true, which is denoted `trivially_true` in Fig. 6.8, and is removed immediately.

Fig. 6.8 essentially implements a (bounded) depth-first proof search, so the order in which the sets of goals  $Obs \in OrSet$  are tried may affect performance greatly but not effectiveness, i.e. the ability to prove the proof goals of our proof framework. The algorithm is parametric in a procedure `OrderByHeuristics` (line 24) that controls the mentioned order. For the experiments considered in this paper, we use the following intuitive order, which follows the fact that our base case is reached by a successful basic proof.

We proceed by a number of passes. In each pass, we first order the goals within each  $Obs \in OrderSet$ . We then consider the order of *OrderSet* by comparing the last goal in each set  $Obs \in OrderSet$ . Subsequent passes will not undo the work of the previous passes, but instead work on the goals and/or sets of goals which are tied in previous passes. Below are a few things to note.

1. Goals without recursive patterns on the right-hand side are prioritized.
2. Goals with recursive patterns on the right-hand side but not on the left-hand side are considered next.
3. Goals with fewer existential variables are prioritized.

### 6.3.2 Two matching algorithms

As discussed in Section 6.1.2, our proof framework is parametric in two matching algorithms: `cm` used by (MATCH-CTX) and `pm` used by (PM). We discuss these two algorithms below.



**Context matching algorithm** Procedure **cm** is used to check whether the inner context can be matched with the outer context. For example, suppose we have the following proof goal:

$$\vdash C_{outer}[\forall \tilde{y}. (C' \multimap \varphi')] \rightarrow \psi \quad (6.5)$$

**cm**( $C_{outer}, C', \tilde{y}$ ) takes as inputs the outer context  $C_{outer}$ , the inner context  $C'$  and a list of quantifier variables  $\tilde{y}$ . To check if  $C'$  can be matched by (a part of)  $C_{outer}$ , it builds the following proof goal:

$$\vdash C_{outer} \rightarrow \exists \tilde{y}. C' \quad (6.6)$$

In (6.5), what we want is to initialize the universal variables  $\forall \tilde{y}$  in order for the inner context  $C'$  to be matched with some part of  $C_{outer}$ . That is also the purpose of using the existential variables  $\exists \tilde{y}$  in (6.6). The change of the quantifier  $\tilde{y}$  from universal to existential is because we have moved the inner context  $C'$  from left-hand side to right-hand side.

To deal with (6.6), **cm** will call the modified version of the **Prove** function in Figure 6.8. The difference between the modified version and the **Prove** function is only on the returning result. Specifically, apart from returning *true* if the **Prove** function returns *true*, the modified version additionally (1) returns the *remaining, unmatched* part of the left-hand side, denoted  $C_{rest}$ , after consuming all the matched constraints from the right-hand side, and (2) collects the instantiation of  $\tilde{y}$ , denoted  $\theta$ , when applying rule (PM). (Note that  $C_{rest}$  may contain structure patterns as we have seen in the SL examples.) Specifically, if we can prove (6.6), we have  $\vdash C_{outer} \rightarrow C'\theta$ . Furthermore,  $C_{rest}$  is the remaining part after removing the constraint of  $C'\theta$  from  $C_{outer}$ , so we have  $C_{rest}[C'\theta[C'\theta \multimap \varphi'\theta]] \rightarrow \psi$ . As a result, we now can proceed to prove new proof goal  $C_{rest}[\varphi'\theta] \rightarrow \psi$ .

**Pattern matching algorithm** Procedure **pm**, used by rule (PM), implements a naive, brute-force algorithm as shown in Fig. 6.9 that does matching modulo associativity and/or associativity-and-commutativity. Procedure **pm** takes as input

- a list of “pattern” patterns  $[\psi_i]_1^m \equiv [\psi_1, \dots, \psi_m]$ ;
- a list of “term” patterns  $[\varphi_j]_1^m \equiv [\varphi_1, \dots, \varphi_m]$ ;
- a set of existential variables  $EV$  with  $EV \cap \bigcup_1^n \text{free Var}(\varphi_j) = \emptyset$  and  $EV \subseteq \bigcup_1^m \text{free Var}(\psi_i)$ .

Then it returns **Failure** or the match result  $\theta$  with  $\text{domains}(\theta) \subseteq EV$  and  $\psi_i\theta \equiv \varphi_i$ , for all  $i$ .

```

function pm( $[\psi_i]_1^m, [\varphi_i]_1^m, EV$ )
<32>   if  $m = 0$  return { }
<33>   if  $\psi_1 \equiv \sigma(\tilde{\psi}_1)$  and  $\varphi_1 \equiv \sigma'(\tilde{\varphi}_1)$ 
<34>     if  $\sigma \neq \sigma'$  return Failure
<35>     else if  $\text{length}(\tilde{\psi}_1) \neq \text{length}(\tilde{\varphi}_1)$ 
<36>       return Failure
<37>     else
<38>        $[\psi'_i]_1^{m'} = \tilde{\psi}_1 \cup [\psi_i]_1^m$ 
<39>        $[\varphi'_i]_1^{m'} = \tilde{\varphi}_1 \cup [\varphi_i]_1^m$ 
<40>       return pm( $[\psi'_i]_1^{m'}, [\varphi'_i]_1^{m'}, EV$ )
<41>   if  $\psi_1 \equiv x$  and  $x \notin EV$ 
<42>     if  $\varphi_1 \equiv x$ 
<43>       return pm( $[\psi_i]_2^m, [\varphi_i]_2^m, EV$ )
<44>     else
<45>       return Failure
<46>   if  $\psi_1 \equiv x$  and  $x \in EV$ 
<47>      $[\psi'_i]_2^m = [\psi_i]_2^m \{x \mapsto \varphi_1\}$ 
<48>      $[\varphi'_i]_2^m = [\varphi_i]_2^m \{x \mapsto \varphi_1\}$ 
<49>      $\theta' = \text{pm}([\psi'_i]_2^m, [\varphi'_i]_2^m, EV)$ 
<50>     return  $\{x \mapsto \varphi_1\} \cup \theta'$ 
<51>   return Failure
endfunction

```

Figure 6.9: Pattern Matching Algorithm

## 6.4 EVALUATION

We implemented our proof framework in the  $\mathbb{K}$  framework (<http://kframework.org>); see Section 2.15.  $\mathbb{K}$  has a modular notation for defining rewrite systems. Since our proof framework is essentially a rewriting system that *rewrites/reduces* proof goals to sub-goals, it is convenient to implement it in  $\mathbb{K}$ .

We evaluated our prototype implementation using four representative logical systems for fixpoint reasoning: first-order logic extended with least fixpoints (LFP), separation logic (SL), linear temporal logic (LTL), and reachability logic (RL). Our evaluation plan is as follows. For SL, we used the 280 benchmark properties collected by the SL-COMP'19 competition [96]. These properties are entailment properties about various inductively-defined heap structures, including several hand-crafted, challenging structures. For LTL, we considered the (inductive) axioms in the complete LTL proof system (see, e.g., [100, 101]). For LFP and RL, we considered the program verification of a simple program `sum` that computes the total sum from 1 to a symbolic input  $n$ . We shall use two different encodings to capture the underlying transition relation: the LFP encoding defines it as a binary predicate and the RL encoding

defines it as a reachability rule.

Before we discuss our evaluation results, we would like to point out that it would be unreasonable to expect that a unified proof framework can outperform the state-of-the-art provers and algorithms for all specialized domains from the first attempt. We believe that this *is* possible and within our reach in the near future, but it will likely take several years of sustained effort. We firmly believe that such effort will be worthwhile spending, because if successful then it will be transformative for the field of automated deduction and thus program verification. Here, we focus on demonstrating the generality of our proof framework. We shall also report the difficulties that we experienced.

Our first evaluation is based on the standard separation logic benchmark set collected by SL-COMP’19 [96]. These benchmarks are considered challenging because they are related to heap-allocated data structures along with *user-defined* recursive predicates crafted by participants to challenge the competitors. Among the benchmarks, we focus on the `qf_shid_entl` division that contains entailment problems about inductive definitions. This division is considered the hardest one, specifically because many of its tests require proofs by induction. As such, this division is a good case study for testing the generality of our generic proof framework. Furthermore, heap provers are currently considered to have the most powerful implementations of automated inductive reasoning, so we would not be far from the truth considering a comparison of our prototype with these as a comparison with the state-of-the-art in automated inductive reasoning.

To set up our prover for the SL benchmarks, we *instantiate* it with the set  $\Gamma^{\text{SL}}$  of axioms that captures SL, as given in Section 5.3. Note that the associativity and commutativity of  $\varphi_1 * \varphi_2$  are handled by the *built-in* pattern matching algorithms (see Fig. 6.9), so the most important axioms are the two specifying non-zero locations and no-overlapped heap unions. The experimental results show that our generic prover can prove 265 of the 280 benchmark tests, placing it third place among all participants.

Interestingly, we noted that (FRAME) is not necessary for most tests. Only 12 out of the 265 tests used (FRAME) reasoning. More experiments are needed to draw any firm conclusion, but it could be (FRAME) reasoning mostly improves performance, as it reduces the matching search space and thus proof search terminates faster, but does not necessarily increase the expressiveness of the prover. The 15 tests that our prover cannot handle come from the benchmarks of automata-based heap provers [89, 102]. These benchmarks demand more sophisticated *SL-specific* reasoning that require more complex properties about heaps/maps than what our prover can naively derive from the  $\Gamma^{\text{SL}}$  theory with its current degree of automation; while we certainly plan to handle those as well in the near future, we would like to note that they are *not* related to fixpoint reasoning, but rather to reasoning about maps.

Table 6.1: Selected separation logic properties, automatically proved by our prover

$\text{sorted\_list}(x, \text{min}) \rightarrow \text{list}(x)$ $\text{sorted\_list}_1(x, \text{len}, \text{min}) \rightarrow \text{list}_1(x, \text{len})$ $\text{sorted\_list}_1(x, \text{len}, \text{min}) \rightarrow \text{sorted\_list}(x, \text{min})$ $\text{sorted\_ls}(x, y, \text{min}, \text{max}) * \text{sorted\_list}(y, \text{min}_2) \wedge \text{max} \leq \text{min}_2 \rightarrow \text{sorted\_list}(x, \text{min})$
$\text{lr}(x, y) * \text{list}(y) \rightarrow \text{list}(x)$ $\text{lr}(x, y) \rightarrow \text{ll}(x, y)$ $\text{ll}(x, y) \rightarrow \text{lr}(x, y)$ $\text{ll}_1(x, y, \text{len}_1) * \text{ll}_1(y, z, \text{len}_2) \rightarrow \text{ll}_1(x, z, \text{len}_1 + \text{len}_2)$ $\text{lr}_1(x, y, \text{len}_1) * \text{list}_1(y, \text{len}_2) \rightarrow \text{list}_1(x, \text{len}_1 + \text{len}_2)$ $\text{ll}_1(x, \text{last}, \text{len}) * (\text{last} \mapsto \text{new}) \rightarrow \text{ll}_1(x, \text{new}, \text{len} + 1)$
$\text{dls}(x, y) * \text{dlist}(y) \rightarrow \text{dlist}(x)$ $\widehat{\text{dls}}_1(x, y, \text{len}_1) * \widehat{\text{dls}}_1(y, z, \text{len}_2) \rightarrow \widehat{\text{dls}}_1(x, z, \text{len}_1 + \text{len}_2)$ $\text{dls}_1(x, y, \text{len}_1) * \text{dlist}_1(y, \text{len}_2) \rightarrow \text{dlist}_1(x, \text{len}_1 + \text{len}_2)$
$\text{avl}(x, \text{hgt}, \text{min}, \text{max}, \text{balance}) \rightarrow \text{bstree}(x, \text{hgt}, \text{min}, \text{max})$ $\text{bstree}(x, \text{height}, \text{min}, \text{max}) \rightarrow \text{bintree}(x, \text{height})$

Table 6.2: Axioms in the complete LTL proof system, automatically proved by our prover

(K $\Box$ )	$\Box(\varphi_1 \rightarrow \varphi_2) \rightarrow (\Box\varphi_1 \rightarrow \Box\varphi_2)$
(IND)	$\varphi \wedge \Box(\varphi \rightarrow \circ\varphi) \rightarrow \Box\varphi$
(U <sub>1</sub> )	$\varphi_1 U \varphi_2 \rightarrow \Diamond\varphi_2$
(U <sub>2</sub> .1)	$\varphi_1 U \varphi_2 \rightarrow \varphi_2 \vee \varphi_1 \wedge \circ(\varphi_1 U \varphi_2)$
(U <sub>2</sub> .2)	$\varphi_2 \vee \varphi_1 \wedge \circ(\varphi_1 U \varphi_2) \rightarrow \varphi_1 U \varphi_2$

The two provers that outperform our generic prover, Songbird and S2S, are both specialized for SL. Compared with generic provers such as CYCLIST [103], our prover proves 13 more tests.

Table 6.1 illustrates some of the more interesting SL properties that our prover can verify automatically. These are common lemmas about heap structures that arise and are collected when verifying real-world heap-manipulating programs. For example, the property on the first line says that a sorted list is also a list, a typical verification condition arising in formal verification. Table 6.1 also shows several proof goals about singly-linked lists and list segments (specified by predicates `ls`, `list`, `ll`, `lr`, etc.), doubly-linked lists and list segments (specified by predicates `dls`, `dlist`, etc.), and trees.

Our second study is to automatically prove the inductive axioms in the complete LTL proof system, shown in Table 6.2, whereas the proof tree of the most interesting of them, (IND), has been given in Section 6.2.4. Note that LTL is essentially a structure-less logic, as its formulas are only built from temporal operators and propositional connectives, and

its models are infinite traces of states that have no internal structures and are modeled as “points”. The structure-less-ness of LTL made fixpoint reasoning for it simpler, as no context reasoning or frame reasoning was needed.

Our final study considers a simple program `sum` that computes the total from 1 to a symbolic input  $n$ . We do the verification of `sum` following two approaches: RL and LFP. The reachability logic (RL) approach has been illustrated in Section 2.14. For LFP, program configurations are encoded as FOL terms and the program semantics is encoded as a binary FOL predicate that captures the transition relation. In particular, reachability is defined as a recursive predicate based on the semantics. Our prover then becomes *a (language-independent) program verifier*, different from Hoare-style verification (where a language-specific verification condition generator is required).

We ran these tests on a single core virtual machine with 8GB of RAM. The SL-COMP’19 tests took a total 13 hours to finish, including two outliers that took approximately one and three hours to complete. The two LTL tests took approximately three minutes, while the ten first order logic tests took seven minutes to complete and the `sum` program takes a minute to complete. To reiterate, we do not expect our prover to outperform specialized provers this early in its development. These results do, however, show that a unified, powerful and efficient proof framework is within reach.

## Chapter 7: APPLICATIVE MATCHING $\mu$ -LOGIC (AML)

### 7.1 MOTIVATION

In an ideal unifying semantics-based language framework, all programming languages must have their formal syntax and semantics definitions. In addition, all execution and formal analysis tools of a given programming language  $L$  must be automatically generated from its formal definition  $\Gamma^L$ , where  $\Gamma^L$  is a logical theory that axiomatically define the static configurations and dynamic behaviors of all programs of  $L$ . As discussed in Section 2.15, the  $\mathbb{K}$  framework is one of the many efforts in pursuing above vision of an ideal unifying semantics-based language framework.  $\mathbb{K}$  has been used to define the complete formal semantics of many large programming languages and generate their execution and formal analysis tools.

A major research question has been this: *what is the right logical foundation of  $\mathbb{K}$ ?* This is a challenging question to answer, for  $\mathbb{K}$  is such a complex artifact whose implementation has over 500,000 lines of code in multiple programming languages. Previously, a tentative answer was given by matching logic (Section 2.13) and reachability logic (Section 2.14). Matching logic was used to specify and reason about the static configurations of programs as well as any (FOL) constraints over them. Reachability logic was used to specify and reason about the dynamic reachability properties. In particular, reachability logic has a language-independent proof system (Figure 2.12) that supports sound and relatively complete verification for all programming languages. Unfortunately, reachability logic cannot express more dynamic behaviors/properties such as liveness properties, which can be expressed using a temporal logic or modal  $\mu$ -calculus. Besides the combination of matching logic and reachability logic, there are also other attempts to find a logical foundation for  $\mathbb{K}$ , including one using (double-pushout) graph transformations [104], one based on a translation to Isabelle [105], and one based on a translation to (coinduction in) Coq [106]. None of these are incorporated within  $\mathbb{K}$ 's codebase because none of them are satisfactory: not only they result in heavy translations with a big representational distance from the original definition, but also they focus on one aspect of  $\mathbb{K}$  (e.g., reachability, or partial correctness, or coinductive).

To overcome these limitations, we propose matching  $\mu$ -logic in Chapter 4 that not only unifies matching logic and reachability logic but also captures many important logics and calculi as its theories. We carry out an extensive study on its expressive power and show that LFP, separation logic with recursive predicates, modal  $\mu$ -calculus, temporal logics, dynamic logic,  $\lambda$ -calculus, and type systems can be defined in matching  $\mu$ -logic as theories. Therefore, matching  $\mu$ -logic is a good candidate for serving as the logical foundation of  $\mathbb{K}$ .

However, matching  $\mu$ -logic suffers from at least two main technical inconveniences.

Firstly, matching  $\mu$ -logic is more complex than necessary. As a *many-sorted* logic, matching  $\mu$ -logic has theories that can contain multiple, sometimes infinitely many *sorts*, each with its own carrier set in models. Matching  $\mu$ -logic uses many-sorted symbols, such as  $\sigma \in \Sigma_{s_1 \dots s_n, s}$ , of fixed arities, to build patterns of the appropriate sorts. This places a burden on implementations, which need to store the sorts and the symbols arities, carry out well-formedness checking, and implement a more-complex-than-needed proof checker. More importantly, the fact that the structure of a many-sorted universe is *hardwired* in matching  $\mu$ -logic actually makes it less general, when it comes to more complex sort structures such as parametric sorts or ordered sorts (i.e., subsorts). As an example, suppose we have a sort **Nat** of natural numbers and we want to define parametric lists. To do that, we have to introduce a new sort  $\text{List}\{s\}$  for every sort  $s$ , where  $\text{List}\{s\}$  is the sort of all the lists over elements of sort  $s$ . As a result, we introduce infinitely many sorts: **Nat**,  $\text{List}\{\text{Nat}\}$ ,  $\text{List}\{\text{List}\{\text{Nat}\}\}$ , etc.. Even though we can handle infinitely many sorts in theory, no implementation can handle them unless we come up with certain finite representations. In addition, we have to introduce infinitely many symbols for the common parametric operations over the parametric lists and define infinite many axioms for them (we only show the axioms for **append** as an example):

$$\begin{aligned} \text{nil}\{s\} &\in \Sigma_{\epsilon, \text{List}\{s\}} \\ \text{cons}\{s\} &\in \Sigma_{s \text{List}\{s\}, \text{List}\{s\}} \\ \text{append}\{s\} &\in \Sigma_{\text{List}\{s\} \text{List}\{s\}, \text{List}\{s\}} \\ \text{append}\{s\}(\text{cons}\{s\}(x : s, l : \text{List}\{s\}), l' : \text{List}\{s\}) \\ &= \text{cons}\{s\}(x : s, \text{append}\{s\}(l : \text{List}\{s\}, l' : \text{List}\{s\})) \end{aligned}$$

This is at best inconvenient for matching  $\mu$ -logic implementations. Either we incorporate parametric lists as a built-in feature into the implementations, or we invent some ad-hoc meta-level notation to specify the infinite theory of parametric lists in some finite way. Neither approach is optimal: the former lacks generality while the latter is heavy and superficial. Most many-sorted FOL systems forgo parametricity all together.

Secondly, matching  $\mu$ -logic enforces a strict separation among elements, sorts, and symbols. Intuitively, elements represent data; sorts represent the types of data; and symbols represent operations or predicates over data. Thus in matching  $\mu$ -logic, there is a distinction among data, types, and operations/predicates. While such a distinction is beneficial in a classic, algebraic setting, it can get inconvenient when it comes to, say, functional programming, where functions *are* data.

The purpose of this chapter is to introduce a methodological solution, called *applicative matching  $\mu$ -logic*, abbreviated as AML, which addresses the above technical inconveniences. We call AML a methodological solution because it is not an extension nor a modified version of matching  $\mu$ -logic. Instead, AML is an instance of matching  $\mu$ -logic where we restrict the use of sorts and many-sorted symbols to an absolute minimum. In AML, we define only one sort for all patterns and enforce all symbols to be constant symbols except for one, which is a binary symbol called *application* (see Section 7.2). We will show that AML has the same expressive power as matching  $\mu$ -logic despite it being much more simpler. What are hardwired in matching  $\mu$ -logic, such as sorts and many-sorted symbols, can now be axiomatically defined in AML as theories, just like how other mathematical instruments such as equality and functions are axiomatically defined in matching  $\mu$ -logic as theories. AML is matching  $\mu$ -logic at extreme simplicity without loss of expressive power.

It should be noted that we do not intend to argue which is better, AML or matching  $\mu$ -logic. As said, AML is not a new logic, but rather a restricted use of matching  $\mu$ -logic, a self-restraint version of it. This is why we call AML a *methodology*. Recall the above parametric lists example. Now we have two ways to define them. One is to use the full power of matching  $\mu$ -logic by introducing infinitely many sorts and symbols like we have seen earlier. The other is to use AML and axiomatically define sorts and the many-sorted symbols. Both approaches have their merits. The former reduces the handling of sorts to the logic while the latter can give us a finite theory, which is easier to handle in implementations. Another example is order-sorted structures, where a sort  $s$  can be a *subsort* of another sort  $s'$ , written  $s \leq s'$ . It is enforced that the carrier set of  $s$  is a subset of that of  $s'$ . Here, the many-sorted infrastructure of matching  $\mu$ -logic has few advantages but burdens. To define subsorts we need to introduce a function  $c_s^{s'} : s \rightarrow s'$ , called a coercion function from  $s$  to  $s'$ , for every  $s \leq s'$ . Furthermore, we need to specify that  $c_s^{s'}$  is injective. For any  $s \leq s' \leq s''$ , we also need to specify the following triangle property  $\text{inj}_s^{s''}(\text{inj}_s^{s'}(x : s)) = \text{inj}_s^{s''}(x : s)$ . All these extra mechanisms regarding the coercion functions will not be needed in AML.

In what follows we will present AML in full detail. We will present a reduction from matching  $\mu$ -logic to AML, which implies that AML has the same expressive power as matching  $\mu$ -logic. Then we will revisit the examples of subsorts and parametric sorts and use them to illustrate the unique advantage of AML (as a methodology) when it comes to specifying more advanced sort structures. Finally, we will present a proof checker based on AML. Thanks to the simplicity of AML, our proof checker has only 240 lines of code in Metamath [13].



## 7.2 AML AS AN INSTANCE OF MATCHING $\mu$ -LOGIC

AML is an instance of matching  $\mu$ -logic when, given a matching  $\mu$ -logic signature  $(S, \Sigma)$ , we require that  $S = \{\star\}$  and  $\Sigma = C \cup \{\mathbf{app}\}$ , where  $\star$  is a (dummy) sort,  $C$  is a set of constant symbols, and  $\mathbf{app} \in \Sigma_{\star, \star}$  is a binary symbol, called *application*. The syntax, semantics, and proof system of AML follows from the syntax, semantics, and proof system of matching  $\mu$ -logic over the above restricted signatures, but we can introduce more simplified notations for AML.

Firstly, we can drop the dummy sort  $\star$  and keep things unsorted. In particular, we have a set  $EV$  of unsorted element variables and a set  $SV$  of unsorted set variables. We denote them by  $x, X$ , etc., instead of  $x : \star$  or  $X : \star$ . Secondly,  $\mathbf{app}$  is the only non-constant symbol in AML so let us add it directly to the syntax of AML patterns. Furthermore, we write  $\varphi_1 \varphi_2$  for  $\mathbf{app}(\varphi_1, \varphi_2)$  and write  $\varphi_1 \varphi_2 \dots \varphi_n$  for  $(\dots (\varphi_1 \varphi_2) \dots \varphi_n)$ , i.e., application is left associative. Now we can present AML as follows:

**Definition 7.1.** An AML signature  $\Sigma$  is a set of constant symbols. The set of AML  $\Sigma$ -patterns, written  $\text{AMLPATTERN}(\Sigma)$ , is inductively defined by the following grammar:

$$\varphi ::= x \mid X \mid \sigma \mid \varphi_1 \varphi_2 \mid \neg \varphi \mid \varphi_1 \wedge \varphi_2 \mid \exists x. \varphi \mid \mu X. \varphi$$

where  $\mu X. \varphi$  requires that  $\varphi$  is positive in  $X$ . An AML  $\Sigma$ -model is a tuple  $(M, \{\_ \bullet\_M \_ \}, \{\sigma_M \mid \sigma \in \Sigma\})$  where  $M$  is a nonempty set,  $\_ \bullet\_M \_ : M \times M \rightarrow \mathcal{P}(M)$ , and  $\sigma_M \subseteq M$  for every  $\sigma \in \Sigma$ . An AML valuation  $\rho = (\rho_{EV}, \rho_{SV})$  where  $\rho_{EV} : EV \rightarrow M$  and  $\rho_{SV} : SV \rightarrow \mathcal{P}(M)$ . Given  $M$  and  $\rho$ , the evaluation of  $\varphi \in \text{AMLPATTERN}(\Sigma)$  is also denoted by  $|\varphi|_{M, \rho}$  and is defined the same way in matching  $\mu$ -logic.

The name of AML comes from applicative structures, which are algebras with a binary operation for application. Applicative structures are important in the study of combinations and  $\lambda$ -calculus. Here, we only show that applicative structures are an special case of AML models, where the application symbol is interpreted as a total function.

**Proposition 7.1.** *An applicative structure  $(A, \_ \bullet\_A \_)$  is a pair of a nonempty set  $A$  and a function  $\_ \bullet\_A \_ : A \times A \rightarrow A$  [32, Definition 5.1.1]. Then, applicative structures are AML  $\emptyset$ -models  $M$  where  $\text{card}(\mathbf{app}_M(a, b)) = 1$  for all  $a, b \in M$ .*

The common mathematical instruments such as sorts, equality, membership, and functions can be defined in AML as theories. Since we have shown that all of them can be defined in matching  $\mu$ -logic as theories, we only need to show that matching  $\mu$ -logic can be defined in AML as theories. We do that in Section 7.3.

### 7.3 DEFINING MATCHING $\mu$ -LOGIC IN AML

To define matching  $\mu$ -logic in AML, we need to define sorts and many-sorted symbols. The idea is straightforward. We first introduce a distinguished symbol  $\top_-$  called the inhabitant symbol, and write  $((\top_-) \varphi)$ , which is the result of applying  $\top_-$  to  $\varphi$  using the AML application symbol, as  $\top_\varphi$ . Then for every sort  $s$  we introduce it as a (constant) symbol in AML. Then the pattern  $\top_s$  is matched by all the elements of sort  $s$ . Any properties about sorts can thus be defined as theories about  $\top_-$ . For example,  $\top_s \neq \emptyset$  states that the carrier set of  $s$  is nonempty. The axiom  $\top_{s_1} \subseteq \top_{s_2}$  states that all elements of sort  $s_1$  have sort  $s_2$ , i.e.,  $s_1$  is a subsort of  $s_2$ .

Now we define in detail the reduction/translation from matching  $\mu$ -logic to AML. Let us fix a matching  $\mu$ -logic signature  $(S^{MmL}, \Sigma^{MmL})$  where  $EV^{MmL} = \{EV_s^{MmL}\}_{s \in S^{MmL}}$  and  $SV^{MmL} = \{SV_s^{MmL}\}_{s \in S^{MmL}}$  are the two  $S^{MmL}$ -indexed sets of element and set variables, respectively. Let  $EV^{AML} = \{x^s \mid x:s \in EV_s^{MmL}\}$  be the set of AML unsorted element variables. Similarly, let  $SV^{AML} = \{X^s \mid X:s \in SV_s^{MmL}\}$  be the set of AML unsorted set variables. We keep track of the original sorts as superscripts so we can restore the sort information after translation. We also feel free to use  $x, y, X, Y$ , etc. in AML whenever we are defining AML axioms. Let  $\Sigma^{AML} = \{[\_], \top\_ \} \cup S^{MmL} \cup \Sigma^{MmL}$ , where  $[\_]$  and  $\top_-$  are two distinguished symbols. Let  $\Gamma^{AML}$  be the AML theory that includes the following axioms:

(DEFINEDNESS, AML version)	$[x]$
(NONEMPTY CARRIER SET)	$\top_s \neq \emptyset$ for each $s \in S^{MmL}$
(SYMBOL ARITY)	$\sigma \top_{s_1} \dots \top_{s_n} \subseteq \top_s$ for each $\sigma \in \Sigma_{s_1 \dots s_n, s}^{MmL}$

We define the translation from matching  $\mu$ -logic  $(S^{MmL}, \Sigma^{MmL})$ -patterns to AML  $(\Sigma^{AML})$ -patterns as follows:

$$\begin{aligned}
AML(\varphi_s) &= wellsorted(\varphi_s) \rightarrow (AML_2(\varphi_s) = \top_s) \\
wellsorted(\varphi_s) &= \bigwedge_{x:s' \in EV^{MmL}} x^{s'} \in \top_s \wedge \bigwedge_{X:s' \in SV^{MmL}} X^{s'} \subseteq \top_s \\
AML_2(x:s) &= x^s \\
AML_2(X:s) &= X^s \\
AML_2(\sigma(\varphi_1, \dots, \varphi_n)) &= \sigma AML_2(\varphi_1) \dots AML_2(\varphi_n) \\
AML_2(\varphi_s \wedge \varphi'_s) &= AML_2(\varphi_s) \wedge AML_2(\varphi'_s) \\
AML_2(\neg \varphi_s) &= \neg_s AML_2(\varphi_s) \equiv \neg AML_2(\varphi_s) \wedge \top_s
\end{aligned}$$

$$AML_2(\exists x : s' . \varphi) = \exists x^s . (x^s \in \top_s) \wedge AML_2(\varphi)$$

$$AML_2(\mu X : s . \varphi) = \mu X^s . (AML_2(\varphi) \wedge \top_s)$$

$$AML(\Gamma) = \{AML(\psi) \mid \psi \in \Gamma\}$$

**Proposition 7.2.** *For any matching  $\mu$ -logic theory  $\Gamma$  and pattern  $\varphi$ ,  $\Gamma \models \varphi$  iff  $\Gamma^{AML} \cup AML(\Gamma) \models AML(\varphi)$ .*

## 7.4 CASE STUDY: DEFINING ADVANCED SORT STRUCTURES IN AML

We use simple examples to illustrate how to go beyond matching  $\mu$ -logic and handle more advanced sort structures in AML, such as subsorts, parametric sorts, function types, and dependent types, using the same infrastructure in Section 7.3.

### 7.4.1 Defining subsorts

As said, a subsort relation  $s \leq s'$  enforces the carrier set of  $s$  to be a subset of the carrier set of  $s'$ . In AML, the carrier set of  $s$  is specified by  $\top_s$ . Similarly, the carrier set of  $s'$  is specified by  $\top_{s'}$ . Thus, it is straightforward to capture the subsort relation  $s \leq s'$  in AML using the following axiom:

$$(\text{SUBSORT}) \quad \top_s \subseteq \top_{s'}$$

More interestingly, AML supports subsort overloading of operations. For example, let **Nat** and **Int** be two sorts with the axiom  $\top_{\text{Nat}} \subseteq \top_{\text{Int}}$  stating that **Nat** is a subsort of **Int**. We can define *plus* as an overloaded operation over **Nat** and **Int** as follows:

$$(\text{PLUS, ARITY 1}) \quad \text{plus } \top_{\text{Nat}} \top_{\text{Nat}} \subseteq \top_{\text{Nat}}$$

$$(\text{PLUS, ARITY 2}) \quad \text{plus } \top_{\text{Int}} \top_{\text{Int}} \subseteq \top_{\text{Int}}$$

Using the above axioms, we can prove that *plus*(1, 2) has sort both **Nat** and **Int** while *plus*(−1, −2) has only sort **Int** but not **Nat**.

It is known (see, e.g., [107]) that ordered sorts can be defined in a many-sorted setting, where the subsort relation is captured by the *coercion functions*  $c_s^{s'} \in \Sigma_{s,s'}$  for all  $s \leq s'$ . Intuitively,  $c_s^{s'}$  denotes the embedding from sort  $s$  to sort  $s'$ . This approach, however, is not practically useful, as noticed in [108, pp. 9]. We explain why by an example. Suppose there are three sorts  $s \leq s' \leq s''$ , a constant  $a$  of sort  $s$ , and a function  $f \in \Sigma_{s'',s''}$ . Then, the term  $f(x)$  has multiple parses when translated to FOL, e.g.:  $f(c_s^{s''}(a))$  and  $f(c_{s'}^{s''}(c_s^{s'}(a)))$ .

This means that all tools for OSA based on FOL need to do reasoning *modulo* the triangle property  $c_s^{s''}(a) = c_{s'}^{s''}(c_s^{s'}(a))$ , which is inconvenient and causes huge overhead. In contrast, AML provides a more succinct and native approach to handling subsorts, by directly defining subsort axioms, without needing to introduce coercion functions.

#### 7.4.2 Defining parametric sorts

A parametric sort, such as  $\text{List}\{s\}$ , can be viewed as a function over sorts. Indeed, given a sort  $s$ ,  $\text{List}\{s\}$  returns its list sort. Since AML treats sorts and functions as regular elements, parametric sorts can be directly defined as functions. Let us define an AML symbol  $\text{Sort}$  whose (intended) elements are sorts. We add an axiom  $\text{Nat} \in \text{Sort}$  so  $\text{Nat}$  becomes a sort. Then, we define a function  $\text{List}: \text{Sort} \rightarrow \text{Sort}$ , called *sort constructor*, which takes a sort  $s$  and produces the sort  $\text{List } s$  of lists parametric in  $s$ . Standard list operations can be also defined as functions:

$$\begin{aligned} \forall s : \text{Sort} . \exists l' : \text{List } s . \text{nil} &= l' \\ \forall s : \text{Sort} . \forall x : s . \forall l : \text{List } s . \exists l' : \text{List } s . \text{cons } x \ l &= l' \\ \forall s : \text{Sort} . \forall l_1 : \text{List } s . \forall l_2 : \text{List } s . \exists l' : \text{List } s . \text{append } l_1 \ l_2 &= l' \end{aligned}$$

Note that the above axioms are similar to the (FUNCTION) axioms in matching  $\mu$ -logic but here  $s$  is a variable ranging over  $\text{Sort}$ .

#### 7.4.3 Defining function types

Functions are also elements in AML, and function sorts can be built using a function (which is also a sort constructor like  $\text{List}$ )  $\text{Function}: \text{Sort} \times \text{Sort} \rightarrow \text{Sort}$ , with the following axiom

$$(\text{FUNCTION SORT}) \quad \forall s : \text{Sort} . \forall s' : \text{Sort} . \top_{\text{Function } s \ s'} = \exists f . f \wedge \forall x : s . \exists y : s' . f x = y$$

stating that  $\text{Function } s \ s'$  consists of all  $f$  that behaviors as a function from  $s$  to  $s'$ . As an example, we define two higher-order list operations: *fold* and *map*, which are common in functional programming languages.

$$\begin{aligned} \forall s : \text{Sort} . \forall s' : \text{Sort} . \text{fold} : \text{Function } s' \ s \ s' \times s' \times \text{List } s &\rightarrow s' \\ \forall f : \text{Function } s' \ s \ s' . \forall x : s' . \text{fold } f \ x \ \text{nil} &= x \end{aligned}$$

$$\begin{aligned}
&\forall g : \text{Function } s \ s' . \forall y : s . \forall l : \text{List } s . \text{fold } f \ x \ (\text{cons } y \ l) = \text{fold } f \ (fxy) \ l \\
&\forall s : \text{Sort} . \forall s' : \text{Sort} . \text{map} : \text{Function } s \ s' \times \text{List } s \rightarrow \text{List } s' \\
&\forall g : \text{Function } s \ s' . \text{map } g \ \text{nil} = \text{nil} \\
&\forall f : \text{Function } s' \ s' . \forall x : s' . \forall y : s . \forall l : \text{List } s . \text{map } g \ (\text{cons } y \ l) = \text{cons } (g \ y) \ (\text{map } g \ l)
\end{aligned}$$

#### 7.4.4 Defining dependent types

Dependent types are also functions over sorts/types except that the parameters are data instead of sorts. That, however, makes no big difference in AML, for it makes no distinction between elements, sorts, and operations at all. All of them are uniformly defined using patterns. Therefore, we can define dependent types the same way we define parametric sorts. As an example, suppose we want to define a dependent sort **MInt** of *machine integers*, such that **MInt**  $n$  for  $n \geq 1$  is the sort of machine integers of *size*  $n$ , i.e., natural numbers less than  $2^n$ . For clarity, we define a new sort **Size** for positive natural numbers and axiomatize **MInt** as follows:

$$\begin{aligned}
&\top_{\text{Size}} = \text{suc } \top_{\text{Nat}} \\
&\text{MInt} : \text{Size} \rightarrow \text{Sort} \\
&\forall n : \text{Size} . \top_{\text{MInt } n} = \exists x : \text{Nat} . x \wedge x < \text{power2 } n
\end{aligned}$$

where **power2** :  $\text{Nat} \rightarrow \text{Nat}$  (power of 2) and  $\_ < \_$  (less-than) are defined in the usual way. We can then define functions over machine integers, such as **mplus** and **mmult**, by defining their arities and then *reusing* the addition *plus* and the multiplication *mult* over natural numbers:

$$\begin{aligned}
&\forall n : \text{Size} . \text{mplus} : \text{MInt } n \times \text{MInt } n \rightarrow \text{MInt } (\text{suc } n) \\
&\forall n : \text{Size} . \forall x : \text{MInt } n \forall y : \text{MInt } n . \text{mplus } x \ y = \text{plus } x \ y \\
&\forall n : \text{Size} . \forall m : \text{Size} . \text{mmult} : \text{MInt } n \times \text{MInt } m \rightarrow \text{MInt } (\text{plus } n \ m) \\
&\forall n : \text{Size} . \forall m : \text{Size} . \forall x : \text{MInt } n \forall y : \text{MInt } m . \text{mmult } x \ y = \text{mult } x \ y
\end{aligned}$$

#### 7.5 AML PROOF CHECKER

We will present an AML proof checker implemented in metamath [13], which is a tiny language to state abstract mathematics and their proofs in a machine-checkable style. We will use Metamath to formalize AML and to encode its proofs. Metamath is known for its

```

1  $c \imp ( ) #Pattern |- $.
2
3  $v ph1 ph2 ph3 $.
4  ph1-is-pattern $f #Pattern ph1 $.
5  ph2-is-pattern $f #Pattern ph2 $.
6  ph3-is-pattern $f #Pattern ph3 $.
7  imp-is-pattern
8    $a #Pattern ( \imp ph1 ph2 ) $.
9
10 axiom-1
11   $a |- ( \imp ph1 ( \imp ph2 ph1 ) ) $.
12
13 axiom-2
14   $a |- ( \imp ( \imp ph1 ( \imp ph2 ph3 ) )
15             ( \imp ( \imp ph1 ph2 )
16                   ( \imp ph1 ph3 ) ) ) $.
17
18 ${
19   rule-mp.0 $e |- ( \imp ph1 ph2 ) $.
20   rule-mp.1 $e |- ph1 $.
21   rule-mp   $a |- ph2 $.
22 $}

23 imp-refl $p |- ( \imp ph1 ph1 )
24 $=
25   ph1-is-pattern ph1-is-pattern
26   ph1-is-pattern imp-is-pattern
27   imp-is-pattern ph1-is-pattern
28   ph1-is-pattern imp-is-pattern
29   ph1-is-pattern ph1-is-pattern
30   ph1-is-pattern imp-is-pattern
31   ph1-is-pattern imp-is-pattern
32   imp-is-pattern ph1-is-pattern
33   ph1-is-pattern ph1-is-pattern
34   imp-is-pattern imp-is-pattern
35   ph1-is-pattern ph1-is-pattern
36   imp-is-pattern imp-is-pattern
37   ph1-is-pattern ph1-is-pattern
38   ph1-is-pattern imp-is-pattern
39   ph1-is-pattern axiom-2
40   ph1-is-pattern ph1-is-pattern
41   ph1-is-pattern imp-is-pattern
42   axiom-1 rule-mp ph1-is-pattern
43   ph1-is-pattern axiom-1 rule-mp
44 $.

```

Figure 7.1: An extract of the Metamath formalization of AML

simplicity and efficient proof checking. Metamath proof checkers can be implemented in a few hundreds lines of code and can check thousands of theorems in a second. Our formalization follows closely the syntax of AML. We also need to formalize some metalevel operations such as free variables and capture-free substitution. An innovative contribution is a generic way to handling notations.

### 7.5.1 Metamath overview

At a high level, a Metamath source file consists of a list of *statements*. The main ones are:

1. *constant statements* (\$c) that declare Metamath constants;
2. *variable statements* (\$v) that declare Metamath variables, and *floating statements* (\$f) that declare their intended ranges;
3. *axiomatic statements* (\$a) that declare Metamath axioms, which can be associated with some *essential statements* (\$e) that declare the premises;
4. *provable statements* (\$p) that states a Metamath theorem and its proof.

Figure 7.1 defines the fragment of AML with only implications. We declare five constants in a row in line 1, where `\imp`, `(`, and `)` build the syntax, `#Pattern` is the type of patterns, and `|-` is the provability relation. We declare three metavariables of patterns in lines 3-6, and the syntax of implication  $\varphi_1 \rightarrow \varphi_2$  as `( \imp ph1 ph2 )` in line 7. Then, we define

AML proof rules as Metamath axioms. For example, lines 18-22 define the rule (MODUS PONENS). In line 23, we show an example (meta-)theorem and its formal proof in Metamath. The theorem states that  $\vdash \varphi_1 \rightarrow \varphi_1$  holds, and its proof (lines 25-43) is a sequence of labels referring to the previous axiomatic/provable statements.

Metamath proofs are very easy to proof-check, which is why we use it in our work. The proof checker reads the labels in order and push them to a *proof stack*  $S$ , which is initially empty. When a label  $l$  is read, the checker pops its premise statements from  $S$  and pushes  $l$  itself. When all labels are consumed, the checker checks whether  $S$  has exactly one statement, which should be the original proof goal. If so, the proof is checked. Otherwise, it fails.

As an example, we look at the first 5 labels of the proof in Figure 7.1, line 25:

```

                                // Initially, the proof stack  $S$  is empty
ph1-is-pattern                 //  $S = [ \text{\#Pattern ph1} ]$ 
ph1-is-pattern                 //  $S = [ \text{\#Pattern ph1} ; \text{\#Pattern ph1} ]$ 
ph1-is-pattern                 //  $S = [ \text{\#Pattern ph1} ; \text{\#Pattern ph1} ; \text{\#Pattern ph1} ]$ 
imp-is-pattern                 //  $S = [ \text{\#Pattern ph1} ; \text{\#Pattern ( \imp ph1 ph1 )} ]$ 
imp-is-pattern                 //  $S = [ \text{\#Pattern ( \imp ph1 ( \imp ph1 ph1 ) )} ]$ 
```

where we show the stack status in comments. The first label `ph1-is-pattern` refers to a  $\$f$ -statement without premises, so nothing is popped off, and the corresponding statement `\#Pattern ph1` is pushed to the stack. The same happens, for the second and third labels. The fourth label `imp-is-pattern` refers to a  $\$a$ -statement with two metavariables of patterns, and thus has 2 premises. Therefore, the top two statements in  $S$  are popped off, and the corresponding conclusion `\#Pattern ( \imp ph1 ph1 )` is pushed to  $S$ . The last label does the same, popping off two premises and pushing `\#Pattern ( \imp ph1 ( \imp ph1 ph1 ) )` to  $S$ . Thus, these five proof steps prove the wellformedness of  $\varphi_1 \rightarrow (\varphi_1 \rightarrow \varphi_1)$ .

## 7.5.2 Main components

We go through the formalization of AML and emphasize some highlights. The entire formalization has 240 lines of metamath code and can be found in Section 7.5.3.

**Formalizing AML syntax** The syntax of AML patterns is formalized below:

```

$c \bot \imp \app \exists \mu ( ) $.
var-is-pattern                 $a \#Pattern xX $.
symbol-is-pattern             $a \#Pattern sg0 $.
bot-is-pattern                 $a \#Pattern \bot $.
imp-is-pattern                 $a \#Pattern ( \imp ph0 ph1 ) $.
app-is-pattern                 $a \#Pattern ( \app ph0 ph1 ) $.
exists-is-pattern             $a \#Pattern ( \exists x ph0 ) $.
${ mu-is-pattern.0 $e \#Positive X ph0 $.
```

```
mu-is-pattern    $a #Pattern ( \mu X ph0 ) $.  $}
```

Note that we omit the declarations of metavariables (such as  $xX$ ,  $sg0$ , ...) because their meaning can be easily inferred. The only nontrivial case above is `mu-is-pattern`, where we require that  $ph0$  is positive in  $X$ , discussed below.

**Metalevel assertions** To formalize AML, we need the following metalevel operations and/or assertions:

1. positive (and negative) occurrences of variables;
2. free variables;
3. capture-free substitution;
4. application contexts;
5. notations.

Item 1 is needed to define the syntax of  $\mu X.\varphi$ , while Items 2-5 are needed to define the proof system. As an example, we show how to define capture-free substitution. We first define a Metamath constant

```
$c #Substitution $.
```

which serves as an assertion symbol. The intuition of `#Substitution` is that if we can prove `#Substitution ph ph' ph'' xX`, then we have  $ph \equiv ph'[ph''/xX]$ . The definition is given based on the structure of  $ph'$ . For example, the following defines `#Substitution` when  $ph'$  is an implication:

```
{ substitution-imp.0  $e #Substitution ph1 ph3 ph0 xX $.
  substitution-imp.1  $e #Substitution ph2 ph4 ph0 xX $.
  substitution-imp
    $a #Substitution ( \imp ph1 ph2 ) ( \imp ph3 ph4 ) ph0 xX $. }
```

When  $ph'$  is  $\exists x.\varphi$  or  $\mu X.\varphi$ , we need to consider  $\alpha$ -renaming to avoid variable capture. We show the case when  $ph'$  is  $\exists x.\varphi$  below:

```
substitution-exists-shadowed
  $a #Substitution ( \exists x ph1 ) ( \exists x ph1 ) ph0 x $.
{ $d xX x $.
  $d y ph0 $.
  substitution-exists.0 $e #Substitution ph2 ph1 y x $.
  substitution-exists.1 $e #Substitution ph3 ph2 ph0 xX $.
  substitution-exists
    $a #Substitution ( \exists y ph3 ) ( \exists x ph1 ) ph0 xX $. }
```



There are two cases. The first case `substitution-exists-shadowed` is when the substitution is shadowed. The second case `substitution-exists` is the general case, where we first rename  $x$  to a fresh variable  $y$  and then continue the substitution. The `$d`-statements state that the substitution is not shadowed and  $y$  is fresh.

**Supporting notations** Notations (e.g.,  $\neg$  and  $\wedge$ ) play an important role in AML. Many proof rules such as `(PROPAGATIONV)` and `(SINGLETON)` directly use notations. However, Metamath has no built-in support for defining notations. To define a notation, say  $\neg\varphi \equiv \varphi \rightarrow \perp$ , we need to (1) declare a constant `\not` and add it to the pattern syntax; (2) define the equivalence relation  $\neg\varphi \equiv \varphi \rightarrow \perp$ ; and (3) add a new case for `\not` to *every metalevel assertions*. While (1) and (2) are reasonable, we want to avoid (3) because there are many metalevel assertions and thus it creates duplication.

We implement an innovative and generic method that allows us to define *any notations* in a compact way. Our method is to declare a new constant `#Notation` and use it to capture the *congruence relation of sugaring/desugaring*. Using `#Notation`, it takes only three lines to define the notation  $\neg\varphi \equiv \varphi \rightarrow \perp$ :

```
$c \not $.
not-is-pattern $a #Pattern ( \not ph0 ) $.
not-is-sugar $a #Notation ( \not ph0 ) ( \imp ph0 \bot ) $.
```

where we declare the constant `\not`, add it to the pattern syntax, and then define the sugaring/desugaring equivalence  $\neg\varphi \equiv \varphi \rightarrow \perp$ . We define all notations as above using `#Notation`.

To make the above work, we need to state that `#Notation` is a congruence relation with respect to the syntax of patterns and all the other metalevel assertions. Firstly, we state that it is reflexive, symmetric, and transitive:

```
notation-reflexivity $a #Notation ph0 ph0 $.
${ notation-symmetry.0 $e #Notation ph0 ph1 $.
  notation-symmetry $a #Notation ph1 ph0 $. $}
${ notation-transitivity.0 $e #Notation ph0 ph1 $.
  notation-transitivity.1 $e #Notation ph1 ph2 $.
  notation-transitivity $a #Notation ph0 ph2 $. $}
```

And the following is an example where we state that `#Notation` is a congruence with respect to provability:

```
${ notation-provability.0 $e #Notation ph0 ph1 $.
  notation-provability.1 $e |- ph0 $.
  notation-provability $a |- ph1 $. $}
```

This way, we only need a *fixed* number of statements that state that `#Notation` is a congruence, making it more compact and less duplicated to define notations.

**Formalizing proof system** With metalevel assertions and notations, it is now straightforward to formalize the AML proof rules. We have seen the formalization of (MODUS PONENS) in Figure 7.1. In the following, we formalize the fixpoint proof rule (KANASTER-TARSKI), whose premises use capture-free substitution:

```
{ rule-kt.0 $e #Substitution ph0 ph1 ph2 X $.
  rule-kt.1 $e |- ( \imp ph0 ph2 ) $.
  rule-kt    $a |- ( \imp ( \mu X ph1 ) ph2 ) $. }
```

Note that these proof rules collectively define the provability predicate `|-`. We also add the following axiom so that `#Notation` also preserves provability:

```
{
  notation-proof.0 $e |- ph0 $.
  notation-proof.1 $e #Notation ph1 ph0 $.
  notation-proof    $a |- ph1 $.
}
```

### 7.5.3 Entire source code

We present the entire 240 lines of source code of the AML proof checker below.

```
$( AML Proof Checker in 240 lines (excluding this comment) $)

$c #Pattern #ElementVariable #SetVariable #Variable #Symbol $.
$c #Positive #Negative #Fresh #ApplicationContext #Substitution #Notation |- $.
$c \bot \imp \app \exists \mu ( ) $.
$v ph0 ph1 ph2 ph3 ph4 ph5 x y X Y xX yY sg0 $.

$( Syntax $)

ph0-is-pattern    $f #Pattern ph0 $.
ph1-is-pattern    $f #Pattern ph1 $.
ph2-is-pattern    $f #Pattern ph2 $.
ph3-is-pattern    $f #Pattern ph3 $.
ph4-is-pattern    $f #Pattern ph4 $.
ph5-is-pattern    $f #Pattern ph5 $.
x-is-element-var  $f #ElementVariable x $.
y-is-element-var  $f #ElementVariable y $.
X-is-element-var  $f #SetVariable X $.
Y-is-element-var  $f #SetVariable Y $.
xX-is-var         $f #Variable xX $.
yY-is-var         $f #Variable yY $.
sg0-is-symbol     $f #Symbol sg0 $.
```

```
element-var-is-var $a #Variable x $.
set-var-is-var      $a #Variable X $.
var-is-pattern      $a #Pattern xX $.
symbol-is-pattern   $a #Pattern sg0 $.
bot-is-pattern      $a #Pattern \bot $.
imp-is-pattern      $a #Pattern ( \imp ph0 ph1 ) $.
app-is-pattern      $a #Pattern ( \app ph0 ph1 ) $.
exists-is-pattern   $a #Pattern ( \exists x ph0 ) $.
${ mu-is-pattern.0 $e #Positive X ph0 $.
  mu-is-pattern     $a #Pattern ( \mu X ph0 ) $. }
```

\$( Positive occurrence \$)

```
positive-in-var      $a #Positive xX yY $.
positive-in-symbol   $a #Positive xX sg0 $.
positive-in-bot      $a #Positive xX \bot $.
${ positive-in-imp.0 $e #Negative xX ph0 $.
  positive-in-imp.1 $e #Positive xX ph1 $.
  positive-in-imp    $a #Positive xX ( \imp ph0 ph1 ) $. }
${ positive-in-app.0 $e #Positive xX ph0 $.
  positive-in-app.1 $e #Positive xX ph1 $.
  positive-in-app    $a #Positive xX ( \app ph0 ph1 ) $. }
${ positive-in-exists.0 $e #Positive xX ph0 $.
  positive-in-exists $a #Positive xX ( \exists x ph0 ) $. }
${ positive-in-mu.0 $e #Positive xX ph0 $.
  positive-in-mu     $a #Positive xX ( \mu X ph0 ) $. }
${ $d xX ph0 $.
  positive-disjoint $a #Positive xX ph0 $. }
```

\$( Negative occurrence \$)

```
negative-in-symbol $a #Negative xX sg0 $.
negative-in-bot    $a #Negative xX \bot $.
${ $d xX yY $.
  negative-in-var $a #Negative xX yY $. }
${ negative-in-imp.0 $e #Positive xX ph0 $.
  negative-in-imp.1 $e #Negative xX ph1 $.
  negative-in-imp    $a #Negative xX ( \imp ph0 ph1 ) $. }
${ negative-in-app.0 $e #Negative xX ph0 $.
  negative-in-app.1 $e #Negative xX ph1 $.
  negative-in-app    $a #Negative xX ( \app ph0 ph1 ) $. }
${ negative-in-exists.0 $e #Negative xX ph0 $.
  negative-in-exists $a #Negative xX ( \exists x ph0 ) $. }
${ negative-in-mu.0 $e #Negative xX ph0 $.
  negative-in-mu     $a #Negative xX ( \mu X ph0 ) $. }
${ $d xX ph0 $.
  negative-disjoint $a #Negative xX ph0 $. }
```

\$( Free variable \$)

```
fresh-in-symbol      $a #Fresh xX sg0 $.
```

```

fresh-in-bot          $a #Fresh xX \bot $.
fresh-in-exists-shadowed $a #Fresh x ( \exists x ph0 ) $.
fresh-in-mu-shadowed   $a #Fresh X ( \mu X ph0 ) $.
${ $d xX ph0 $.
  fresh-disjoint $a #Fresh xX ph0 $. $}
${ fresh-in-imp.0 $e #Fresh xX ph0 $.
  fresh-in-imp.1 $e #Fresh xX ph1 $.
  fresh-in-imp    $a #Fresh xX ( \imp ph0 ph1 ) $. $}
${ fresh-in-app.0 $e #Fresh xX ph0 $.
  fresh-in-app.1 $e #Fresh xX ph1 $.
  fresh-in-app    $a #Fresh xX ( \app ph0 ph1 ) $. $}
${ $d xX x $.
  fresh-in-exists.0 $e #Fresh xX ph0 $.
  fresh-in-exists   $a #Fresh xX ( \exists x ph0 ) $. $}
${ $d xX X $.
  fresh-in-mu $a #Fresh xX ( \mu X ph0 ) $. $}
${ fresh-in-substitution.0 $e #Fresh xX ph1 $.
  fresh-in-substitution.1 $e #Substitution ph2 ph0 ph1 xX $.
  fresh-in-substitution   $a #Fresh xX ph2 $. $}

$( Substitution $)

substitution-var-same      $a #Substitution ph0 xX ph0 xX $.
substitution-symbol       $a #Substitution sg0 sg0 ph0 xX $.
substitution-bot          $a #Substitution \bot \bot ph0 xX $.
substitution-identity     $a #Substitution ph0 ph0 xX xX $.
substitution-exists-shadowed $a #Substitution ( \exists x ph1 ) ( \exists x ph1 )
  ph0 x $.
substitution-mu-shadowed   $a #Substitution ( \mu X ph1 ) ( \mu X ph1 ) ph0 X $.
${ substitution-imp.0 $e #Substitution ph1 ph3 ph0 xX $.
  substitution-imp.1 $e #Substitution ph2 ph4 ph0 xX $.
  substitution-imp    $a #Substitution ( \imp ph1 ph2 ) ( \imp ph3 ph4 ) ph0 xX
    $. $}
${ substitution-app.0 $e #Substitution ph1 ph3 ph0 xX $.
  substitution-app.1 $e #Substitution ph2 ph4 ph0 xX $.
  substitution-app    $a #Substitution ( \app ph1 ph2 ) ( \app ph3 ph4 ) ph0 xX
    $. $}
${ $d xX x $.
  $d y ph0 $.
  substitution-exists.0 $e #Substitution ph2 ph1 y x $.
  substitution-exists.1 $e #Substitution ph3 ph2 ph0 xX $.
  substitution-exists   $a #Substitution ( \exists y ph3 ) ( \exists x ph1 ) ph0
    xX $. $}
${ $d xX X $.
  $d Y ph0 $.
  substitution-mu.0 $e #Substitution ph2 ph1 Y X $.
  substitution-mu.1 $e #Substitution ph3 ph2 ph0 xX $.
  substitution-mu    $a #Substitution ( \mu Y ph3 ) ( \mu X ph1 ) ph0 xX $. $}
${ yY-free-in-ph0 $e #Fresh yY ph0 $.
  ph1-definition $e #Substitution ph1 ph0 yY xX $.
  ${ substitution-fold.0 $e #Substitution ph2 ph1 ph3 yY $.

```

```
substitution-fold      $a #Substitution ph2 ph0 ph3 xX $. $}
${ substitution-unfold.0 $e #Substitution ph2 ph0 ph3 xX $.
  substitution-unfold  $a #Substitution ph2 ph1 ph3 yY $. $} $}
${ substitution-inverse.0 $e #Fresh xX ph0 $.
  substitution-inverse.1 $e #Substitution ph1 ph0 xX yY $.
  substitution-inverse  $a #Substitution ph0 ph1 yY xX $. $}
${ substitution-fresh.0 $e #Fresh xX ph0 $.
  substitution-fresh    $a #Substitution ph0 ph0 ph1 xX $. $}

$( Application contexts $)

application-context-var $a #ApplicationContext xX xX $.
${ $d xX ph1 $.
  application-context-app-left.0 $e #ApplicationContext xX ph0 $.
  application-context-app-left  $a #ApplicationContext xX ( \app ph0 ph1 ) $.
  $}
${ $d xX ph0 $.
  application-context-app-right.0 $e #ApplicationContext xX ph1 $.
  application-context-app-right  $a #ApplicationContext xX ( \app ph0 ph1 ) $.
  $}

$( Notations $)

notation-reflexivity $a #Notation ph0 ph0 $.
${ notation-symmetry.0 $e #Notation ph0 ph1 $.
  notation-symmetry    $a #Notation ph1 ph0 $. $}
${ notation-transitivity.0 $e #Notation ph0 ph1 $.
  notation-transitivity.1 $e #Notation ph1 ph2 $.
  notation-transitivity  $a #Notation ph0 ph2 $. $}
${ notation-positive.0 $e #Positive xX ph0 $.
  notation-positive.1 $e #Notation ph1 ph0 $.
  notation-positive     $a #Positive xX ph1 $. $}
${ notation-negative.0 $e #Negative xX ph0 $.
  notation-negative.1 $e #Notation ph1 ph0 $.
  notation-negative     $a #Negative xX ph1 $. $}
${ notation-fresh.0 $e #Fresh xX ph0 $.
  notation-fresh.1 $e #Notation ph1 ph0 $.
  notation-fresh       $a #Fresh xX ph1 $. $}
${ notation-substitution.0 $e #Substitution ph0 ph1 ph2 xX $.
  notation-substitution.1 $e #Notation ph3 ph0 $.
  notation-substitution.2 $e #Notation ph4 ph1 $.
  notation-substitution.3 $e #Notation ph5 ph2 $.
  notation-substitution  $a #Substitution ph3 ph4 ph5 xX $. $}
${ notation-application-context.0 $e #ApplicationContext xX ph0 $.
  notation-application-context.1 $e #Notation ph1 ph0 $.
  notation-application-context  $a #ApplicationContext xX ph1 $. $}
${ notation-proof.0 $e |- ph0 $.
  notation-proof.1 $e #Notation ph1 ph0 $.
  notation-proof    $a |- ph1 $. $}
${ notation-imp.0 $e #Notation ph0 ph2 $.
  notation-imp.1 $e #Notation ph1 ph3 $.
```

```
notation-imp $a #Notation ( \imp ph0 ph1 ) ( \imp ph2 ph3 ) $. $}
${ notation-app.0 $e #Notation ph0 ph2 $.
  notation-app.1 $e #Notation ph1 ph3 $.
  notation-app $a #Notation ( \app ph0 ph1 ) ( \app ph2 ph3 ) $. $}
${ notation-exists.0 $e #Notation ph0 ph1 $.
  notation-exists $a #Notation ( \exists x ph0 ) ( \exists x ph1 ) $. $}

$( Defining not, or, and $)

$c \not \or \and $.
not-is-pattern $a #Pattern ( \not ph0 ) $.
or-is-pattern $a #Pattern ( \or ph0 ph1 ) $.
and-is-pattern $a #Pattern ( \and ph0 ph1 ) $.
not-is-sugar $a #Notation ( \not ph0 ) ( \imp ph0 \bot ) $.
or-is-sugar $a #Notation ( \or ph0 ph1 ) ( \imp ( \not ph0 ) ph1 ) $.
and-is-sugar $a #Notation ( \and ph0 ph1 ) ( \not ( \or ( \not ph0 ) ( \not ph1
  ) ) ) $.

$( Proof system $)

proof-rule-prop-1 $a |- ( \imp ph0 ( \imp ph1 ph0 ) ) $.
proof-rule-prop-2 $a |- ( \imp ( \imp ph0 ( \imp ph1 ph2 ) ) ( \imp ( \imp ph0 ph1
  ) ( \imp ph0 ph2 ) ) ) $.
proof-rule-prop-3 $a |- ( \imp ( \imp ( \imp ph0 \bot ) \bot ) ph0 ) $.

${ proof-rule-mp.0 $e |- ( \imp ph0 ph1 ) $.
  proof-rule-mp.1 $e |- ph0 $.
  proof-rule-mp $a |- ph1 $. $}

${ proof-rule-exists.0 $e #Substitution ph0 ph1 y x $.
  proof-rule-exists $a |- ( \imp ph0 ( \exists x ph1 ) ) $. $}

${ proof-rule-gen.0 $e |- ( \imp ph0 ph1 ) $.
  proof-rule-gen.1 $e #Fresh x ph1 $.
  proof-rule-gen $a |- ( \imp ( \exists x ph0 ) ph1 ) $. $}

${ proof-rule-propagation-bot.0 $e #ApplicationContext xX ph0 $.
  proof-rule-propagation-bot.1 $e #Substitution ph1 ph0 \bot xX $.
  proof-rule-propagation-bot $a |- ( \imp ph1 \bot ) $. $}

${ proof-rule-propagation-or.0 $e #ApplicationContext xX ph0 $.
  proof-rule-propagation-or.1 $e #Substitution ph1 ph0 ( \or ph4 ph5 ) xX $.
  proof-rule-propagation-or.2 $e #Substitution ph2 ph0 ph4 xX $.
  proof-rule-propagation-or.3 $e #Substitution ph3 ph0 ph5 xX $.
  proof-rule-propagation-or $a |- ( \imp ph1 ( \or ph2 ph3 ) ) $. $}

${ proof-rule-propagation-exists.0 $e #ApplicationContext xX ph0 $.
  proof-rule-propagation-exists.1 $e #Substitution ph1 ph0 ( \exists y ph3 ) xX
    $.
  proof-rule-propagation-exists.2 $e #Substitution ph2 ph0 ph3 xX $.
  proof-rule-propagation-exists.3 $e #Fresh y ph0 $.
```

```

proof-rule-propagation-exists    $a |- ( \imp ph1 ( \exists y ph2 ) ) $. $}

${ proof-rule-frame.0 $e #ApplicationContext xX ph0 $.
  proof-rule-frame.1 $e #Substitution ph1 ph0 ph3 xX $.
  proof-rule-frame.2 $e #Substitution ph2 ph0 ph4 xX $.
  proof-rule-frame.3 $e |- ( \imp ph3 ph4 ) $.
  proof-rule-frame    $a |- ( \imp ph1 ph2 ) $. $}

${ proof-rule-prefixpoint.0 $e #Substitution ph0 ph1 ( \mu X ph1 ) X $.
  proof-rule-prefixpoint    $a |- ( \imp ph0 ( \mu X ph1 ) ) $. $}

${ proof-rule-kt.0 $e #Substitution ph0 ph1 ph2 X $.
  proof-rule-kt.1 $e |- ( \imp ph0 ph2 ) $.
  proof-rule-kt    $a |- ( \imp ( \mu X ph1 ) ph2 ) $. $}

${ proof-rule-set-var-substitution.0 $e #Substitution ph0 ph1 ph2 X $.
  proof-rule-set-var-substitution.1 $e |- ph1 $.
  proof-rule-set-var-substitution    $a |- ph0 $. $}

proof-rule-existence $a |- ( \exists x x ) $.
${ proof-rule-singleton.0 $e #ApplicationContext xX ph0 $.
  proof-rule-singleton.1 $e #ApplicationContext yY ph1 $.
  proof-rule-singleton.2 $e #Substitution ph3 ph0 ( \and x ph2 ) xX $.
  proof-rule-singleton.3 $e #Substitution ph4 ph1 ( \and x ( \not ph2 ) ) yY $.
  proof-rule-singleton    $a |- ( \not ( \and ph3 ph4 ) ) $. $}

```

## Chapter 8: PROOF-CERTIFYING PROGRAM EXECUTION

Unlike natural languages that allow vagueness and ambiguity, programming languages must be precise and unambiguous. Only with rigorous definitions of programming languages, called the *formal semantics*, can we guarantee the reliability, safety, and security of computing systems.

Our vision is thus an *ideal language framework* based on the formal semantics of programming languages. Shown in Figure 1.1, an ideal language framework is one where language designers only need to define the formal syntax and semantics of their language, and all language tools are automatically generated by the framework. The *correctness* of these language tools is established by generating complete mathematical proofs as certificates that can be automatically machine-checked by a trustworthy proof checker.

The  $\mathbb{K}$  framework (Section 2.15) is in pursuit of the above ideal vision. It provides a simple and intuitive front end language (i.e., a meta-language) for language designers to define the formal syntax and semantics of other programming languages. From such a formal language definition, the framework automatically generates a set of language tools, including a parser, an interpreter, a deductive verifier, a program equivalence checker, among many others [3, 4].  $\mathbb{K}$  has obtained much success in practice, and has been used to define the complete executable formal semantics of many large programming languages.

What is *missing* in  $\mathbb{K}$  (compared to the ideal vision in Figure 1.1) is its ability to generate proof objects as correctness certificates. The current  $\mathbb{K}$  implementation is a complex artifact with over 500,000 lines of code written in 4 programming languages, with new code committed on a weekly basis. Its code base includes complex data structures, algorithms, optimizations, and heuristics to support the various features such as defining formal language syntax using BNF grammar, defining computation configurations as constructor terms, defining formal semantics using rewrite rules, specifying arbitrary evaluation strategies, and defining the binding behaviors of binders (Section 2.15). The large code base and rich features make it challenging to formally verify the correctness of  $\mathbb{K}$ .

We will propose a practical approach to establishing the correctness of a complex language framework, such as  $\mathbb{K}$ , via proof object generation. Our approach consists of the following main components:

1. A small logical foundation of  $\mathbb{K}$  based on AML (Chapter 7);
2. Proof parameters that are provided by  $\mathbb{K}$  as the hints for proof generation;
3. A proof object generator that generates *proof objects* from proof parameters;



4. A fast and trustworthy third-party proof checker based on metamath (??) that verifies proof objects.

The key idea that makes our approach practical is that we establish the correctness not for the entire framework, but for each individual language tasks that it conducts, on a case-by-case basis. This idea is not limited to  $\mathbb{K}$  but also applicable to the existing language frameworks and/or formal semantics approaches.

## 8.1 APPROACH OVERVIEW

We give an overview of our approach via the following four main components: (1) a logical foundation of  $\mathbb{K}$  based on AML, (2) proof parameters, (3) proof object generation, and (4) a trustworthy proof checker.

**Logical foundation of  $\mathbb{K}$**  Our approach is based on AML, which is the logical foundation of  $\mathbb{K}$  in the following sense:

1. The  $\mathbb{K}$  definition (i.e., the language definition in Figure 1.1) of a programming language  $L$  corresponds to a theory  $\Gamma^L$  in AML, which, roughly speaking, consists of a set of logical symbols that represents the formal syntax of  $L$ , and a set of logical axioms that specify the formal semantics.
2. All language tools in Figure 1.1 and all language tasks that  $\mathbb{K}$  conducts are formally specified by AML patterns. For example, program execution is specified (in our approach) by the following pattern:

$$\varphi_{init} \Rightarrow_{exec} \varphi_{final} \tag{8.1}$$

where  $\varphi_{init}$  is the formula that specifies the initial state of the execution,  $\varphi_{final}$  specifies the final state, and  $\varphi_{init} \Rightarrow_{exec} \varphi_{final}$ , which is a notation for  $\varphi_{init} \rightarrow \diamond \varphi_{final}$ , states the (finite-step) rewriting relation between states.

3. The AML proof system defines the provability relation  $\vdash$  between theories and patterns. For example, the correctness of the above execution from  $\varphi_{init}$  to  $\varphi_{final}$  is witnessed by the formal proof:

$$\Gamma^L \vdash \varphi_{init} \Rightarrow_{exec} \varphi_{final} \tag{8.2}$$

Therefore, AML is the logical foundation of  $\mathbb{K}$ . The correctness of  $\mathbb{K}$  conducting one language task is reduced to the existence of a formal proof in AML. Such formal proofs are encoded as proof objects, discussed below.

**Proof parameters** A proof parameter is the necessary information that  $\mathbb{K}$  should provide to help generate proof objects. For program execution, such as Equation (8.2), the proof parameter includes the following information:

- the complete execution trace  $\varphi_0, \varphi_1, \dots, \varphi_n$ , where  $\varphi_0 \equiv \varphi_{init}$  and  $\varphi_n \equiv \varphi_{final}$ ; we call  $\varphi_0, \dots, \varphi_n$  the intermediate snapshots of the execution;
- for each step from  $\varphi_i$  to  $\varphi_{i+1}$ , the rewriting information that consists of the rewrite/semantic rule  $\varphi_{lhs} \Rightarrow_{exec} \varphi_{rhs}$  that is applied, and the corresponding substitution  $\theta$  such that  $\varphi_{lhs}\theta \equiv \varphi_i$ .

In other words, a proof parameter of a program execution trace contains the complete information about how such an execution is carried out by  $\mathbb{K}$ . The proof parameter, once generated by  $\mathbb{K}$ , is passed to the proof object generator to generate the corresponding proof object, discussed below.

**Proof object generation** In our approach, a proof object is an encoding of AML proofs. Proof objects are generated by a proof object generator from the proof parameters provided by  $\mathbb{K}$ . At a high level, a proof object for program execution, such as Equation (8.2), consists of:

1. the formalization of AML and its provability relation  $\vdash$  (see Section 8.1);
2. the formalization of the formal semantics  $\Gamma^L$  as a logical theory, which includes axioms that specify the rewrite/semantic rules  $\varphi_{lhs} \Rightarrow_{exec} \varphi_{rhs}$ ;
3. the formal proofs of all one-step executions, i.e.,  $\Gamma^L \vdash \varphi_i \Rightarrow_{exec} \varphi_{i+1}$  for all  $i$ ;
4. the formal proof of the final proof goal  $\Gamma^L \vdash \varphi_{init} \Rightarrow_{exec} \varphi_{final}$ .

Our proof objects have a linear structure, which implies a nice separation of concerns. Indeed, Item 1 is only about AML and is not specific to any programming languages/language tasks, so we only need to develop and proof-check it once and for all. Item 2 is specific to the language semantics  $\Gamma^L$  but is independent of the actual program executions, so it can be reused in the proof objects of various language executions for the same programming language  $L$ .

```

1  module TWO-COUNTERS
2    imports INT
3    syntax State ::= "<" Int "," Int ">"
4    configuration <T> $PGM:State </T>
5    rule <M, N> => <M -Int 1, N +Int M>
6        requires M >Int 0
7  endmodule

```

Figure 8.1: Running example TWO-COUNTERS.

**A small proof checker** A proof checker is a small program that checks whether the formal proofs encoded in a proof object are correct. The proof checker is the main trust base of our work. We use Metamath [13]—a third-party proof checking tool that is simple, fast, and trustworthy—to formalize AML and encode its formal proofs, as discussed in Section 8.1.

To sum up, our approach to establishing the correctness of  $\mathbb{K}$  is based on its logical foundation—AML. We formalize language semantics as logical theories, and program executions as formulas and proof goals, whose proof objects are automatically generated and proof-checked. Our proof objects have a linear structure that allows easy reuse of their components. The key characteristics of our logical-based approach are the following:

- It is *faithful* to the real  $\mathbb{K}$  implementation because proof objects are generated from proof parameters, which include all execution snapshots and the actual rewriting information, provided by  $\mathbb{K}$ .
- It is *practical* because proof objects are generated for each program executions on a case-by-case bases, avoiding the verification of the entire  $\mathbb{K}$ .
- It is *trustworthy* because the auto-generated proof objects are checked using the trustworthy third-party Metamath proof checker.

## 8.2 A RUNNING EXAMPLE

We introduce a simple running example called TWO-COUNTERS, shown in Figure 8.1. TWO-COUNTERS is a tiny language that defines a state machine with two counters. Its computation configuration is simply a pair  $\langle m, n \rangle$  of two integers  $m$  and  $n$ , and its semantics is defined by the following (conditional) rewrite rule:

$$\langle m, n \rangle \Rightarrow \langle m - 1, n + m \rangle \quad \text{if } m > 0 \quad (8.3)$$

Therefore, TWO-COUNTERS adds  $n$  by  $m$  and reduces  $m$  by 1. Starting from the initial

state  $\langle m, 0 \rangle$ , **TWO-COUNTERS** carries out  $m$  execution steps and terminates at the final state  $\langle 0, m(m+1)/2 \rangle$ , where  $m(m+1)/2 = m + (m-1) + \dots + 1$ . Although simple, **TWO-COUNTERS** uses the core features of defining formal syntax as grammars and formal semantics as rewrite rules.

The following shows a concrete program execution trace of **TWO-COUNTERS** starting from the initial state  $\langle 100, 0 \rangle$ :

$$\langle 100, 0 \rangle, \langle 99, 100 \rangle, \langle 98, 199 \rangle, \dots, \langle 1, 5049 \rangle, \langle 0, 5050 \rangle \quad (8.4)$$

To make  $\mathbb{K}$  generate the above execution trace, we need to follow these steps:

1. Prepare the initial state  $\langle 100, 0 \rangle$  in a source file, say `100.two-counters`.
2. Compile the formal semantics **TWO-COUNTERS** into an AML theory, explained in Section 8.3.
3. Use the  $\mathbb{K}$  execution tool `krun` and pass the source file to it:

```
$ krun 100.two-counters --depth N
```

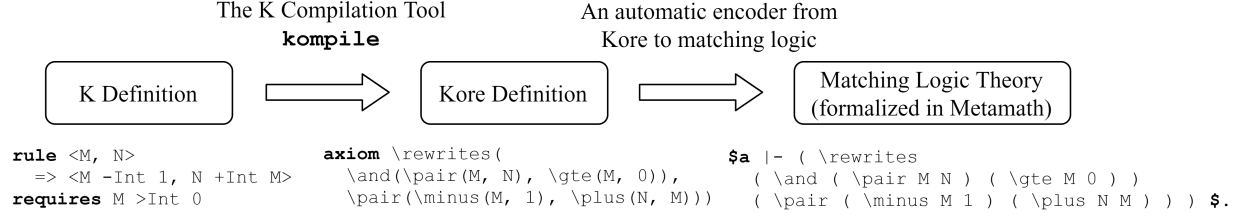
The option `--depth N` tells  $\mathbb{K}$  to execute for  $N$  steps and output the (intermediate) snapshot. By letting  $N$  be  $1, 2, \dots$ , we collect all snapshots in Equation (8.4).

The proof parameter of Equation (8.4) includes the additional rewriting information for each execution step. That is, we need to know the rewrite rule that is applied and the corresponding substitution. In **TWO-COUNTERS**, there is only one rewrite rule, and the substitution can be easily obtained by pattern matching, where we simply match the snapshot with the left-hand side of the rewrite rule.

Note that we regard  $\mathbb{K}$  as a “black box”. We are not interested in its complex internal algorithms. Instead, we hide such complexity by letting  $\mathbb{K}$  generate proof parameters that include enough information for proof object generation. This way, we create a separation of concerns between  $\mathbb{K}$  and proof object generation.  $\mathbb{K}$  can aim at optimizing the performance of the auto-generated language tools, *without* making proof object generation more complex.

### 8.3 COMPILING $\mathbb{K}$ INTO AML

To execute programs using  $\mathbb{K}$ , we need to compile the  $\mathbb{K}$  language definition for language  $L$  into an AML theory, written  $\Gamma^L$ . The existing  $\mathbb{K}$  compilation tool `kcompile` (explained shortly) is what compiles a  $\mathbb{K}$  language definition into an AML theory  $\Gamma^L$ , written in a formal

Figure 8.2: Automatic translation from  $\mathbb{K}$  to AML, via Kore

language called Kore. For legacy reasons, the Kore language is not the same as the syntax of AML but an axiomatic extension with equality, sorts, and rewriting. Thus, to formalize  $\Gamma^L$  in proof objects, we need to (1) formalize the AML theories of equality, sorts, and rewriting; and (2) automatically translate Kore definitions into the corresponding AML theories. Figure 8.2 shows the 2-phase translation from  $\mathbb{K}$  to AML, via Kore.

The first translation phase is from  $\mathbb{K}$  to Kore. To compile a  $\mathbb{K}$  definition such as `two-counters.k` in Figure 8.1, we pass it to the  $\mathbb{K}$  compilation tool `kcompile` as follows:

```
$ kcompile two-counters.k
```

The result is a compiled Kore definition `two-counters.kore`. We show the auto-generated Kore axiom in Figure 8.2 that corresponds to the rewrite rule in Equation (8.3). As we can see, Kore is a much lower-level language than  $\mathbb{K}$ , where the programming language concrete syntax and  $\mathbb{K}$ 's front end syntax are parsed and replaced by the abstract syntax trees, represented by the constructor terms.

The second translation phase is from Kore to AML. We develop an automatic encoder that translates Kore syntax into AML patterns. Since Kore is essentially the theory of equality, sorts, and rewriting, we can define the syntactic constructs of the Kore language as AMLnotations and theories.

## 8.4 GENERATING PROOFS FOR PROGRAM EXECUTION

In this section, we discuss how to generate proof objects for program execution, based on the formalization of AML and  $\mathbb{K}$ /Kore. The key step is to generate proof objects for *one-step executions*, which are then put together to build the proof objects for multi-step executions using the transitivity of the rewriting relation. Thus, we focus on the process of generating proof objects for one-step executions from the proof parameters provided by  $\mathbb{K}$ .

#### 8.4.1 Problem formulation

Consider the following  $\mathbb{K}$  definition that consists of  $K$  (conditional) rewrite rules:

$$S = \{t_k \wedge p_k \Rightarrow_{exec} s_k \mid k = 1, 2, \dots, K\}$$

where  $t_k$  and  $s_k$  are the left- and right-hand sides of the rewrite rule, respectively, and  $p_k$  is the rewriting condition. Consider the following execution trace:

$$\varphi_0, \varphi_1, \dots, \varphi_n \tag{8.5}$$

where  $\varphi_0, \dots, \varphi_n$  are snapshots. We let  $\mathbb{K}$  generate the following proof parameter:

$$\Theta \equiv (k_0, \theta_0), \dots, (k_{n-1}, \theta_{n-1}) \tag{8.6}$$

where for each  $0 \leq i < n$ ,  $k_i$  denotes the rewrite rule that is applied on  $\varphi_i$  ( $1 \leq k_i \leq K$ ) and  $\theta_i$  denotes the corresponding substitution such that  $t_{k_i}\theta_i = \varphi_i$ .

As an example, the rewrite rule of **TWO-COUNTERS**, restated below:

$$\langle m, n \rangle \Rightarrow_{exec} \langle m - 1, n + m \rangle \quad \text{if } m > 0 \quad // \text{ Same as Equation (8.3)}$$

has the left-hand side  $t_k \equiv \langle m, n \rangle$ , the right-hand side  $s_k \equiv \langle m - 1, n + m \rangle$ , and the condition  $p_k \equiv m \geq 0$ . Note that the right-hand side pattern  $s_k$  contains the arithmetic operations “+” and “−” that can be further evaluated to a value, if concrete instances of the variables  $m$  and  $n$  are given. Generally speaking, the right-hand side of a rewrite rule may include (built-in or user-defined) functions that are not constructors and thus can be further evaluated. We call such evaluation process a *simplification*.

#### 8.4.2 Applying rewrite rules and applying simplifications

In the following, we list all proof objects for one-step executions.

$$\begin{array}{ll} \Gamma^L \vdash \varphi_0 \Rightarrow s_{k_0}\theta_0 & // \text{ by applying } t_{k_0} \wedge p_{k_0} \Rightarrow s_{k_0} \text{ using } \theta_0 \\ \Gamma^L \vdash s_{k_0}\theta_0 = \varphi_1 & // \text{ by simplifying } s_{k_0}\theta_0 \\ \dots & \\ \Gamma^L \vdash \varphi_{n-1} \Rightarrow s_{k_{n-1}}\theta_{n-1} & // \text{ by applying } t_{k_{n-1}} \wedge p_{k_{n-1}} \Rightarrow s_{k_{n-1}} \text{ using } \theta_{n-1} \\ \Gamma^L \vdash s_{k_{n-1}}\theta_{n-1} = \varphi_n & // \text{ by simplifying } s_{k_{n-1}}\theta_{n-1} \end{array}$$

As we can see, there are two types of proof objects: one that proves the results of *applying rewrite rules* and one that *applies simplification*.

**Applying rewrite rules** The main steps in proving  $\Gamma^L \vdash \varphi_i \Rightarrow s_{k_i} \theta_i$  are (1) to *instantiate* the rewrite rule  $t_{k_i} \wedge p_{k_i} \Rightarrow s_{k_i}$  using the substitution

$$\theta_i = [c_1/x_1, \dots, c_m/x_m]$$

given in the proof parameter, and (2) to show that the (instantiated) rewriting condition  $p_{k_i} \theta_i$  holds. Here,  $x_1, \dots, x_m$  are the variables that occur in the rewrite rule and  $c_1, \dots, c_m$  are terms by which we instantiate the variables. For (1), we need to first prove the following lemma, called (FUNCTIONAL SUBSTITUTION) in [109], which states that  $\forall$ -quantification can be instantiated by functional patterns:

$$\frac{\forall \vec{x}. t_{k_i} \wedge p_{k_i} \Rightarrow s_{k_i} \quad \exists y_1. \varphi_1 = y_1 \quad \dots \quad \exists y_m. \varphi_m = y_m}{t_{k_i} \theta_i \wedge p_{k_i} \theta_i \Rightarrow s_{k_i} \theta_i} \quad y_1, \dots, y_m \text{ fresh}$$

Intuitively, the premise  $\exists y_1. \varphi_1 = y_1$  states that  $\varphi_1$  is a functional pattern because it equals to some element  $y_1$ .

If  $\Theta$  in Equation (8.6) is the correct proof parameter,  $\theta_i$  is the correct substitution and thus  $t_{k_i} \theta_i \equiv \varphi_i$ . Therefore, to prove the original proof goal for one-step execution, i.e.  $\Gamma^L \vdash \varphi_i \Rightarrow s_{k_i} \theta_i$ , we only need to prove that  $\Gamma^L \vdash p_{k_i} \theta_i$ , i.e., the rewriting condition  $p_{k_i}$  holds under  $\theta_i$ . This is done by simplifying  $p_{k_i} \theta_i$  to  $\top$ , discussed together with the simplification process in the following.

**Applying simplifications**  $\mathbb{K}$  carries out simplification exhaustively before trying to apply a rewrite rule, and simplifications are done by applying (oriented) equations. Generally speaking, let  $s$  be a functional pattern and  $p \rightarrow t = t'$  be a (conditional) equation, we say that  $s$  can be *simplified* w.r.t.  $p \rightarrow t = t'$ , if there is a sub-pattern  $s_0$  of  $s$  (written  $s \equiv C[s_0]$  where  $C$  is a context) and a substitution  $\theta$  such that  $s_0 = t\theta$  and  $p\theta$  holds. The resulting *simplified pattern* is denoted  $C[t'\theta]$ . Therefore, a proof object of the above simplification consists of two proofs:  $\Gamma^L \vdash s = C[t'\theta]$  and  $\Gamma^L \vdash p\theta$ . The latter can be handled recursively, by simplifying  $p\theta$  to  $\top$ , so we only need to consider the former.

The main steps of proving  $\Gamma^L \vdash s = C[t'\theta]$  are the following:

1. to find  $C$ ,  $s_0$ ,  $\theta$ , and  $t = t'$  in  $\Gamma^L$  such that  $s \equiv C[s_0]$  and  $s_0 = t\theta$ ; in other words,  $s$  can be simplified w.r.t.  $t = t'$  at the sub-pattern  $s_0$ ;

2. to prove  $\Gamma^L \vdash s_0 = t'\theta$  by instantiating  $t = t'$  using the substitution  $\theta$ , using the same (FUNCTIONAL SUBSTITUTION) lemma as above;
3. to prove  $\Gamma^L \vdash C[s_0] = C[t']$  using the transitivity of equality.

Finally, we repeat the above one-step simplifications until no sub-patterns can be simplified further. The resulting proof objects are then put together by the transitivity of equality.

## 8.5 DISCUSSION

We discuss the trust base of the auto-generated proof objects by pointing out the main threats to validity, caused by the limitations of our preliminary implementation. It should be noted that these limitations are about the implementation, and *not* our approach. We shall address these limitations in future work.

**Limitation 1: Need to trust Kore** Our current implementation is based on the existing  $\mathbb{K}$  compilation tool `kompile` that compiles  $\mathbb{K}$  into Kore definitions. Recall that Kore is a (legacy) formal language with built-in support for equality, sorts, and rewriting, and thus is different (and more complex) than the syntax of AML. By using Kore as the intermediate between  $\mathbb{K}$  and AML (Figure 8.2), we need to trust Kore and the  $\mathbb{K}$  compilation tool `kompile`. In the future, we will eliminate Kore entirely from the picture and formalize  $\mathbb{K}$  *directly*. To do that, we need to formalize the “front end matters” of  $\mathbb{K}$ , such as concrete programming language syntax and  $\mathbb{K}$  attributes, currently handled by `kompile`. That is, we need to formalize and generate proof objects for `kompile`.

**Limitation 2: Need to trust domain reasoning**  $\mathbb{K}$  has built-in support for domain reasoning such as integer arithmetic. Our current proof objects do not include the formal proofs of such domain reasoning, but instead regard them as assumed lemmas. In the future, we will incorporate the existing research on generating proof objects for SMT solvers [110] into our implementation, in order to generate proof objects also for domain reasoning; see also Section 10.4.

**Limitation 3: Do not support more complex  $\mathbb{K}$  features** Our current implementation only supports the core  $\mathbb{K}$  features of defining programming language syntax and of defining formal semantics as rewrite rules. Some more complex features, such as matching/unification modulo associativity and commutativity, are not supported. We should extend our algorithms in Section 8.4 with unification modulo these collection datatypes.



Table 8.1: Proof Generation and Proof Checking Performance: Program Execution

Program	Proof Generation			Proof Checking			Proof Size	
	sem	rewrite	total	logic	task	total	kLOC	MB
10.two-counters	5.95	12.19	18.13	3.26	0.19	3.44	963.8	77
20.two-counters	6.31	24.33	30.65	3.41	0.38	3.79	1036.5	83
50.two-counters	6.48	73.09	79.57	3.52	0.98	4.50	1259.2	100
100.two-counters	6.75	177.55	184.30	3.50	2.10	5.60	1635.6	130
add8	11.59	153.34	164.92	3.40	3.09	6.48	1986.8	159
factorial	3.84	34.63	38.46	3.57	0.90	4.47	1217.9	97
fibonacci	4.50	12.51	17.01	3.44	0.21	3.65	971.7	77
benchexpr	8.41	53.22	61.62	3.61	0.80	4.41	1191.3	95
benchsym	8.79	47.71	56.50	3.53	0.72	4.25	1163.4	93
benchtree	8.80	26.86	35.66	3.47	0.32	3.80	1021.5	81
langton	5.26	23.07	28.33	3.46	0.40	3.86	1048.0	84
mul8	14.39	279.97	294.36	3.48	7.18	10.66	3499.2	280
revlt	4.98	51.83	56.81	3.35	1.10	4.45	1317.4	105
revnat	4.81	123.44	128.25	3.37	5.28	8.65	2691.9	215
tautologyhard	5.16	400.89	406.05	3.55	14.50	18.04	6884.7	550

## 8.6 EVALUATION

In this section, we evaluate the performance of our implementation and discuss the experiment results, summarized in Table 8.1. We use two sets of benchmarks. The first is our running example **TWO-COUNTERS** with different inputs (10, 20, 50, and 100). The second is **REC** [111], which is a popular performance benchmark for rewriting engines. We evaluate both the performance of proof object *generation* and that of proof *checking*. Our implementation can be found in [112].

The main takeaways of our experiments are:

1. Proof checking is efficient and takes a few seconds; in particular, the *task-specific* checking time is often less than one second (“task” column in Table 8.1).
2. Proof object generation is slower and takes several minutes.
3. Proof objects are huge, often of millions LOC (wrapped at 80 characters).

**Proof object generation** We measure the proof object generation time as the time to generate complete proof objects following the algorithms in Section 8.4, from the compiled language semantics (i.e., Kore definitions) and proof parameters. As shown in Table 8.1,

proof generation takes around 17–406 seconds on the benchmarks, and the average is 107 seconds.

Proof object generation can be divided into two parts: that of the language semantics  $\Gamma^L$  and that of the (one-step and multi-step) program executions. Both parts are shown in Table 8.1 under columns “sem” and “rewrite”, respectively. For the same language, the time to generate language semantics  $\Gamma^L$  is the same (up to experimental error). The time for executions is linear to the number of steps.

**Proof checking** Proof checking is efficient and takes a few seconds on our benchmarks. We can divide the proof checking time into two parts: that of the logical foundation and that of the actual program execution tasks. Both parts are shown in Table 8.1 under columns “logic” and “task”. The “logic” part includes formalization of AML and its basic theories, and thus is *fixed* for any programming language and program and has the same proof checking time (up to experimental error). The “task” part includes the language semantics and proof objects for the one-step and multi-step executions. Therefore, the time to check the “task” part is a *more valuable and realistic* measure, and according to our experiments, it is often less than 1 second, making it acceptable in practice.

As a pleasant surprise, the time for “task-specific” proof checking is roughly the same as the time that it takes  $\mathbb{K}$  to parse and execute the programs. In other words, there is *no significant performance difference* on our benchmarks between running the programs directly in  $\mathbb{K}$  and checking the proof objects.

There exists much potential to optimize the performance of proof checking and make it even faster than program execution. For example, in our approach proof checking is an *embarrassingly parallel problem*, because each meta-theorems can be proof-checked entirely independently. Therefore, we can significantly reduce the proof checking time by running multiple checkers in parallel.

## Chapter 9: PROOF-CERTIFYING FORMAL VERIFICATION

In Chapter 8 we have discussed how to generate AML proof objects for program execution based on the  $\mathbb{K}$  framework. Here we push the idea further and apply it to formal verification. We first review the verification algorithm (Algorithm 9.1) that automates the reachability proof rules in Figure 2.12 in Section 9.1. Then, we describe the main procedures for proof certificate generation, including those for symbolic execution (Section 9.2), pattern subsumption (Section 9.3), and coinductive reasoning (Section 9.4). We discuss the interesting implementation details in Section 9.5 and present evaluation in Section 9.6.

### 9.1 VERIFICATION ALGORITHM OVERVIEW

We show the language-agnostic verification algorithm of  $\mathbb{K}$  in Algorithm 9.1, which is an optimized implementation of the reachability proof rules in Figure 2.12. The input  $R$  is a set of reachability claims to be verified, including the necessary invariant claims. The algorithm consists of two procedures: **proveAllClaims** and **proveOneClaim**. The first calls the latter on every input claim. The procedure **proveOneClaim** starts by checking the subsumption  $\Gamma^L \vdash \varphi \rightarrow \varphi'$ . If it holds, then the claim  $\varphi \Rightarrow_{\text{reach}} \varphi'$  is trivially true. If the direct subsumption is false, we perform symbolic execution for one step from  $\varphi$  to get a set  $Q$  of all its successors. Both **successors** (Line 8) and **successors<sub>R</sub>** (Line 12) calculate all the successors of a given configuration. **successors** uses only the formal semantics in  $\Gamma^L$  while **successors<sub>R</sub>** uses both the semantic rules and the claims in  $R$ . This is sound because at least one real semantic step has been made in Line 8. If  $Q \neq \emptyset$ , the algorithm *nondeterministically* chooses a frontier pattern  $\psi_{\text{front}}$  from  $Q$  and checks whether  $\psi_{\text{front}}$  satisfies  $\varphi'$ . If yes, the verification succeeds (Line 11). Otherwise, the algorithm symbolically executes  $\psi_{\text{front}}$  and continues with its successors (Line 12), following both the semantic rules and the claims in  $R$ . This is sound because in Line 8, before the **while** loop, we have computed the successors of  $\varphi$  using only the semantic rules. Immediately after that, when we entered the loop for the first time, we chose one successor of  $\varphi$ , say  $\varphi_s$  (Line 10). Therefore, we have  $\Gamma^L \vdash \varphi \Rightarrow_{\text{reach}}^+ \varphi_s$ . Since at least one execution step has been made, the (TRANSITIVITY) rule in Figure 2.12 moves all the circularity claims (i.e., the claims in  $R$ ) to the axiom set so they can be used as semantic axioms in computing further successors (Line 12).

In this work we only consider verifying reachability claims on one path, called *one-path reachability*. The procedure **proveOneClaim** nondeterministically chooses a frontier pattern  $\psi_{\text{front}}$  from all the possible successors in  $Q$  (see Line 10), which amounts to looking for

**Algorithm 9.1:** Algorithm for Proving One-Path Reachability Claims

---

```

1 procedure proveAllClaims( $R$ )
2   foreach  $\varphi \Rightarrow_{reach} \varphi' \in R$  do
3     if proveOneClaim( $R, \varphi \Rightarrow_{reach} \varphi'$ ) = failure then return failure;
4   return success;
5 // a nondeterministic algorithm for proving one reachability claim
6 procedure proveOneClaim( $R, \varphi \Rightarrow_{reach} \varphi'$ )
7   if  $\Gamma^L \vdash \varphi \rightarrow \varphi'$  then return success;
8    $Q := \text{successors}(\varphi)$ ;
9   while  $Q \neq \emptyset$  do
10     $\psi_{front} := \text{choose}(Q)$ ; // a nondeterministic choice
11    if  $\Gamma^L \vdash \psi_{front} \rightarrow \varphi'$  then return success;
12    else  $Q := \text{successors}_R(\psi_{front})$ ;
13  return failure;

```

---

the one execution path that satisfies the reachability claim. Therefore, `proveOneClaim` is successful if there exists a successful run, in which case a particular execution trace is found as the witness of the claim being verified. Based on this execution trace, we can generate an AML proof certificate. On the other hand, `proveOneClaim` fails if there is no successful run. A deterministic implementation of `proveOneClaim` will require backtracking for all the nondeterministic choice(s) in Line 10. In this work we consider proof generation for *successful* verification runs so we always assume that there is a successful run of Line 10. Finally, the procedure `proveAllClaims` calls `proveOneClaim` on all claims in  $R$  and the entire verification is successful if `proveAllClaims` is successful.

Our goal is to generate proof certificates for Algorithm 9.1. For clarity, we divide it into three proof generation procedures:

- Generating proofs for symbolic execution (corresponding to Lines 8 and 12);
- Generating proofs for pattern subsumption (corresponding to Line 11);
- Generating proofs for coinductive reasoning (corresponding to the use of  $R$  in Line 12).

We discuss these proof generation procedures in the following.

## 9.2 GENERATING PROOFS FOR SYMBOLIC EXECUTION

We use  $\Gamma^L$  to denote the AML theory of the formal semantics of a language  $L$ .

**Problem formulation** Consider the following  $\mathbb{K}$  language definition, which consists of  $K$  (conditional) rewrite rules:

$$\{lhs_k \wedge q_k \Rightarrow_{exec}^1 rhs_k \mid k = 1, 2, \dots, K\} \subseteq \Gamma^L$$

where  $lhs_k$  represents the left-hand side of the rewrite rule,  $rhs_k$  represents the right-hand side, and  $q_k$  denotes the rewriting condition. Unconditional rules can be regarded as conditional rules where  $q_k$  is  $\top$ . The notation  $\Rightarrow_{exec}^1$  stands for one-step execution, defined as  $\varphi_1 \Rightarrow_{exec}^1 \varphi_2 \equiv \varphi_1 \rightarrow \bullet \varphi_2$ .

In symbolic execution, program configurations often appear with their corresponding *path conditions*. We represent them as  $t \wedge p$ , where  $t$  is a configuration and  $p$  is a logical constraint/predicate over the free variables of  $t$ . We call such patterns *constrained terms*. Constrained terms are AML patterns.

Unlike concrete execution, symbolic execution can create *branches*. Therefore, we formulate proof generation for symbolic execution as follows. The input is an initial constrained term  $t \wedge p$  and a list of final constrained terms  $t_1 \wedge p_1, \dots, t_n \wedge p_n$ , which are returned by  $\mathbb{K}$  as the result(s) of symbolic executing  $t$  under the condition  $p$ . Each  $t_i \wedge p_i$  represents one possible execution trace. Our goal is to generate a proof for the following goal:

$$\Gamma^L \vdash t \wedge p \Rightarrow_{exec} (t_1 \wedge p_1) \vee \dots \vee (t_n \wedge p_n) \quad (\text{Goal})$$

In other words, here we are certifying the correctness of the **successors** (and **successors<sub>R</sub>**) methods used by Algorithm 9.1, by proving that  $\Gamma^L \vdash \varphi \Rightarrow_{exec} \mathbf{successors}(\varphi)$ , which further implies  $\Gamma^L \vdash \varphi \Rightarrow_{reach} \mathbf{successors}(\varphi)$ .

**Proof hints** To help generating the proof of (Goal), we instrument  $\mathbb{K}$  to output *proof hints*, which include rewriting details such as the semantic rules that are applied and the substitutions that are used. Formally, the proof hint for the  $j$ -th rewrite step consists of:

- a constrained term  $t_j^{\text{hint}} \wedge p_j^{\text{hint}}$  that represents the configuration before step  $j$ ;
- $l_j$  constrained terms  $t_{j,1}^{\text{hint}} \wedge p_{j,1}^{\text{hint}}, \dots, t_{j,l_j}^{\text{hint}} \wedge p_{j,l_j}^{\text{hint}}$  that represent the configurations after step  $j$ , where for each  $1 \leq l \leq l_j$ , we also annotate it with an index  $1 \leq k_{j,l} \leq K$  that refers to the  $k_{j,l}$ -th semantic rule in  $\Gamma^L$  and a substitution  $\theta_{j,l}$ ;
- an (optional) constrained term  $t_j^{\text{rem}} \wedge p_j^{\text{rem}}$ , where  $p_j^{\text{rem}} \equiv p_j^{\text{hint}} \wedge \neg (p_{j,1}^{\text{hint}} \vee \dots \vee p_{j,l_j}^{\text{hint}})$ , called the *remainder* of step  $j$ , representing the part/fragment of the original configuration that “gets stuck”.

Intuitively, each constrained term  $t_{j,l}^{\text{hint}} \wedge p_{j,l}^{\text{hint}}$  represents one execution branch, obtained by applying the  $k_{j,l}$ -th semantic rule (i.e.,  $lhs_{k_{j,l}} \wedge q_{k_{j,l}} \Rightarrow_{exec}^1 rhs_{k_{j,l}}$ ) using substitution  $\theta_{j,l}$ . The remainder  $t_j^{\text{rem}} \wedge p_j^{\text{rem}}$  denotes the branch where no semantic rules can be applied further and thus the execution gets stuck. Note that  $t_j^{\text{hint}}$  and  $t_j^{\text{rem}}$  may not be syntactically identical, even if no execution has been made. This is because the path condition  $p_j^{\text{rem}}$  is stronger than the original condition  $p_j^{\text{hint}}$ . With this stronger path condition,  $\mathbb{K}$  can simplify  $t_j^{\text{hint}}$  further to  $t_j^{\text{rem}}$ .

From the above proof hint, we can generate the proof for one symbolic execution step. For example, the following specifies the  $j$ -th symbolic execution step:

$$\Gamma^L \vdash (t_j^{\text{hint}} \wedge p_j^{\text{hint}}) \Rightarrow_{exec} (t_{j,1}^{\text{hint}} \wedge p_{j,1}^{\text{hint}}) \vee \dots \vee (t_{j,l_j}^{\text{hint}} \wedge p_{j,l_j}^{\text{hint}}) \vee (t_j^{\text{rem}} \wedge p_j^{\text{rem}}) \quad (\text{Step}_j)$$

Recall that  $\Rightarrow_{exec}$  is the reflexive and transitive closure of the one-step execution relation, so the remainder configuration can appear at the right-hand side even if no execution step has been made on that branch. To prove  $(\text{Step}_j)$ , we need to prove the correctness of each execution branch, for  $1 \leq l \leq l_j$ :

$$\Gamma^L \vdash (t_j^{\text{hint}} \wedge p_j^{\text{hint}}) \Rightarrow_{exec}^1 (t_{j,l}^{\text{hint}} \wedge p_{j,l}^{\text{hint}}) \quad (\text{Branch}_{j,l})$$

And for the remainder branch, we need to prove

$$\Gamma^L \vdash (t_j^{\text{hint}} \wedge p_j^{\text{rem}}) \rightarrow (t_j^{\text{rem}} \wedge p_j^{\text{rem}}) \quad (\text{Remainder}_j)$$

**Proof generation procedures** Therefore, the proof goal (Goal) for symbolic execution is proved in three phases:

- (Phase 1) Prove  $(\text{Branch}_{j,l})$  and  $(\text{Remainder}_j)$  for each step  $j$  and branch  $1 \leq l \leq l_j$ .
- (Phase 2) Combine  $(\text{Branch}_{j,l})$  and  $(\text{Remainder}_j)$  to obtain a proof of  $(\text{Step}_j)$ .
- (Phase 3) Combine  $(\text{Step}_j)$  to obtain a proof of (Goal).

**Remark 9.1.** We need many lemmas about the program execution relation “ $\Rightarrow_{exec}$ ” when we generate proof certificates for symbolic execution. The most important and relevant lemmas are stated explicitly in this paper. In total, 196 new lemmas are formally encoded, and their proofs have been completely worked out based on the Metamath formalization of the proof system [112, 113], as a part of the new contribution of the paper. These lemmas can be easily reused for future development.

In the following, we explain each proof generation step.

**Phase 1: Proving** (Branch<sub>*j,l*</sub>) **and** (Remainder<sub>*j*</sub>) Recall that (Branch<sub>*j,l*</sub>) is obtained by applying the  $k_{j,l}$ -th semantic rule from the language semantics (where  $1 \leq k_{j,l} \leq K$ ):

$$lhs_{k_{j,l}} \wedge q_{k_{j,l}} \Rightarrow_{exec}^1 rhs_{k_{j,l}}$$

From the proof hint, we know that the corresponding substitution is  $\theta_{j,l}$ . Therefore, we instantiate the semantic rule using  $\theta_{j,l}$  and obtain the following result

$$\Gamma^L \vdash lhs_{k_{j,l}}\theta_{j,l} \wedge q_{k_{j,l}}\theta_{j,l} \Rightarrow_{exec}^1 rhs_{k_{j,l}}\theta_{j,l} \quad (9.1)$$

where we use  $t\theta$  to denote the result of applying the substitution  $\theta$  to  $t$ . Note that  $q_{k_{j,l}}\theta_{j,l}$  is a predicate on the free variables of Equation (9.1) that holds on the left-hand side, by propositional reasoning, it also holds on the right-hand side. Therefore, we prove that:

$$\Gamma^L \vdash lhs_{k_{j,l}}\theta_{j,l} \wedge q_{k_{j,l}}\theta_{j,l} \Rightarrow_{exec}^1 rhs_{k_{j,l}}\theta_{j,l} \wedge q_{k_{j,l}}\theta_{j,l} \quad (9.2)$$

To proceed, we need the following lemma:

**Lemma 9.1** ( $\Rightarrow_{exec}^1$  Consequence).

$$\frac{\Gamma^L \vdash \varphi \rightarrow \varphi' \quad \Gamma^L \vdash \varphi' \Rightarrow_{exec}^1 \psi' \quad \Gamma^L \vdash \psi' \rightarrow \psi}{\Gamma^L \vdash \varphi \Rightarrow_{exec}^1 \psi}$$

Intuitively, Lemma 9.1 allows us to strengthen the left-hand side and/or weaken the right-hand side of an execution relation. Using Lemma 9.1, and by comparing our proof goal (Branch<sub>*j,l*</sub>) with Equation (9.2), we only need to prove the following two implications between constrained terms, which we call *subsumptions*:

$$\underbrace{\Gamma^L \vdash (t_j^{\text{hint}} \wedge p_{j,l}^{\text{hint}}) \rightarrow (lhs_{k_{j,l}}\theta_{k_{j,l}} \wedge q_{k_{j,l}}\theta_{k_{j,l}})}_{\text{left-hand side strengthening}} \quad \underbrace{\Gamma^L \vdash (rhs_{k_{j,l}}\theta_{k_{j,l}} \wedge q_{k_{j,l}}\theta_{k_{j,l}}) \rightarrow (t_j^{\text{hint}} \wedge p_{j,l}^{\text{hint}})}_{\text{right-hand side weakening}}$$

These subsumption proofs are common in our proof generation procedure (e.g. (Remainder<sub>*j*</sub>) is also a subsumption). We elaborate on subsumption proofs in Section 9.3.

**Phase 2: Proving** (Step<sub>*j*</sub>) We combine the proofs for each branch and the remainder as follows:

$$\Gamma^L \vdash t_j^{\text{hint}} \wedge p_{j,1}^{\text{hint}} \Rightarrow_{exec}^1 t_{j,1}^{\text{hint}} \wedge p_{j,1}^{\text{hint}} \quad (\text{Branch}_{j,1})$$

$$\begin{array}{ll}
\vdots & \\
\Gamma^L \vdash t_j^{\text{hint}} \wedge p_{j,l_j}^{\text{hint}} \Rightarrow_{\text{exec}}^1 t_{j,l_j}^{\text{hint}} \wedge p_{j,l_j}^{\text{hint}} & (\text{Branch}_{j,l_j}) \\
\Gamma^L \vdash t_j^{\text{hint}} \wedge p_j^{\text{rem}} \rightarrow t_j^{\text{rem}} \wedge p_j^{\text{rem}} & (\text{Remainder}_j)
\end{array}$$

Note that our proof goal ( $\text{Step}_j$ ) uses “ $\Rightarrow_{\text{exec}}$ ”, while the above use either one-step execution (“ $\Rightarrow_{\text{exec}}^1$ ”) or implication (“ $\rightarrow$ ”). The following lemma allows us to turn one-step execution and implication (i.e. “zero-step execution”) into the reflexive-transitive execution relation “ $\Rightarrow_{\text{exec}}$ ”:

**Lemma 9.2** ( $\Rightarrow_{\text{exec}}$  Introduction).

$$\frac{\Gamma^L \vdash \varphi \rightarrow \psi}{\Gamma^L \vdash \varphi \Rightarrow_{\text{exec}} \psi} \quad \frac{\Gamma^L \vdash \varphi \Rightarrow_{\text{exec}}^1 \psi}{\Gamma^L \vdash \varphi \Rightarrow_{\text{exec}} \psi}$$

Then, we need to verify that the disjunction of all path conditions in the branches (including the remainder) is implied from the initial path condition:

$$\Gamma^L \vdash p_j^{\text{hint}} \rightarrow p_{j,1}^{\text{hint}} \vee \dots \vee p_{j,l_j}^{\text{hint}} \vee p_j^{\text{rem}} \quad (9.3)$$

The above implication includes only logical constraints and no configuration terms, and thus involves only *domain reasoning*. Therefore, we translate it into an equivalent FOL formula and delegate it to SMT solvers, such as Z3 [97].

From Equation (9.3), we can prove that the left-hand side of ( $\text{Step}_j$ ),  $t_j^{\text{hint}} \wedge p_j^{\text{hint}}$ , can be broken down into  $l_j + 1$  branches by propositional reasoning:

$$\Gamma^L \vdash (t_j^{\text{hint}} \wedge p_j^{\text{hint}}) \rightarrow (t_j^{\text{hint}} \wedge p_{j,1}^{\text{hint}}) \vee \dots \vee (t_j^{\text{hint}} \wedge p_{j,l_j}^{\text{hint}}) \vee (t_j^{\text{hint}} \wedge p_j^{\text{rem}}) \quad (9.4)$$

Note that the right-hand side of Equation (9.4) is exactly the disjunction of all the left-hand sides of ( $\text{Branch}_{j,l}$ ) and ( $\text{Remainder}_j$ ). Therefore, to prove the proof goal ( $\text{Step}_j$ ), we use the following lemma, which allows us to combine the executions in different branches into one (we will also need a consequence rule for  $\Rightarrow_{\text{exec}}$  like Lemma 9.1, which is derivable from Lemmas 9.1 and 9.2):

**Lemma 9.3** ( $\Rightarrow_{\text{exec}}$  Merge).

$$\frac{\Gamma^L \vdash \varphi_1 \Rightarrow_{\text{exec}} \psi_1 \quad \dots \quad \Gamma^L \vdash \varphi_n \Rightarrow_{\text{exec}} \psi_n}{\Gamma^L \vdash \bigvee_{i=1}^n \varphi_i \Rightarrow_{\text{exec}} \bigvee_{i=1}^n \psi_i}$$



**Phase 3: Proving (Goal)** We are now ready to generate the final proof certificate for symbolic execution. At a high level, the proof uses the reflexivity and transitivity of the program execution relation  $\Rightarrow_{exec}$ . Therefore, our proof generation method is an iterative procedure. We start with the reflexivity of  $\Rightarrow_{exec}$ , that is:

$$\Gamma^L \vdash (t \wedge p) \Rightarrow_{exec} (t \wedge p) \quad (9.5)$$

Then, we repeatedly apply the following steps to *symbolically execute* the right-hand side of Equation (9.5), until it becomes the same as the right-hand side of (Goal):

1. Suppose we have obtained a proof certificate for

$$\Gamma^L \vdash (t \wedge p) \Rightarrow_{exec} (t_1^{im} \wedge p_1^{im}) \vee \dots \vee (t_m^{im} \wedge p_m^{im}) \quad (9.6)$$

where  $t_1^{im}, p_1^{im}$ , etc. represent the intermediate configurations and constraints, respectively.

2. Look for a (Step<sub>j</sub>) claim of the form

$$\Gamma^L \vdash (t_j^{hint} \wedge p_j^{hint}) \Rightarrow_{exec} (t_{j,1}^{hint} \wedge p_{j,1}^{hint}) \vee \dots \vee (t_{j,l_j}^{hint} \wedge p_{j,l_j}^{hint}) \vee (t_j^{rem} \wedge p_j^{rem}) \quad (\text{Step}_j)$$

such that  $t_j^{hint} \wedge p_j^{hint} \equiv t_i^{im} \wedge p_i^{im}$ , for some intermediate constrained term  $t_i^{im} \wedge p_i^{im}$ . Without loss of generality, let us assume that  $i = 1$ , i.e., the first intermediate constrained term  $t_1^{im} \wedge p_1^{im}$  can be rewritten/executed using (Step<sub>j</sub>).

3. Symbolically execute  $t_1^{im} \wedge p_1^{im}$  in Equation (9.6) for one step by applying (Step<sub>j</sub>), and obtain the following proof:

$$\begin{aligned} \Gamma^L \vdash (t \wedge p) \Rightarrow_{exec} & \underbrace{(t_{j,1}^{hint} \wedge p_{j,1}^{hint}) \vee \dots \vee (t_{j,l_j}^{hint} \wedge p_{j,l_j}^{hint}) \vee (t_j^{rem} \wedge p_j^{rem})}_{\text{right-hand side of (Step}_j\text{)}} \\ & \vee \underbrace{(t_2^{im} \wedge p_2^{im}) \vee \dots \vee (t_m^{im} \wedge p_m^{im})}_{\text{same as Equation (9.6)}} \end{aligned}$$

Finally, after all symbolic execution steps are applied, we check if the resulting proof goal is the same as (Goal), potentially after permuting the disjuncts on the right-hand side. If yes, then the proof generation method succeeds and we generate a proof certificate for (Goal). Otherwise, the proof generation method fails, indicating potential mistakes made by  $\mathbb{K}$ 's symbolic execution engine.

### 9.3 GENERATING PROOFS FOR PATTERN SUBSUMPTION

It is common in generating proof certificates for symbolic execution that we need to generate the proof certificates for implications between constrained terms. We call such implications *subsumptions*. Formally, a subsumption has the form  $\Gamma^L \vdash (t \wedge p) \rightarrow (t' \wedge p')$ . We reduce it into the following two sub-goals that are sufficient for the subsumption to hold:

$$\Gamma^L \vdash p \rightarrow p' \qquad \Gamma^L \vdash p \rightarrow (t = t')$$

To prove the first sub-goal  $\Gamma^L \vdash p \rightarrow p'$ , we note that both  $p$  and  $p'$  are logical constraints. Therefore, its proof is delegated to external SMT solvers. To prove the second sub-goal  $\Gamma^L \vdash p \rightarrow (t = t')$ , we first try an SMT solver with all constructors abstracted to uninterpreted functions. If the SMT solver proves the goal with such abstraction, our proof generation method succeeds. Otherwise, we break down  $t$  and  $t'$  into sub-terms. Specifically, if  $t \equiv f(t_1, \dots, t_n)$  and  $t' \equiv f(t'_1, \dots, t'_n)$ , we reduce the sub-goal into a set of goals:

$$\Gamma^L \vdash p \rightarrow (t_1 = t'_1) \quad \dots \quad \Gamma^L \vdash p \rightarrow (t_n = t'_n)$$

Then we call our proof generation method recursively on the above sub-goals. Note that the second type of sub-goals corresponds to the *unification* between  $t$  and  $t'$ .

Our method here for pattern subsumption is incomplete but covers most simplifications done by  $\mathbb{K}$ . Generally speaking, it is undecidable to prove such subsumptions as it requires to prove first-order theorems in an initial algebra of an equational/algebraic specification. However, there exist techniques that are shown to be effective in automating *inductive theorem proving*, such as Maude ITP [59], which can be integrated by our work in the future.

### 9.4 GENERATING PROOFS FOR COINDUCTION

Recall that the verification algorithm (Algorithm 9.1) performs symbolic execution from the left-hand side of each claim until all branches are subsumed by the right-hand side. While the proof generation procedures in previous sections Sections 9.2 and 9.3 can cover symbolic execution already, the missing part is line 12 in Algorithm 9.1, where we apply not the semantic rules but the claims in  $R$  to perform symbolic execution, which forms a circular argument. Our purpose is to generate proof certificates that justify the soundness of such circular reasoning, by showing that the algorithm is performing a coinduction on the (potentially infinite) execution trace.

We start with the simplest case when  $R$  has only one claim  $\varphi \Rightarrow_{\text{reach}} \psi$ . We assume that

we have already rewritten  $\varphi$  to some intermediate configuration  $\varphi'$  using at least one steps (so logically speaking, the set of claims  $R = \{\varphi \Rightarrow_{\text{reach}} \psi\}$  has been flushed to the reachability logic axiom set by (TRANSITIVITY) in Figure 2.12):

$$\Gamma^L \vdash \varphi \Rightarrow_{\text{reach}}^+ \varphi' \quad (9.7)$$

Further, suppose that the proof hint indicates that we need to apply the original claim  $\varphi \Rightarrow_{\text{reach}} \psi$  (as a coinduction hypothesis) to  $\varphi'$ . We generate a proof certificate for this single step

$$\Gamma^L \vdash \Box(\forall \text{freeVar}(\varphi, \psi) . \varphi \Rightarrow_{\text{reach}} \psi) \rightarrow \varphi' \Rightarrow_{\text{reach}} \varphi'' \quad (9.8)$$

where  $\text{freeVar}(\varphi, \psi)$  is the set of all free variables in  $\varphi$  and  $\psi$ . Intuitively, we instantiate all the free variables using the substitution specified by the proof hint, where  $\varphi''$  is the result of applying the claim  $\varphi \Rightarrow_{\text{reach}} \psi$  as a regular semantic rule on  $\varphi'$ . Recall that Equation (9.8) is the encoding of the reachability judgment  $\{\varphi \Rightarrow_{\text{reach}} \psi\} \vdash_{\emptyset}^{\text{reach}} \varphi' \Rightarrow \varphi''$  (see ?? and the discussion followed).

Now, we apply (TRANSITIVITY) to Equations (9.7) and (9.8) and obtain the proof certificate for

$$\Gamma^L \vdash \Box(\forall \text{freeVar}(\varphi, \psi) . \varphi \Rightarrow_{\text{reach}} \psi) \rightarrow \varphi \Rightarrow_{\text{reach}}^+ \varphi''$$

which is the encoding of the reachability judgment  $\vdash_{\{\varphi \Rightarrow_{\text{reach}} \psi\}}^{\text{reach}} \varphi \Rightarrow \varphi''$ , where  $\varphi \Rightarrow_{\text{reach}} \psi$  belongs to the circularity set. Then, we reuse the proof generation procedure in Section 9.2 to generate the proof certificate for the symbolic execution of  $\varphi''$ , except that now there is an additional premise  $\Box(\forall \text{freeVar}(\varphi, \psi) . \varphi \Rightarrow_{\text{reach}} \psi)$  that encodes the semantics of circularity.

Finally, if the verification algorithm successfully terminates, we will obtain the proof certificate

$$\Gamma^L \vdash \Box(\forall \text{freeVar}(\varphi, \psi) . \varphi \Rightarrow_{\text{reach}} \psi) \rightarrow \varphi \Rightarrow_{\text{reach}} \psi$$

which by (CIRCULARITY), derives  $\Gamma^L \vdash \varphi \Rightarrow_{\text{reach}} \psi$ , as desired.

Generally speaking, Algorithm 9.1 allows to have  $n$  claims in  $R = \{\varphi_1 \Rightarrow_{\text{reach}} \psi_1, \dots, \varphi_n \Rightarrow_{\text{reach}} \psi_n\}$  and their proofs could arbitrarily invoke each other's coinduction hypothesis. This is called *set circularity*, which is derivable in reachability logic (see [114, Lemma 5])

$$(\text{SET CIRCULARITY}) \quad \frac{A \vdash_R^{\text{reach}} \varphi \Rightarrow \psi \quad \text{for all } (\varphi \Rightarrow \psi) \in R}{A \vdash_{\emptyset}^{\text{reach}} \varphi \Rightarrow \psi \quad \text{for all } (\varphi \Rightarrow \psi) \in R}$$

Here, all the claims in  $R$  are simultaneously added to the circularity set, featuring a mutual coinduction among all the coinduction hypotheses. Our current implementation does not

support (SET CIRCULARITY) in its full generality. We assume that the proof of each claim only invokes itself as the coinduction hypothesis. This is not a restriction in theory because using [114, Lemma 5], any proof using (SET CIRCULARITY) can be mechanically translated to one using only (CIRCULARITY), which is fully supported by our implementation.

## 9.5 DISCUSSION

Here we provide some interesting details about the implementation and discuss its limitations.

### 9.5.1 Trust base and proof checkers

There is an intrinsic distinction between mechanically proving/checking/verifying the correctness of a tool and *trusting* that it is correct. Formal verification *transfers* the trust on the system in question to that on the verifier, which in some cases can be more complex than the system being verified. The system can itself be a verifier, which can then be verified/certified further, following the once-and-for-all or case-by-case approaches above. Most state-of-the-art verified/verifying tools, including ours, involve a large number of nontrivial logical transformations and/or encodings of a formal system into another. In the end, they produce proof certificates that can be automatically checked by a proof checker, which belongs to the *trust base*. The simpler and smaller the proof checker is, the higher trustworthiness we achieve.

Most existing works use a proof assistant such as Coq [11] or Isabelle [115] to encode and check the final proof certificates. While proof assistants are commonly used in specifying and reasoning about computer systems, they are complex artifacts. For example, Coq has 200,000 lines of OCaml, and the safety-critical kernel still has 18,000 lines [116]. It means that if Coq is used as the final proof checker, there is *at least* 18,000 lines of OCaml code to be trusted. It is difficult for us to find the statistics for other proof assistants and/or theorem provers but we expect they are similar.

Metamath [13], on the other hand, is a *tiny* language that can express theorems in abstract mathematics, accompanied by proofs that can be checked by a program, called a Metamath verifier. Internally, the Metamath verifier behaves like an automaton with a stack. Axioms and theorems are associated with unique labels and a proof is a sequence of such labels. To check a proof, one maintains a stack that is empty initially, scans the proof, and pushes/pops the axioms and/or the hypotheses/conclusions of theorems accordingly. If in the end the stack contains exactly one statement that is identical to the theorem being proved, the proof

is checked. In particular, it does not need to do any complex inference such as pattern matching or unification, making proof checking very simple. As a result, Metamath has dozens of independently-developed verifiers. [13] lists 19 of them, some of which are very small: 550 lines of C#, 400 lines of Haskell, 380 lines of Lua, and 350 lines of Python. As a proof-of-concept, we also implemented a Metamath verifier in 740 lines of Rust [117], which supports both regular and compressed proofs, and used it in our experiments.

In our work, we use Metamath to encode the proof certificates. Also, we build on an existing formalization of AML and its proof system in 240 lines of Metamath code [112]. As for what counts as the *actual proof checker* in our approach, there can be different opinions, depending on whether Metamath is regarded as a programming language, or as another calculus whose inference system is implemented in a mainstream language, on top of which the proof system of AML is formalized. If Metamath is considered as a programming language, our proof checker has 240 lines. Otherwise, our proof checker consists of the 240-line Metamath definition plus an implementation of Metamath (550 lines of C#, 400 lines of Haskell, etc.), which in total has fewer than 1000 lines.

In our (maybe biased) view, there is no reason to *not* regard Metamath as a programming language like C# and Haskell. Metamath is much simpler than (almost) all programming languages. The fact that Metamath has many independent implementations using different programming languages makes it depend *less* on any particular programming language and its runtime environment, such as compilers and underlying operating systems. Metamath is also bootstrapping, in the sense that the executable of its own verifier (as a piece of machine code run on x86-64 Linux) is formally defined in Metamath itself [118, Section 6]. What is the highest possible correctness guarantee that we can expect from a proof checker? [118] proposes five possible levels to which we can prove the correctness of the checker, from the level of a logical rendering of the code to that of the logic gates that make up the computer and even the fabrication process relative to some electrical or physical model (although one may not want to do so because the result will be too specific to that particular computer or digital setup). It is clearly out of the scope of this paper to address all the above questions. The meta-point we want to make here is that proof checking systems such as Metamath have perhaps not received the attention they deserve from the formal verification and theorem proving community.

Finally, we should clarify that the proof checker is not the only code that needs to be trusted with the current implementation of our approach. While we do eliminate the need to trust the verification algorithm, which accounts for about 120,000 lines of Haskell code, we still need to trust that the  $\mathbb{K}$  frontend is generating the correct logical encoding of the reachability claims from the user input.

### 9.5.2 Implementation

Firstly, we implemented a higher-level tactic language for writing proofs about types/sorts, from which the lower-level Metamath proofs are constructed. Note that  $\mathbb{K}$  operates in a sorted setting while AML is unsorted. Instead, sorts are defined axiomatically using theories. To bridge this gap and reduce human engineering effort, we developed and used the tactic language to automate the generation of all the sort-related proofs. For example, to specify that the free variables  $x$  and  $y$  in a pattern  $\varphi$  have sorts  $s_1$  and  $s_2$ , respectively, we write  $\vdash (x : s_1 \wedge y : s_2) \rightarrow \varphi$ , where  $x : s_1$  and  $y : s_2$  are predicates, stating that  $x$  and  $y$  belong to the inhabitants of  $s_1$  and  $s_2$ , respectively. Now, suppose we have proved  $\vdash x : s_1 \rightarrow \psi$  and  $\vdash (y : s_2 \wedge x : s_1) \rightarrow (\psi \rightarrow \varphi)$  and we want to prove  $\vdash (x : s_1 \wedge y : s_2) \rightarrow \varphi$  using the following propositional lemma:

$$\frac{\vdash \theta \rightarrow \varphi \quad \vdash \theta \rightarrow (\varphi \rightarrow \psi)}{\vdash \theta \rightarrow \psi}$$

The tactic language will automatically rearrange the sort premises by proving that  $\vdash (x : s_1 \wedge y : s_2) \leftrightarrow y : s_2 \wedge x : s_1$ . A lot of such simple but tedious sort-related proofs are handled by the tactic language.

Secondly, we developed a library of 196 lemmas about the rewriting and reachability relations such as Lemma 9.2 in Chapter 9. These lemmas were proved manually in Metamath in  $\sim 4,000$  lines and have been added to the existing Metamath database of AML. Note that all these lemmas are checked by the Metamath verifiers so they do not belong to the trust base.

Thirdly, we implemented several optimizations for constructing proof certificates to improve performance. To avoid reproducing a (sub)-proof over and over again, we cache an incomplete work-in-progress proof when its size exceeds a certain threshold and add it as a lemma, which can be used in future proofs to reduce duplicates. To save runtime memory, we represent proof trees as directed acyclic graphs (DAGs) where the common subtrees are shared. When we apply an intermediate lemma or combine multiple DAGs, we use a greedy algorithm to merge the subtrees that have the same conclusion. Even with these optimizations, proofs are still huge (in the order of tens of megabytes), which is primarily due to the space-inefficient text-based encoding. To reduce the proof sizes further, we can compress the proofs using a generic compression tool such as `xz` [119], which provides  $>95\%$  reduction in size; see Section 9.6 for more details.

The  $\mathbb{K}$  deductive verifier consists of a powerful symbolic execution tool that supports many complex features such as evaluation order, conditional rewriting, “otherwise” rules (which are catch-all rules if no other semantic rules can be applied), user-defined contexts, unification

modulo axioms, etc. Our current prototype implementation supports proof generation for a significant subset of these features. For evaluation orders,  $\mathbb{K}$  specifies them using strictness attributes (Section 2.15), which are reduced to a special case of conditional rewriting, which is supported by our tool. The “otherwise” rules are also reduced to conditional rewriting where the condition states that no other semantic rules are applicable, and thus are also supported by our tool.  $\mathbb{K}$  also provides a more advanced (but also much less often used) way to define evaluation orders using explicit user-defined contexts, which is not supported by our tool yet. Finally, unification modulo maps (i.e., unification modulo associativity, commutativity, and units) is supported. Currently, the logical encoding of a  $\mathbb{K}$  semantics is computed by a frontend tool called `kompile` (see Figure 8.2), which lacks a clear documentation of the axioms it generates. This makes developing the proof generation procedure harder because we need to manually find suitable classes of axioms in `kompile`’s output. Therefore, we expect supporting proof generation for large real-world  $\mathbb{K}$  developments to be a long-term endeavor, which involves a formalization of `kompile` and requires a close collaboration with the  $\mathbb{K}$  team (see Section 9.5.3 for more discussion on `kompile`).

### 9.5.3 Trust base of $\mathbb{K}$

$\mathbb{K}$  is a complicated artifact under active development. Among its 550,000 lines of code base, roughly 40,000 lines are for the frontend, implemented in Java. There is also 160,000 lines of C++/Java code that focuses mainly on efficient concrete program execution. The most relevant code base is the 120,000-line Haskell backend that supports symbolic reasoning and formal verification. The language-agnostic deductive verifier is implemented in the Haskell backend of  $\mathbb{K}$ .

The  $\mathbb{K}$  frontend provides an intuitive frontend syntax that allows to write formal semantics more easily. For example, the frontend syntax swallows the entire concrete syntax of the programming language being defined and allows language designers to use directly the concrete syntax in writing the semantic rules, without needing to write their abstract syntax trees. Also, the frontend syntax includes shortcuts and notations for writing program configurations. In a semantic rule, only the necessary part of a configuration needs to be explicitly mentioned, while the other part can be omitted and automatically inferred by  $\mathbb{K}$ . The frontend also implements type inference for the variables in semantic rules, so the users usually do not need to explicitly specify the variable types.

All the above frontend shortcuts and notations will be eliminated by the frontend of  $\mathbb{K}$ . The frontend tool `kompile` translates the formal language semantics into an intermediate formal language called Kore [120], which is used to specify patterns and axioms. `kompile` parses

all the concrete syntax into abstract syntax trees, represented as patterns. It also infers the omitted parts of configurations in semantic rules and the types of all the variables. In the end, `kompile` produces one Kore definition— as one source file `definition.kore`—that includes the entire AML encoding of the formal language semantics. The compiled Kore file is then passed to  $\mathbb{K}$ 's backends to generate the corresponding language tools.

Therefore, Kore behaves as the intermediate interface between the frontend and the backends. It is also the boundary between the informal and formal worlds. Since Kore is a formal specification language for writing AML theories, the formal semantics of a Kore definition *is* the AML theory that it defines. However, the frontend syntax of  $\mathbb{K}$  (as shown in Figure 2.13) does not (yet) have a formal semantics. Its meaning is completely determined by `kompile`, which lacks a formal specification.

In this paper, we are interested in certifying *backend* correctness. More precisely, we are certifying the language-agnostic deductive verifier, implemented by the Haskell backend. Previously, the correctness of formal verification in  $\mathbb{K}$  depends on the 120,000-line Haskell backend and its internal verification algorithm (Algorithm 9.1) as well as optimized, complex algorithms for symbolic execution and pattern matching/subsumption. By generating proof certificates for these algorithms, we eliminate them from the trust base.

We should also clarify that the entire trust base for *end-to-end* verification in  $\mathbb{K}$  is still large and should be further reduced in the future. Firstly, the `kompile` tool belongs to the trust base. Secondly, the automatic encoder (developed in [113]) that translates Kore into Metamath belongs to the trust base (Figure 8.2), although the translation is very simple; it only parses the Kore definition and prints it in the Metamath format. Thirdly, the formalization of AML in Metamath belongs to the trust base, which is very small (240 lines). However, all the *backend* algorithms are no longer in the trust base. They are certified by AML proofs and the proof checker.

#### 9.5.4 Future directions

We identify some main future directions of the current work.

Firstly, as discussed in Section 9.5.3, the frontend tool `kompile` needs to be trusted. It is not satisfying, because the frontend consists of roughly 40,000 lines of Java, while many tasks that it performs, such as configuration inference and completion, can also be formalized as AML proofs, the same way how program execution and deductive verification are AML proofs. In the long run, we see no reason to not formalize the *entire*  $\mathbb{K}$  frontend, even including the parser. Indeed, the concrete syntax given by a context-free grammar can be regarded as the initial algebra of an equational/algebraic specification [121]. A parser can then be specified



as a function from the domain of strings (sequences of characters) to that initial algebra. Since initial algebra semantics can be defined in AML [122], the parsing function can be inductively axiomatized and certified by AML proofs.

The second future direction is to incorporate proofs for SMT solvers. Currently, our implementation trusts SMT solvers and does not generate proof objects for them.  $\mathbb{K}$  uses SMT solvers for domain reasoning, such as  $\Gamma^L \vdash \varphi \rightarrow \psi$ , where  $\varphi$  and  $\psi$  are logical constraints about domain values such as integers. To prove such domain properties, we encode them as equivalent FOL formulas and query an SMT solver, thus resulting in a gap in our proof certificates that needs to be addressed separately in the future, following existing research such as [110, 123].

The third future direction is to address the current incompleteness of the proof generation procedure (i.e. failure to produce a proof even when the verifier succeeds). Currently, we can identify two sources of incompleteness:

- The subsumption proof generation (Section 9.3) may not match the actual simplification procedure of the  $\mathbb{K}$  verifier, thus resulting in subsumptions that are correctly done by  $\mathbb{K}$  but cannot be proved by our proof generation tool.
- Our proof generation procedure does not support the (SET CIRCULARITY) rule as discussed in Section 9.4, while the  $\mathbb{K}$  verifier does use (SET CIRCULARITY) in general.

These sources of incompleteness arise from the inconsistency between our proof generation procedure and the actual implementation of the  $\mathbb{K}$  verifier. Therefore, a long-term collaboration with the  $\mathbb{K}$  team is required to improve the completeness of our proof generation tool.

Finally, as discussed in Section 9.1, we plan to extend our proof generation method to support proof generation for all-path reachability reasoning [3, 124]. In the current work, we only consider one-path reachability logic, which captures the partial correctness of one execution trace. For nondeterministic and concurrent programs, we need all-path reachability logic to prove the correctness of all execution traces. All-path reachability logic is proposed for precisely that purpose. An all-path reachability claim  $\varphi \Rightarrow_{reach}^{\forall} \psi$  holds iff for every maximal and finite execution traces starting from  $\varphi$ ,  $\psi$  is reachable. The proof system of all-path reachability logic has identical proof rules as one-path reachability logic in Figure 2.12 (replacing  $\Rightarrow$  with  $\Rightarrow_{reach}^{\forall}$ ), except one additional axiom called (STEP)

$$(STEP) \quad A \vdash_{\emptyset}^{reach} \varphi \Rightarrow_{reach}^{\forall} (\psi_1 \vee \dots \vee \psi_K)$$

where  $A = \{lhs_1 \Rightarrow rhs_1, \dots, lhs_K \Rightarrow rhs_K\}$  is the set of all the semantic rules, which are one-path rules in nature. The (STEP) axiom derives all-path claims from these semantic rules,

Table 9.1: Proof Generation and Proof Checking Performance: Formal Verification

Task	Spec. LOC	Step #	Hint Size	Proof Size	Time (seconds)			
					$\mathbb{K}$	Gen.	Check 1	Check 2
sum.imp	40	42	0.58 MB	37/1.6 MB	4.2	105	1.8	9.6
sum.reg	46	108	2.24 MB	111/3.6 MB	9.1	259	5.4	15.9
sum.pcf	18	22	0.29 MB	38/1.5 MB	2.9	119	2.4	12.2
exp.imp	27	31	0.5 MB	37/1.5 MB	3.7	108	2.0	10.5
exp.reg	27	43	0.96 MB	70/2.3 MB	4.7	177	3.1	13.3
exp.pcf	20	29	0.5 MB	65/2.3 MB	3.8	199	3.1	13.7
collatz.imp	25	55	1.14 MB	49/1.7 MB	4.8	138	2.6	12.4
collatz.reg	37	100	3.66 MB	209/4.7 MB	9.3	414	5.5	31.6
collatz.pcf	26	39	1.51 MB	110/2.2 MB	5.3	247	5.2	23.6
product.imp	44	42	0.62 MB	44/1.8 MB	3.9	124	2.4	11.0
product.reg	24	42	0.81 MB	65/2.3 MB	4.3	164	4.0	11.8
product.pcf	21	48	0.82 MB	80/2.8 MB	5.3	234	4.9	18.4
gcd.imp	51	93	1.9 MB	74/2.3 MB	22.9	237	2.7	17.8
gcd.reg	27	73	1.92 MB	124/3.3 MB	18.6	306	3.6	16.9
gcd.pcf	22	38	1.35 MB	150/3.2 MB	12.8	367	5.2	28.5
ln/count-by-1	44	25	0.24 MB	28/1.3 MB	2.7	81	1.6	8.0
ln/count-by-2	44	25	0.26 MB	28/1.3 MB	9.0	88	1.4	8.1
ln/gauss-sum	51	39	0.53 MB	38/1.6 MB	4.6	107	2.0	10.2
ln/half	62	65	1.3 MB	63/2.2 MB	13.1	173	3.0	11.8
ln/nested-1	92	84	1.88 MB	104/3.4 MB	7.5	231	5.9	20.1

where  $\psi_k$  is the result of executing  $\varphi$  for one step, using the  $k$ -th semantic rule  $lhs_k \Rightarrow rhs_k$  for  $1 \leq k \leq K$ . Thus, the (STEP) axiom states that the only way to make an execution step is to use one of the semantic rules in  $A$ . Since the current  $\mathbb{K}$  pipeline that translates  $\mathbb{K}$  into AML (Figure 8.2) is incomplete and the resulting theory  $\Gamma^L$  does not have the (STEP) axiom, proof generation for all-path reachability claims is left as future work.

## 9.6 EVALUATION

We evaluated our proof generation method using two benchmark sets. The first benchmark set consists of some verification problems of programs written in three programming languages, which aims at showing that our method is indeed language-agnostic. The second benchmark set is a selection of C verification examples from the SV-COMP competition [125]. We used a machine with Intel i7-12700K processors and 32 GB of RAM. The evaluation results are shown in Table 9.1. From left to right, we list the verification tasks, **specification LOC**, number of symbolic execution **steps**, proof **hint size**, **proof object size** (uncompressed/compressed),

$\mathbb{K}$  verifier time (without proof generation), proof **generation** time, and proof **checking** time (check 1 using `smetamath` [126] and check 2 using our own implementation in Rust [117]). Tasks with prefix `ln/` are from the `loop-new` benchmark of SV-COMP [125]. In the following, we discuss the benchmark sets and the evaluation results in detail.

### 9.6.1 Benchmarks

To demonstrate that our proof generation method is language-agnostic, we defined three different programming languages in  $\mathbb{K}$ :

- IMP (see Figure 2.13): a simple imperative language with C-like syntax;
- REG: an assembly language for a register-based virtual machine;
- PCF, i.e., programming computable functions [127]: a typed functional language with a fixed-point operator.

We considered the following verification examples:

- SUM, which computes  $1 + \dots + n$  for input  $n$ ;
- EXP, which computes  $n^k$  for inputs  $n$  and  $k$ ;
- COLLATZ, which computes the Collatz sequence [128] for input  $n$  until it reaches 1;
- PRODUCT, which computes the product of integers using a loop.
- GCD, which computes the greatest common divisor of two integers using the Euclidean algorithm.

All benchmark programs and their formal specifications are implemented/specified in the three programming languages IMP, REG, and PCF. Table 9.1 shows that our prototype can generate proof certificates for all these programs without additional effort. Besides these verification examples, we also considered the C programs from the `loop-new` benchmark set in the SV-COMP competition [125].

Even for simple arithmetic programs such as SUM, the symbolic execution process is complicated, as one can see from the proof object sizes in Table 9.1. A lot of seemingly innocuous operations that are performed by the  $\mathbb{K}$  deductive verifier, such as substitution and equational simplification, result in very long proof certificates, which encode proof steps down to the lowest possible level—the proof system.

### 9.6.2 Evaluation results

We measured the performance of both proof generation and proof checking. For proof generation, we measured the generation time, the number of symbolic execution steps, the sizes of the proof hint and the final proof certificates. We also measured the sizes of compressed proof certificates using a generic compression tool `xz` [119]; these compressed proofs can be decompressed and checked on-the-fly using an online Metamath verifier such as `mmverify` [129]. The *key highlights* of our evaluation are:

1. Proof checking using Metamath is very fast, even for very long proofs;.
2. Proof generation takes more time, often in the order of minutes, depending on the number of symbolic execution steps that are conducted during verification but not much relevant to the size of the program being verified.
3. Proof certificates are very large, but they are simply plain text files and their sizes can be greatly reduced using any mainstream compression tool. Compressed proofs can be decompressed on-the-fly for proof checking by using an online Metamath verifier, as a space-time trade-off.

We explain the experimental results in detail.

**Proof generation** At a high level, the proof generation time consists of (1) the time to generate the AML theory  $\Gamma^L$  from the  $\mathbb{K}$  formal language semantics of  $L$ , and (2) the time to generate the proof certificates using the procedures described in Chapter 9. In our experiments, (1) only takes a few seconds and is linear to the number of semantic rules. Most time is spent on (2), which is linear to the number of symbolic execution steps conducted during verification and the sizes of the intermediate configurations. Generally speaking, deductive verifiers are slow, and it takes even more time for users to propose the right invariants. In our view, it is therefore acceptable to spend the extra time on generating rigorous and machine-checkable proof certificates for deductive verifiers and their verification runs, which help establish the correctness of the verification results on a smaller trust base.

**Proof checking** Due to the simplicity of Metamath and the 240-line formalization of AML, it is very fast to check proof certificates. Once the proofs are generated, they can be made public as *machine-checkable correctness certificates* of the verification tasks. Anyone concerning about the correctness of the verification can access the public proof certificates, set up a proof checking environment (which is much simpler than setting up a verification

environment), and check the proofs independently. We are optimistic about the scalability of our method on large  $\mathbb{K}$  developments because proof checking scales well. The sizes of proof certificates are linear to the number of symbolic execution steps and the sizes of configurations. The complexity of proof checking is also linear to the sizes of proof certificates. We do not see a nonlinear factor or an exponential explosion in our proof generation method.

**Proof compression** Metamath has its own format to compress proofs (see [13, Appendix B]). On top of that, proof certificates can be compressed as plain text files using any mainstream compression tool such as **xz** [119], which leads to  $>95\%$  reduction in the proof sizes, as shown in Table 9.1, at the expense of spending more time in decompressing the proofs for proof checking and using an online proof checker, which can be slower than an offline one. It is left as future work to study such space-time trade-off in proof checking and find the right balance.

## Chapter 10: RELATED WORK

We present related work and compare them with this work on the following topics: (1) existing approaches to programming language frameworks; (2) existing approaches to defining binders; (3) existing approaches to automated fixpoint reasoning; and (4) existing approaches to trustworthy programming language tools.

### 10.1 FORMAL SEMANTICS AND PROGRAMMING LANGUAGE FRAMEWORKS

It is hard to discuss, even summarily, the over half a century of research in formal semantics and programming language frameworks. Since the 1960s, various semantics notions and styles have been proposed and become canonical approaches to defining formal semantics, including Floyd-Hoare axiomatic semantics [14, 15], Scott-Strachey denotational semantics [16], initial algebra semantics [121], and various types of operational semantics [17, 18, 19, 20]. A nice survey about the earlier research in formal semantics and semantic frameworks in the past centenary can be found in [130]. More recent work will be discussed shortly after. By collecting these references to related work, we realize how much progress we have been made since the first paper on formal semantics of programs published in 1960s, and how close we are to reaching the ideal language framework vision (Figure 1.1).

CENTAUR [131] is one of the earliest attempts in developing a system that takes formal language definitions and automatically generates programming environments, which consist of many language tools, including interpreters and debuggers, equipped with graphic interfaces.

Proof assistants such as Coq [11] and Isabelle [115] represent an important trend to define the formal semantics of programming languages. Programs and program configurations are defined as data structures, and various types of formal semantics can be defined as functions or relations on these data structures. Program execution and verification can be done in a manual, semi-automatic, or fully automatic manner, with or without human interference. Meta-properties, such as the equivalence between the two different semantics of a language, can be proved, but often require remarkably effort. Formal syntax is often not considered.

Due to the complexity of aforementioned proof assistants, lightweight tools such as Ott [132] occurred, serving as an expressive and intuitive front-end to write formal syntax and semantics definitions of programming languages and calculi. Automatic tools such as those which sanity-check the formal definitions or translate definitions to proof assistants, are implemented.

Component-based specification (CBS) framework [133] observes that many programming languages share a variety of many fundamental programming constructs, or simply *foncons*.

CBS framework allows one to define the formal semantics of programming languages by translating them to foncons in a component-based and modular way, aiming at good reusability of formal definitions.

Spoofax [134] is a platform for designing programming languages, in particular domain specific languages (DSL), with an integration of language tools, including syntax definition formalism such as SDF [135], program translation and code generation tools such as Stratego [136], program analysis tools such as data flow analyzer FlowSpec [137].

PLT Redex [138], which is now embedded in the programming language Racket, is a DSL for designing formal syntax and operational semantics as reduction rules. Random programs can be automatically generated that serve as tests of the semantics.

Rosette [139] is a solver-aided programming language that extends Racket with a small set of language constructs for program verification and synthesis. Language designers, often of DSL, implement interpreters in Rosette, and by symbolic evaluation, the language synthesis and verification tools are generated for free. Racket has helped non-expert users to design and create solver-aided tools for various domains. Research on symbolic profiling examines process of symbolic evaluation and proposes techniques that automatically fix performance bottlenecks of the generated tools [140].

## 10.2 EXISTING APPROACHES TO DEFINING BINDERS

We discuss some existing approaches to defining binders and compare them with our approach using matching  $\mu$ -logic, as presented in Sections 5.12 and 5.13. These approaches include: (1) de Bruijn techniques [141], which give  $\alpha$ -equivalent terms identical encodings; (2) combinators [31], which translate terms with binders to binder-free combinator terms; (3) nominal logic [142], which uses first-order logic (FOL) to axiomatize name-swapping and freshness, and uses them to axiomatize object-level binding; (4) higher-order abstract syntax [143] (abbreviated HOAS), which uses fixed binders in the meta-language, often a variant of typed  $\lambda$ -calculus, to define arbitrary binders in the object-level systems; (5) explicit substitution [144], which uses customized calculi where the meta-level operation of capture-free substitution is incarnated in an object-level operation as part of the calculi; (6) term-generic logic [33] (abbreviated TGL), which is a FOL variant parametric in a generic term set, defined axiomatically and not constructively, which can be instantiated by a concrete binder syntax. We discuss how these approaches handle binders and binding behavior using the following  $\lambda$ -expression as an example (a closed expression with distinct bound variables,

which requires  $\alpha$ -renaming during reduction to avoid variable-capture):

$$(\lambda z . (zz))(\lambda x . \lambda y . (xy)) \quad (\dagger)$$

De Bruijn encodings eliminate bound variables by replacing them with indexes that denote the number of (nested) binders that are in scope between them and their corresponding binders.<sup>1</sup> For example, the de Bruijn encoding of  $(\dagger)$  is  $(\lambda(11))(\lambda\lambda(21))$ , where 1 means that it is bound by the closest binder and 2 means that it is bound by the second closest binder. Bound variables are eliminated so  $\alpha$ -equivalent expressions have the same de Bruijn encoding. However, substitution requires index shifting, to adjust the indexes. De Bruijn techniques are used as the internal representations of terms in several theorem provers, but the encoding is not human readable, implementations are often tricky to get right, and efficiency problems can still appear on large terms.

Combinators translate binders to binder-free terms, which are built with constants like  $k$  and  $s$ , and application. This translation is called abstraction elimination, and can be implemented using term rewriting [145]. It may cause exponential growth in the translated term size. Reduction of combinatory terms is done using equations like  $kxy = x$  and  $sxyz = (xz)(yz)$  regarded as rewrite rules. Combinatory terms are not human readable; for example, (one of) the equivalent combinator term of  $(\dagger)$  is  $s(skk)(skk)s(s(ks)(s(kk)(skk)))(k(skk))$ . Using combinators, the binding behavior of  $\lambda$  is captured *implicitly* through abstraction elimination.

Nominal logic refers to a family of FOL theories whose signatures contain a name-swapping operation  $(xy) \cdot e$  that swaps all (free and bound) occurrences of  $x$  and  $y$  in  $e$ , and a freshness predicate  $x \# e$  stating that  $x$  has no free occurrences in  $e$ . The notions of  $\alpha$ -equivalence and capture-free substitution are then axiomatized using additional FOL axioms on top of the axioms of name-swapping and freshness. As an example, the following is an axiom in [142, Appendix A.3] that states that swapping two fresh names that do not occur free in a term has not effect:

$$(F1) \quad \forall x : V . \forall y : V . \forall e : \mathbf{Exp} . x \# e \wedge y \# e \rightarrow (xy) \cdot e = e$$

where  $V$  and  $\mathbf{Exp}$  are the sorts of variables (also called atoms) and expressions, respectively. Nominal logic also defines a new sort  $[V]\mathbf{Exp}$  and a FOL binary function  $\_.\_ : V \times \mathbf{Exp} \rightarrow [V]\mathbf{Exp}$  for binding, whose properties such as  $\alpha$ -equivalence are axiomatized. Then,  $\beta$ -reduction

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<sup>1</sup>Other de Bruijn encodings count the binders from the top of the terms.



in  $\lambda$ -calculus, e.g., can be defined in the following way [146, pp. 251, Eq. (12.17)]:

$$(\beta \text{ IN NOMINAL LOGIC}) \quad \forall x : V . \forall e : \text{Exp} . \forall e' : \text{Exp} . \text{app}(\text{lam}(x.e), e') = \text{subst}((x.e), e')$$

where  $\text{subst}(\_, \_)$  is a binary function defined by four axioms (see [142, pp. 8]), in accordance to the four possible forms that  $e$  can take (i.e., the variable  $x$ ; a variable distinct from  $x$ ; application; or abstraction). E.g., the following is the substitution axiom for abstraction [146, Eq. (12.20)]:

$$\forall x : V . \forall y : V . \forall e : \text{Exp} . \forall e' : \text{Exp} . y \# e' \rightarrow \text{subst}(x . \text{lam}(y . e), e') = \text{lam}(y . \text{subst}(x . e, e'))$$

Note that  $x$  and  $e$  are meta-variables in  $\lambda$ -calculus and become normal variables in nominal logic, so the whole embedding is a deep embedding. (Compare this to our encoding in Fig. ?? where meta-variables  $x$  and  $e$  in  $\lambda$ -calculus are still meta-variables in matching  $\mu$ -logic.)

Besides nominal logic and its metatheory [147, 148, 149], there is a wider range of research on nominal techniques in general, including studies on using Fraenkel-Mostowski sets [150], nominal sets [151] or similar set-theoretic structures [152] as well as category-theoretic notions [153] to formalize and reason about binders and operations on them, and have resulted in practical implementations that support complex recursive and inductive reasoning over terms with bindings as well as algorithms for unification [154] and narrowing [155]. These nominal approaches deal with variable names and bindings directly, treat variable names as normal data that can be manipulated, quantified, and reasoned about, and give explicit definitions to operations such as free variables and capture-free substitution (via name-swapping and freshness). Note that nominal approaches can be directly exploited in matching  $\mu$ -logic because FOL is a methodological fragment of matching  $\mu$ -logic.

Higher-order abstract syntax (HOAS) is a design pattern where some expressive higher-order calculus, usually one of the variants of typed  $\lambda$ -calculus [143, 156, 157, 158, 159, 160] or second-order equational logic [159, 161], is used as a foundation to define object-level binders. As an example, we show (part of) the HOAS-style definition of (untyped)  $\lambda$ -calculus in the Twelf system [162]:

```

exp : type.                                // the type of  $\lambda$ -expressions
app : exp -> exp -> exp.                  // function application
lam : (exp -> exp) -> exp.                // function abstraction
red : exp -> exp -> type.                 // reduction relation
red-beta : red (app (lam ([x] (F x))) E) (F E). //  $\beta$ -reduction

```

where in **red-beta**,  $[x] \_$  is the built-in binder of (the HOAS variant underlying) Twelf;  $E$  is a variable of type **exp**;  $F$  is a variable of the function type **exp**  $\rightarrow$  **exp**; and  $(F \ x)$  is the (metalevel) application of  $F$  to  $x$ . Higher-order matching is needed when **red-beta** is applied, and the internal substitution mechanism of Twelf is triggered when  $F$  is applied to  $E$ . The binding behavior of  $\lambda$  is obtained from the binding behavior of the built-in binder  $[x] \_$ , via a constant **lam**; specifically,  $\lambda x.e$  is encoded as **lam**  $([x] \ e)$ . Object-level substitution is avoided, but clearly this is not how  $\beta$ -reduction is usually defined (for the usual definition, see  $(\beta, \text{REDUCTION})$  below). Application in  $\lambda$ -calculus is defined by a simple desugaring to the builtin application, using a different constant **app**; that is,  $e_1 \ e_2$  is defined as **app**  $e_1 \ e_2$  (rather than  $e_1 \ e_2$ ). Thus, the definition needs to be justified by proving *adequacy theorems* that establish a bijection between the expressions and formal proofs of  $\lambda$ -calculus, and the HOAS terms and type derivations, which is a tedious and nontrivial task [163].

Explicit substitution turns the implicit meta-level substitution operation into more explicit and atomic steps, in order to provide a better understanding of the operational semantics and execution models of higher-order calculi (see [164, pp. 1–2]; see also [165, pp. 4] for historical remarks). By doing so, it bridges the gap between higher-order formalisms and their implementations, and has resulted in several practical tools. For example, [166] proposes a calculus for explicit substitution whose implementation allows us to define executable formal representations of many logical systems featuring binders with a close-to-zero representational distance.

Term-generic logic (TGL), as we have seen in Section 2.12, is a FOL variant, where the set of terms  $T$  is generic and given as a *parameter* that exports two operations—free variables and capture-free substitution—satisfying certain properties [33, Definition 2.1]. TGL formulas are then defined constructively as in FOL, from predicates  $\pi(e_1, \dots, e_n)$  and equations  $e_1 = e_2$ , to compound formulas built using  $\wedge$ ,  $\neg$ , and  $\exists$ , with the important exception that  $e_1, \dots, e_n$  are not constructive terms like in FOL, but generic terms in  $T$ . In the case of  $\lambda$ -calculus, the set of  $\lambda$ -expressions  $\Lambda$  can be proved to satisfy the definition of a generic term set in TGL, so we can *instantiate* TGL by  $\Lambda$ . The binding behavior of  $\lambda$  is inherited automatically, through the  $T$  instance. The metalevel of  $\lambda$ -calculus can be defined by TGL axioms. For example,  $\beta$ -reduction is captured either as an equation or as a relation:

$$(\beta, \text{EQUATION}) \quad (\lambda x.e) \ e' = e'[e/x] \qquad (\beta, \text{REDUCTION}) \quad \text{reduces}((\lambda x.e) \ e', e'[e/x])$$

where *reduces* is a binary predicate;  $(\lambda x.e) \ e', e'[e/x] \in \Lambda$  are generic terms (schemas) that represent all the concrete instances. TGL has been used to define various systems featuring bindings. In this paper, we use TGL as an intermediate to capture other systems with binders

within matching  $\mu$ -logic.

We have shown that matching  $\mu$ -logic provides a general approach to defining binders in Sections 5.12 and 5.13. An important aspect of the matching  $\mu$ -logic-based approach is that it yields *models*. Models are insightful. They help us understand a logical system better, from a different angle. It is not unusual that more than one notion or class of models are proposed for one logic, because each has its unique merit in helping us understand the logic from a certain perspective. Since matching  $\mu$ -logic has a built-in notion of models, by defining a logical system as a matching  $\mu$ -logic theory we can immediately study its resulting model theory and properties. For example, the matching  $\mu$ -logic theory of  $\lambda$ -calculus yields a precise and insightful description of how  $\lambda x.e$  is interpreted (semantically) in matching  $\mu$ -logic models, which leads us to a new semantics of  $\lambda$ -calculus that is representationally complete for all  $\lambda$ -theories (Section 5.12.4).

### 10.3 EXISTING APPROACHES TO AUTOMATED FIXPOINT REASONING

Here we discuss other approaches to automated fixpoint reasoning and compare them with our unified proof framework from a methodology point of view.

We were inspired and challenged by work on automation of inductive proofs for separation logic [25], which resulted in several automatic separation logic provers; see [96] for those that participated in the recent SL-COMP’19 competition. Since separation logic is undecidable [167], many provers implement only decision procedures to decidable fragments [26, 91, 92, 102] or incomplete algorithms [88, 89, 90]. There is also work on decision procedures for other heap logics [168, 169, 170, 171, 172, 173], which achieve full automation but suffer from lack of expressiveness and generality. It is worth noting that significant performance improvements can be obtained by incorporating first-order theorem proving and SMT solvers [97, 98] into separation logic provers [174, 175].

Compared with our unified proof framework, the above provers are specialized to separation logic reasoning. Some are based on reductions from separation logic formulas to certain decidable computational domains, such as the satisfiability problem for monadic second-order logic on graphs with bounded tree width [89]. Others are based on separation logic proof trees, where the syntax of separation logic has been hardwired in the prover. For example, most separation logic provers require the following canonical form of separation logic formulas:  $\varphi_1 * \dots * \varphi_n \wedge \psi$  where  $\varphi_1, \dots, \varphi_n$  are basic spacial formulas built from singleton heaps  $x \mapsto y$  or user-defined recursive structures such as  $list(x)$ , and  $\psi$  is a FOL logical constraint. This built-in separation logic syntax limits the use of these provers to separation logic, even though the inductive proof rules proposed by the above provers might be more general. The major

advantage of our unified proof framework, which was the motivation fueling our effort, is that the inductive principle can be applied to any structures, not only those representing heap structures. In Section 6.1.1, we show the key elements of our proof framework that supports the fixpoint reasoning for arbitrary structures.

Hoare-style formal verification represents another important but specialized approach to fixpoint reasoning, where the objects of study are program executions and the properties to prove are program correctness claims. There is a vast literature on verification tools based on classical logics and SMT solvers such as Dafny [176], VCC [177] and Verifast [178]. To use these tools, the users often need to provide annotations that explicitly express and manipulate frames, whose proofs are based on user-provided lemmas. The correctness of the lemmas is either taken for granted or manually proved using an interactive proof assistant (e.g., [177, Section 6] mentions several tools that are based on Coq [11] or Isabelle [115]). While it is acceptable for deductive verifiers to take additional annotations and/or program invariants, the use of manually-proved lemmas is not ideal because it makes the verification tools *not fully automatic*.

An interesting approach to formal verification that inspired this paper is reachability logic [3], which uses the operational semantics of a programming language to verify the programs of that language, using one fixed proof system. In that sense, it shares a similar vision with our unified proof framework, where the formal semantics of programming languages are defined as the logical theories and only one proof system is needed to verify all programs written in all languages. In Sections 2.14, we will show how our proof framework can carry out reachability-style formal reasoning, and thus support program verification in a unified way.

There is recent work that considers inductive reasoning for more general data structures, beyond only heap structures [103, 179, 180, 181, 182, 183]. Tac [184] is an automated theorem prover for a variant of FOL extended with fixpoints that uses the techniques of *focusing* to reduce the nondeterminism involved in proof search. [103] proposes CYCLIST, a proof framework that implements a generic notion of *cyclic proof* as a “design pattern” about how to do inductive reasoning, which generalize the proof systems of LFP and SL. In CYCLIST, inductive reasoning is achieved not by an explicit induction proof rule, but implicitly by cyclic proof trees with “back-links”. In contrast, our unified proof framework uses one fixed logic (matching  $\mu$ -logic) and relies on an explicit induction proof rule (KNASTER-TARSKI). Therefore, CYCLIST represents a different approach from ours but towards a similar goal of a unified framework for fixpoint reasoning.

## 10.4 EXISTING APPROACHES TO TRUSTWORTHY PROGRAMMING LANGUAGE TOOLS

There has been a lot of effort in providing formal guarantees for programming language tools such as compilers or deductive verifiers. At a high level, we may identify two approaches. One approach is to formalize and prove the correctness of the *entire* tool. For example, CompCert C [185] is a C compiler that has been formally verified to be exempt from miscompilation issues. The other approach is to generate proof certificates on a case-by-case basis for *each run* of the tool. For example, [186] presents the translation validation technique to check the result of each compilation against the source program and [187] presents an approach where successful runs of the Boogie verifier are validated using Isabelle proofs. Our work belongs to the second approach, where proof certificates are generated for each verification task carried out using  $\mathbb{K}$ .

The first approach tends to yield proofs that are more technically involved and does not work well on an existing tool implementation, and is often conducted on a new implementation that aims at being correct-by-construction from the beginning. However, once it is done, it gives the highest formal guarantee for the correctness of the entire tool, once and for all. Besides CompCert C that we mentioned above, there is also CakeML [188], which is an implementation of Standard ML [189] that is formally verified in HOL4 [190]. In this approach, the proof certificates are often written and proved in an interactive theorem prover such as Coq [11] and Isabelle [115], because they provide the expressive power needed to state the correctness claims, which are often higher-order, in the sense that they are quantified over all possible programs and/or inputs.

The other “case-by-case” approach generates simpler proof certificates and works better on an existing tool implementation, compared to the above “once-and-for-all” approach. In this approach, the proof certificates only relate the input and output of the language tool in question, without needing to depend on the actual implementation of the tool. For example, the technique of translation validation [186] checks the correctness of each compilation of an optimized compiler, producing a *verifying* compiler, in contrast to a *verified* compiler such as CompCert C. Recently, the idea was applied to not only compilers but also interpreters and deductive verifiers. For example, [113] generates proof certificates for a language-agnostic interpreter, where each (concrete) execution of a program is certified by a machine-checkable mathematical proof. [187] generates proof certificates for the intermediate verification language (IVL) Boogie, where each transformation from programs to their verification conditions is certified. [191] generates proof certificates for the Why3 verifier [192], which is also equipped with an IVL to generate verification conditions. [193] generates proof

certificates for the VeriFast verifier for C [194], where each successful verification run is certified with respect to CompCert’s Clight big step semantics [195]. There have also been works that generate proofs for the decision procedures in SMT solvers to certify their correctness [110, 123, 196].

Both the once-and-for-all and case-by-case approaches provide the same (high) level of correctness guarantee when it comes to one successful run of the tool. Our work follows the case-by-case approach, where proof certificates are generated for each successful verification run of the language-agnostic deductive program verifier of  $\mathbb{K}$ . Since our proof generation method is parametric in the formal semantics of programming languages, it is language-agnostic.

## Chapter 11: CONCLUSION

We have presented matching  $\mu$ -logic, which is a unifying logic for specifying and reasoning about programs and programming languages. We have studied the proof theory and expressive power of matching  $\mu$ -logic and proved the soundness theorem and a few important completeness results. We have studied automated fixpoint reasoning and proposed a set of high-level automated proof rules for matching  $\mu$ -logic. We have proposed applicative matching  $\mu$ -logic (AML) as a simple instance of matching  $\mu$ -logic that remains all of its expressive power, and implemented a proof checker for AML. Finally, we have studied study proof-certifying program execution and formal verification by implementing proof generation procedures for a language-independent interpreter and a language-independent formal verifier of the  $\mathbb{K}$  framework. This way, the correctness of program execution or formal verification is reduced to checking the corresponding AML proof objects using the 240-line proof checker.

We hope to demonstrate the feasibility of having matching  $\mu$ -logic serve as a unifying foundation for programming, where programming languages are defined as logical theories, and the correctness of language implementations and tools is established by logical proof objects, which can be checked by a small proof checker.

## References

- [1] “K Framework Tools,” <https://github.com/runtimeverification/k>, 2023.
- [2] G. Roşu, “Matching logic,” *Logical Methods in Computer Science*, vol. 13, no. 4, pp. 1–61, 2017.
- [3] A. Ştefănescu, D. Park, S. Yuwen, Y. Li, and G. Roşu, “Semantics-based program verifiers for all languages,” in *Proceedings of the 2016 ACM SIGPLAN International Conference on Object-Oriented Programming, Systems, Languages, and Applications (OOPSLA’16)*. ACM, 2016, pp. 74–91.
- [4] G. Rosu, “K—A semantic framework for programming languages and formal analysis tools,” in *Dependable Software Systems Engineering*. IOS Press, 2017.
- [5] C. Hathhorn, C. Ellison, and G. Roşu, “Defining the undefinedness of C,” in *Proceedings of the 36<sup>th</sup> annual ACM SIGPLAN Conference on Programming Language Design and Implementation (PLDI’15)*. Portland, OR: ACM, 2015, pp. 336–345.
- [6] D. Bogdănaş and G. Roşu, “K-Java: A complete semantics of Java,” in *Proceedings of the 42<sup>nd</sup> Symposium on Principles of Programming Languages (POPL’15)*. Mumbai, India: ACM, 2015, pp. 445–456.
- [7] D. Park, A. Ştefănescu, and G. Roşu, “KJS: A complete formal semantics of JavaScript,” in *Proceedings of the 36<sup>th</sup> annual ACM SIGPLAN Conference on Programming Language Design and Implementation (PLDI’15)*. Portland, OR: ACM, 2015, pp. 346–356.
- [8] D. Guth, “A formal semantics of Python 3.3,” M.S. thesis, University of Illinois at Urbana-Champaign, Aug. 2013. [Online]. Available: <http://hdl.handle.net/2142/45275>
- [9] E. Hildenbrandt, M. Saxena, X. Zhu, N. Rodrigues, P. Daian, D. Guth, B. Moore, Y. Zhang, D. Park, A. Ştefănescu, and G. Roşu, “KEVM: A complete semantics of the Ethereum virtual machine,” in *Proceedings of the 2018 IEEE Computer Security Foundations Symposium (CSF’18)*. Oxford, UK: IEEE, 2018, <http://jellopaper.org>. pp. 204–217.
- [10] S. Dasgupta, D. Park, T. Kasampalis, V. S. Adve, and G. Roşu, “A complete formal semantics of x86-64 user-level instruction set architecture,” in *Proceedings of the 40<sup>th</sup> ACM SIGPLAN Conference on Programming Language Design and Implementation (PLDI’19)*. Phoenix, Arizona, USA: ACM, 2019, pp. 1133–1148.
- [11] Coq Team, *The Coq proof assistant*, LogiCal Project, 2020. [Online]. Available: <http://coq.inria.fr>



- [12] G. Roşu, A. Ştefănescu, Ş. Ciobâcă, and B. M. Moore, “One-path reachability logic,” in *Proceedings of the 28<sup>th</sup> Symposium on Logic in Computer Science (LICS’13)*. IEEE, 2013, pp. 358–367.
- [13] N. D. Megill and D. A. Wheeler, *Metamath: a computer language for mathematical proofs*. Morrisville, North Carolina: Lulu Press, 2019, <http://us.metamath.org/downloads/metamath.pdf>.
- [14] V. Pratt, “Semantical consideration on Floyd-Hoare logic,” in *Proceedings of the 17<sup>th</sup> Annual Symposium on Foundations of Computer Science (SFCS’76)*. IEEE, 1976, pp. 109–121.
- [15] C. A. R. Hoare, “An axiomatic basis for computer programming,” *Communications of the ACM*, vol. 12, no. 10, pp. 576–580, 1969.
- [16] D. Scott, “Domains for denotational semantics,” in *International Colloquium on Automata, Languages, and Programming*, Springer. Berlin Heidelberg, Germany: Springer, 1982, pp. 577–610.
- [17] G. D. Plotkin, “A structural approach to operational semantics,” *Journal of Logic & Algebraic Programming*, vol. 60-61, pp. 17–139, 2004.
- [18] G. Kahn, “Natural semantics,” in *Proceedings of the 4<sup>th</sup> Annual Symposium on Theoretical Aspects of Computer Science (STACS’87)*, vol. 247, Passau, Germany, 1987, pp. 22–39.
- [19] P. D. Mosses, “Modular structural operational semantics,” *Journal of Logic & Algebraic Programming*, vol. 60-61, pp. 195–228, 2004.
- [20] G. Berry and G. Boudol, “The chemical abstract machine,” *Theoretical Computer Science*, vol. 96, no. 1, pp. 217–248, 1992.
- [21] A. Tarski, “A lattice-theoretical fixpoint theorem and its applications,” *Pacific Journal of Mathematics*, vol. 5, no. 2, pp. 285–309, 1955.
- [22] J. A. Goguen and J. Meseguer, “Completeness of many-sorted equational logic,” *Houston Journal of Mathematics*, vol. 11, no. 3, pp. 307–334, 1985.
- [23] J. Meseguer and J. A. Goguen, “Initiality, induction, and computability,” in *Algebraic Methods in Semantics*. New York, USA: Cambridge University Press, 1985, pp. 459–543.
- [24] R. M. Burstall and J. A. Goguen, *Algebras, theories and freeness: an introduction for computer scientists*, ser. NATO Advanced Study Institutes Series (Series C — Mathematical and Physical Sciences). Dordrecht, Netherlands: Springer, 1982, vol. 91, ch. 11, pp. 329–349. [Online]. Available: [https://doi.org/10.1007/978-94-009-7893-5\\_11](https://doi.org/10.1007/978-94-009-7893-5_11)

- [25] J. C. Reynolds, “Separation logic: a logic for shared mutable data structures,” in *Proceedings of the 17<sup>th</sup> Annual IEEE Symposium on Logic in Computer Science (LICS’02)*. Copenhagen, Denmark: IEEE, 2002, pp. 55–74.
- [26] J. Brotherston, C. Fuhs, J. A. N. Pérez, and N. Gorogiannis, “A decision procedure for satisfiability in separation logic with inductive predicates,” in *Proceedings of the Joint Meeting of the 23<sup>rd</sup> EACSL Annual Conference on Computer Science Logic (CSL’14) and the 29<sup>th</sup> Annual ACM/IEEE Symposium on Logic in Computer Science (LICS’14)*, ser. CSL-LICS ’14. New York, NY, USA: ACM, 2014. [Online]. Available: <http://doi.acm.org/10.1145/2603088.2603091> pp. 25:1–25:10.
- [27] D. Kozen, “Results on the propositional  $\mu$ -calculus,” *Theoretical Computer Science*, vol. 27, no. 3, pp. 333–354, 1983.
- [28] M. J. Fischer and R. E. Ladner, “Propositional dynamic logic of regular programs,” *Journal of Computer and System Sciences*, vol. 18, no. 2, pp. 194–211, 1979.
- [29] D. Harel, “Dynamic logic,” in *Handbook of Philosophical Logic*. Springer, 1984, vol. 165, pp. 497–604.
- [30] D. Harel, J. Tiuryn, and D. Kozen, *Dynamic logic*. MIT Press, 2000.
- [31] A. Church, *The calculi of lambda-conversion*. Princeton, New Jersey, USA: Princeton University Press, 1941.
- [32] H. Barendregt, *The lambda calculus, its syntax and semantics*, ser. Studies in Logic. King’s College London, Strand, London WC2R 2LS, UK: College Publications, 1984.
- [33] A. Popescu and G. Roşu, “Term-generic logic,” *Theoretical Computer Science*, vol. 577, pp. 1–24, 2015.
- [34] F. Lucio-Carrasco and A. Gavilanes-Franco, “A first order logic for partial functions,” in *Proceedings of the 6<sup>th</sup> Annual Symposium on Theoretical Aspects of Computer Science (STACS’89)*. Paderborn, Germany: Springer, 1989, pp. 47–58.
- [35] M. Clavel, F. Durán, S. Eker, S. Escobar, P. Lincoln, N. Martí-Oliet, J. Meseguer, R. Rubio, and C. Talcott, *Maude manual (version 3.0)*, SRI International, 2020. [Online]. Available: <http://maude.lcc.uma.es/maude30-manual-html/maude-manual.html>
- [36] J. R. Shoenfield, *Mathematical logic*. Addison-Wesley Pub. Co, 1967.
- [37] P. Blackburn, M. d. Rijke, and Y. Venema, *Modal logic*. One Liberty Plaza, New York, NY: Cambridge University Press, 2001.
- [38] I. Leustean and N. Moanga, “A many-sorted polyadic modal logic,” *CoRR*, vol. abs/1803.09709, 2018. [Online]. Available: <http://arxiv.org/abs/1803.09709>
- [39] A. G. Hamilton, *Logic for mathematicians*. Cambridge, UK: Cambridge University Press, 1978.

- [40] P. Blackburn and M. Tzakova, “Hybrid completeness,” *Logic Journal of IGPL*, vol. 6, no. 4, pp. 625–650, 1998.
- [41] M. Schönfinkel, “Über die bausteine der mathematischen logik,” *Mathematische annalen*, vol. 92, no. 3-4, pp. 305–316, 1924.
- [42] H. B. Curry, *Combinatory logic*. Amsterdam: North-Holland Pub. Co., 1958.
- [43] A. Church, “A formulation of the simple theory of types,” *The Journal of Symbolic Logic*, vol. 5, no. 2, pp. 56–68, 1940.
- [44] A. I. Malc’ev, “Axiomatizable classes of locally free algebras of various type,” *The Metamathematics of Algebraic Systems: Collected Papers*, vol. 1, no. 1, pp. 262–281, 1936.
- [45] L. Kovács, S. Robillard, and A. Voronkov, “Coming to terms with quantified reasoning,” in *Proceedings of the 44<sup>th</sup> ACM SIGPLAN Symposium on Principles of Programming Languages (POPL’17)*. Paris, France: ACM, 2017, pp. 260–270.
- [46] L. Löwenheim, “Über möglichkeiten im relativkalkül,” *Mathematische Annalen*, vol. 76, no. 4, pp. 447–470, 1915.
- [47] R. M. Burstall, “Proving properties of programs by structural induction,” *The Computer Journal*, vol. 12, no. 1, pp. 41–48, 1969.
- [48] J. McCarthy, “A basis for a mathematical theory of computation,” in *Computer Programming and Formal Systems*, ser. Studies in Logic and the Foundations of Mathematics, P. Braffort and D. Hirschberg, Eds. Amsterdam, The Netherlands: Elsevier, 1963, vol. 35, pp. 33–70.
- [49] D. C. Cooper, “The equivalence of certain computations,” *The Computer Journal*, vol. 9, no. 1, pp. 45–52, May 1966.
- [50] J. McCarthy and J. Painter, “Correctness of a compiler for arithmetic expressions,” in *Proceedings of Symposium in Applied Mathematics*, vol. 19. Rhode Island, USA: American Mathematical Society, 1967, pp. 33–41.
- [51] R. M. Burstall, “Semantics of assignment,” *Machine Intelligence*, vol. 2, pp. 3–20, 1968.
- [52] J. A. Painter, “Semantic correctness of a compiler for an Algol-like language,” *Stanford Artificial Intelligence Memo. No. 44*, vol. 1, no. 1, pp. 1–260, 1967.
- [53] D. M. Kaplan, “Correctness of a compiler for Algol-like programs,” *Stanford Artificial Intelligence Memo No. 48*, vol. 48, no. 1, pp. 1–35, 1967.
- [54] H. Comon, “Inductionless induction,” in *Handbook of automated reasoning*, A. Robinson and A. Voronkov, Eds. Amsterdam: North Holland, 2001, ch. 14, pp. 913–962.
- [55] J. Meseguer, “Twenty years of rewriting logic,” *The Journal of Logic and Algebraic Programming*, vol. 81, no. 7–8, pp. 721–781, 2012.

- [56] J. Hendrix, J. Meseguer, and H. Ohsaki, “A sufficient completeness checker for linear order-sorted specifications modulo axioms,” in *Automated Reasoning*, U. Furbach and N. Shankar, Eds. Berlin, Heidelberg: Springer, 2006, pp. 151–155.
- [57] J. Hendrix and J. Meseguer, “On the completeness of context-sensitive order-sorted specifications,” in *Term Rewriting and Applications*, F. Baader, Ed. Berlin, Heidelberg: Springer, 2007, pp. 229–245.
- [58] C. Rocha and J. Meseguer, “Constructors, sufficient completeness, and deadlock freedom of rewrite theories,” in *Logic for Programming, Artificial Intelligence, and Reasoning*, C. G. Fermüller and A. Voronkov, Eds. Berlin, Heidelberg: Springer, 2010, pp. 594–609.
- [59] J. D. Hendrix, “Decision procedures for equationally based reasoning,” Ph.D. dissertation, University of Illinois at Urbana-Champaign, 2008.
- [60] H. Comon, M. Dauchet, R. Gilleron, C. Löding, F. Jacquemard, D. Lugiez, S. Tison, and M. Tommasi, “Tree automata techniques and applications,” Available on: <http://www.grappa.univ-lille3.fr/tata>, 2007, release October 12<sup>th</sup>, 2007.
- [61] J.-P. Jouannaud and E. Kounalis, “Automatic proofs by induction in theories without constructors,” *Information and Computation*, vol. 82, no. 1, pp. 1–33, 1989.
- [62] J. Goguen, T. Winkler, J. Meseguer, K. Futatsugi, and J.-P. Jouannaud, *Software engineering with OBJ: Algebraic specification in action*. Massachusetts, USA: Springer, 2000, ch. Introducing OBJ, pp. 3–167.
- [63] R. Diaconescu and K. Futatsugi, *CafeOBJ report: the language, proof techniques, and methodologies for object-oriented algebraic specification*, ser. AMAST Series in Computing. Singapore: World Scientific, 1998, vol. 6.
- [64] I. Walukiewicz, “Completeness of Kozen’s axiomatisation of the propositional  $\mu$ -calculus,” *Information and Computation*, vol. 157, no. 1-2, pp. 142–182, 2000.
- [65] G. Lenzi, “The modal  $\mu$ -calculus: A survey,” *Task quarterly*, vol. 9, no. 3, pp. 293–316, 2005.
- [66] R. Alur, C. Courcoubetis, T. A. Henzinger, and P.-H. Ho, “Hybrid automata: An algorithmic approach to the specification and verification of hybrid systems,” in *Hybrid systems*. Springer, 1993, pp. 209–229.
- [67] E. A. Lee, “Cyber physical systems: Design challenges,” in *Proceedings of the 11<sup>th</sup> IEEE Symposium on Object Oriented Real-Time Distributed Computing (ISORC’08)*. IEEE, 2008, pp. 363–369.
- [68] P. Blackburn, J. van Benthem, and F. Wolter, Eds., *Handbook of modal logic*, 1st ed. Elsevier, 2006, vol. 3.

- [69] G. Hasenjaeger, “Eine bemerkung zu Henkin’s beweis für die vollständigkeit des prädikatenkalküls der ersten stufe,” *The Journal of Symbolic Logic*, vol. 18, no. 1, pp. 42–48, 1953.
- [70] R. W. Quackenbush, “Completeness theorems for universal and implicational logics of algebras via congruences,” *Proceedings of the American Mathematical Society*, vol. 103, no. 4, pp. 1015–1021, 1988.
- [71] J. Bell and M. Machover, *A course in mathematical logic*. Amsterdam, Netherlands: North Holland, 1977.
- [72] C. Berline, “Graph models of  $\lambda$ -calculus at work, and variations,” *Mathematical Structures in Computer Science*, vol. 16, no. 2, pp. 185–221, 2006.
- [73] G. Manzonetto, “Models and theories of lambda calculus,” Ph.D. dissertation, Università Ca’ Foscari di Venezia, 2008. [Online]. Available: <https://tel.archives-ouvertes.fr/tel-00715207>
- [74] D. Scott, “Continuous lattices,” in *Toposes, Algebraic Geometry and Logic*. Berlin, Heidelberg: Springer, 1972, pp. 97–136.
- [75] G. Berry, “Stable models of typed  $\lambda$ -calculi,” in *Automata, Languages and Programming*. Berlin, Heidelberg: Springer, 1978, pp. 72–89.
- [76] J.-Y. Girard, “The system F of variable types, fifteen years later,” *Theoretical Computer Science*, vol. 45, pp. 159–192, 1986. [Online]. Available: <http://www.sciencedirect.com/science/article/pii/0304397586900447>
- [77] A. Bucciarelli and T. Ehrhard, “A theory of sequentiality,” *Theoretical Computer Science*, vol. 113, no. 2, pp. 273–291, 1993.
- [78] A. Bucciarelli and A. Salibra, “The sensible graph theories of lambda calculus,” in *Proceedings of the 19<sup>th</sup> Annual IEEE Symposium on Logic in Computer Science (LICS’04)*. Turku, Finland: IEEE, July 2004, pp. 276–285.
- [79] J.-Y. Girard, “Interprétation fonctionnelle et élimination des coupures de l’arithmétique d’ordre supérieur,” Ph.D. dissertation, Paris Diderot University, Paris, France, 1972.
- [80] J. C. Reynolds, “Towards a theory of type structure,” in *Programming Symposium*. Berlin, Heidelberg: Springer, 1974, pp. 408–425.
- [81] H. Barendregt, “Lambda calculi with types,” in *Handbook of Logic in Computer Science*. UK: Oxford University Press, 1993, vol. 2, background: computational structures, ch. 2, pp. 117–309.
- [82] R. Milner, J. Parrow, and D. Walker, “A calculus of mobile processes (part 1),” *Information and Computation*, vol. 100, no. 1, pp. 1–40, 1992. [Online]. Available: <http://www.sciencedirect.com/science/article/pii/0890540192900084>

- [83] A. Popescu and G. Roşu, “Term-generic logic (extended technical report),” Technische Universität München, University of Illinois at Urbana-Champaign, Tech. Rep., 2013.
- [84] L. Cardelli, S. Martini, J. C. Mitchell, and A. Scedrov, “An extension of system F with subtyping,” *Information and Computation*, vol. 109, no. 1, pp. 4–56, 1994.
- [85] J.-Y. Girard, “Linear logic,” *Theoretical Computer Science*, vol. 50, no. 1, pp. 1–101, 1987. [Online]. Available: <http://www.sciencedirect.com/science/article/pii/0304397587900454>
- [86] P. Lincoln and J. Mitchell, “Operational aspects of linear lambda calculus,” in *Proceedings of the 7<sup>th</sup> Annual IEEE Symposium on Logic in Computer Science (LICS’92)*. California, USA: IEEE, June 1992, pp. 235–246.
- [87] P. Martin-Löf, *Twenty five years of constructive type theory*, ser. Oxford Logic Guides Book. Oxford, UK: Oxford University Press, 1998, vol. 36, ch. An intuitionistic theory of types, pp. 127–172.
- [88] J. Berdine, C. Calcagno, and P. W. O’Hearn, “Symbolic execution with separation logic,” in *Proceedings of the 3<sup>rd</sup> Asian conference on Programming Languages and Systems (APLAS’05)*, vol. 3780. Tsukuba, Japan: Springer, Nov. 2005, pp. 52–68.
- [89] R. Iosif, A. Rogalewicz, and J. Simacek, “The tree width of separation logic with recursive definitions,” in *Proceedings of the 24<sup>th</sup> International Conference on Automated Deduction (CADE’13)*, vol. 7898. Springer, 2013, pp. 21–38.
- [90] W.-N. Chin, C. David, H. H. Nguyen, and S. Qin, “Automated verification of shape, size and bag properties via user-defined predicates in separation logic,” *Journal of Science of Computer Programming*, vol. 77, no. 9, pp. 1006–1036, 2012.
- [91] J. Berdine, C. Calcagno, and P. W. O’Hearn, “A decidable fragment of separation logic,” in *Proceedings of the 24<sup>th</sup> International Conference on Foundations of Software Technology and Theoretical Computer Science (FSTTCS’04)*, vol. 3328. Springer, 2004, pp. 97–109.
- [92] J. Katelaan, C. Matheja, and F. Zuleger, “Effective entailment checking for separation logic with inductive definitions,” in *Tools and Algorithms for the Construction and Analysis of Systems*, T. Vojnar and L. Zhang, Eds. Cham: Springer International Publishing, 2019, pp. 319–336.
- [93] D. Lucanu and G. Roşu, “CIRC: a circular coinductive prover,” in *CALCO*, 2007, pp. 372–378.
- [94] G. J. Holzmann, “The model checker SPIN,” *IEEE Trans. Softw. Eng.*, vol. 23, no. 5, pp. 279–295, 1997.
- [95] Z. Ésik, “Completeness of Park induction,” *Theoretical Computer Science*, vol. 177, no. 1, pp. 217–283, 1997.

- [96] M. Sighireanu, J. A. Navarro Pérez, A. Rybalchenko, N. Gorogiannis, R. Iosif, A. Reynolds, C. Serban, J. Katelaan, C. Matheja, T. Noll, F. Zuleger, W.-N. Chin, Q. L. Le, Q.-T. Ta, T.-C. Le, T.-T. Nguyen, S.-C. Khoo, M. Cyprian, A. Rogalewicz, T. Vojnar, C. Enea, O. Lengal, C. Gao, and Z. Wu, “SL-COMP: Competition of solvers for separation logic,” in *Tools and Algorithms for the Construction and Analysis of Systems*, D. Beyer, M. Huisman, F. Kordon, and B. Steffen, Eds. Cham: Springer International Publishing, 2019, pp. 116–132.
- [97] L. De Moura and N. Bjørner, “Z3: An efficient SMT solver,” in *Proceedings of the 14<sup>th</sup> International Conference on Tools and Algorithms for the Construction and Analysis of Systems (TACAS’08)*. Springer, 2008, pp. 337–340.
- [98] C. Barrett, C. L. Conway, M. Deters, L. Hadarean, D. Jovanović, T. King, A. Reynolds, and C. Tinelli, “CVC4,” in *Proceedings of the 23<sup>rd</sup> International Conference on Computer Aided Verification (CAV’11)*. Berlin, Heidelberg: Springer, 2011, pp. 171–177.
- [99] W. W. Boone, “The word problem,” *Proceedings of the National Academy of Sciences*, vol. 44, no. 10, pp. 1061–1065, 1958.
- [100] R. Goldblatt, *Logics of Time and Computation*, 2nd ed., ser. CSLI Lecture Notes. Stanford, CA: Center for the Study of Language and Information, 1992, no. 7.
- [101] O. Lichtenstein and A. Pnueli, “Propositional temporal logics: Decidability and completeness.” *Logic Journal of the IGPL*, vol. 8, no. 1, pp. 55–85, 2000. [Online]. Available: <http://dblp.uni-trier.de/db/journals/igpl/igpl8.html#LichtensteinP00>
- [102] C. Enea, O. Lengál, M. Sighireanu, and T. Vojnar, “Compositional entailment checking for a fragment of separation logic,” *Formal Methods in System Design*, vol. 51, no. 3, pp. 575–607, Dec. 2017. [Online]. Available: <https://doi.org/10.1007/s10703-017-0289-4>
- [103] J. Brotherston, N. Gorogiannis, and R. L. Petersen, “A generic cyclic theorem prover,” in *Programming Languages and Systems*, R. Jhala and A. Igarashi, Eds. Kyoto, Japan: Springer, 2012, pp. 350–367.
- [104] T. F. Şerbănuță and G. Roşu, “A truly concurrent semantics for the K framework based on graph transformations,” in *Proceedings of the 6<sup>th</sup> International Conference on Graph Transformation (ICGT’12)*. Bremen, Germany: Springer, 2012, pp. 294–310.
- [105] L. Li and E. Gunter, “IsaK-static A complete static semantics of K,” in *Formal Aspects of Component Software*. Springer, 2018, pp. 196–215.
- [106] B. Moore, L. Peña, and G. Roşu, “Program verification by coinduction,” in *Proceedings of the 27<sup>th</sup> European Symposium on Programming (ESOP’18)*. Springer, 2018, pp. 589–618.
- [107] J. Goguen and J. Meseguer, “Order-sorted algebra, part I: equational deduction for multiple inheritance, overloading, exceptions and partial operations,” *Theoretical Computer Science*, vol. 105, no. 2, pp. 217–273, 1992.

- [108] T. Nelson, D. Dougherty, K. Fisler, and S. Krishnamurthi, “On the finite model property in order-sorted logic,” Worcester Polytechnic Institute, Brown University, Tech. Rep., 2010.
- [109] X. Chen and G. Roşu, “Matching  $\mu$ -logic,” in *Proceedings of the 34<sup>th</sup> Annual ACM/IEEE Symposium on Logic in Computer Science (LICS’19)*. Vancouver, Canada: IEEE, 2019, pp. 1–13.
- [110] C. Barrett, L. De Moura, and P. Fontaine, “Proofs in satisfiability modulo theories,” *All about proofs, Proofs for all*, vol. 55, no. 1, pp. 23–44, 2015.
- [111] F. Durán and H. Garavel, “The rewrite engines competitions: a RECTrospective,” in *Tools and Algorithms for the Construction and Analysis of Systems*, D. Beyer, M. Huisman, F. Kordon, and B. Steffen, Eds. Cham: Springer International Publishing, 2019, pp. 93–100.
- [112] K Team, “Matching logic proof checker,” GitHub page <https://github.com/kframework/matching-logic-prover/tree/master/checker>, 2020.
- [113] X. Chen, Z. Lin, M.-T. Trinh, and G. Roşu, “Towards a trustworthy semantics-based language framework via proof generation,” in *Proceedings of the 33<sup>rd</sup> International Conference on Computer-Aided Verification*. Virtual: ACM, July 2021.
- [114] G. Roşu, A. Ştefănescu, Ştefan Ciobăcă, and B. M. Moore, “Reachability logic,” University of Illinois Urbana-Champaign, Tech. Rep. <http://hdl.handle.net/2142/32952>, July 2012.
- [115] The Isabelle development team, “Isabelle,” 2018, <https://isabelle.in.tum.de/>.
- [116] Coq Team, “Coq github repository,” <https://github.com/coq/coq>, 2021.
- [117] K Team, “Metamath proof checker in Rust,” GitHub page <https://github.com/oopsla23-paper-23/k-proof-generation/tree/main/deps/rust-metamath>, 2022.
- [118] M. Carneiro, “Metamath zero: Designing a theorem prover prover,” in *International Conference on Intelligent Computer Mathematics*. Springer, 2020, pp. 71–88.
- [119] Tukaani Team, “Xz utils,” <https://tukaani.org/xz/>, 2021.
- [120] K Team, “K framework haskell backend,” <https://github.com/kframework/kore>, 2022.
- [121] J. Goguen, J. Thatcher, E. Wagner, and J. Wright, “Initial algebra semantics and continuous algebras,” *Journal of the ACM*, vol. 24, no. 1, pp. 68–95, 1977.
- [122] X. Chen, D. Lucanu, and G. Roşu, “Initial algebra semantics in matching logic,” University of Illinois at Urbana-Champaign, Tech. Rep. <http://hdl.handle.net/2142/107781>, July 2020.



- [123] A. Stump, D. Oe, A. Reynolds, L. Hadarean, and C. Tinelli, “Smt proof checking using a logical framework,” *Formal Methods in System Design*, vol. 42, no. 1, pp. 91–118, 2013.
- [124] A. Ștefănescu, c. Ciobăcă, R. Mereuță, B. M. Moore, T. F. Șerbănuță, and G. Roșu, “All-path reachability logic,” in *Proceedings of the Joint 25<sup>th</sup> International Conference on Rewriting Techniques and Applications and 12<sup>th</sup> International Conference on Typed Lambda Calculi and Applications (RTA-TLCA’14)*, vol. 8560. Springer, 2014, pp. 425–440.
- [125] SV-COMP, “Benchmark for sv-comp,” <https://gitlab.com/sosy-lab/benchmarking/sv-benchmarks>, 2021.
- [126] S. O’Rear and M. Carneiro, “Metamath verifier in rust,” <https://github.com/sorear/smetamath-rs>, 2019.
- [127] G. D. Plotkin, “Lcf considered as a programming language,” *Theoretical computer science*, vol. 5, no. 3, pp. 223–255, 1977.
- [128] R. Guy, *Unsolved problems in number theory*. Berlin, Heidelberg: Springer Science & Business Media, 2004, vol. 1.
- [129] R. Levien and D. A. Wheeler, “Metamath verifier in python,” <https://github.com/david-a-wheeler/mmverify.py>, 2019.
- [130] Y. Zhang and B. Xu, “A survey of semantic description frameworks for programming languages,” *ACM SIGPLAN Notices*, vol. 39, no. 3, pp. 14–30, 2004. [Online]. Available: <http://doi.acm.org.proxy2.library.illinois.edu/10.1145/981009.981013>
- [131] P. Borras, D. Clément, T. Despeyroux, J. Incerpi, G. Kahn, B. Lang, and V. Pascual, “CENTAUR: The system,” in *Proceedings of the 3<sup>rd</sup> ACM SIGSOFT/SIGPLAN Software Engineering Symposium on Practical Software Development Environments (SDE’88)*. ACM, 1988, pp. 14–24.
- [132] P. Sewell, F. Z. Nardelli, S. Owens, G. Peskine, T. Ridge, S. Sarkar, and R. Strniša, “Ott: Effective tool support for the working semanticist,” *Journal of Functional Programming*, vol. 20, no. 1, pp. 71–122, 2010.
- [133] L. T. van Binsbergen, N. Sculthorpe, and P. D. Mosses, “Tool support for component-based semantics,” in *Companion Proceedings of the 15<sup>th</sup> International Conference on Modularity*. ACM, 2016, pp. 8–11.
- [134] M. van den Brand, J. Heering, P. Klint, and P. A. Olivier, “Compiling language definitions: The ASF+SDF compiler,” *ACM Transactions on Programming Languages and Systems (TOPLAS’02)*, vol. 24, no. 4, pp. 334–368, 2002.
- [135] E. Visser, “Syntax definition for language prototyping,” Ph.D. dissertation, University of Amsterdam, 1997.

- [136] E. Visser, Z.-e.-A. Benaissa, and A. Tolmach, “Building program optimizers with rewriting strategies,” in *Proceedings of the Third ACM SIGPLAN International Conference on Functional Programming (ICFP’98)*. ACM, 1998, pp. 13–26.
- [137] J. Smits and E. Visser, “FlowSpec: Declarative dataflow analysis specification,” in *Proceedings of the 10<sup>th</sup> ACM SIGPLAN International Conference on Software Language Engineering (SLE’17)*. ACM, 2017, pp. 221–231.
- [138] M. Felleisen, R. B. Findler, and M. Flatt, *Semantics engineering with PLT Redex*. MIT Press, 2009.
- [139] E. Torlak and R. Bodik, “A lightweight symbolic virtual machine for solver-aided host languages,” in *Proceedings of the 35<sup>th</sup> ACM SIGPLAN Conference on Programming Language Design and Implementation (PLDI’14)*, ser. PLDI ’14. New York, NY, USA: Association for Computing Machinery, 2014. [Online]. Available: <https://doi.org/10.1145/2594291.2594340> pp. 530–541.
- [140] J. Bornholt and E. Torlak, “Finding code that explodes under symbolic evaluation,” *Proceedings of the ACM on Programming Languages*, vol. 2, no. 149, pp. 1–26, 2018.
- [141] N. G. de Bruijn, “Lambda calculus notation with nameless dummies, a tool for automatic formula manipulation, with application to the Church-Rosser theorem,” *Indagationes Mathematicae*, vol. 75, no. 5, pp. 381–392, 1972. [Online]. Available: <http://www.sciencedirect.com/science/article/pii/1385725872900340>
- [142] A. M. Pitts, “Nominal logic, a first order theory of names and binding,” *Information and Computation*, vol. 186, no. 2, pp. 165–193, 2003.
- [143] F. Pfenning and C. Elliott, “Higher-order abstract syntax,” in *Proceedings of the ACM SIGPLAN Conference on Programming Language Design and Implementation (PLDI’88)*. New York, NY, USA: ACM, 1988, pp. 199–208.
- [144] M. Abadi, L. Cardelli, P.-L. Curien, and J.-J. Lévy, “Explicit substitutions,” *Journal of Functional Programming*, vol. 1, no. 4, pp. 375–416, 1991.
- [145] J. W. Klop, “Term rewriting systems,” in *Handbook of Logic in Computer Science*. USA: Oxford University Press, Inc., 1993, vol. 2, Background: computational structures, ch. 1, pp. 1–116.
- [146] A. M. Pitts, *Nominal sets: names and symmetry in computer science*, ser. Cambridge Tracts in Theoretical Computer Science. New York, NY, USA: Cambridge University Press, 2013.
- [147] J. Cheney, “Completeness and Herbrand theorems for nominal logic,” *Journal of Symbolic Logic*, vol. 71, no. 1, pp. 299–320, 2006.
- [148] J. Cheney, “A simple sequent calculus for nominal logic,” *Journal of Logic and Computation*, vol. 26, no. 2, pp. 699–726, 2014.

- [149] M. Gabbay and J. Cheney, “A sequent calculus for nominal logic,” in *Proceedings of the 19<sup>th</sup> Annual IEEE Symposium on Logic in Computer Science (LICS’04)*. Washington, DC, USA: IEEE, 2004, pp. 139–148.
- [150] M. Gabbay and A. Pitts, “A new approach to abstract syntax involving binders,” in *Proceedings of the 14<sup>th</sup> Symposium on Logic in Computer Science (LICS’19)*. Trento, Italy: IEEE, July 1999, pp. 214–224.
- [151] A. M. Pitts, “Alpha-structural recursion and induction,” in *Theorem Proving in Higher Order Logics*, J. Hurd and T. Melham, Eds. Berlin, Heidelberg: Springer Berlin Heidelberg, 2005, pp. 17–34.
- [152] C. Urban, “Nominal techniques in Isabelle/HOL,” *Journal of Automated Reasoning*, vol. 40, no. 4, pp. 327–356, May 2008. [Online]. Available: <https://doi.org/10.1007/s10817-008-9097-2>
- [153] M. Gabbay and M. Gabbay, “Representation and duality of the untyped  $\lambda$ -calculus in nominal lattice and topological semantics, with a proof of topological completeness,” *Annals of Pure and Applied Logic Volume*, vol. 168, no. 3, pp. 501–621, Oct. 2017.
- [154] M. Ayala-Rincón, W. de Carvalho-Segundo, M. Fernández, and D. Nantes-Sobrinho, “Nominal C-unification,” in *Proceedings of the 27<sup>th</sup> International Symposium on Logic-Based Program Synthesis and Transformation (LOPSTR’17)*, ser. Lecture Notes in Computer Science, vol. 10855. Namur, Belgium: Springer International Publishing, 2018, pp. 235–251.
- [155] M. Ayala-Rincón, M. Fernández, and D. Nantes-Sobrinho, “Nominal narrowing,” in *Proceedings of the 1<sup>st</sup> International Conference on Formal Structures for Computation and Deduction (FSCD’16)*, ser. Leibniz International Proceedings in Informatics (LIPIcs), vol. 52. Dagstuhl, Germany: Schloss Dagstuhl–Leibniz-Zentrum fuer Informatik, 2016. [Online]. Available: <http://drops.dagstuhl.de/opus/volltexte/2016/5983> pp. 1–17.
- [156] R. Harper, F. Honsell, and G. Plotkin, “A framework for defining logics,” *Journal of the ACM*, vol. 40, no. 1, pp. 143–184, 1993.
- [157] R. C. McDowell and D. A. Miller, “Reasoning with higher-order abstract syntax in a logical framework,” *ACM Transactions on Computational Logic*, vol. 3, no. 1, pp. 80–136, 2002.
- [158] L. C. Paulson, “The foundation of a generic theorem prover,” *Journal of Automated Reasoning*, vol. 5, no. 3, pp. 363–397, 1989. [Online]. Available: <https://doi.org/10.1007/BF00248324>
- [159] A. Felty and A. Momigliano, “Hybrid, a definitional two-level approach to reasoning with higher-order abstract syntax,” *Journal of Automated Reasoning*, vol. 48, no. 1, pp. 43–105, 2012.

- [160] A. Gacek, D. Miller, and G. Nadathur, “A two-level logic approach to reasoning about computations,” *Journal of Automated Reasoning*, vol. 49, no. 2, pp. 241–273, 2012. [Online]. Available: <https://doi.org/10.1007/s10817-011-9218-1>
- [161] M. Fiore and O. Mahmoud, “Second-order algebraic theories,” in *Mathematical Foundations of Computer Science 2010*, P. Hliněný and A. Kučera, Eds. Berlin, Heidelberg: Springer Berlin Heidelberg, 2010, pp. 368–380.
- [162] F. Pfenning and C. Schürmann, “System description: Twelf—a meta-logical framework for deductive systems,” in *Proceedings of the 16<sup>th</sup> International Conference on Automated Deduction (CADE 99)*. Trento, Italy: Springer, 1999, pp. 202–206.
- [163] J. Cheney, M. Norrish, and R. Vestergaard, “Formalizing adequacy: a case study for higher-order abstract syntax,” *Journal of Automated Reasoning*, vol. 49, no. 2, pp. 209–239, 2012.
- [164] D. Kesner, “A theory of explicit substitutions with safe and full composition,” *Logical Methods in Computer Science*, vol. 5, no. 3, pp. 1–29, 2009.
- [165] C. J. Bloo, “Preservation of termination for explicit substitution,” Ph.D. dissertation, Technische Universiteit Eindhoven, 1997.
- [166] M.-O. Stehr, “CINNI—a generic calculus of explicit substitutions and its application to  $\lambda$ - $\varsigma$ - and  $\phi$ -calculi,” *Electronic Notes in Theoretical Computer Science*, vol. 36, pp. 70–92, 2000.
- [167] J. Brotherston and M. Kanovich, “Undecidability of propositional separation logic and its neighbours,” *Journal of the ACM*, vol. 61, no. 2, Apr. 2014. [Online]. Available: <https://doi.org/10.1145/2542667>
- [168] Z. Rakamarić, J. Bingham, and A. J. Hu, “An inference-rule-based decision procedure for verification of heap-manipulating programs with mutable data and cyclic data structures,” in *Proceedings of the 8<sup>th</sup> International Conference on Verification, Model Checking, and Abstract Interpretation (VMCAI’07)*, vol. 4349. California, USA: Springer, Jan. 2007, pp. 106–121.
- [169] Z. Rakamarić, R. Bruttomesso, A. J. Hu, and A. Cimatti, “Verifying heap-manipulating programs in an SMT framework,” in *Proceedings of the 5<sup>th</sup> International Symposium on Automated Technology for Verification and Analysis (ATVA’07)*, vol. 4762. Tokyo, Japan: Springer, Oct. 2007, pp. 237–252.
- [170] S. Lahiri and S. Qadeer, “Back to the future: revisiting precise program verification using SMT solvers,” in *Proceedings of the 35<sup>th</sup> annual ACM SIGPLAN-SIGACT Symposium on Principles of Programming Languages (POPL’08)*. ACM, 2008, pp. 171–182.
- [171] S. Ranise and C. Zarba, “A theory of singly-linked lists and its extensible decision procedure,” in *Proceedings of the 4<sup>th</sup> IEEE International Conference on Software Engineering and Formal Methods (SEFM’06)*. IEEE, 2006, pp. 206–215.

- [172] A. Bouajjani, C. Drăgoi, C. Enea, and M. Sighireanu, “A logic-based framework for reasoning about composite data structures,” in *Proceedings of the 20<sup>th</sup> International Conference on Concurrency Theory (CONCUR’09)*, vol. 5710. Springer, 2009, pp. 178–195.
- [173] N. Bjørner and J. Hendrix, “Linear functional fixed-points,” in *Proceedings of the 21<sup>st</sup> International Conference on Computer Aided Verification (CAV’09)*, vol. 5643. Springer, 2009, pp. 124–139.
- [174] J. A. N. Pérez and A. Rybalchenko, “Separation logic + superposition calculus = heap theorem prover,” in *Proceedings of the 32<sup>nd</sup> annual ACM SIGPLAN Conference on Programming Language Design and Implementation (PLDI’11)*. ACM, 2011, pp. 556–566.
- [175] R. Piskac, T. Wies, and D. Zufferey, “Automating separation logic using SMT,” in *Proceedings of the 25<sup>th</sup> International Conference on Computer Aided Verification (CAV’13)*, vol. 8044. Springer, 2013, pp. 773–789.
- [176] K. R. M. Leino and M. Moskal, “Co-induction simply,” in *Proceedings of the 19<sup>th</sup> International Symposium on Formal Methods (FM’14)*, no. 8442. Springer, 2014, pp. 382–398.
- [177] E. Cohen, M. Dahlweid, M. Hillebrand, D. Leinenbach, M. Moskal, T. Santen, W. Schulte, and S. Tobies, “VCC: A practical system for verifying concurrent C,” in *Proceedings of the 22<sup>nd</sup> International Conference on Theorem Proving in Higher Order Logics (TPHOLs’09)*, vol. 5674. Springer, 2009, pp. 23–42.
- [178] B. Jacobs, J. Smans, and F. Piessens, “A quick tour of the VeriFast program verifier,” in *Proceedings of the 8<sup>th</sup> Asian Symposium of Programming Languages and Systems (APLAS’10)*, vol. 6461. Springer, 2010, pp. 304–311.
- [179] D.-H. Chu, J. Jaffar, and M.-T. Trinh, “Automatic induction proofs of data-structures in imperative programs,” in *Proceedings of the 36<sup>th</sup> annual ACM SIGPLAN Conference on Programming Language Design and Implementation (PLDI’15)*. ACM, 2015, pp. 457–466.
- [180] H. Unno, S. Torii, and H. Sakamoto, “Automating induction for solving Horn clauses,” in *Proceedings of the 29<sup>th</sup> International Conference on Computer Aided Verification (CAV’17)*, vol. 10427. Springer, 2017, pp. 571–591.
- [181] Q.-T. Ta, T. C. Le, S.-C. Khoo, and W.-N. Chin, “Automated mutual induction proof in separation logic,” *Formal Aspects of Computing*, vol. 31, no. 2, pp. 207–230, Apr. 2019.
- [182] C. Löding, M. Parthasarathy, and L. Peña, “Foundations for natural proofs and quantifier instantiation,” *Proceedings of the ACM on Programming Languages*, vol. 2, no. 10, pp. 1–30, 2017.

- [183] J. Brotherston, D. Distefano, and R. L. Petersen, “Automated cyclic entailment proofs in separation logic,” in *Proceedings of the 23<sup>rd</sup> International Conference on Automated Deduction (CAV’11)*. Utah, USA: Springer, 2011, pp. 131–146.
- [184] D. Baelde, D. Miller, and Z. Snow, “Focused inductive theorem proving,” in *Proceedings of the 5<sup>th</sup> International Joint Conference on Automated Reasoning (IJCAR’10)*. Edinburgh, UK: Springer, 2010, pp. 278–292.
- [185] X. Leroy, “The CompCert verified compiler, software and commented proof,” Available at <https://compcert.org/>, Mar. 2020.
- [186] A. Pnueli, M. Siegel, and E. Singerman, “Translation validation,” in *Tools and Algorithms for the Construction and Analysis of Systems*, B. Steffen, Ed. Berlin, Heidelberg: Springer Berlin Heidelberg, 1998, pp. 151–166.
- [187] G. Parthasarathy, P. Müller, and A. J. Summers, “Formally validating a practical verification condition generator,” in *Computer Aided Verification*, A. Silva and K. R. M. Leino, Eds. Cham: Springer International Publishing, 2021, pp. 704–727.
- [188] R. Kumar, M. O. Myreen, M. Norrish, and S. Owens, “Cakeml: a verified implementation of ml,” *ACM SIGPLAN Notices*, vol. 49, no. 1, pp. 179–191, 2014.
- [189] R. Harper, D. MacQueen, and R. Milner, *Standard ML*. Edinburgh, UK: Department of Computer Science, University of Edinburgh, 1986. [Online]. Available: <http://www.lfcs.inf.ed.ac.uk/reports/86/ECS-LFCS-86-2/>
- [190] K. Slind and M. Norrish, “A brief overview of HOL4,” in *International Conference on Theorem Proving in Higher Order Logics*, Springer. Montreal, Canada: Springer-Verlag Berlin Heidelberg, 2008, pp. 28–32.
- [191] Q. Garchery, “A framework for proof-carrying logical transformations,” *Electronic Proceedings in Theoretical Computer Science*, vol. 336, pp. 5–23, July 2021. [Online]. Available: <https://doi.org/10.4204%2Feptcs.336.2>
- [192] J.-C. Filliâtre and A. Paskevich, “Why3 — where programs meet provers,” in *Programming Languages and Systems*, M. Felleisen and P. Gardner, Eds. Berlin, Heidelberg: Springer Berlin Heidelberg, 2013, pp. 125–128.
- [193] S. Wils and B. Jacobs, “Certifying c program correctness with respect to compcert with verifast,” 2021. [Online]. Available: <https://arxiv.org/abs/2110.11034>
- [194] B. Jacobs, J. Smans, P. Philippaerts, F. Vogels, W. Penninckx, and F. Piessens, “Verifast: A powerful, sound, predictable, fast verifier for c and java,” in *NASA Formal Methods*, M. Bobaru, K. Havelund, G. J. Holzmann, and R. Joshi, Eds. Berlin, Heidelberg: Springer Berlin Heidelberg, 2011, pp. 41–55.
- [195] S. Blazy and X. Leroy, “Mechanized semantics for the clight subset of the c language,” *Journal of Automated Reasoning*, vol. 43, no. 3, pp. 263–288, 2009.

- [196] G. C. Necula and P. Lee, “Proof generation in the touchstone theorem prover,” in *Proceedings of the 17<sup>th</sup> International Conference on Automated Deduction*, Springer. Pittsburgh, Pennsylvania, USA: Springer-VerlagBerlin, Heidelberg, 2000, pp. 25–44.