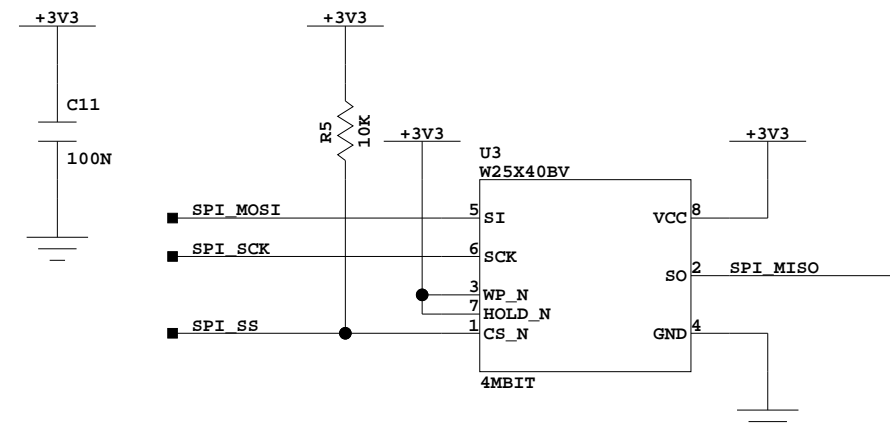
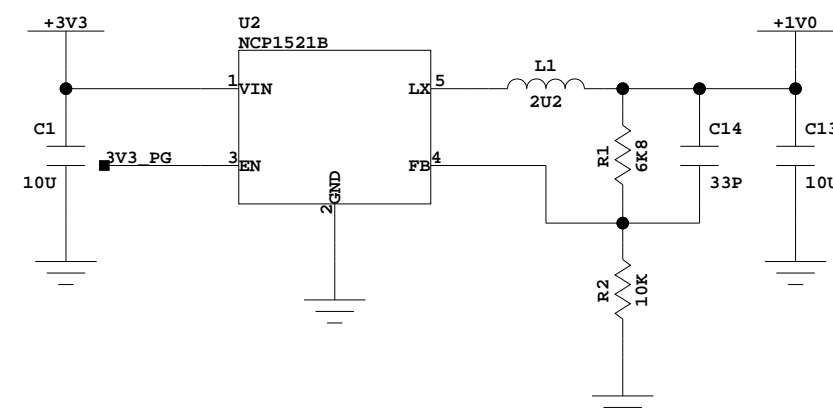


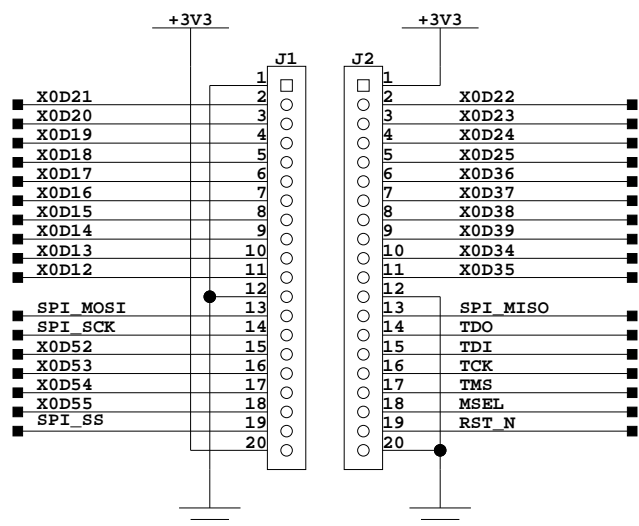
XS1-L1-48TQFP XCORE



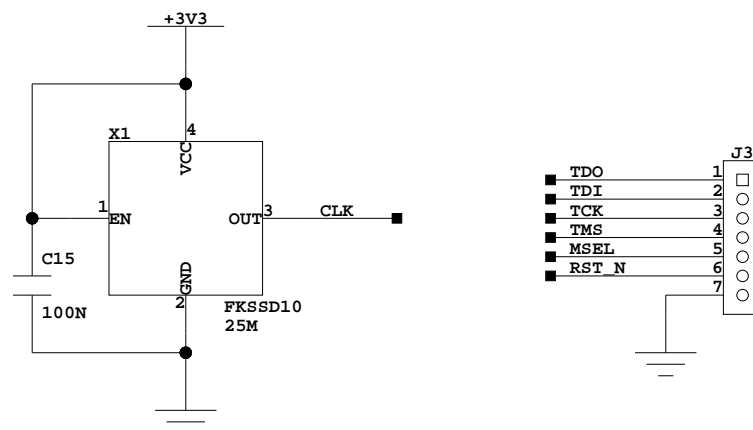
SPI FLASH



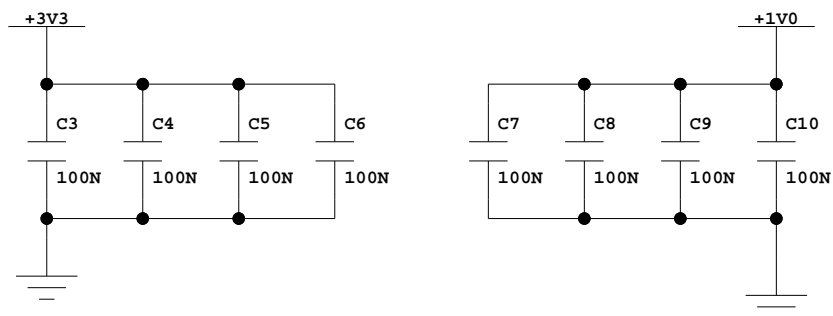
1V0 DC-DC REGULATOR



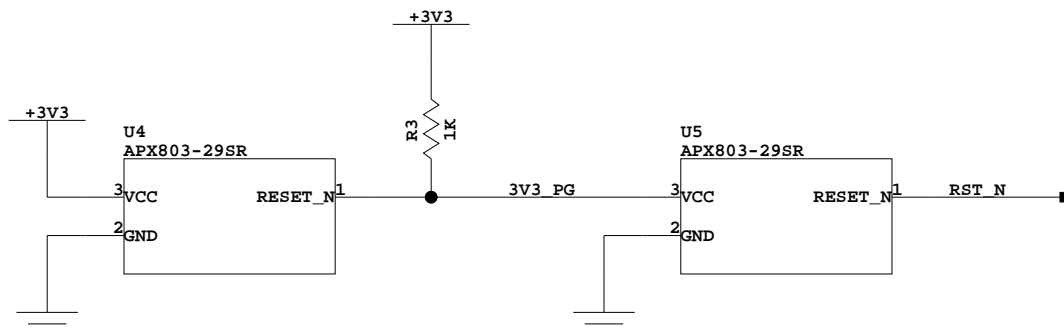
40-PIN DIP HEADERS



25MHZ CLOCK + JTAG HEADER



XCORE DECOUPLING



POWER SEQUENCING & POWER ON RESET

**XMOS**

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PROJECT NAME

**EDAM**

SIZE

**A3**

SHEET NAME

**TOP\_LEVEL**

REV

**1V0**

DATE

APR 26 2011

SHEET

1

OF

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