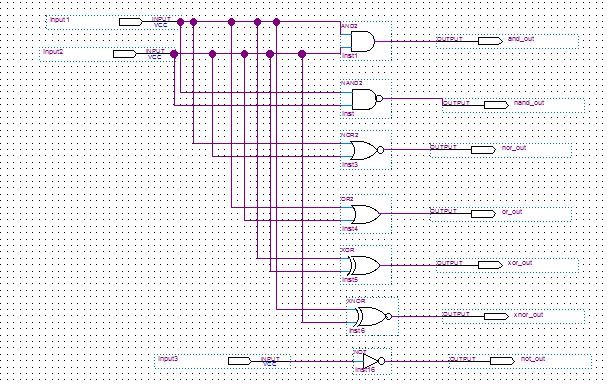
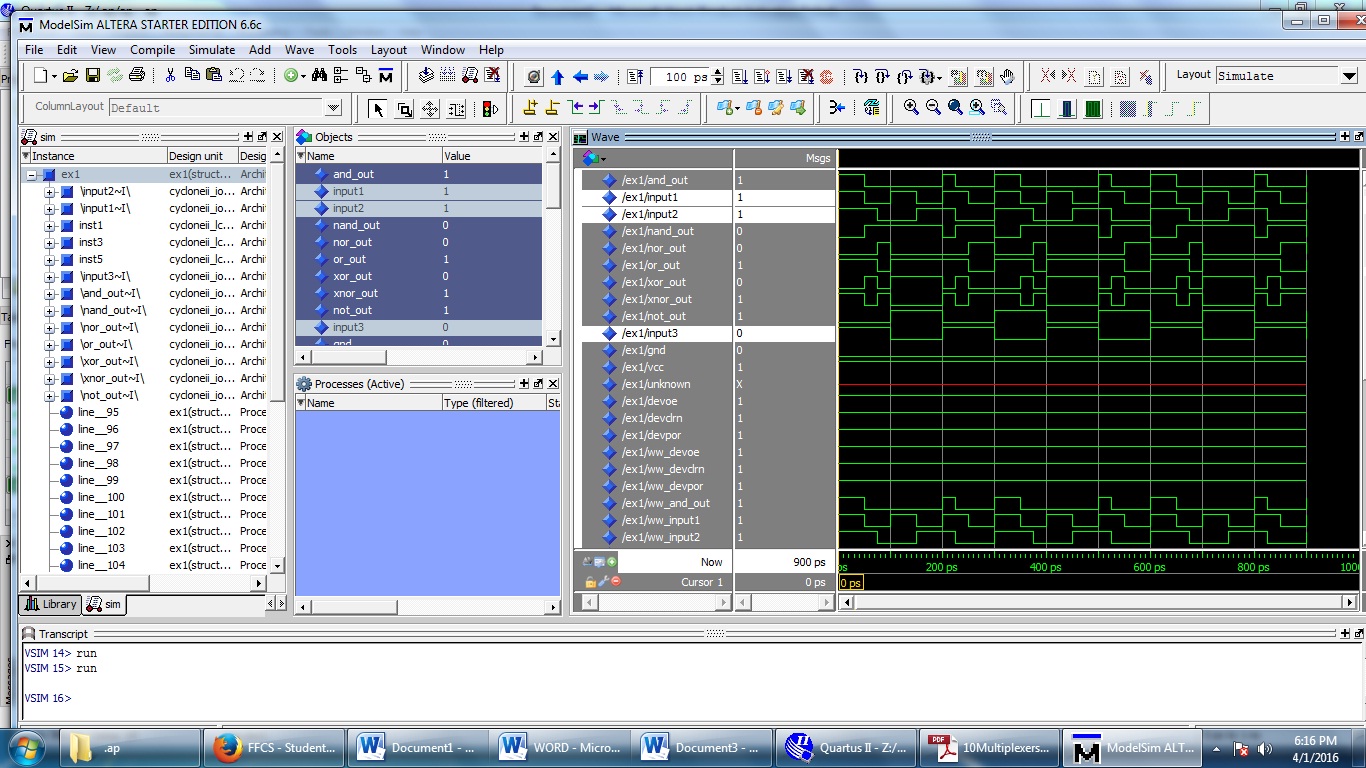
**NAME:RAHUL RAGHUNADHAN**

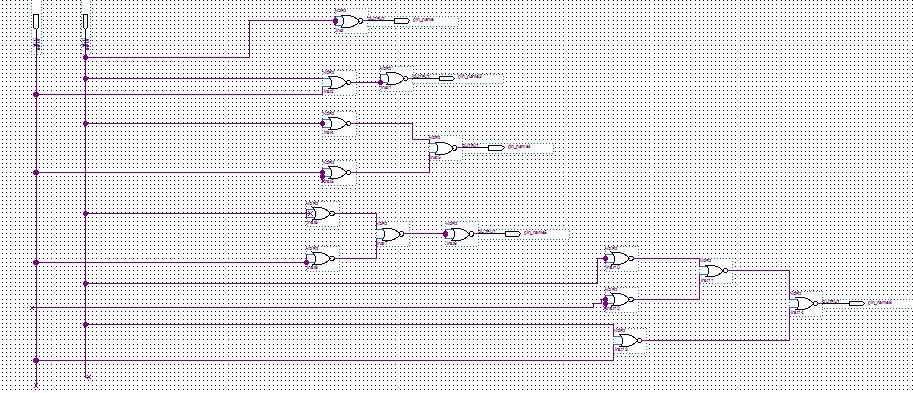
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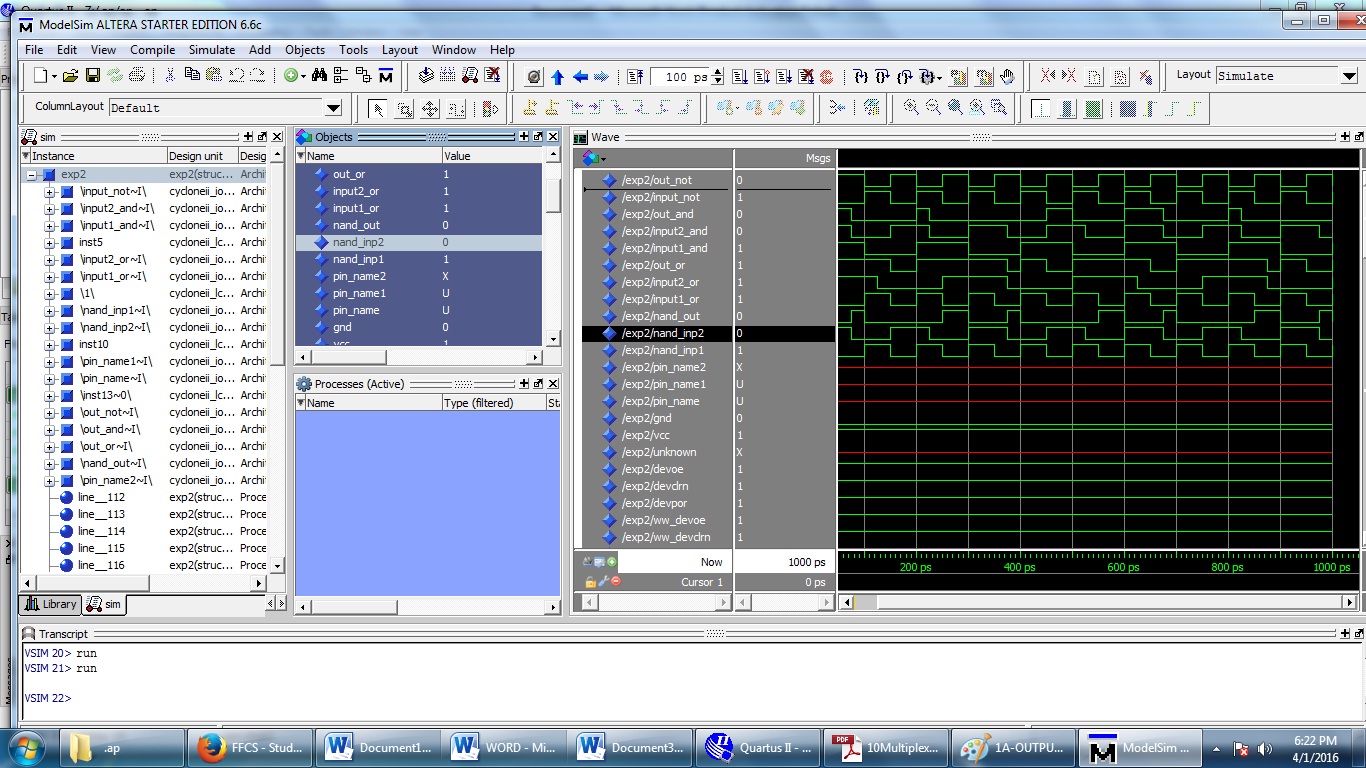
VERIFICATION OF LOGIC GATES

CIRCUIT DIAGRAM:

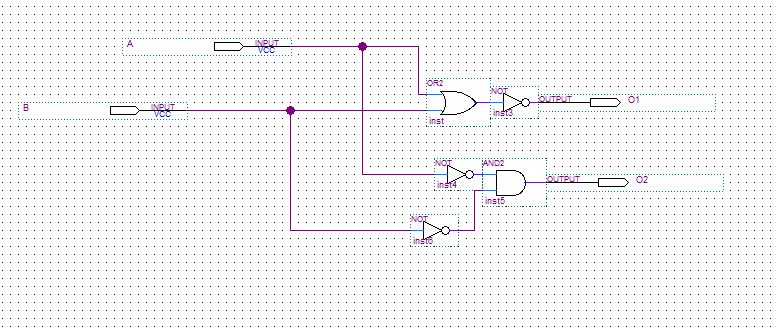


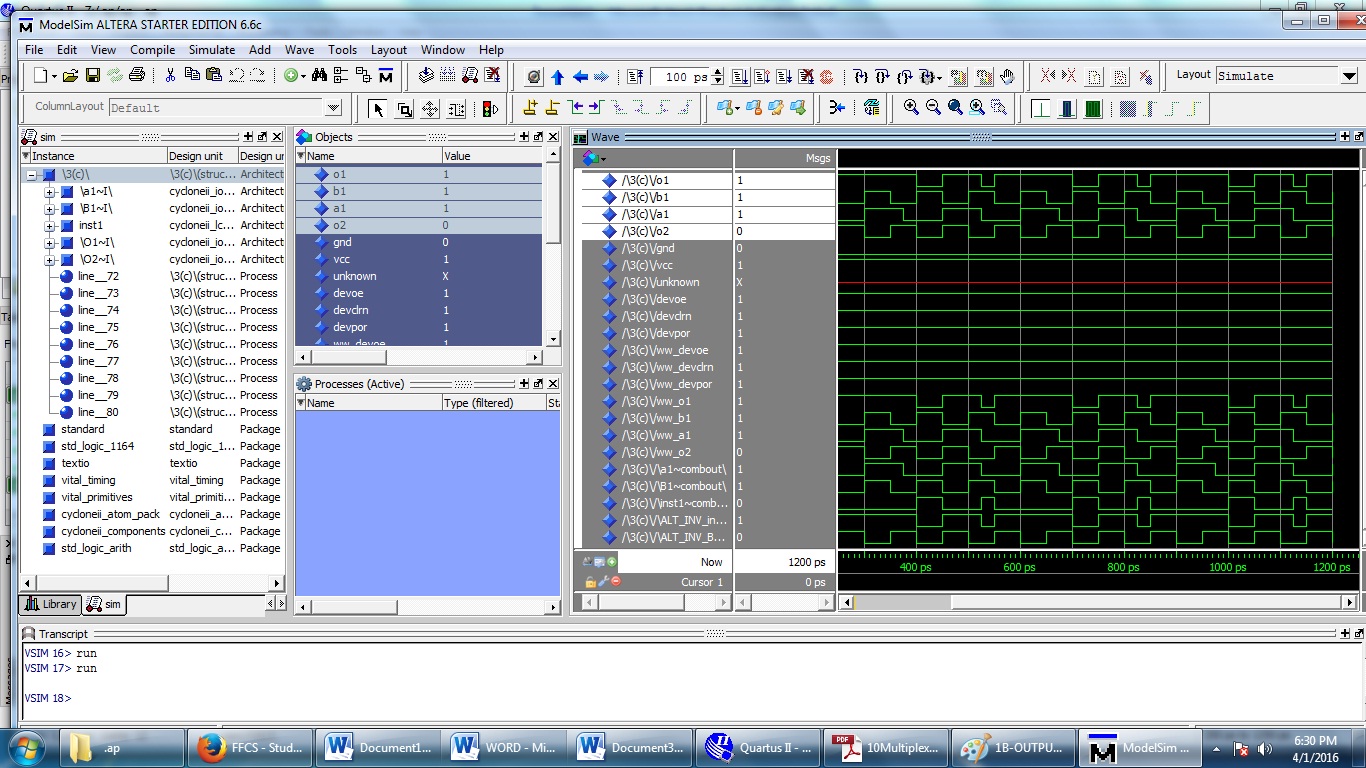






DEMORGAN’S LAW VERIFICATION





VERIFICATION OF BOOLEAN LAWS

