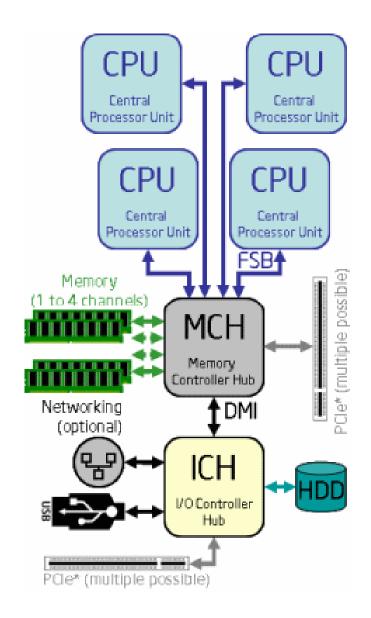
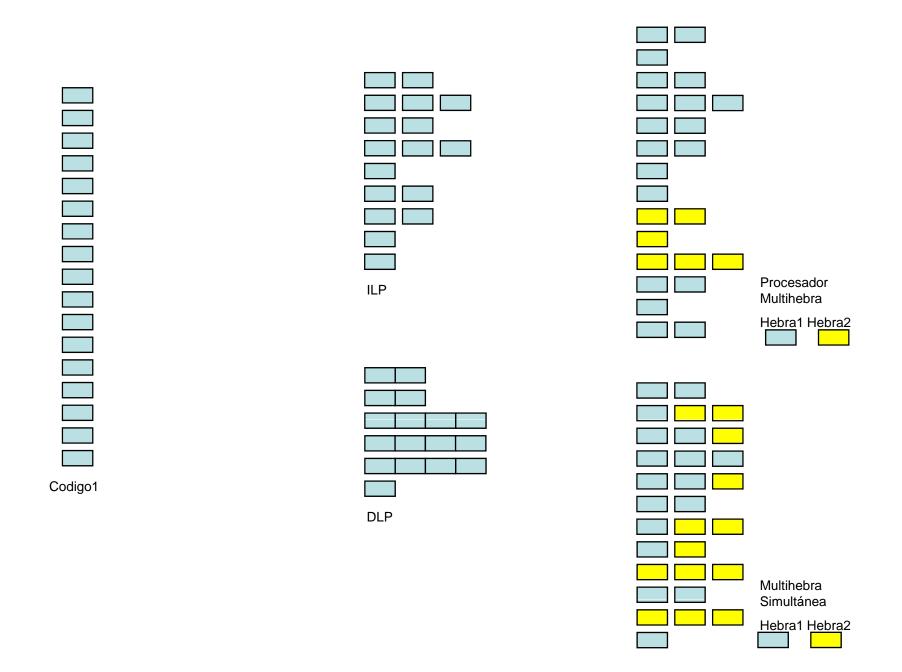
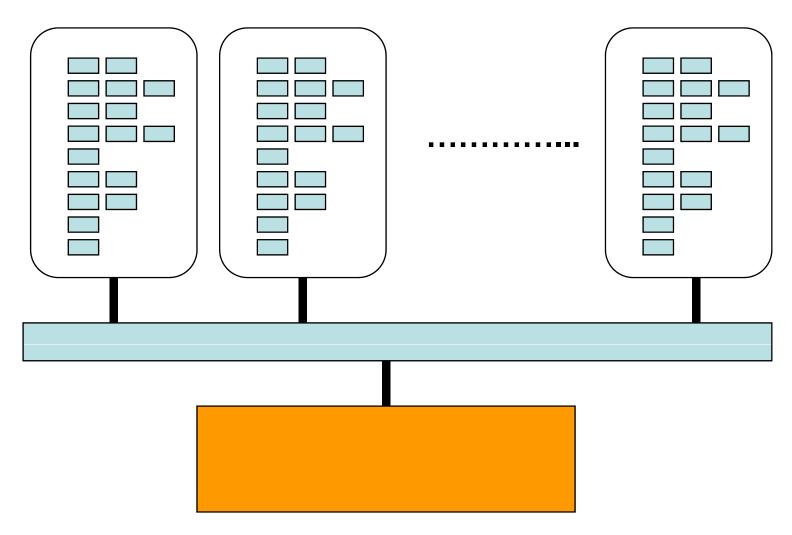


(ICH & other connects same as bottom if needed) IOH Input/Output Hub Up to 4 DDR3 Intel® Scalable channels per Memory Buffers Processor not shown QPI Processor Processor Processor ← Processor QPI IOH DMI Input/Output **ICH** I/O Controller Hub (optional) Networking

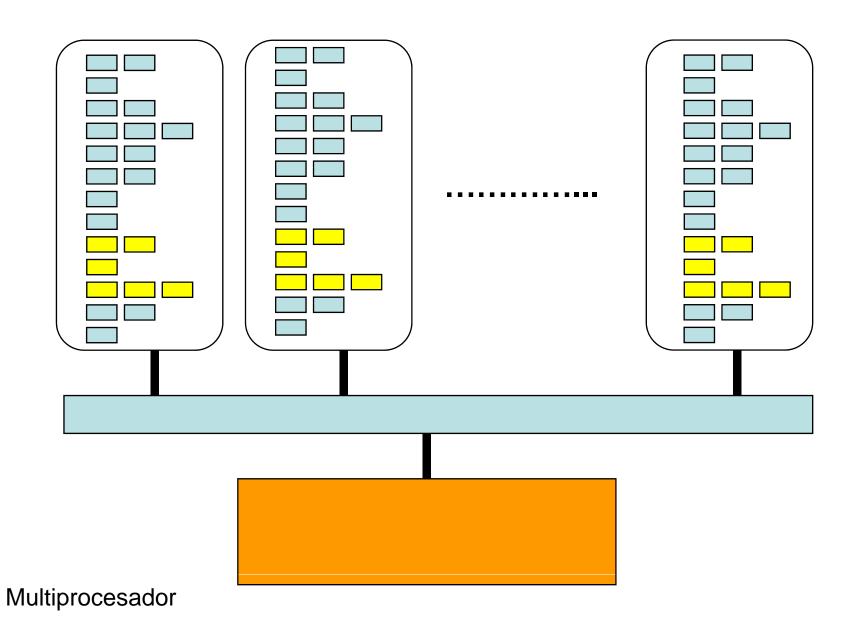
PCle\* (multiple possible)

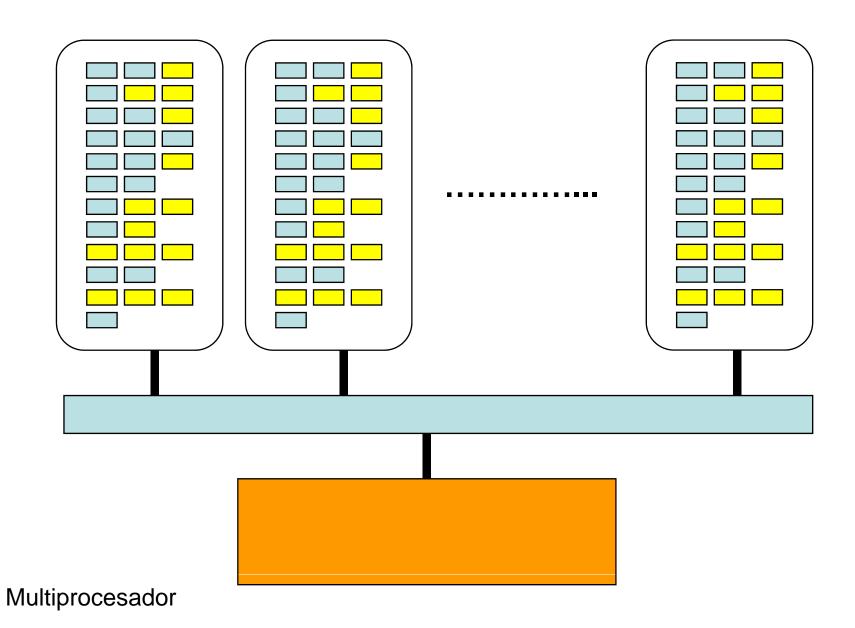




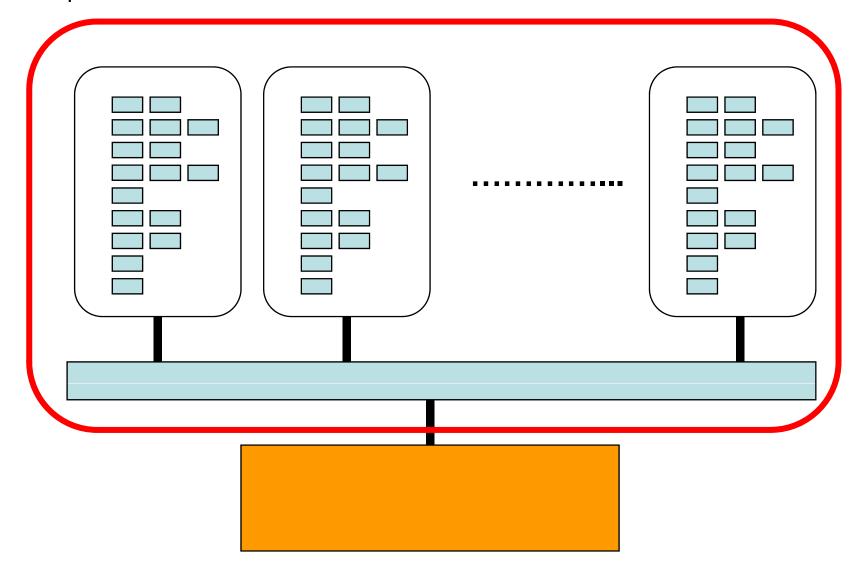


Multiprocesador



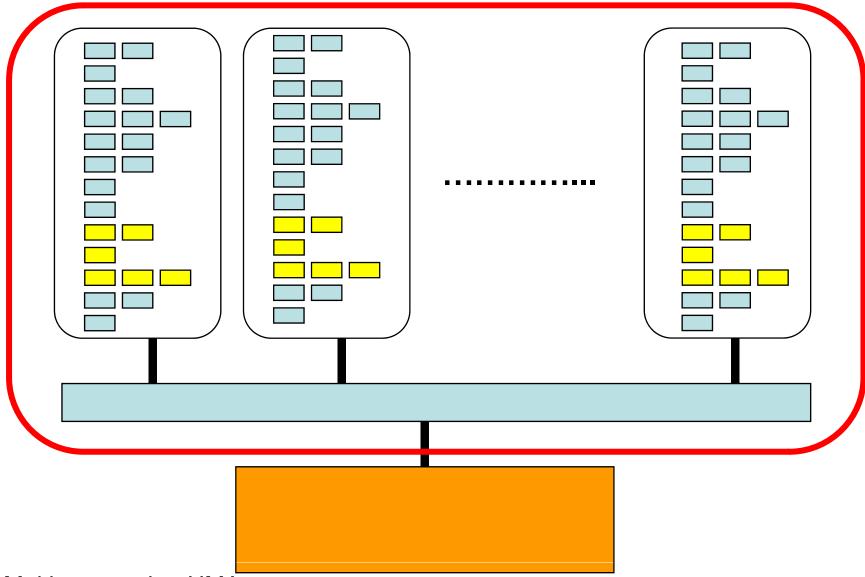


## Microprocesador multinúcleo

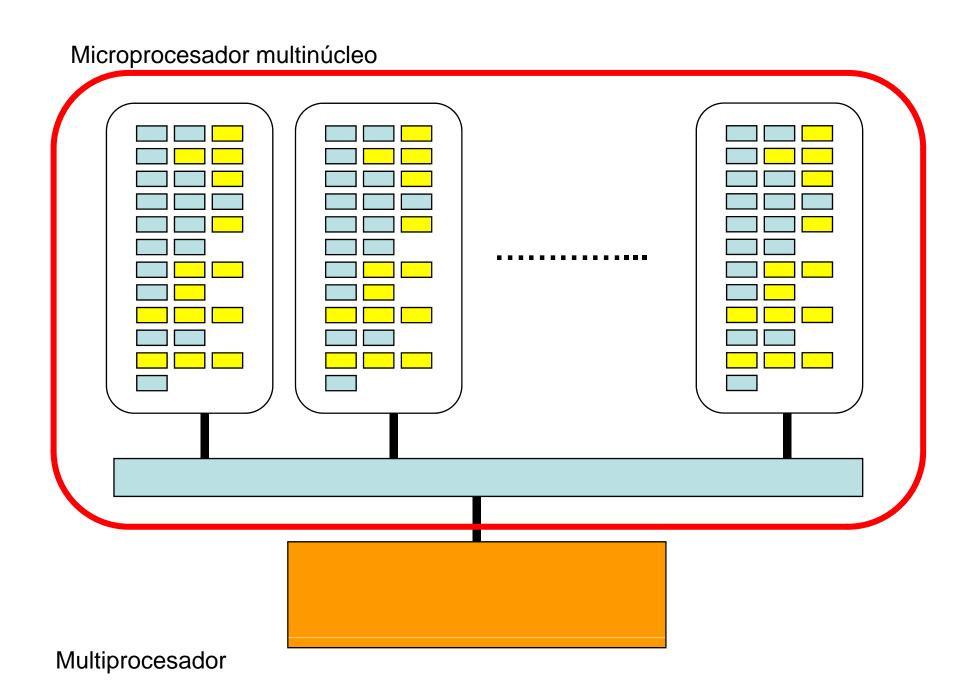


Multiprocesador

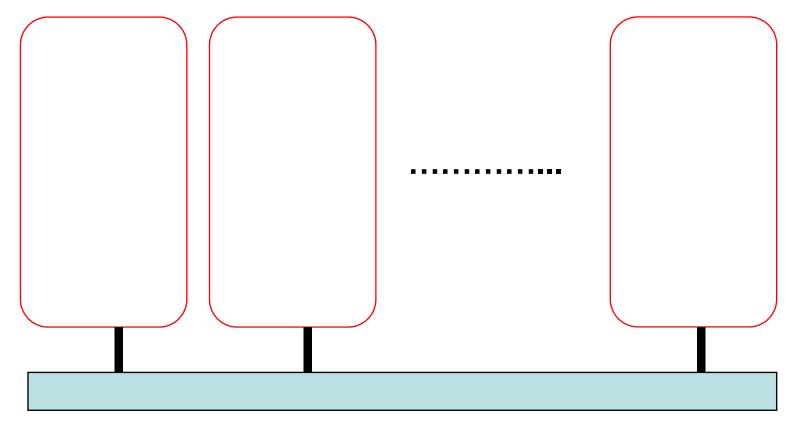
## Microprocesador multinúcleo



Multiprocesador: UMA



Microprocesadores: una o varias hebras en uno o varios núcleos



Red de interconexión

Multicomputador: Varios procesos