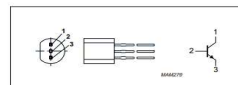


PIN	DESCRIPTION
1	collector
2	base
3	emitter



Title		
Author		
File		Document
C: ... Circuit diagram VSS reductor input.dsn		
Revision	Date	Sheets
1.0		1 of 1