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Problem 1

a. $\text{CPU time} = \text{IC} \times \text{CPI} \times \text{Clock Cycle time} = \text{Execution time}$. IC means Instruction Counts

$$\text{Compiler A's CPU time}_A = 1.0 \times 10^9 \times \text{CPI}_A \times 1.0 \times 10^{-9} = 1.1$$

$$\text{Compiler B's CPU time}_B = 1.2 \times 10^9 \times \text{CPI}_B \times 1.0 \times 10^{-9} = 1.5$$

$$\therefore \text{CPI}_A = \frac{1.1}{1.0 \times 10^9 \times 1.0 \times 10^{-9}} = 1.1$$

$$\text{CPI}_B = \frac{1.5}{1.2 \times 10^9 \times 1.0 \times 10^{-9}} = 1.25$$

\therefore Answer

$$\text{CPI}_A = 1.1$$

$$\text{CPI}_B = 1.25$$

b. $\text{CPU time} = \frac{\text{IC} \times \text{CPI}}{\text{Clock Rate}}$

Since both execution times are same.

$$\text{CPU time}_1 = \text{CPU time}_2$$

a Processor that is running Compiler A's Code a processor that is running Compiler B's code

$$\Rightarrow \frac{1.0 \times 10^9 \times 1.1}{\text{clock Rate}_1} = \frac{1.2 \times 10^9 \times 1.25}{\text{clock Rate}_2}$$

$$\Rightarrow \text{Clock Rate}_1 = \frac{1.0 \times 10^9 \times 1.1}{1.2 \times 10^9 \times 1.25} \text{ Clock Rate}_2 \approx 0.73 \text{ Clock Rate}_2$$

\therefore So a Processor which is running Compiler A's code is 27% slower than a Processor which is running Compiler B's code.

c. $\text{CPU time new} = 6.0 \times 10^8 \times 1.1 \times 1 \times 10^{-9}$ 1ns (original Processor's clock cycle time in a)

$$\frac{\text{Performance new}}{\text{Performance}_A} = \frac{\text{CPU time}_A}{\text{CPU time new}} = \frac{1.0 \times 10^9 \times 1.1 \times 1 \times 10^{-9}}{6.0 \times 10^8 \times 1.1 \times 1 \times 10^{-9}} \approx 1.67$$

$$\frac{\text{Performance new}}{\text{Performance}_B} = \frac{\text{CPU time}_B}{\text{CPU time new}} = \frac{1.2 \times 10^9 \times 1.25 \times 1 \times 10^{-9}}{6.0 \times 10^8 \times 1.1 \times 1 \times 10^{-9}} \approx 2.27$$

\therefore A new compiler's code is 1.67 faster than A's code and 2.27 faster than B's code

Speed Up: 1.67 (relative to A's code)
2.27 (relative to B's code)

A	1.1
B	1.25

Problem 2

1.9.1 $\text{CPU time} = \frac{\text{Clock Cycles}}{\text{Clock Rate}} = \text{Execution time.}$

IC means instruction counts.

Case 1 Single Processor

$$\text{Clock Cycles}_1 = \sum_{i=1}^3 \text{IC}_i \times \text{CPI}_i$$

$$\begin{aligned} \text{Number of Processors} &= 2.56 \times 10^9 \times 1 + 1.28 \times 10^9 \times 12 + 256000000 \times 5 \\ &= 2.56 \times 10^9 \times 1 + 1.28 \times 10^9 \times 12 + 2.56 \times 10^8 \times 5 \\ &= 1.92 \times 10^{10} \end{aligned}$$

$$\text{Execution time}_1 = \frac{1.92 \times 10^{10}}{2 \times 10^9} = 9.6 \text{ S (single Processor)}$$

Case 2 $P > 1$

$$\text{Clock Cycles}_P = \frac{2.56 \times 10^9}{0.7 \times P} \times 1 + \frac{1.28 \times 10^9}{0.7 \times P} \times 12 + 2.56 \times 10^8 \times 5$$

$$= \frac{2.56 \times 10^9 + 1.28 \times 10^9 \times 12}{0.7 \times P} + 2.56 \times 10^8 \times 5$$

$$= \frac{2.56 \times 10^{10}}{P} + 1.28 \times 10^9$$

$$\text{Execution time}_P = \frac{\frac{2.56 \times 10^{10}}{P} + 1.28 \times 10^9}{2 \times 10^9}$$

answer

execution time ₁ = 9.6 S	} →	Speed up (relative to single Processor)
execution time ₂ = 7.04 S		2 Processors: 1.36
execution time ₄ = 3.84 S		4 Processors: 2.5
execution time ₈ = 2.24 S		8 Processors: 4.29

1.9.2

Case 1 Single Processor

$$\begin{aligned} \text{Clock Cycles}_1 &= 2.56 \times 10^9 \times 2 + 1.28 \times 10^9 \times 12 + 2.56 \times 10^8 \times 5 \\ &= 2.176 \times 10^{10} \end{aligned}$$

$$\text{Execution time}_1 = \frac{2.176 \times 10^{10}}{2 \times 10^9} = 10.88 \text{ S}$$

Case 2 $P > 1$

$$\text{Clock Cycles}_P = \frac{2.56 \times 10^9}{0.7 \times P} \times 2 + \frac{1.28 \times 10^9}{0.7 \times P} \times 12 + 2.56 \times 10^8 \times 5$$

$$= \frac{2.93 \times 10^{10}}{P} + 1.28 \times 10^9$$

$$\text{Execution time}_P = \frac{\frac{2.93 \times 10^{10}}{P} + 1.28 \times 10^9}{2 \times 10^9} = \frac{14.65}{P} + 0.64$$

answer

execution time ₁ = 10.88 S	} →	CPI doubled in arithmetic instruction
execution time ₂ = 7.96 S		make execution time slower than
execution time ₄ = 4.3025 S		CPI not doubled execution
execution time ₈ = 2.47 S		time in all case.

(1, 2, 4, 8 Processors)

1.9.3

B) 1.9.1

$$\text{execution time}_4 = 3.84 \text{ s}$$

$$\text{execution time}_{\text{new}} = \text{execution time}_4$$

(1 Processor)

$$\text{execution time}_{\text{new}} = \frac{2.56 \times 10^9 \times 1 + 1.28 \times 10^9 \times \text{CPI}_{\text{new}} + 2.56 \times 10^8 \times 5}{2.0 \times 10^9}$$

$$= 1.92 + 0.64 \text{CPI}_{\text{new}} = 3.84$$

$$\Rightarrow \text{CPI}_{\text{new}} = \frac{3.84 - 1.92}{0.64} = 3$$

$$\therefore \text{CPI}_{\text{new}} = 3$$

Problem 3

1.1.1.1 $\text{execution time} = \text{CPU time} = \text{IC} \times \text{CPI} \times \text{clock cycle time}$

$$\text{execution time} = 750 = 2.389 \times 10^{12} \times \text{CPI} \times 0.333 \times 10^{-9}$$

$$\therefore \text{CPI} = \frac{750}{2.389 \times 10^{12} \times 0.333 \times 10^{-9}}$$

$$\approx 0.94$$

1.1.1.2 $\text{SPEC ratio} = \frac{\text{Reference time}}{\text{Execution time}} = \frac{9650}{750} = 12.87$

1.1.1.4

$$\text{CPU time}_{\text{new}} = \frac{2.389 \times 10^{12} \times 1.1 \times 0.94 \times 1.05 \times 0.333 \times 10^{-9}}{\text{Instruction Count}_{\text{new}} \times \text{CPI}_{\text{new}}}$$

$$\approx 863.71 \text{ s}$$

$$\Rightarrow \frac{863.71}{750} \approx 1.15 \text{ increased } 15\% \text{ CPU time}$$

1.1.1.6

$$\text{CPU time}_{\text{new}} = \frac{2.389 \times 10^{12} \times 0.85 \times \text{CPI}_{\text{new}}}{4.0 \times 10^9} = 700$$

$$\therefore \text{CPI}_{\text{new}} = \frac{700 \times 4.0 \times 10^9}{2.389 \times 10^{12} \times 0.85} \approx 1.38$$

1.1.1.7

$$\frac{4 \text{ GHz}}{3 \text{ GHz}} \approx 1.333 \text{ and } \frac{1.38}{0.94} \approx 1.468$$

Clock Rate increased 1.333 times and CPI increased 1.468.

because we reduced the instructions 15%, too, not only clock rate.

1.1.1.8

$$\frac{\text{CPU time}_{\text{new}}}{\text{CPU time}_{\text{old}}} = \frac{700}{750} \approx 0.933$$

CPU time reduced 6.7%

Problem 4

2.3

```
sub $s5, $3, $4
sll $s5, $s5, 2
add $s5, $s6, $s5
lw $t0, 0($s5)
sw $t0, 32($s7)
```

- Store $i-j$ in $s5$
- Store $(i-j) \times 4$ in $s5$
- $s5$ has $A[i-j]$'s address
- $t0$ has $A[i-j]$'s value
- Store $A[i-j]$'s value in $32(\$s7) \{B[B]\}$

2.10

```
A[i] = A[i]
j = 2 * A[i]
```

2.14

```
OP = 0000 00 → R-Type
RS = 10000 → $16 → $s0
RT = 10000 → $16 → $s0
RD = 10000 → $16 → $s0
shamt = 00000
func = 100000 → add
```

```
R-Type Instruction
add $s0, $s0, $s0
```

2.17

```
OP = 100011 → lw, I-Type Instruction
RS = 00001 → $at
RT = 00010 → $v0
immediate = 0000000000000100
```

∴ I-Type Instruction

```
lw $v0, 4($at)
```

```
1000 1100 0010 0010 0000 0000 0000 0100
```