# Commuter Tracking Sensor Network

Weekly Report - September 21st, 2014

#### Team Members:

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Seth Hendrick (srh7240@rit.edu)
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#### Other Collaborators:

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#### Project Website:

Navigate to Google Drive Share

# Updated Milestone Chart

Milestone	Team Membe r in Charge	Modified Completion Date	Original Completion Date	Comments
5.1 Install software (Django, Apache, etc.)	Seth	6/21/2014	6/17/2014	COMPLETE Apache, Django, MariaDB are installed and ready to go.
6.1 Disable root login test	Seth	6/21/2014	6/16/2014	COMPLETE  Done automatically when  Raspbian was updated
6.2 Set the SSH port to a non-standard port test	Seth	6/21/2014	6/17/2014	COMPLETE SSH Port is set to 1315, not the default port of 22
7.1 Begin playing with Pixy Cam in USB tethered mode	Jared, Alex, Seth	7/11/2014	5/1/2014	complete We've all experimented and interfaced with the PixyCam now, and familiarized ourselves with its basic operation.
5.3 Install and configure fail2ban	Seth	9/1/2014	6/21/2014	COMPLETE
	Alex, Jared, Seth	9/5/2014	6/21/2014	COMPLETE SSH Keys are required to login to the server via SSH
12.1 Create status webpage, hosted somewhere else	Seth	9/5/2014	9/5/2014	COMPLETE Status webpage that pings the gateway is functional. Its currently hosted on one of Seth's pis, located at

				http://people.rit.edu/~srh7240/ct sn_status .
10.1 Code Review for Pixy Software	Alex, Seth, Jared	9/8/2014	9/8/2014	COMPLETE Code review was completed. Information was documented regarding each file's contents.
11. Database Creation		9/23/2014	9/14/2014	
11.1 Create mysql or mariadb database so data from trail can be saved to it	Seth	9/23/2014	9/5/2014	The database has been expanded to have the tables needed for Django and a user table that holds email and phone numbers so admins can be notified if something bad occurs. The node table will be expanded early this coming week to hold each trail node's status and location.
12.3 Link website to database	Seth	9/23/2014	9/21/2014	Database tables needed for DJano user authentication have been created and linked to the website. Database tables needed for the nodes will be done in the next couple days.
3.4 Spice Transient Analysis				Using TI Boost Converter model to simulate while attached to windbelt sin source and half-wave rectifier. Simulations for 200 ohms, 10k ohms and no load are complete. Continuing with Monte Carlo analysis, 10% tolerance on RL.
	Alex, Jared	9/24/2014	9/22/2014	Once complete, add full-wave rectifier and test again to see change in output voltage. Currently getting 3.1V out of the

				boost converter. If full-wave
				doesn't work then attempt to
				increase gain on boost
				converter.
				Once output is decent, tune OV,
				UV and MPPT. TI provides
				spreadsheet to assist with this.
12. Website				
Creation		9/26/2014	9/28/2014	
				Admin panel is up and running.
12.2 Create web	Seth,			Admins can add and remove
front end	Alex	9/26/2014	9/14/2014	users.
				Admin panel is live, but the
16.2 Activate				actual site needs to be done
website	Team	9/29/2014	11/9/2014	still.
13. Website				
Testing (See				
Gantt Chart)	Team	9/30/2014	10/4/2014	
				<b>BLOCKED</b> Voltage is adequate
				for boost converter at 2.2 VDC
				average. Attempting to increase
				Vout by using full-wave rectifier
				instead of half. We should see
				gains in power efficiency by
				about 30% theoretically.
				Breadboard prototyping is on
3.1 Breadboard				hold until simulations of Boost
prototyping				Converter are complete in
				OrCad in order to determine the
				optimal resistor and capacitor
				values for the RC filter.
				Purchase of a QFN to DIP
				converter will allow us to
				prototype the boost converter
	Alex,			on the breadboard as well.
	Jared	10/1/2014	6/1/2014	Pushing back due date to allow

				for this. Next step is to solder the boost converter to the DIP board and test it for continuity.
6.4 White Hat Hacker Test	Seth	10/1/2014	6/21/2014	pushed back to after the website is completed to allow attackers to attempt exploitation of the completed server. Found volunteers to pen test the server.
3. Windbelt power module design		10/2/2014	6/18/2014	
3.2 PCB design	Alex	10/6/2014	6/10/2014	BLOCKED Pushed back to take advantage of slack time prior to start of enclosure task on the 19th. PCB Design is on hold until necessary output current levels are determined for the BQ25504 boost converter. This will require further analysis of datasheets and/or meeting with a resource in the department.
5. Server/Gateway setup	Seth	10/6/2014	7/1/2014	The server is a Raspberry Pi located at ctsn.student.rit.edu.
5.2 Interface XBee with Pi	Seth	10/6/2014	7/1/2014	Ordering new antennas for the XBees as the parts ordered have proprietary connectors.
6. Server/Gateway testing		10/6/2014	7/1/2014	Continuing this when the website is complete.
2.1 Configure XBees for DigiMesh and have them	Seth, Jared	10/7/2014	6/1/2014	

communicating in close proximity				
2.2 Range Test	Seth, Jared	10/9/2014	6/9/2014	
8.1 Use CAD tools to design sensor enclosure	Jared	10/10/2014	7/1/2014	DEFERRED Date was changed due to breadboard protyping, transient analysis, and Eagle design taking longer than expected. Everything that is not dependent on the size of the custom PCB is complete. Awaiting dimensions to continue.
7.2 Interface Pixy Cam with an XBee	Jared, Alex	10/10/2014	6/22/2014	
2. Networking Architecture Configuration and Testing		10/13/2014	6/15/2014	Soldered the xbee usb programmer and downloaded the program that will help program the XBees
2.3 Small-scale trail deployment	Seth, Jared	10/13/2014	6/15/2014	
14.1 Sensors communicate target data with each other	Seth, Alex	10/14/2014	10/4/2014	
10.3 Train camera for identifying walkers, bikers, and horses	Seth	10/15/2014	8/1/2014	Algorithms researched and added to project database. Continuing research. Jared will be moving to assist with hardware, and Seth will continue with this after finishing the website (hence the date change).

8.2 Use 3D printer to print	lava d	40/45/2044	7/45/2044	8.1 Must be done first
the enclosures	Jared	10/15/2014	7/15/2014	
3.3 Ship design for stamping	Alex	10/17/2014	6/18/2014	3.2 Must be completed first
4.1 Solder on components	Alex	10/20/2014	6/29/2014	3.3 Must be completed first
4. Windbelt power module construction and testing		10/22/2014	6/30/2014	
4.2 Continuity tests	Alex	10/22/2014	6/30/2014	4.1 Must be completed first
8. Sensor Enclosure Design / Testing		10/22/2014	8/7/2014	
8.3 Test (See Gantt Chart)	Jared	10/22/2014	8/7/2014	Dependent on 8.2
10. Sensor Software - Identify targets		10/24/2014	9/1/2014	
10.4 Train camera to figure out what direction the target is going	Jared	10/24/2014	9/1/2014	10.3 Must be done first
1. Contact Monroe County Discuss deployment options for sensor nodes.	Jared	10/28/2014	10/27/2014	Not a priority until we complete more of the technical requirments.
14. Target Data Communication		10/28/2014	10/5/2014	

44000				
14.2 Sensors				
can				
communicate				
and write target				
data to database	Seth	10/28/2014	10/5/2014	
6.5 Ping disabled test	Seth, Security Majors	10/31/2014	6/21/2014	DEFERRED While the server is on the RIT campus, this is completed since outsiders can not ping rit.edu. If the server moves of campus for whatever reason, this will need to be revisited
10.2 Compile				<b>DEFERRED</b> According to the
GCC version of				pixy cam developers, GCC
Pixy software				version is not ready to use yet.
and note				That's fine though, we can use
differences	Seth	10/31/2014	9/8/2014	Keil instead.
7. Sensor				
hardware				
testing and				
integration		10/31/2014	7/11/2014	
7.3 Integrate				Will be done upon completion of
with existing	Jared,			the power module.
power module	Alex	10/31/2014	7/11/2014	·
15. Computer				
Vision Testing				
(See Gantt	Alex,			
Chart)	Seth	11/1/2014	10/28/2014	
,		_		BLOCKED Awaiting completion
				of transient analysis and
				breadboard prototyping to
9. Windbelt				continue testing with focus on
Testing (See				_
Gantt Chart)				smoothing levels for a half-wave and full-wave rectifier with the
	Alos	11/5/0011	E/07/004 4	boost converter QFN to DIP
	Alex	11/5/2014	5/27/2014	package installed.

17.1 Advanced II integration testing with focus on single node in controlled				
environment	Team	11/5/2014		
17.2 Advanced II integration testing with focus on single node in an outdoor environment	Team	11/10/2014		
17.Integration				
Testing		11/13/2014		
17.3 Advanced II testing with focus on operation in outdoor environment for multiple nodes	Team	11/13/2014		
16. Deployment		11/17/2014	11/9/2014	
16.1 Deploy nodes on trail	Team	11/17/2014	11/5/2014	

# **Current Milestones**

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3.1 Breadboard prototyping	Alex, Jared	10/1/2014	6/1/2014	is adequate for boost converter at 2.2 VDC average. Attempting to increase Vout by using full-wave rectifier instead of half. We should see gains in power efficiency by about 30% theoretically. Breadboard prototyping is on hold until simulations of Boost Converter are complete in OrCad in order to determine the optimal resistor and capacitor values for the RC filter. Purchase of a QFN to DIP converter will allow us to prototype the boost converter on the breadboard as well. Pushing back due date to allow for this. Next step is to solder the boost converter to
	Jaicu	10/1/2017	0/ 1/20 14	boost converter to

				the DIP board and
				test it for continuity.
3.2 PCB design	Alex	10/6/2014	6/10/2014	BLOCKED Pushed back to take advantage of slack time prior to start of enclosure task on the 19th. PCB Design is on hold until necessary output current levels are determined for the BQ25504 boost converter. This will require further analysis of datasheets and/or meeting with a resource in the
	Alex	10/0/2014	0/10/2014	department.
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				holds email and phone numbers so admins can be notified if something bad occurs. The node table will be expanded early this coming week to hold each trail node's status and location.
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	Alex, Jared	9/24/2014	9/22/2014	full-wave rectifier and test again to see

	1	1	1	1
				change in output voltage. Currently getting 3.1V out of the boost converter. If full-wave doesn't work then attempt to increase gain on boost converter.
				Once output is decent, tune OV, UV and MPPT. TI provides spreadsheet to assist with this.
12.2 Create web front end	Seth, Alex	9/26/2014	9/14/2014	Admin panel is up and running. Admins can add and remove users.
2. Networking Architecture Configuration and Testing		10/13/2014	6/15/2014	Soldered the xbee usb programmer and downloaded the program that will help program the XBees

# **Next Milestones**

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				and half-wave rectifier. Simulations for 200 ohms, 10k ohms and no load are complete. Continuing with Monte Carlo analysis, 10% tolerance on RL.
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12.2 Create web front end	Seth, Alex	9/26/2014	9/14/2014	Admin panel is up and running. Admins can add and remove users.

#### Status

#### **Difficulties**

Other class assignments have been slowing progress with the project.

No difficulties from the software side of things.

Working on the boost converter configuration has proven to be a more difficult task than anticipated. TI has provided spreadsheets to configure the overvoltage, undervoltage and MPPT resistor configurations, which will speed up the process somewhat.

### Surprises

Unit testing is awesome. It has prevented several bugs from escaping and causing problems down the line.

Simulations for the boost converter take 30 minutes on average for a 1.4 second runtime.

The QFN to DIP converters that were ordered for prototyping are ridiculously small, but will serve their purpose well, eliminating dependencies during the Windbelt testing phase on the completed power module circuit board.

#### Successes

Admin page for the website is up and running at ctsn.student.rit.edu/admin. The database construction is still underway, and should be mostly complete enough early this coming week to get started on implementing the actual front end.

If an error on the network occurs, the gateway process can query the databases for admins phone numbers and emails and notify them via text and/or email with what went wrong.

Spice analysis is going smoothly for a half-wave rectifier configuration going into the boost converter. Output at the boost converter was simulated for different resistor values in the RC filter following the rectifier. This will be repeated for a full-wave rectifier. Currently, output voltage is at 3.1V, which is slightly under the necessary 3.7 volts, so

either the full-wave rectifier will provide more power to the boost converter (expected), or the boost converter will be reconfigured to provide higher gain.

A meeting with Professor Cliver resulted in a heightened understanding of the boost converter component, along with connections to several resources that will be able to assist with the application. Namely, Jeff Lonneville will be able to assist with soldering the bq device to the DIP converter using the tools available in the CE Tech building.

## **Open Questions & Problems**

None this week.

### **Gantt Chart**

