

Freaky Friday

16 January, 2025 (Week 1)

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iii. Abbreviations

Abbreviation	Meaning
ELP305	Systems and Design Lab
TC	Tribe Coordinator
IF	Involvement Factor
SPOC	Single Point of Contact
CAD	Computer-Aided Design
LCD	Liquid Crystal Display
USB	Universal Serial Bus
asf	Arduino Serial Programmer
GND	Ground

iv. Glossary

Frequency: The rate at which an electrical signal oscillates.

Amplitude: The height of the wave, indicating signal strength.

Waveform: The shape of the periodic signal.

v. Mind Map

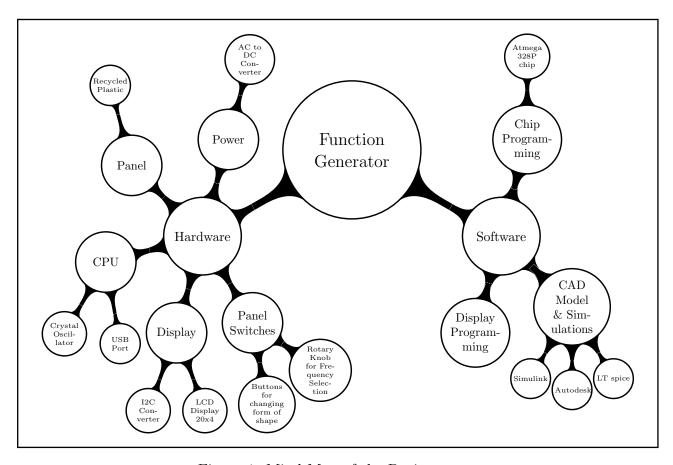


Figure 1: Mind Map of the Project

vi. Project Management Figure

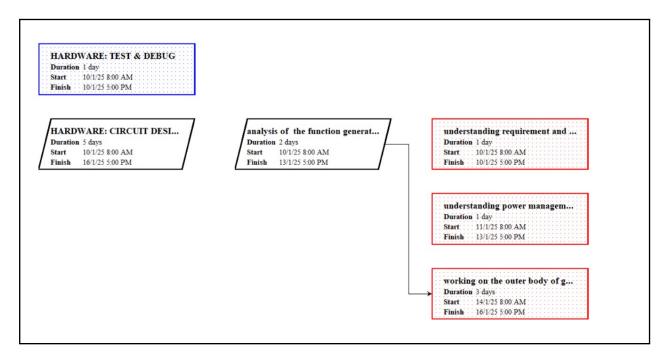


Figure 2: Hardware Test and Debugging Process

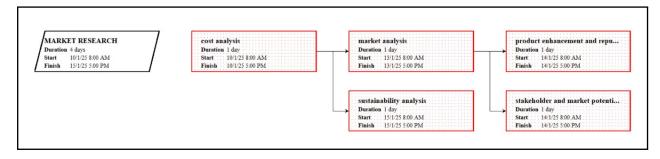


Figure 3: Market Research Analysis Workflow

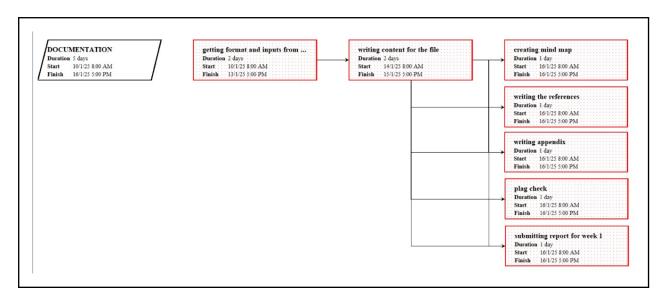


Figure 4: Documentation and File Writing Steps

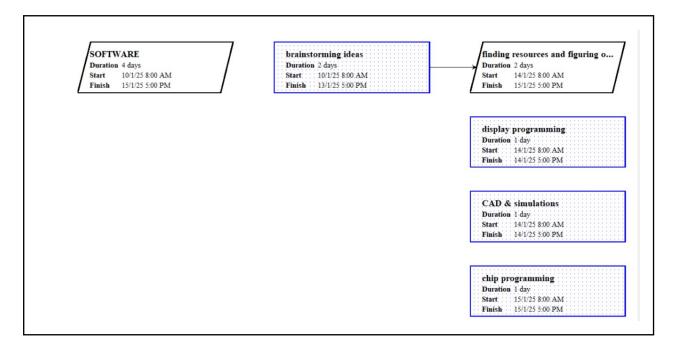


Figure 5: Software Development Flowchart

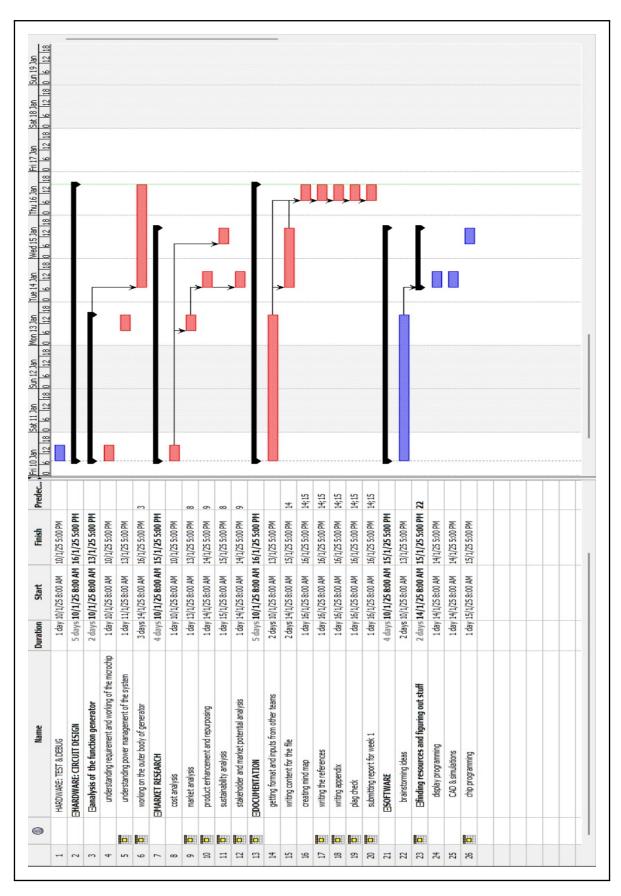


Figure 6: Project Timeline in Gantt Chart

vii. Project Statement

Design and development of a multifunctional signal generator for producing various waveforms with adjustable parameters for laboratory testing and experimentation.

viii. Abstract

As part of Project 1, we are creating a **Function Generator**—a highly adaptable electronic device designed to produce various periodic waveforms, including sine, square, triangular, and sawtooth waves.

Our goal is to replicate the **Scientech 4064S Function Generator** that is used in the lab.

This device allows users to adjust frequency, amplitude, and duty cycle, making it a perfect tool for tasks like electronics testing, debugging, and circuit design.

Key features of the Function Generator:

- 1. Precise frequency adjustments for accurate signal output.
- 2. Multiple waveform options to suit diverse testing needs.
- 3. A user-friendly interface for quick and efficient operation.

Function Generator is an essential tool for any electronics lab, offering flexibility and precision for a wide range of applications. The employment of cheap parts providing sufficient performances is foreseen within the frame of this project. The resulting design becomes a means of instruction for hands-on awareness of techniques of waveform generation in lab work.

Chapter 1

Motivation

The development of an affordable and versatile function generator addresses a significant market opportunity in India's growing electronics and testing sector. We have estimated the total addressable market of Function generators in India to be USD 2.35 Billion by 2030 servicable available market to be USD 940 million by 2030. Our product targets three primary stakeholder segments: educational institutions (including 25,000+ technical institutes) [9], hobbyist communities (approximately 50,000 active electronics enthusiasts), and small businesses (around 10,000 electronics firms). The market size analysis reveals promising potential, with estimates ranging from Rs. 15 Lakhs (worst-case) to Rs. 1.6 Crore (best-case) in revenue, based on projected sales of 1,500 to 16,000 units at Rs. 1,000 per unit .We have estimated the total addressable market of Function generators in India to be USD 2.35 Billion by 2030 servicable available market to be USD 940 million by 2030.[8]

The product's primary Unique Selling Proposition lies in its competitive pricing and enhanced capabilities compared to alternatives like the Techtonics XR2206, offering a superior frequency range (10 MHz vs. 1 MHz) at a comparable price point. This accessibility makes advanced signal generation technology available to budget-conscious institutions and individuals. Furthermore, the product incorporates sustainable practices through the use of recycled materials, appealing to environmentally conscious consumers.

A significant advantage lies in the product's re-purposing potential, particularly in the audio and music sectors [3]. The signal generation technology and digital-to-analog converters can be effectively utilized for sound system calibration, audio equipment diagnostics, and professional studio monitoring. Based on stakeholder expectations, the function generator delivers multiple waveform generations (sine, square, triangle, ramp, and pulse), adjustable output signal amplitude (Vpp), and DC offset capabilities.

The **development methodology** is tailored for small to medium-scale production, focusing on **quality control** and efficient assembly processes, suitable for the projected market size. The product maintenance strategy includes clear **Service Level Agreements** enforced through authorized service centers, ensuring a **24-hour response time** for technical support and repairs, thus maintaining customer satisfaction and product reliability in this competitive market segment.

Chapter 2

Requirements[1]

2.1 Inputs

- 1. Display Module (20x4 LCD Display): [2]
 - HD44780 Compatible 20x4 LCD display.
 - Capable of displaying 20 characters per line and 4 lines.
 - Allows clear visualization of frequency, amplitude, and waveform type.
 - Contrast adjustable via a $10k\Omega$ potentiometer.[10]
- 2. Microcontroller: ATmega328P chip, to be used with pseudo-Arduino.[11]
 - Requirements for pseudo-Arduino: Crystal oscillator and USB cable.[4]
- 3. Frequency Range:
 - For sine wave: 1mHz 10MHz.
 - For other waveforms: 1mHz 3MHz
 - Frequency resolution: Fine adjustments of the order of 1 mHz.
- 4. Waveform Types: Sine, square, triangular, pulse and ramp waves can be generated.
- 5. **Amplitude Settings**: Adjustable from 0 to 5 V (peak-to-peak) with a resolution of 0.01 V.
- 6. Control Interfaces:
 - Physical rotary knobs for coarse adjustments.[5]
 - Additional knob for finer adjustment of frequency values.[5]
- 7. Modulation Inputs:
 - Supports AM, FM, and PM modulation modes.
- 8. Power Supply:
 - Requires a standard AC input (220V/50Hz).

• Internal conversion to DC for stable operation.

9. Additional Inputs:

- Toggle switches for function selection.[12]
- 2/3-pin input connectors for external components.
- A PCB, onto which components would be soldered.[7]

2.2 Outputs

- 1. Output Impedance: Configured to 50 Ω for compatibility with standard test equipment.
- 2. Waveform Accuracy: Less than 1% distortion across all frequencies and amplitudes.
- 3. Signal Stability: Output frequency drift not exceeding 0.01%.
- 4. Voltage Offset: Programmable DC offset adjustable between -5V to +5V.
- 5. Frequency Precision: Accuracy within 0.01% of the programmed value.
- 6. Output Connectors:
 - SMA connectors for signal output. [7]
 - Support for backlight connections with optional 220Ω resistor for current limiting.

2.3 Power Requirements

1. Power Consumption: Maximum 20 W under full load.

2.4 Logistical Requirements

- 1. **Portability**: Compact design with a weight of under 3 kg.
- 2. Accessories: Supplied with BNC cables, probes, knobs, and a user manual.

2.5 Environmental Requirements

- 1. Operating Temperature: Functional from 0 ° C to 50 ° C.
- 2. Storage Temperature: Safe storage from -20°C to 70°C.
- 3. **Humidity Resistance**: Operates in environments with up to 80% relative humidity (non-condensing).
- 4. **EMI/EMC Compliance**: Meets electromagnetic interference and compatibility standards.

2.6 Site (Usage Site) Requirements

- 1. **Laboratory Use**: Designed for standard electronics laboratories with clean and stable workbenches.
- 2. **Power Outlet Compatibility**: Supports both EU and US power outlet standards with adapters.

2.7 Structural Requirements

1. Chassis: Durable recycled plastic enclosure.

2. Control Panel:

- Intuitive layout with labeled controls.
- Backlit buttons for visibility in low-light conditions.

3. Protective Measures:

- Fuse for circuit protection.
- Recycled plastic to shield internal components.

2.8 Time Requirements

- 1. **Design Time Requirement**: Development and testing to be completed within 6 months.
- 2. **Time to Market Requirement**: Ready for commercial launch within 9 months from project initiation.
- 3. **Lifetime Requirements**: Guaranteed operational life of at least 5 years with proper maintenance.
- 4. **End of Life Requirements**: Must support recycling and environmentally safe disposal of components.

2.9 Other Non-Functional Requirements

- 1. **Aesthetic Design**: Modern, sleek appearance.
- 2. **Safety**: Certified for safety under IEC 61010-1 standards.
- 3. **Serviceability**: Modular design for easy part replacement.
- 4. **Reliability**: Mean Time Between Failures (MTBF) of at least 10,000 operational hours.
- 5. Weight: Maximum weight of 3 kg to ensure portability.

Bibliography

- [1] 10MHz AM-FM Modulation Function-Pulse Generator with 50MHz Frequency Counter.

 URL: https://scientechworld.com/wp-content/uploads/2023/06/10mhzam-fm-modulation-function-pulse-generator-with-50mhz-frequencycounter-4064s.pdf?srsltid=AfmBOoqXtFoGwi9X910KwS5pTs5y5iqqWS9GfCEKAAYQxilRAvZfnb
- [2] 20×4 Character (Blue) LCD Display LCD2004 eBhoot Electronics. URL: https://ebhoot.in/shop-2/display-devices/20x4-character-blue-lcd-display-lcd2004/?srsltid=AfmBOoo_mbRK4AWzu2D6t96ZnAC_xRXPEPIZfLZrlhOAGWDMAdONmm-Y8Dw&gQT=1 (visited on 01/16/2025).
- [3] Audio Signal Generator NEW Nagravox updated. Nagravox. URL: https://nagravox.com/products/audio-signal-generator-new-nagravox-updated (visited on 01/16/2025).
- [4] Buy 12 MHz Crystal Oscillator at HNHCart.com. URL: https://www.hnhcart.com/products/12-mhz-crystal-oscillator?_pos=2&_sid=d4b117fb4&_ss=r (visited on 01/16/2025).
- [5] Buy RBS-1-112-20VP-1 Pole 12 Position Metal Rotary Switch (20mm 18 Teeth). Explore the category for more range. URL: https://robu.in/product/rbs-1-112-20vp-1-pole-12-position-metal-rotary-switch-20mm-18-teeth/(visited on 01/16/2025).
- [6] Buy SMA Connector Straight Female Vertical Mount KTRON India. URL: https://www.ktron.in/product/sma-connector-straight/(visited on 01/16/2025).
- 7] Buy Universal 5 x 7 cm PCB Prototype Board Single-Sided 2.54mm Hole Pitch.

 URL: https://robu.in/product/5-x-7-cm-universal-pcb-prototypeboard-single-sided-2-54mm-hole-pitch/?gad_source=1&gclid=CjOKCQiA
 aK8BhCDARIsAL_-H9nzgP-bcPHSij4u2TuPKb_Bb71ifdaXPgMLoZ2tt6Vz70VnS57Tr0EaApYgEALw_
 wcB (visited on 01/16/2025).
- [8] Function Generator Market Size, Share, Growth, Report [2032]. URL: https://www.fortunebusinessinsights.com/function-generator-market-104357.
- [9] Higher education in India Wikipedia. URL: https://en.wikipedia.org/wiki/ Higher_education_in_India (visited on 01/16/2025).
- [10] MACFOS. Buy 10k Ohm 3590S Precision Multiturn Potentiometer Online at Robu.in. Dec. 16, 2023. URL: https://robu.in/product/10k-ohm-3590s-precision-multiturn-potentiometer/ (visited on 01/16/2025).
- [11] MACFOS. BUY Nano V3.0 ATmega328P 5V 16MHz CH340 Type-C with White Type-C Cable 30cm Online at Robu.in. URL: https://robu.in/product/nano-v3-0-16mhz-ch340-type-c-with-whitetype-c-cable-30cm/ (visited on 01/16/2025).

[12] MACFOS. Yellow DS-430 2PIN ONOFF Self-Reset Square Push Button Switch?NO Press Break? URL: https://robu.in/product/yellow-ds-430-2pin-onoff-self-reset-square-push-button-switch%ef%bc%88no-press-break%ef%bc%89/ (visited on 01/16/2025).

Appendix A

Document Statistics

• Word count: 2080

• Number of sentences: 567

• Number of characters: 8901

• Readability Indices:

- Flesch Reading Ease: 70.0

- Gunning Fog Index: $10.5\,$

- Coleman Liau Index: 12.3

Appendix B

Softwares Used

The software(s) we used to prepare this report are as follows:

- 1. **LaTeX** which is a high-quality typesetting system, commonly used for producing scientific and technical documents. It can be downloaded from:
 - LaTeX Project Website: https://www.latex-project.org/get/
- 2. **Zotero** Which is a reference management software. Used to manage data and related research materials. It can be downloaded from:
 - Zotero Website: https://www.zotero.org/download/
- 3. **ProjectLibre** Which is project management software system. It helps in planning, scheduling, and tracking projects. It can be downloaded from:
 - ProjectLibre Website: https://www.projectlibre.com/

Appendix C

Document ID

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