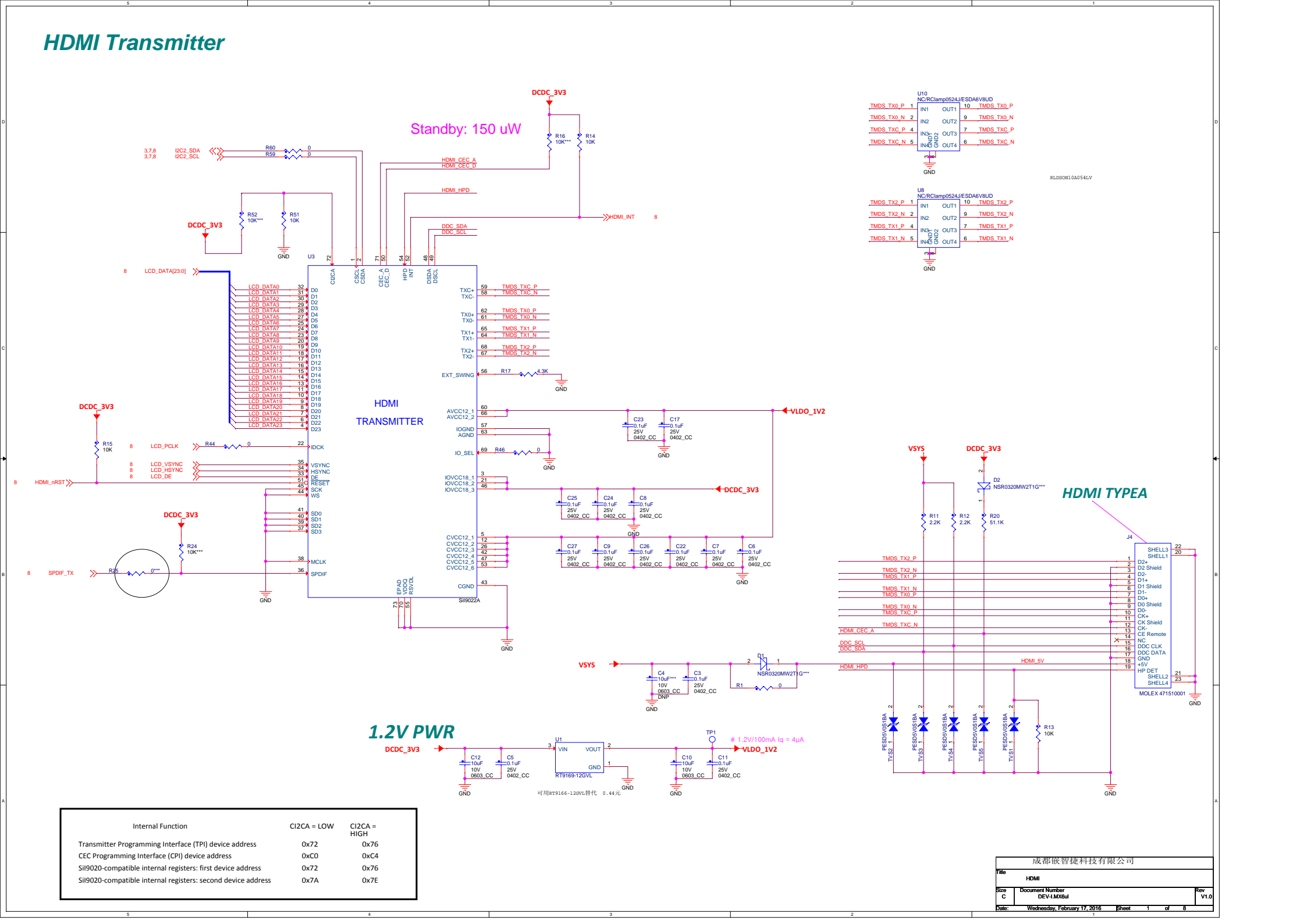
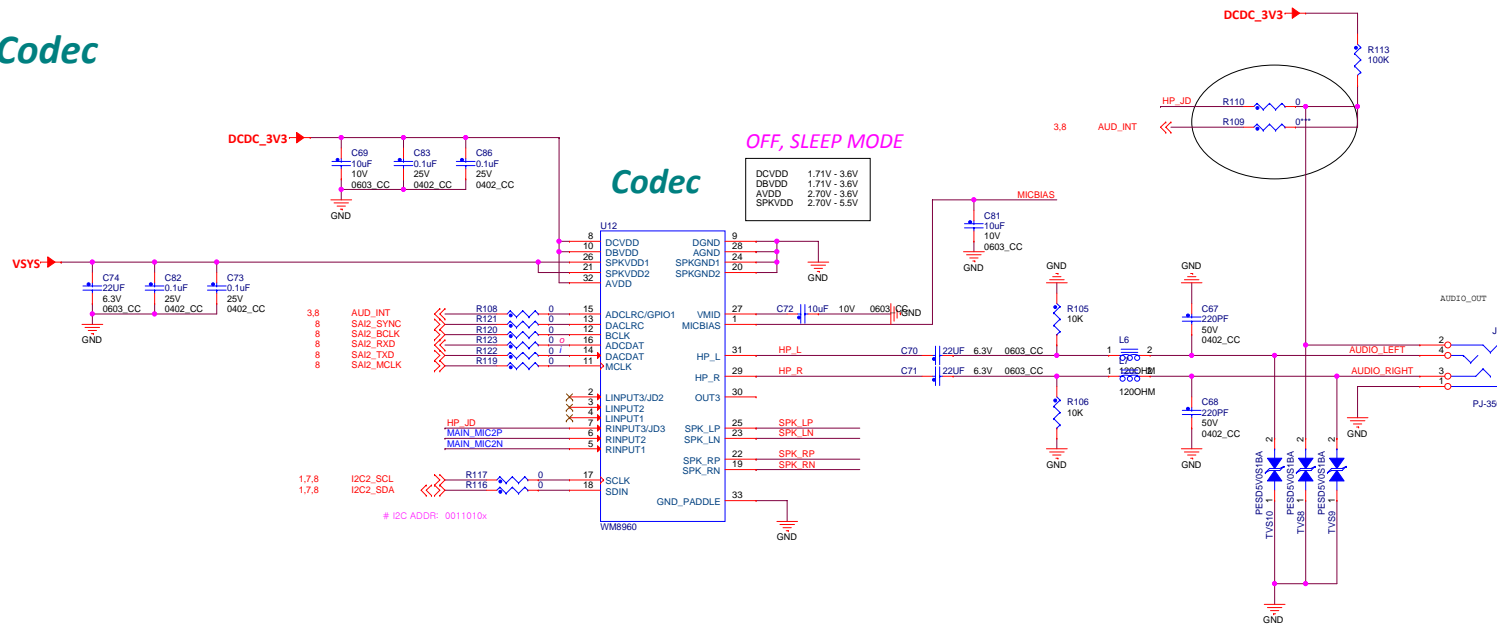


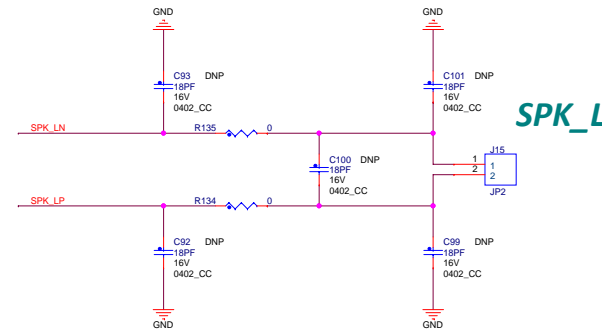
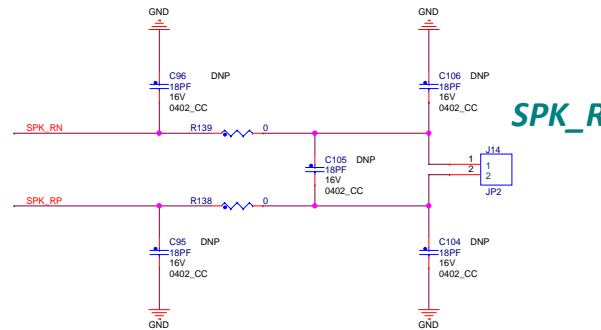
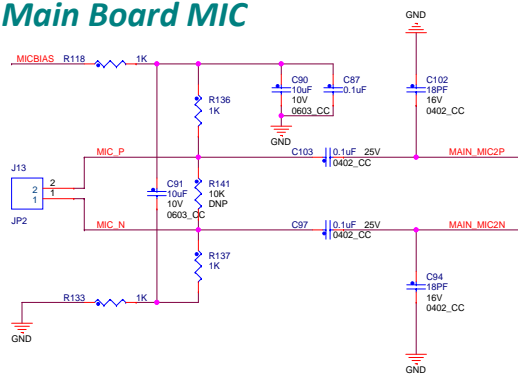
HDMI Transmitter



Audio Codec

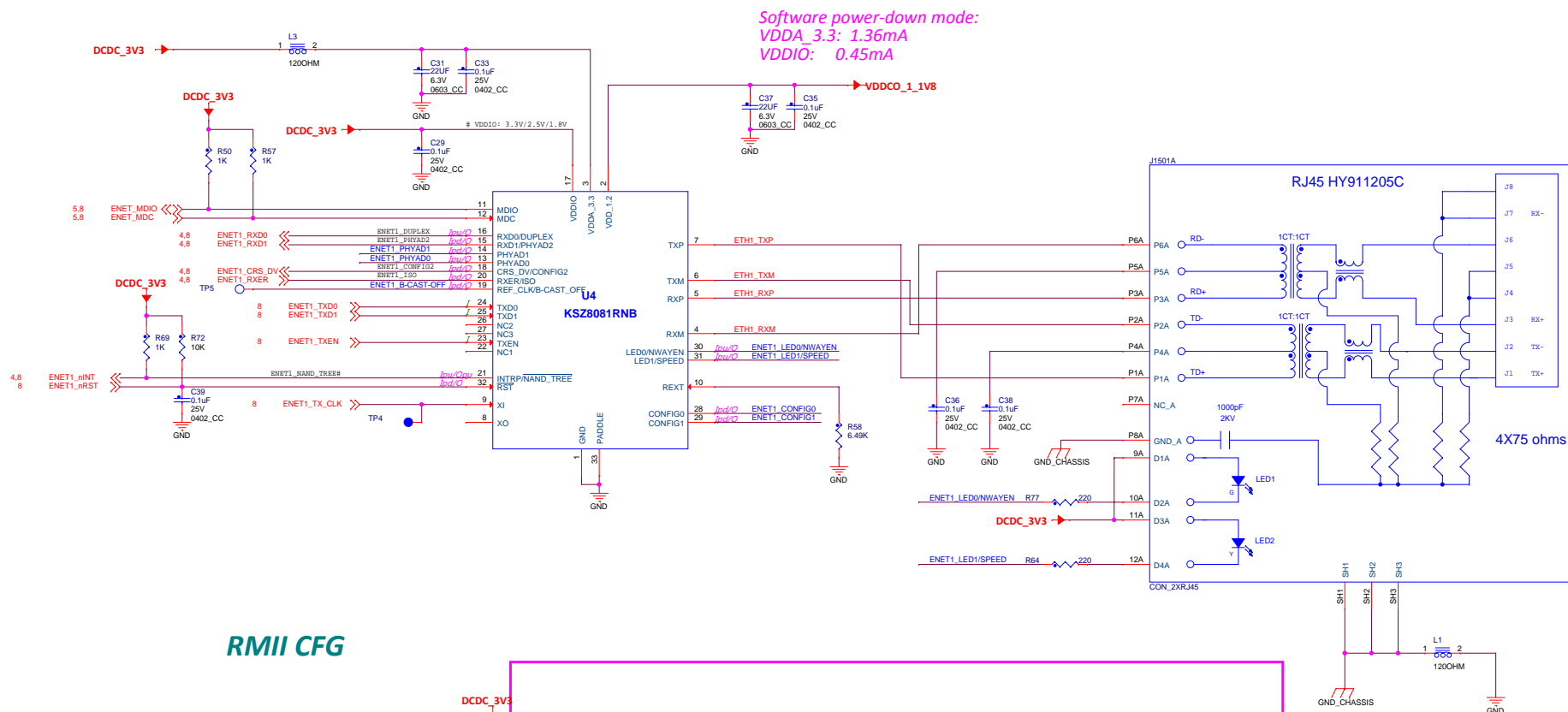
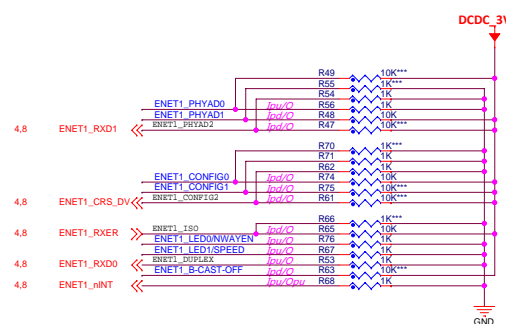


Main Board MIC



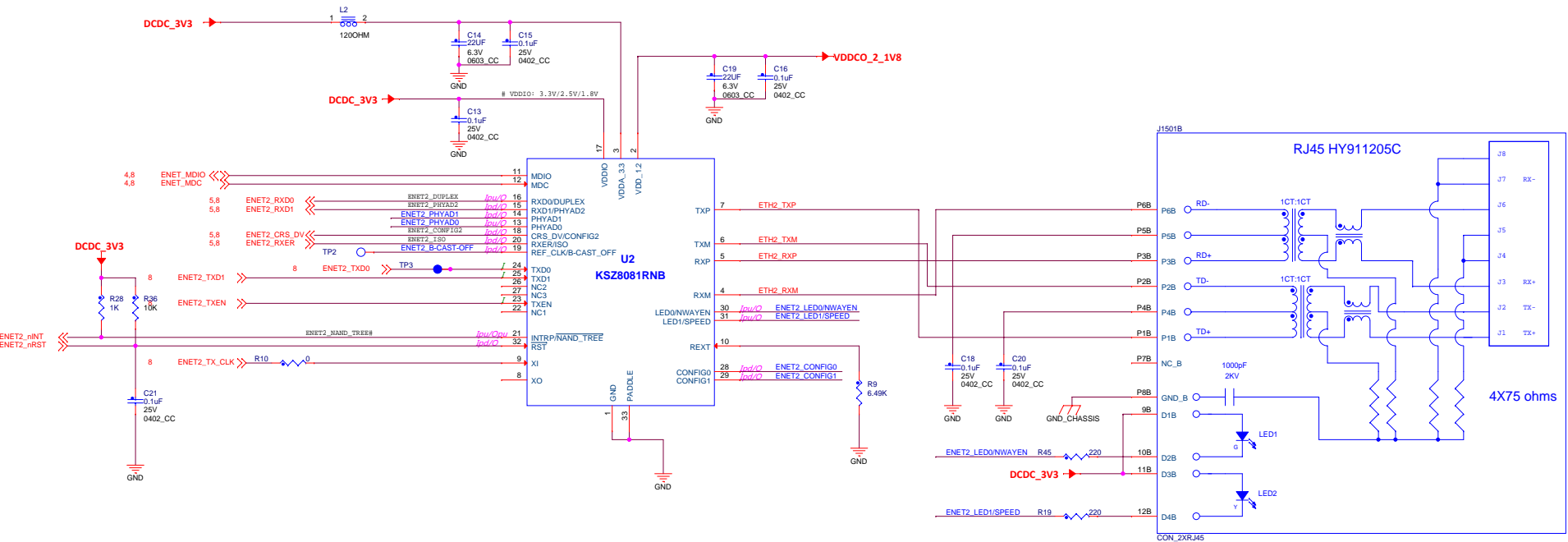
100M ETHERNET RMII PHY x1

PHY PWR

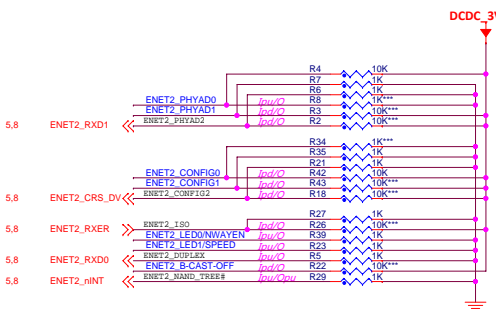
***RMII CFG***

# CFG	Description	# CFG	Description
PHYAD[2:0]	PHY ADDR 00-XXX (00001 DEFAULT)	DUPLEX	DUPLEX mode Pull-up (default) = Half Duplex Pull-down = Full Duplex
CONFIG[2:0]	IF MODE 001 RMII 101 RMII Back-to-Back xxx Reserved-not used	NWAYEN	Nway Auto-Negotiation Pull-up (default) = Enable Pull-down = Disable
ISO	ISOLATE mode Pull-up = Enable Pull-down (default) = Disable	B_CAST_OFF	Broadcast Off - for PHY Address 0 Pull-up = PHY Address 0 set as unique PHY addr Pull-down (default) = PHY Address 0 set as broadcast PHY addr
SPEED	SPEED mode Pull-up (default) = 100Mbps Pull-down = 10Mbps	NAND_TREE#	NAND Tree Mode Pull-up (default) = Disable Pull-down = Enable

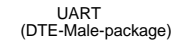
100M ETHERNET RMII PHY x2



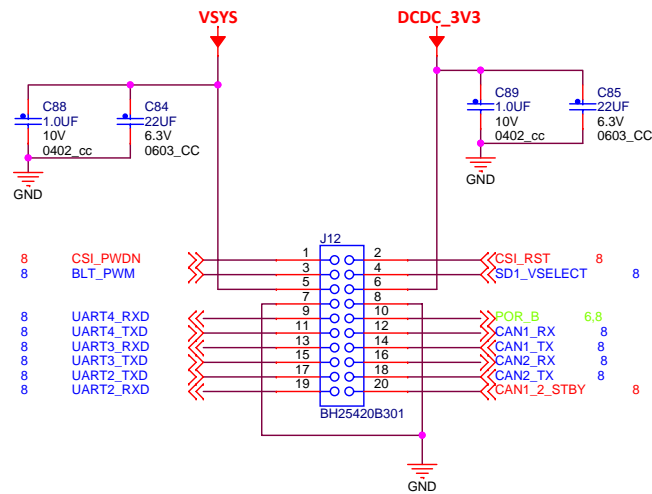
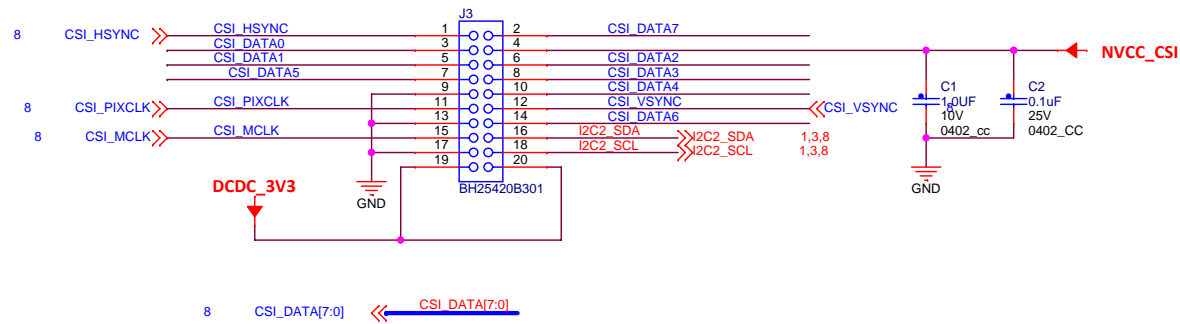
RMII CFG



# CFG	Description	# CFG	Description
PHYAD[2:0]	PHY ADDR 00-XXX (00001 DEFAULT)	DUPLEX	DUPLEX mode Pull-up (defaule) = Half Duplex Pull-down = Full Duplex
CONFIG[2:0]	IF MODE 001 RMII 101 RMII Back-to-Back xxx Reserved-not used	NWAYEN	Nway Auto-Negotiation Pull-up (defaule) = Enable Pull-down = Disable
ISO	ISOLATE mode Pull-up = Enable Pull-down (defaule) = Disable	B_CAST_OFF	Broadcast Off - for PHY Address 0 Pull-up = PHY Address 0 set as unique PHY addr Pull-down (default) = PHY Address 0 set as broadcast PHY addr
SPEED	SPEED mode Pull-up (defaule) = 100Mbps Pull-down = 10Mbps	NAND_TREE#	NAND Tree Mode Pull-up (defaule) = Disable Pull-down = Enable



Camera / SIM



High Speed CAN Transceiver

成都嵌智捷科技有限公司			
Title			
EXTEND CONNECTOR			
Size	Document Number	Rev	
B	DEV-1.MX6ul	V1.0	
Date:	Tuesday, February 16, 2016	Sheet	7 of 8

