

Sunday, 5 November 2023 7:51 PM

Original idea of the project (wearables):

- Proof that ARM Cortex-M chips can run AI
- In the past, ARM Cortex-A:



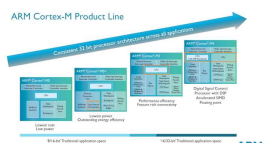
	Application processors	Real-Time processors	Microcontroller processors
<b>Design</b>	High clock frequency, long pipeline, High performance, Mainframe support (IXEN instruction set extension)	High clock frequency, Long to medium pipeline length, Deterministic (low interrupt latency)	Short pipeline, ultra low power, Deterministic (low interrupt latency)
<b>System features</b>	Memory Management Unit (MMU), cache memory, TrustZone® security extension	Memory Protection Unit (MPU), cache memory, tightly Coupled Memory (TCM)	Memory Protection Unit (MPU), Nested Vectored Interrupt Controller (NVIC), Watchdog Interrupt Controller (WIC)
<b>Targeted markets</b>	Mobile computing, smart phones, efficient servers, high end microprocessors	Industrial microcontrollers, automotive systems, hard disk controllers, Baseband modules	Microcontrollers, Discrete embedded systems, servers, MEMS, mixed signal ICs, Internet of Things (IoT)

- Ultra-low power

ARM Cortex-M Performance, Power and Area

	TOLP		GEC		CorrMark® and Gzipbase		
	Ratio (mean / stdev)	Std. var.	Ratio (mean / stdev)	Std. var.	CorrMark (effort)	CorrMark (max optimized)	Gzipbase
Correx-M0	16	0.94	4	0.01	0.84	1.21	2.33
Correx-M0+	9.8	0.035	3	0.009	0.94	1.31	2.42
Correx-M2	22	0.12	7	0.03	1.25	1.89	3.32
Correx-M4	33	0.17	8	0.04	1.25	1.95	3.48

- DSP

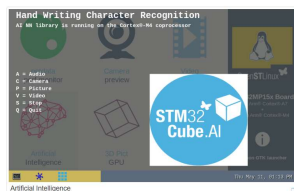


- <https://www.linkedin.com/pulse/arm-cortex-m4-embedded-systems-exploring/>

Demo:

[https://wiki.st.com/stm32mpu/wiki/Getting\\_started/STM32MP1\\_boards/STM32MP157x-DK2/Let%27s\\_start/Use\\_the\\_demo\\_launcher](https://wiki.st.com/stm32mpu/wiki/Getting_started/STM32MP1_boards/STM32MP157x-DK2/Let%27s_start/Use_the_demo_launcher)

## 2.5 Artificial intelligence



This is a handwriting character recognition application:

- 1) It draws a character on the screen. The character is then analyzed by the firmware running on A
- 2) If the character is recognized, it is displayed on the left. Otherwise, a question mark is displayed
- 3) When the recognized character is associated with an application, the application is launched:

- A: launches audio playback
- C: launches camera preview
- P: displays a picture
- V: launches a video playback
- S: stops the launched application
- Q: exits from the Artificial Intelligence demo

## STM MP1 Package + X-Linux-AI (ARM Cortex -M)



### Identification of Arrhythmia:

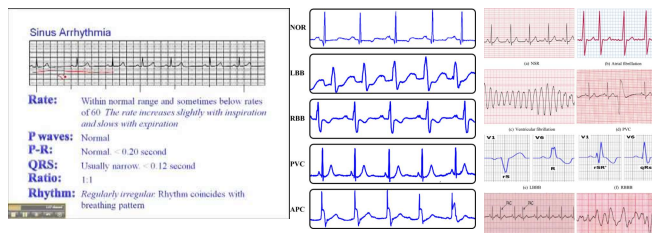
- Left Bundle Branch Block
- Normal
- Premature Atrial Contraction
- Premature Ventricular Contractions
- Right Bundle Branch Block
- Ventricular Fibrillation

Dataset:

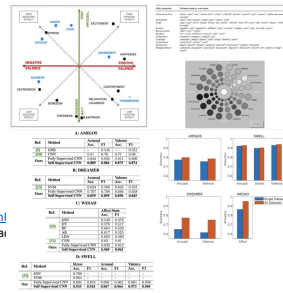
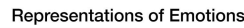
[https://github.com/TriparnoChatterjee/Heart\\_Disease\\_Prediction\\_Using\\_ECG\\_Images](https://github.com/TriparnoChatterjee/Heart_Disease_Prediction_Using_ECG_Images)

<https://www.kaggle.com/datasets/erhmrai/ecg-image-dataset>

<https://www.kaggle.com/datasets/erhmrai/ecg-image-data>  
<https://www.kaggle.com/datasets/mohamedelakrory8/ecg-heart-categorization-dataset-image-version>



- Dataset:  
[https://github.com/TriparnoChatterjee/Heart\\_Disease\\_Prediction\\_Using\\_ECG\\_Images](https://github.com/TriparnoChatterjee/Heart_Disease_Prediction_Using_ECG_Images)  
<https://www.kaggle.com/datasets/erhmrai/ecg-image-data>  
<https://www.kaggle.com/datasets/mohamedeldakrory8/ecg-heart-categorization-dataset-image-version>



- Discrete Emotional Model (DEM) -> happy, sad, angry, fear, etc.
- Affective Dimensional Model (ADM) -> valence, arousal, dominance
- Binary Emotional Model -> negative, positive

1. Amigos (<http://www.crcs.qmul.ac.uk/mmv/datasets/amigos/download.html>)
2. Dreamer (<https://zenodo.org/records/546113#YKLYOWYraHs>)
3. Wesad (<https://archive.ics.uci.edu/dataset/465/wesad+wearable+stress+and+affect+detection>)
4. Case ([https://github.com/nosa999/Emotion-Recognition-from-ECG-Signal/blob/main/Case\\_ECG\\_Emotion](https://github.com/nosa999/Emotion-Recognition-from-ECG-Signal/blob/main/Case_ECG_Emotion))
5. <https://www.kaggle.com/code/daniellesalbon/ecg-signals-emotion-recognition> (Main/Display low accuracy)

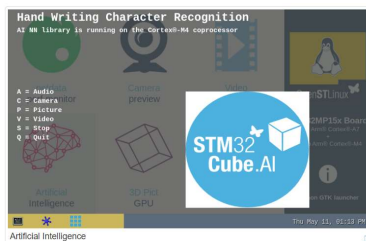
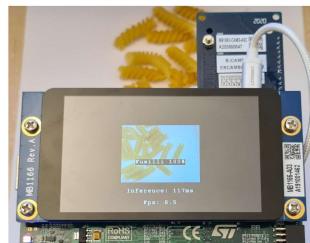
- <https://github.com/joergsimon/SSL-ECG-Paper-Reimplementaton/tree/main>
- Pytorch
- include DEAP (EEG data)

- XNNPACK support for TensorFlow™ Lite and CMMX Runtime, with about 20% to 30% performance gain for quantized networks on a 32-bit device
- TensorFlow™ Lite 2.11.0 with XNNPACK delegate activated
- CMMX Runtime 1.14.0 with TensorFlow™ executors engine activated
- OpenCV 4.7.0
- PyTorch 1.10.0 (prebuilt PyTorch module)
- Good Edge TPU™ accelerator native support
  - Mediatek 2.0.0 (Grouper) aligned with TensorFlow™ Lite 2.11.0
  - Microsoft 2.0.0 (Grouper) aligned with TensorFlow™ Lite 2.11.0
  - PyCoral 2.0.0 (Grouper) aligned with TensorFlow™ Lite 2.11.0

- **All-in-one AI solutions** the entire STM32MPU serie
- **Pre-integrated** into Linux distribution based on ST environment
- **Include AI frameworks** to execute Neural Network models
- **Include AI model benchmark** application tools for MPU
- **Easy application prototyping** using Python language and AI frameworks Python API
- **C++ API** for embedded high-performance applications
- **Optimized open-source solutions** provided with source codes that allow extensive code reuse and time savings

<https://wiki.st.com/stm32mcu/wiki/AI:How to use Teachable Machine to create an image classification application on STM32>

OpenVINO™ Device	Suggested Hardware
CPU  (x86)	Intel® Xeon® with Intel® Advanced Vector Extensions 2 (Intel® AVX2), Intel® Advanced Vector Extensions 512 (Intel® AVX-512), Intel® Advanced Matrix Extensions (Intel® AMX), Intel® Core™ Processors with Intel® AVX, Intel® Atom™ Processors with Intel® SSE4.2, Intel® Xeon Phi™ Processors with Intel® SSE
(ARMv8)	Raspberry Pi 4 Model B, Apple® Mac with Apple Silicon
GPU	Intel® Processor Graphics including Intel® HD Graphics and Intel® Xe Graphics, Intel® Arc™ A-Series Graphics, Intel® Arc™ Desktop GPU F1 Series Bricks, Intel® Arc™ Desktop GPU D1 Series Bricks
GNA (available in Intel® Distribution of OpenVINO™ builds)	Intel® Speech Enabling Technology GNA, Amazon AWS™ Permute Pn-Paid Permute GNA, Intel® Pentium® Silver A0000 Processor, Intel® Pentium® Silver N0000 Processor, Intel® Celeron® J4005 Processor, Intel® Celeron® N4000 Processor, Intel® Celeron® N4950 Processor, Intel® Celeron® Processor N4950, Intel® Core™ i3-1210Y Processor, Intel® Core™ i7- 1265G7 Processor, Intel® Core™ i7-1265G7 Processor, Intel® Core™ i5- 12550A Processor, Intel® Core™ i5-12550T Processor, Intel® Core™ i5- 1250U Processor, Intel® Core™ i5-1260K Processor, Intel® Core™ i5- 12500 Processor, Intel® Core™ i5-12600 Processor, Intel® Core™ i5- 12600K Processor, Intel® Core™ i5-12600K Processor
NPU	



<https://github.com/avnet-iotconnect/iotc-python-examples/tree/main/STM32MP157F-DK2> Demo

<https://docs.iotconnect.io/iotconnect/git-resource/avnet-iotconnect-repository/>

<https://github.com/STMicroelectronics/meta-predmnt>

[https://www.st.com/resource/en/product\\_presentation/dsh-predmnt\\_getting\\_started.pdf](https://www.st.com/resource/en/product_presentation/dsh-predmnt_getting_started.pdf)

Running on:  
AWS