作业（2）：Paper Review

截止时间：11 月 21 日，23:59

提交方式：超算习堂（ https://easyhpc.net/course/133 ）

从以下论文（Paper 1 － 4）中任意选择 2 篇阅读，并按照要求格式英文撰写 Paper Review。
详见模板 **review-form.txt**（其中 A–F 部分为必填，G 部分可选）。

Review 撰写参考：

[1]. O. Mutlu, Guidelines on Paper Reviews, https://course.ece.cmu.edu/~ece740/f13/lib/exe/fetch.php?media=onur–740–fall13–lecture0–3–how–to–do–the–paper–reviews.pdf

[2]. S. Krishnamurthi, How to Write Technical Paper Reviews, https://cs.brown.edu/~sk/Memos/Paper–Reviews/

Paper 1 [Cache, DRAM]:

V. Seshadri, A. Bhowmick *et al.*, The Dirty–Block Index, The 41st International Symposium on Computer Architecture (ISCA), 2014.

链接：http://users.ece.cmu.edu/~omutlu/pub/dirty–block–index_isca14.pdf

Paper 2 [PIM, GPU]:

A. Nag, R. Balasubramonian, OrderLight: Lightweight Memory–Ordering Primitive for Efficient Fine–Grained PIM Computations, The 54th Annual IEEE/ACM International Symposium on Microarchitecture (MICRO), 2021.

链接：https://dl.acm.org/doi/pdf/10.1145/3466752.3480103

Paper 3 [HBM, Cache]:

M. Qureshi and G. Loh, Fundamental Latency Tradeoffs in Architecting DRAM Caches, The 45th Annual IEEE/ACM International Symposium on Microarchitecture (MICRO), 2012.

链接：http://memlab.ece.gatech.edu/papers/MICRO_2012_1.pdf

Paper 4 [VM, TLB]:

H. Alam, T. Zheng *et al.*, Do–It–Yourself Virtual Memory Translation, The 44th International Symposium on Computer Architecture (ISCA), 2017.

链接：https://dl.acm.org/doi/pdf/10.1145/3079856.3080209