

12 11 10 9 8 7 6 5 4 3 2 1

								REVISIONS			
				ITEM		SHEET	DESCRIPTION		DATE	APPROVED	

G											G
F											F

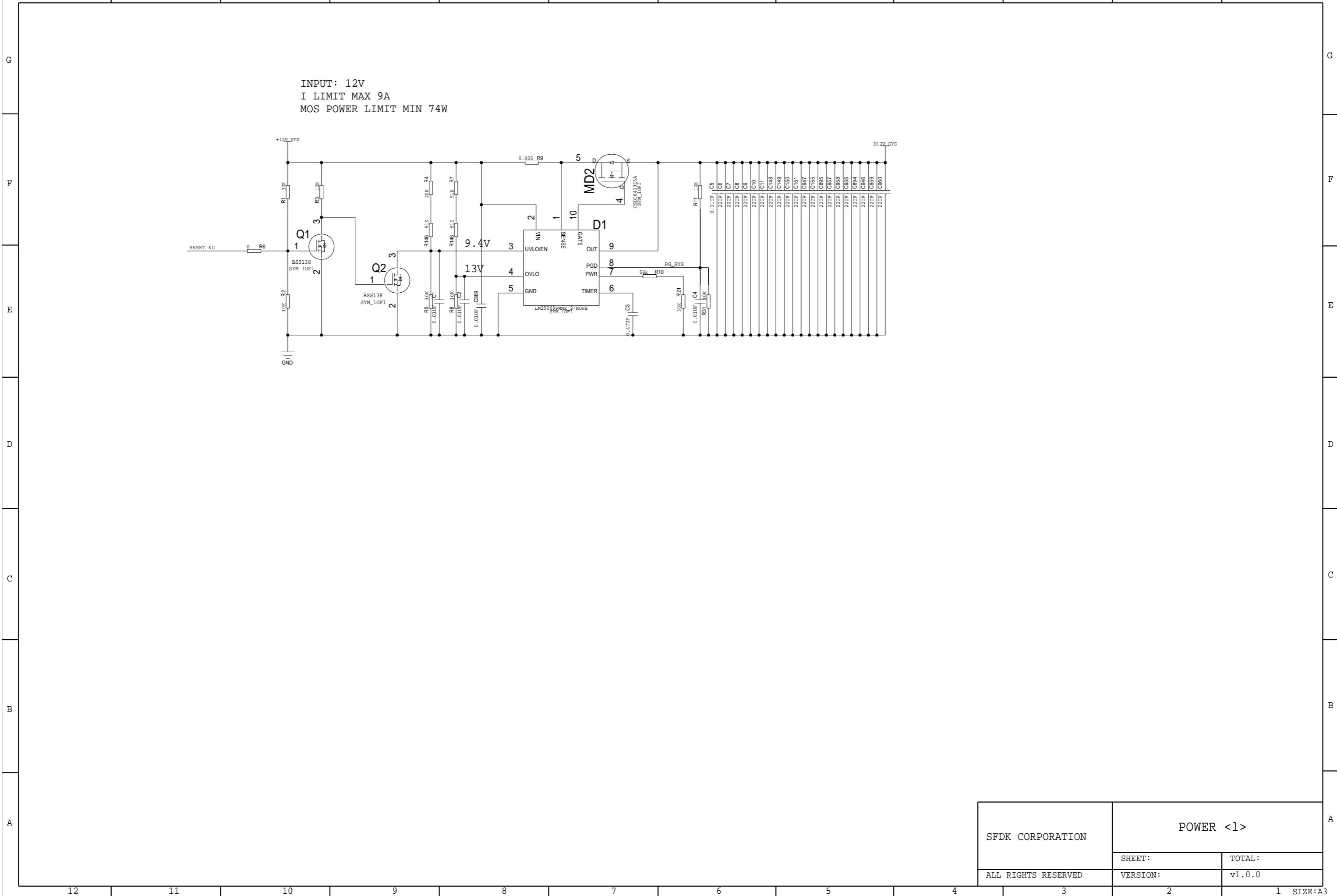
E											E
D											D

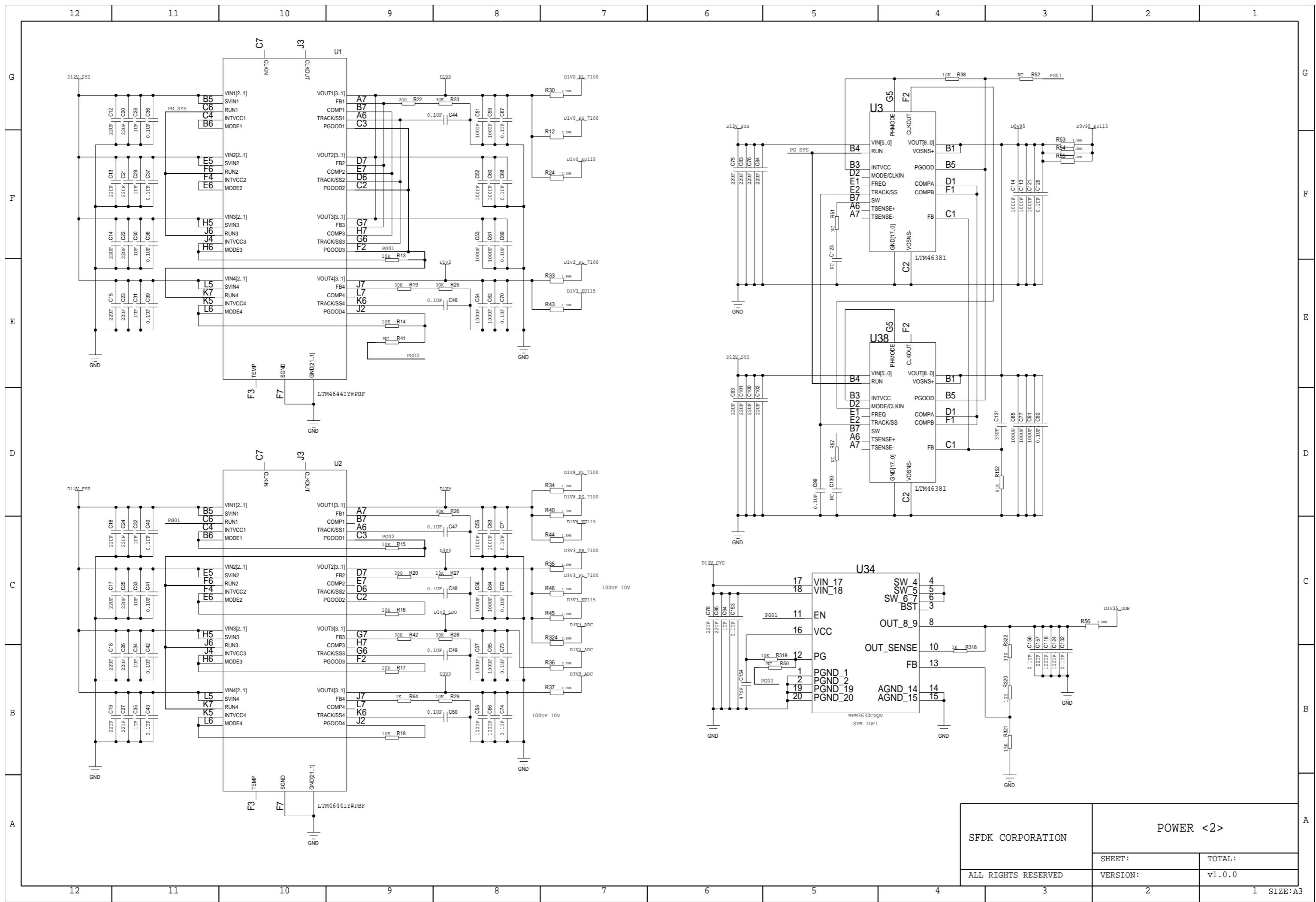
C											C
B											B

A											A

SFDK CORPORATION	SIGN		DATE	BOARD_NAME
	DRAWER:	NAME1	20XX-XX-XX	
	DESIGNER:	NAME2	20XX-XX-XX	
	CHECKED:	NAME3	20XX-XX-XX	
ALL RIGHTS RESERVED	APPROVED:	NAME4	20XX-XX-XX	SHEET: TOTAL:
				VERSION: v1.0.0

12 11 10 9 8 7 6 5 4 3 2 1 SIZE:A3



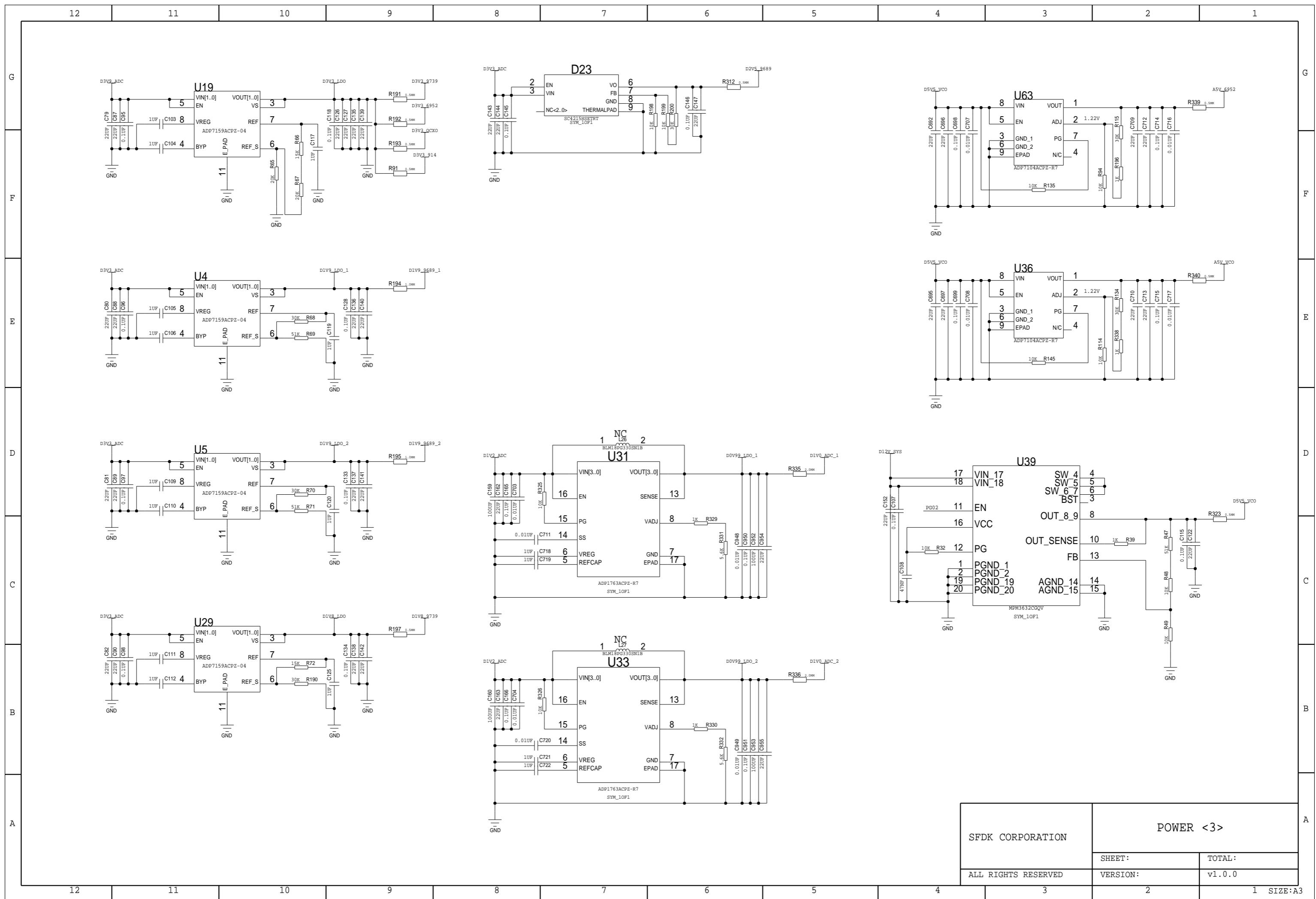


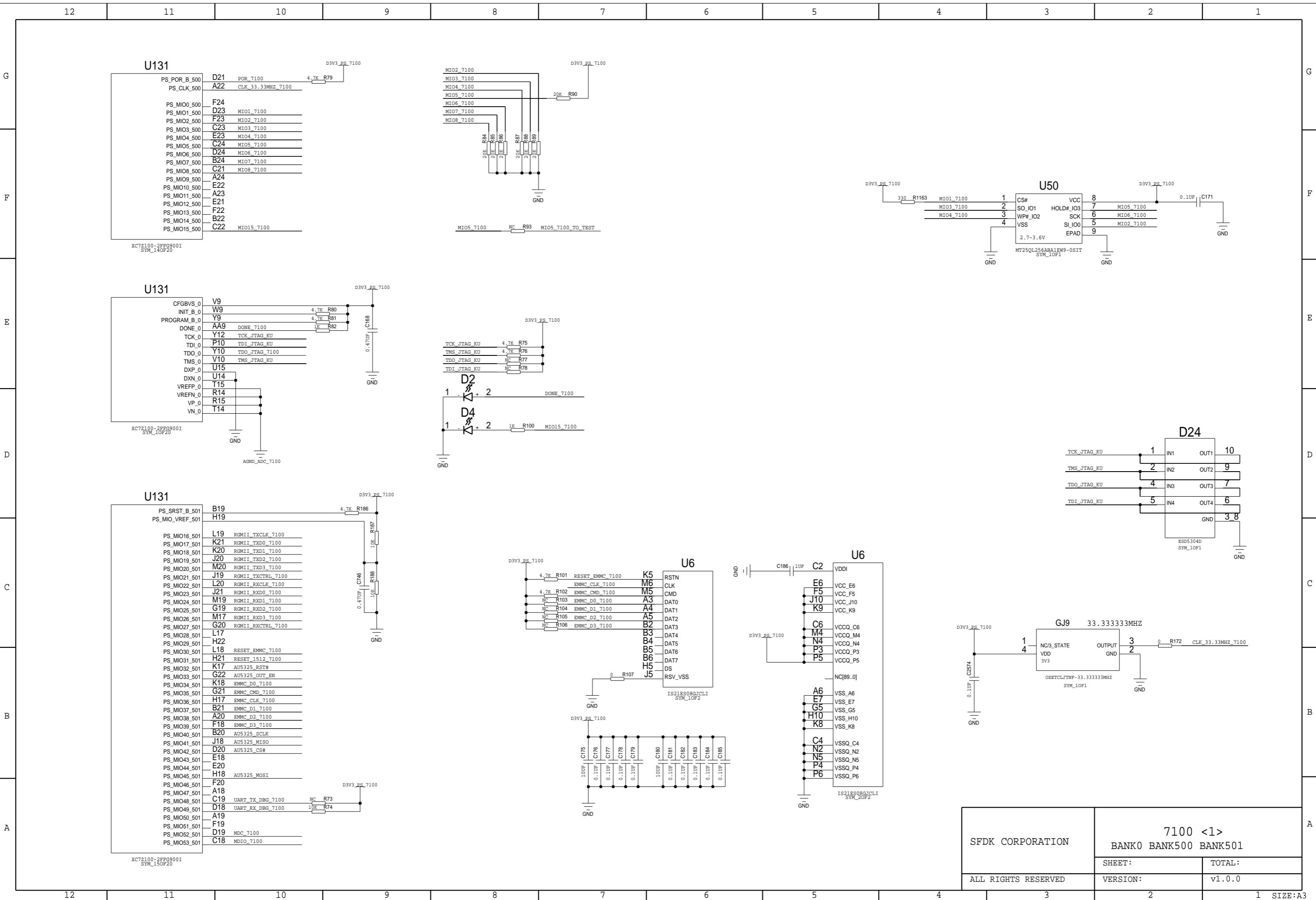
SFDK CORPORATION

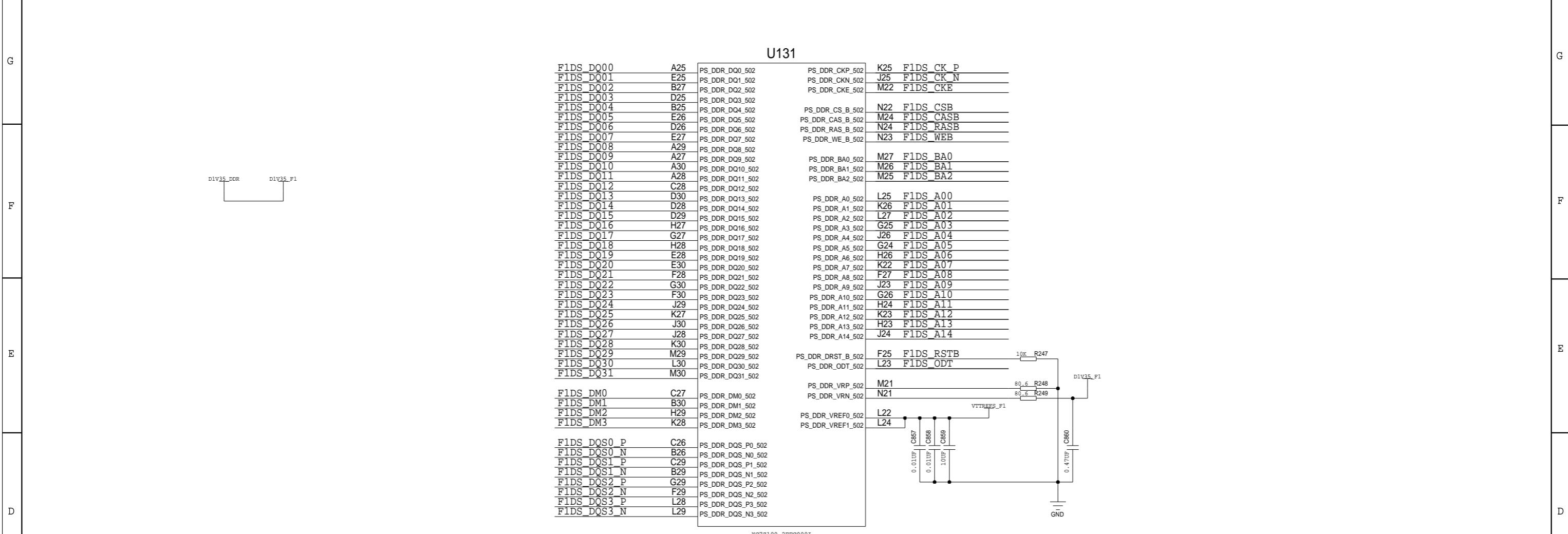
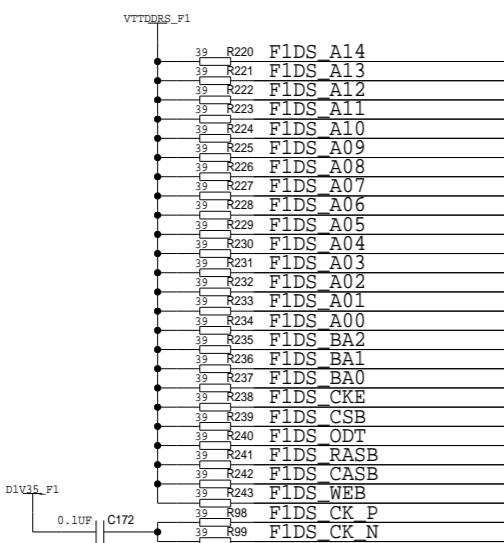
POWER <2>

SHEET:  
ALL RIGHTS RESERVED

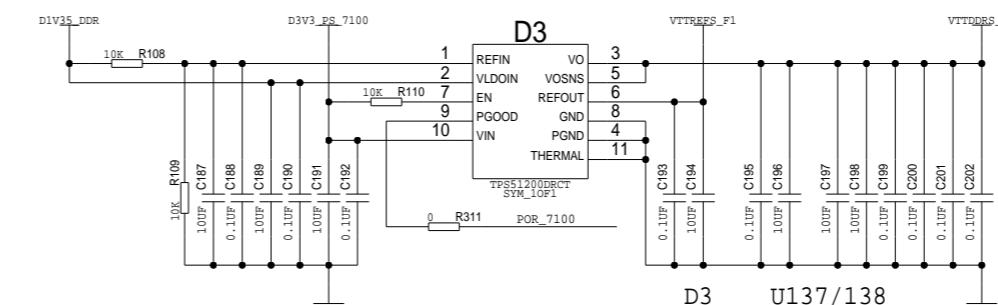
TOTAL:  
VERSION: v1.0.0





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STM\_160F20

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SFDK CORPORATION	7100 <2> PS DDR	
SHEET:	TOTAL:	
ALL RIGHTS RESERVED	VERSION:	v1.0.0

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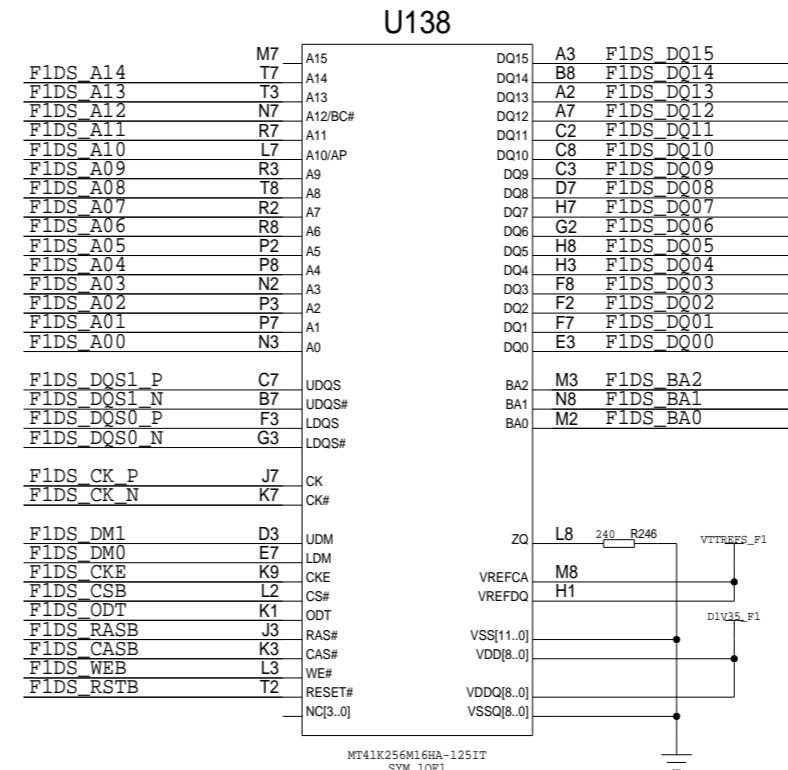
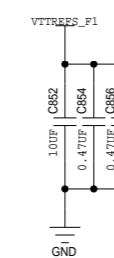
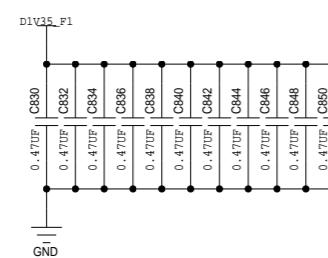
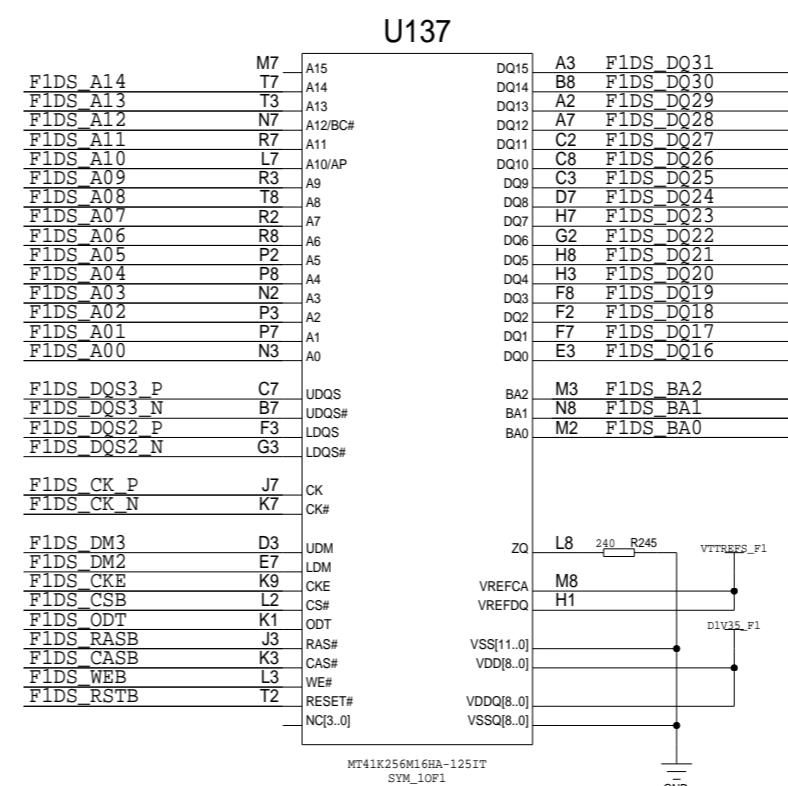
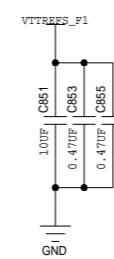
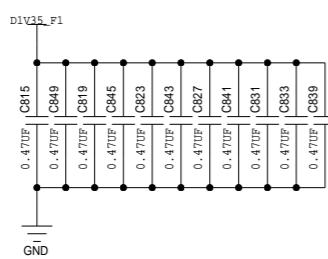
C

B

B

A

A

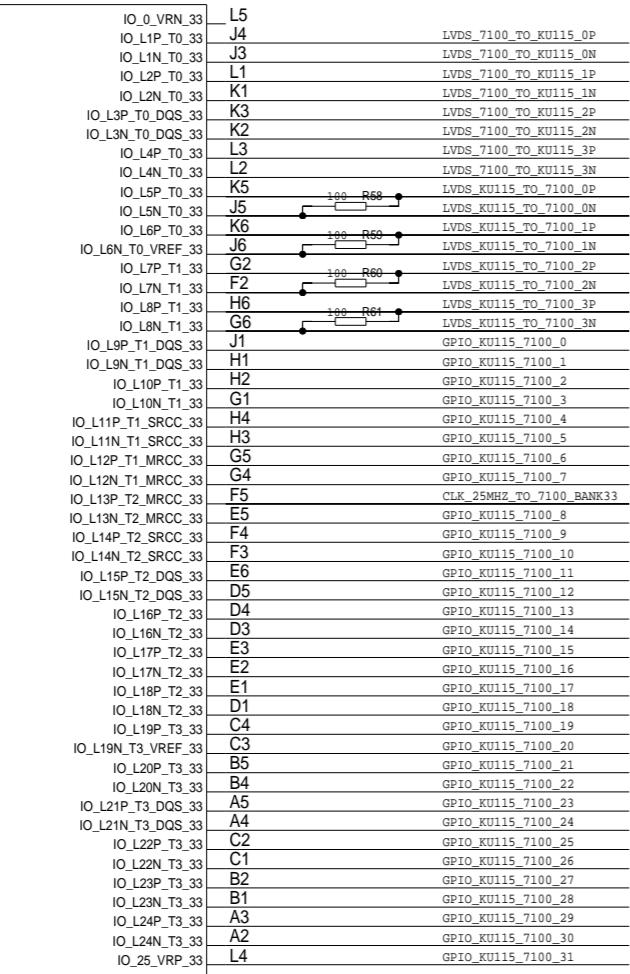


SFDK CORPORATION	DDR3L	
SHEET:	TOTAL:	
ALL RIGHTS RESERVED		VERSION: v1.0.0

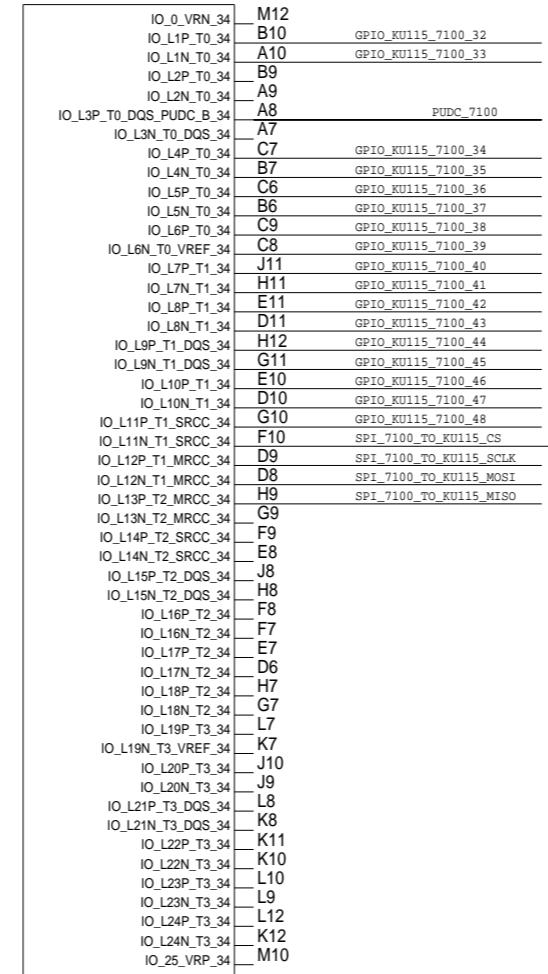
G

G

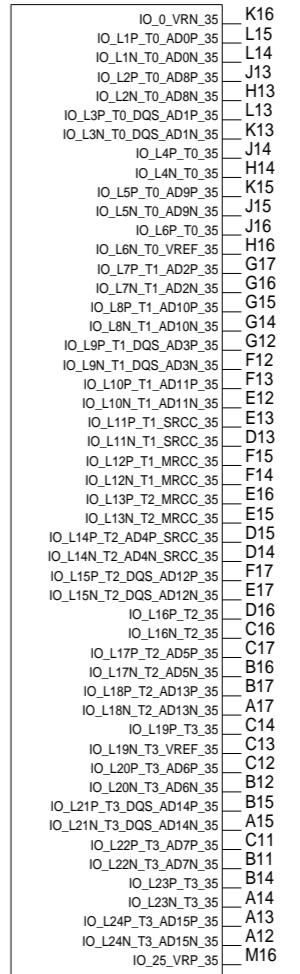
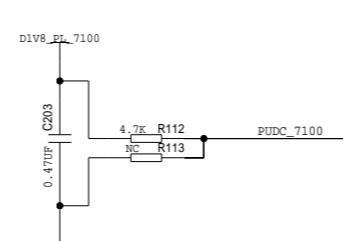
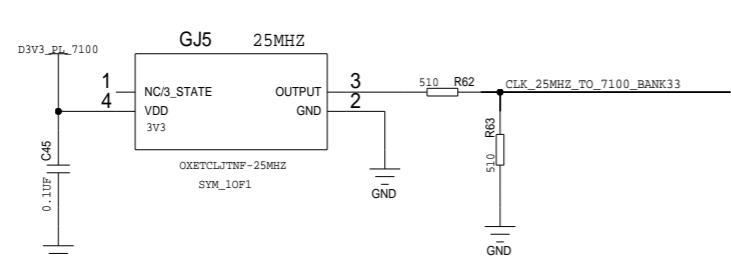
U131

XC7Z100-2FFG900I  
SIR\_70F20

U131

XC7Z100-2FFG900I  
SIR\_80F20

U131

XC7Z100-2FFG900I  
SIR\_80F20

SFDK CORPORATION	7100 <3>
BANK 33 34 35	
SHEET:	TOTAL:
ALL RIGHTS RESERVED	v1.0.0

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C

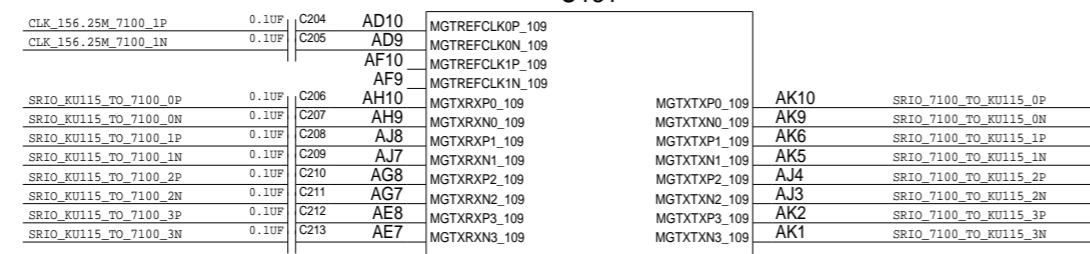
B

B

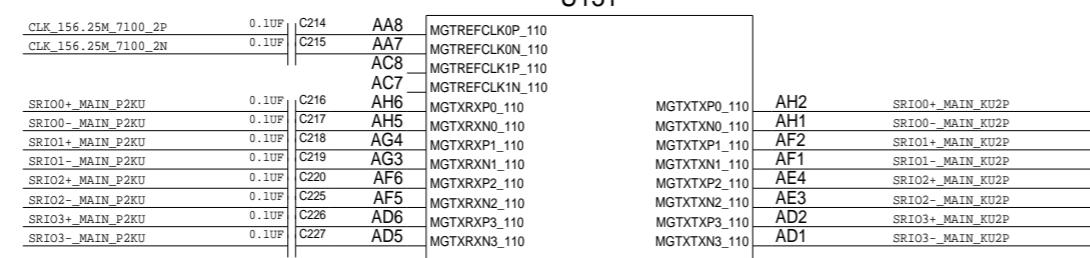
A

A

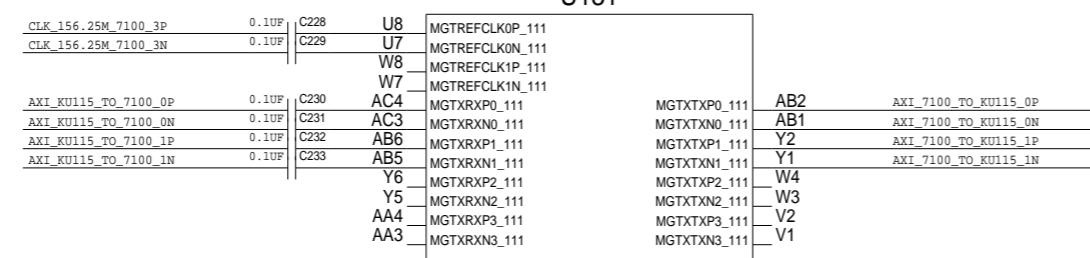
U131



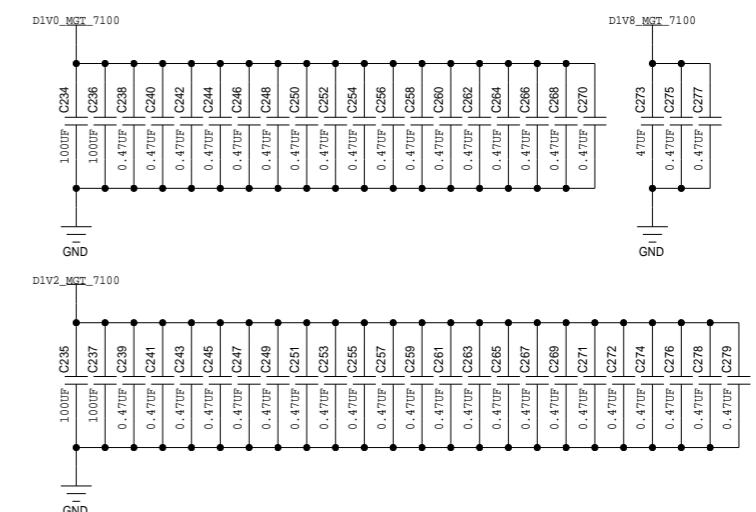
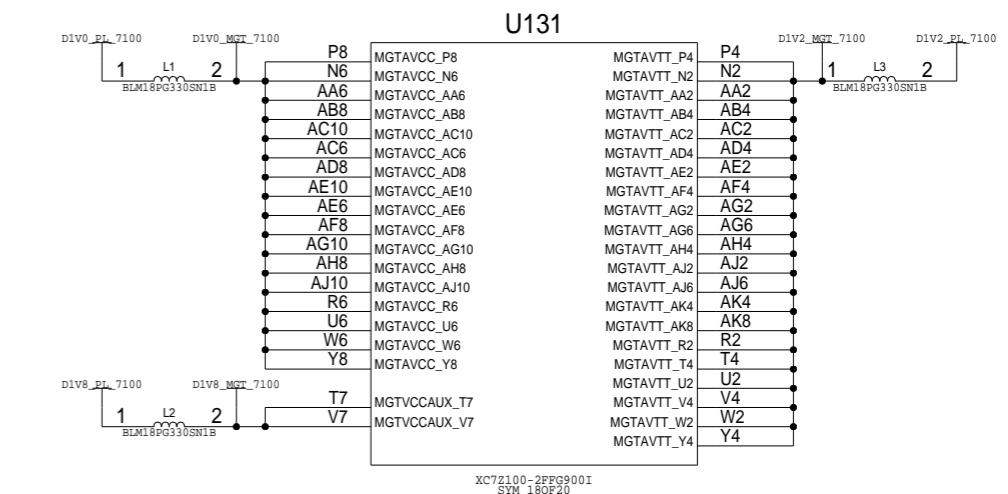
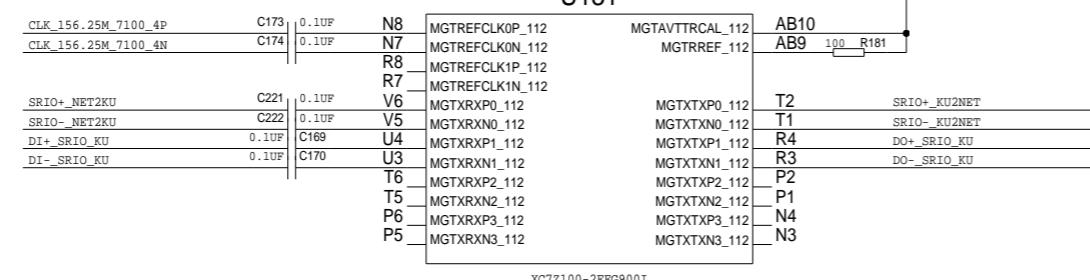
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SFDK CORPORATION

7100 <4>  
MGTSHEET: TOTAL:  
ALL RIGHTS RESERVED VERSION: v1.0.0

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U131

IO_0_10	AA13
IO_L1P_T0_10	AK13
IO_L1N_T0_10	AK12
IO_L2P_T0_10	AH18
IO_L2N_T0_10	AJ18
IO_L3P_T0_DQS_10	AJ14
IO_L3N_T0_DQS_10	AJ13
IO_L4P_T0_10	AJ16
IO_L4N_T0_10	AK16
IO_L5P_T0_10	AJ15
IO_L5N_T0_10	AK15
IO_L6P_T0_10	AH17
IO_L6N_T0_VREF_10	AH16
IO_L7P_T1_10	AE12
IO_L7N_T1_10	AF12
IO_L8P_T1_10	AH14
IO_L8N_T1_10	AH13
IO_L9P_T1_DQS_10	AD14
IO_L9N_T1_DQS_10	AD13
IO_L10P_T1_10	AG12
IO_L10N_T1_10	AH12
IO_L11P_T1_SRCC_10	AE13
IO_L11N_T1_SRCC_10	AF13
IO_L12P_T1_MRCC_10	AF14
IO_L12N_T1_MRCC_10	AG14
IO_L13P_T2_MRCC_10	AG17
IO_L13N_T2_MRCC_10	AG16
IO_L14P_T2_SRCC_10	AF15
IO_L14N_T2_SRCC_10	AG15
IO_L15P_T2_DQS_10	AF18
IO_L15N_T2_DQS_10	AF17
IO_L16P_T2_10	AE16
IO_L16N_T2_10	AE15
IO_L17P_T2_10	AE18
IO_L17N_T2_10	AE17
IO_L18P_T2_10	AD16
IO_L18N_T2_10	AD15
IO_L19P_T3_10	AC14
IO_L19N_T3_VREF_10	AC13
IO_L20P_T3_10	AA15
IO_L20N_T3_10	AA14
IO_L21P_T3_DQS_10	AB12
IO_L21N_T3_DQS_10	AC12
IO_L22P_T3_10	AB15
IO_L22N_T3_10	AB14
IO_L23P_T3_10	AC17
IO_L23N_T3_10	AC16
IO_L24P_T3_10	AB17
IO_L24N_T3_10	AB16
IO_25_10	AA17

XC7Z100-2FFG900I  
SYM\_30F20

U131

IO_0_11	W23
IO_L1P_T0_11	PWRX0_RF_KU
IO_L1N_T0_11	PWRX1_RF_KU
IO_L2P_T0_11	PWRX2_RF_KU
IO_L2N_T0_11	PWRX3_RF_KU
IO_L2N_T0_11	PWRX4_RF_KU
IO_L3P_T0_DQS_11	AJ21
IO_L3N_T0_DQS_11	AK25
IO_L4P_T0_11	AK22
IO_L4N_T0_11	AK23
IO_L5P_T0_11	AK24
IO_L5N_T0_11	AK25
IO_L6P_T0_11	AH22
IO_L6N_T0_VREF_11	AG22
IO_L7P_T1_11	AC24
IO_L7N_T1_11	AD24
IO_L8P_T1_11	AG24
IO_L8N_T1_11	AG25
IO_L9P_T1_DQS_11	AF23
IO_L9N_T1_DQS_11	AF24
IO_L10P_T1_11	AD21
IO_L10N_T1_11	AE21
IO_L11P_T1_SRCC_11	AD23
IO_L11N_T1_SRCC_11	AE23
IO_L12P_T1_MRCC_11	AE22
IO_L12N_T1_MRCC_11	AF22
IO_L13P_T2_MRCC_11	AG21
IO_L13N_T2_MRCC_11	AH21
IO_L14P_T2_SRCC_11	AF20
IO_L14N_T2_SRCC_11	AG20
IO_L15P_T2_DQS_11	UART_RX_GPS_TO_7100
IO_L15P_T2_DQS_11	AJ20
IO_L15N_T2_DQS_11	AK20
IO_L15P_T2_DQS_11	PWM1_FAN
IO_L15N_T2_DQS_11	AK17
IO_L16P_T2_11	AK18
IO_L16N_T2_11	AH19
IO_L17P_T2_11	AJ19
IO_L17N_T2_11	AF19
IO_L18P_T2_11	AG19
IO_L18N_T2_11	AB21
IO_L19P_T3_11	AB22
IO_L19N_T3_VREF_11	AB22
IO_L20P_T3_11	W21
IO_L20N_T3_11	Y21
IO_L21P_T3_DQS_11	Y22
IO_L21N_T3_DQS_11	Y23
IO_L22P_T3_11	AA24
IO_L22N_T3_11	AB24
IO_L23P_T3_11	AA22
IO_L23N_T3_11	AA23
IO_L24P_T3_11	AC22
IO_L24N_T3_11	AC23
IO_25_11	AC21

XC7Z100-2FFG900I  
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U131

IO_0_12	Y25
IO_L1P_T0_12	Y30
IO_L1N_T0_12	AA30
IO_L2P_T0_12	AB29
IO_L2N_T0_12	AB30
IO_L3P_T0_DQS_12	Y26
IO_L3N_T0_DQS_12	Y27
IO_L4P_T0_12	Y28
IO_L4N_T0_12	AA29
IO_L5P_T0_12	AA27
IO_L5N_T0_12	AA28
IO_L6P_T0_12	AB25
IO_L6N_T0_VREF_12	AB26
IO_L7P_T1_12	AC26
IO_L7N_T1_12	AD26
IO_L8P_T1_12	AD30
IO_L8N_T1_12	AE30
IO_L9P_T1_DQS_12	AC29
IO_L9N_T1_DQS_12	AD29
IO_L10P_T1_12	AD25
IO_L10N_T1_12	AE26
IO_L11P_T1_SRCC_12	AB27
IO_L11N_T1_SRCC_12	AC27
IO_L12P_T1_MRCC_12	AC28
IO_L12N_T1_MRCC_12	AD28
IO_L13P_T2_MRCC_12	AE28
IO_L13N_T2_MRCC_12	AF28
IO_L14P_T2_SRCC_12	AE27
IO_L14N_T2_SRCC_12	AF27
IO_L15P_T2_DQS_12	AF29
IO_L15N_T2_DQS_12	AG29
IO_L16P_T2_12	AF30
IO_L16N_T2_12	AG30
IO_L17P_T2_12	AG26
IO_L17N_T2_12	AG27
IO_L18P_T2_12	AE25
IO_L18N_T2_12	AF25
IO_L19P_T3_12	AH28
IO_L19N_T3_VREF_12	AH29
IO_L20P_T3_12	AJ30
IO_L20N_T3_12	AK30
IO_L21P_T3_DQS_12	AJ28
IO_L21N_T3_DQS_12	AJ29
IO_L22P_T3_12	AK27
IO_L22N_T3_12	AK28
IO_L23P_T3_12	AH26
IO_L23N_T3_12	AH27
IO_L24P_T3_12	AJ26
IO_L24N_T3_12	AK26
IO_25_12	AA25

XC7Z100-2FFG900I  
SYM\_50F20

U131

IO_0_13	Y20
IO_L1P_T0_13	AA20
IO_L1N_T0_13	AC18
IO_L11P_T1_SRCC_9	AC19
IO_L11N_T1_SRCC_9	AD18
IO_L12P_T1_MRCC_9	AD19
IO_L12N_T1_MRCC_9	AA18
IO_L13P_T2_MRCC_9	AA19
IO_L13N_T2_MRCC_9	AB19
IO_L14P_T2_SRCC_9	AB20
IO_L14N_T2_SRCC_9	AD20
IO_L19P_T3_9	AE20
IO_19N_T3_VREF_9	AE20
IO_25_9	Y20
IO_L1P_T0_13	R30
IO_L1N_T0_13	T30
IO_L2P_T0_13	U30
IO_L2N_T0_13	N28
IO_L3P_T0_DQS_13	P28
IO_L3N_T0_DQS_13	N29
IO_L4P_T0_13	P29
IO_L4N_T0_13	T29
IO_L5P_T0_13	U29
IO_L5N_T0_13	R28
IO_L6P_T0_13	T28
IO_L6N_T0_VREF_13	V28
IO_L7P_T1_13	W28
IO_L7N_T1_13	V29
IO_L8P_T1_13	W29
IO_L8N_T1_13	W30
IO_L9P_T1_DQS_13	W28
IO_L9N_T1_DQS_13	W25
IO_L10P_T1_13	W26
IO_L10N_T1_13	U25
IO_L11P_T1_SRCC_13	W26
IO_L11N_T1_SRCC_13	U27
IO_L12P_T1_MRCC_13	U27
IO_L12N_T1_MRCC_13	R25
IO_L13P_T2_MRCC_13	W26
IO_L13N_T2_MRCC_13	R26
IO_L14P_T2_SRCC_13	R27
IO_L14N_T2_SRCC_13	T27
IO_L15P_T2_DQS_13	N26
IO_L15N_T2_DQS_13	N27
IO_L16P_T2_13	P25
IO_L16N_T2_13	P26
IO_L17P_T2_13	T24
IO_L17N_T2_13	T25
IO_L18P_T2_13	P23
IO_L18N_T2_13	P24
IO_L19P_T3_13	P21
IO_19N_T3_VREF_13	R21
IO_20P_T3_13	T22
IO_L20N_T3_13	T23
IO_L21P_T3_DQS_13	R22
IO_L21N_T3_DQS_13	R23
IO_L22P_T3_13	U22
IO_L22N_T3_13	V22
IO_L23P_T3_13	U24
IO_L23N_T3_13	V24
IO_L24P_T3_13	V23
IO_L24N_T3_13	W24
IO_25_13	V21

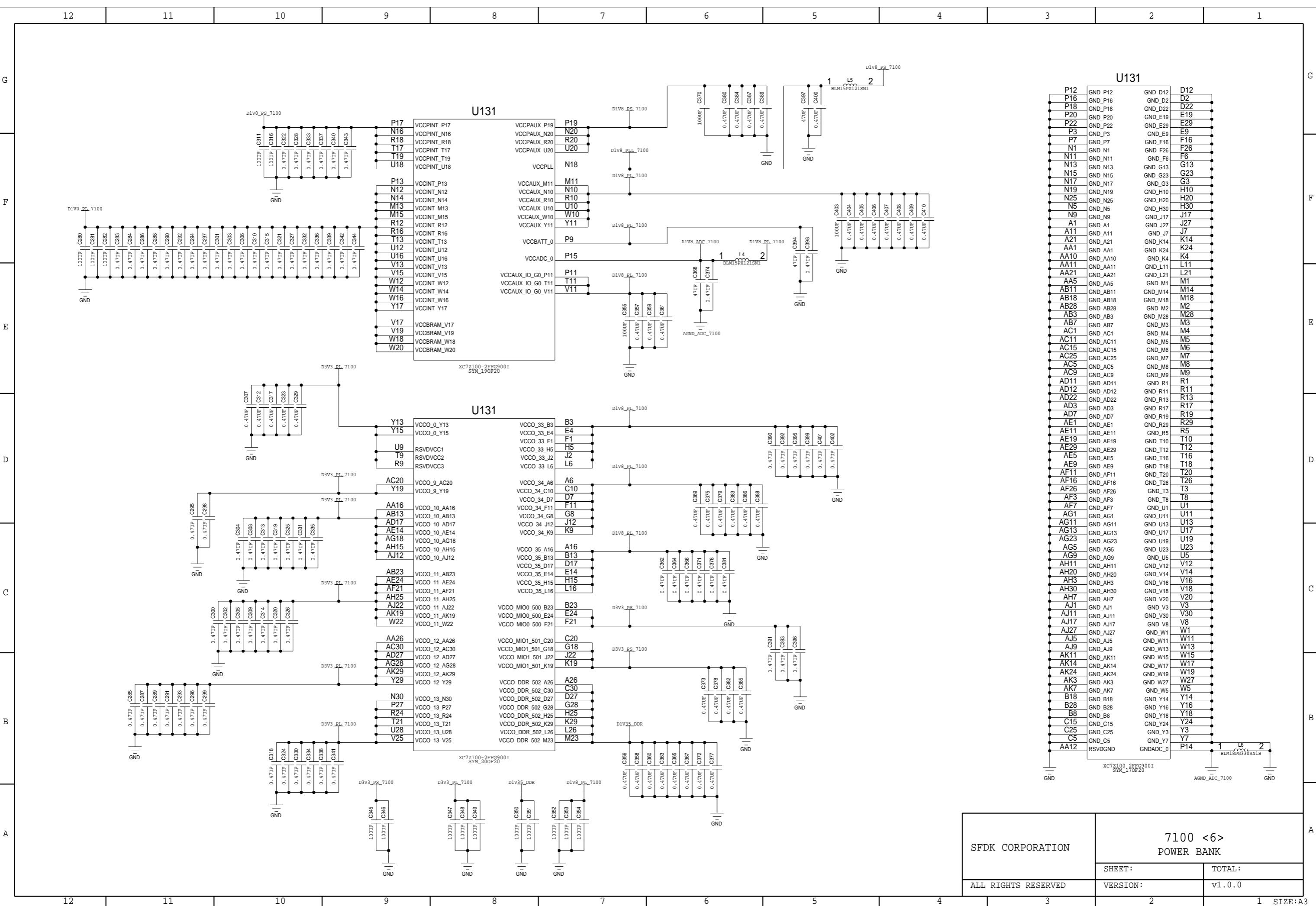
XC7Z100-2FFG900I  
SYM\_60F20

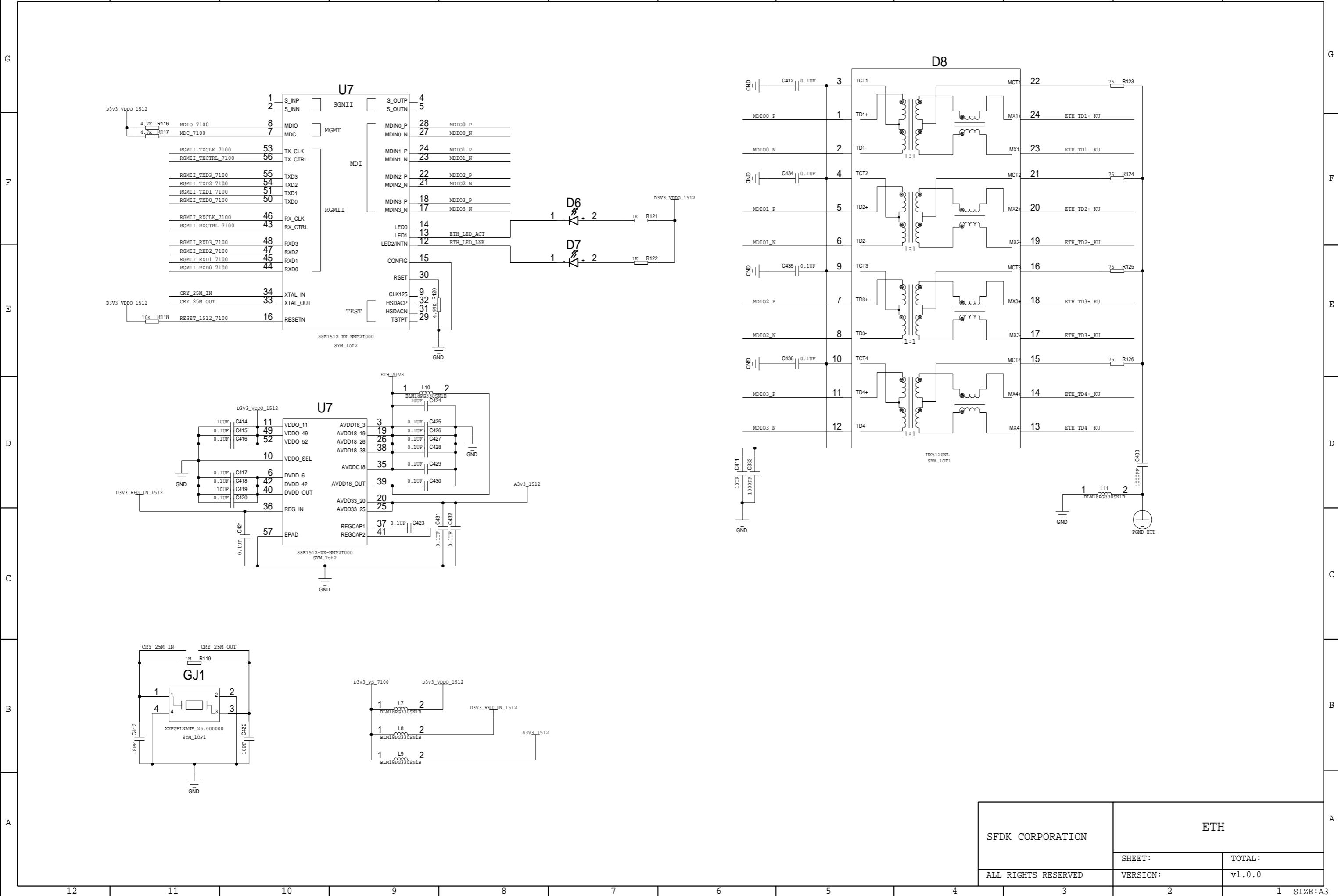
SFDK CORPORATION  
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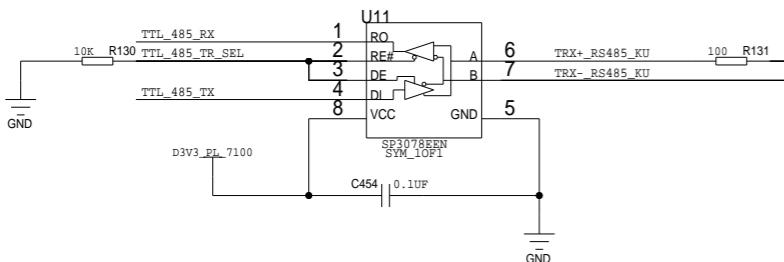
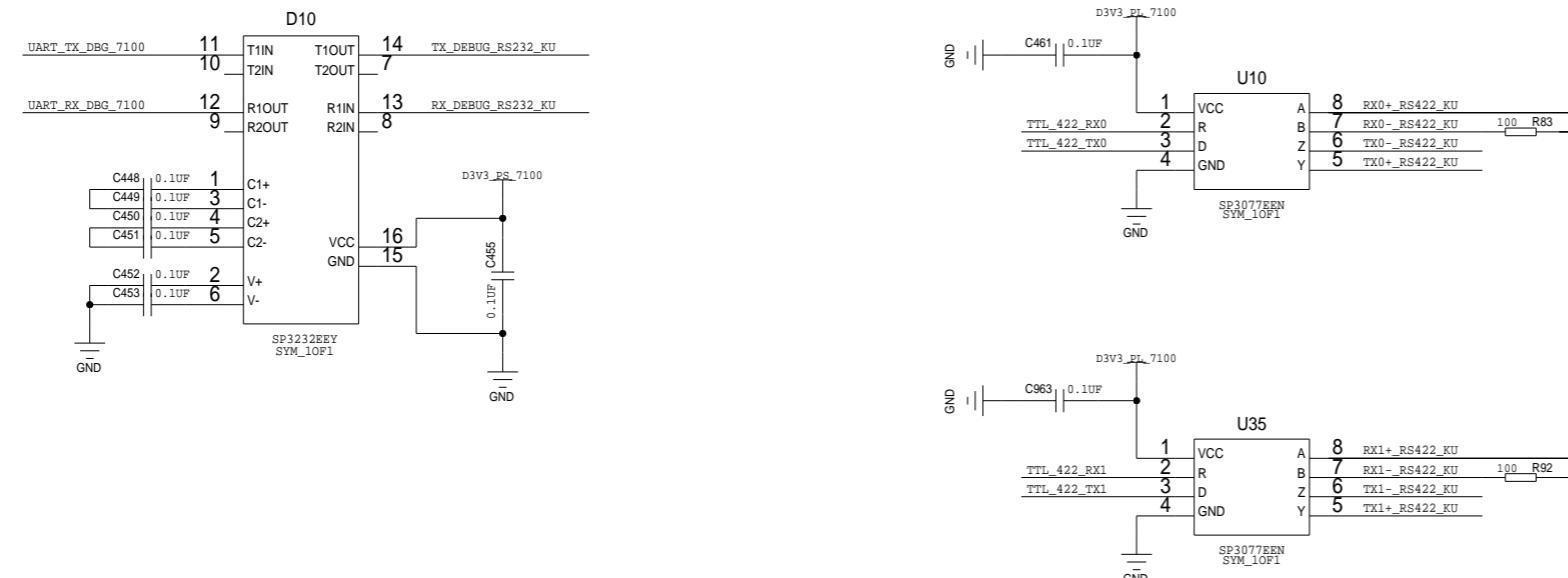
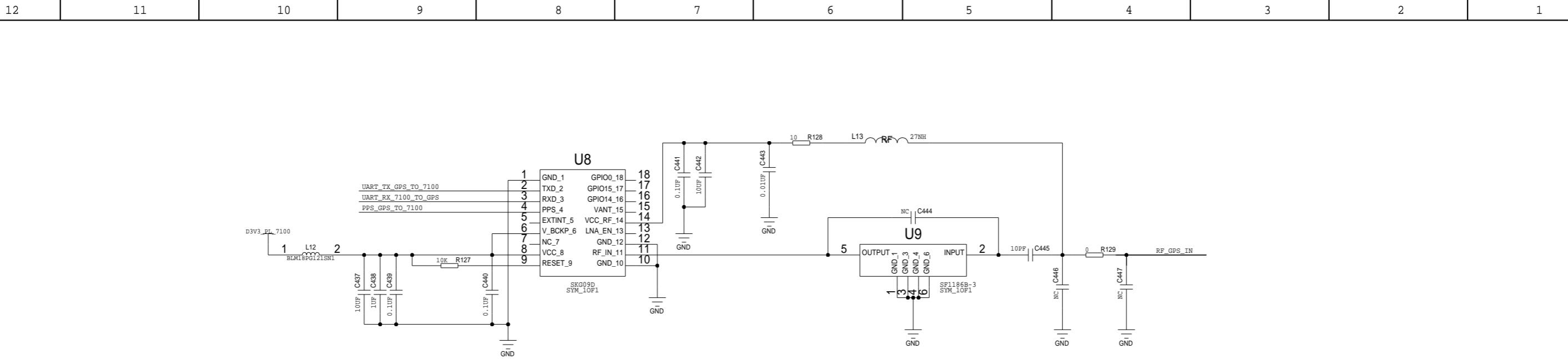
7100 <5>  
BANK 9 10 11 12 13  
SHEET: TOTAL:  
VERSION: v1.0.0

A

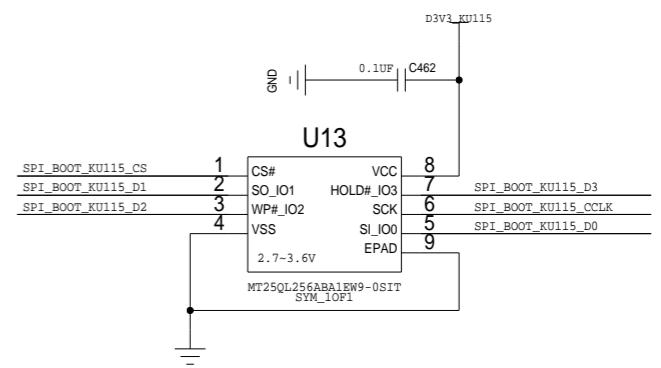
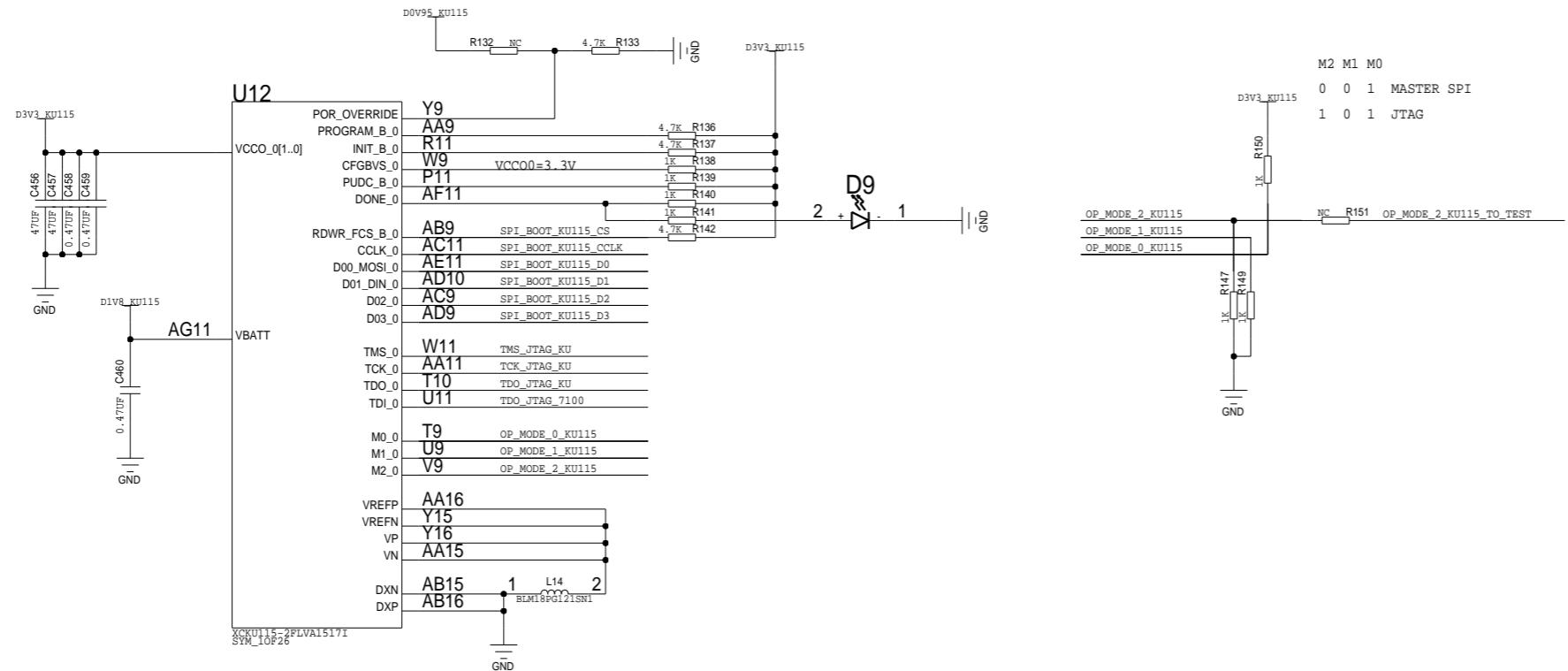
A







SFDK CORPORATION	GPS RS232 RS485	
SHEET:	TOTAL:	
ALL RIGHTS RESERVED	VERSION:	v1.0.0



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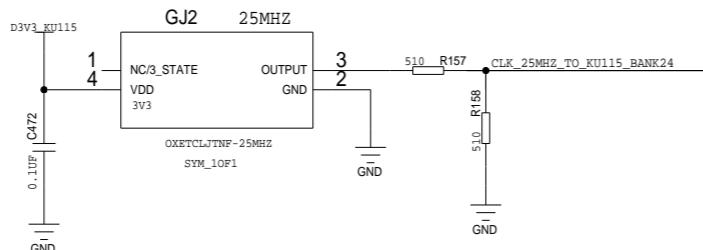
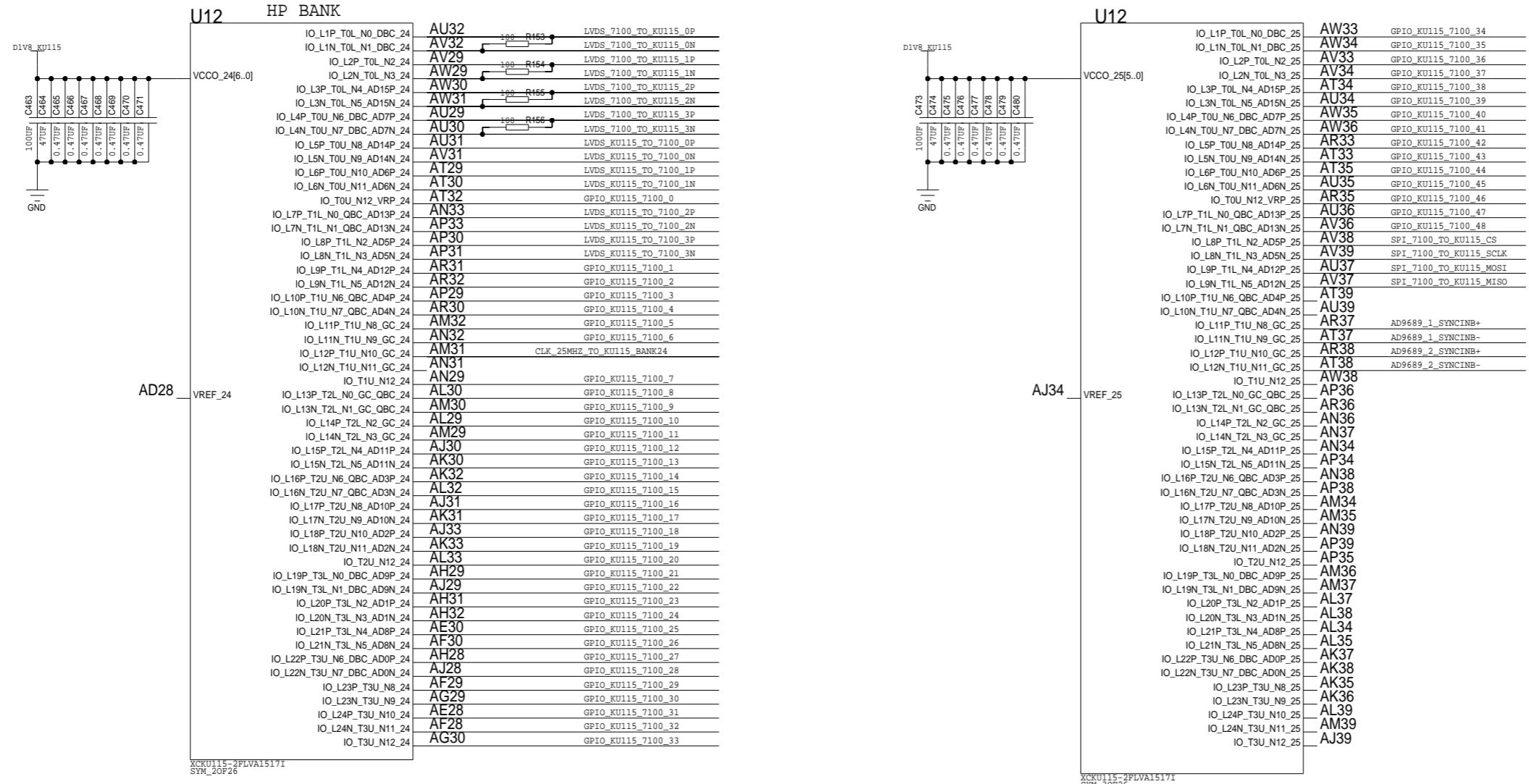
C

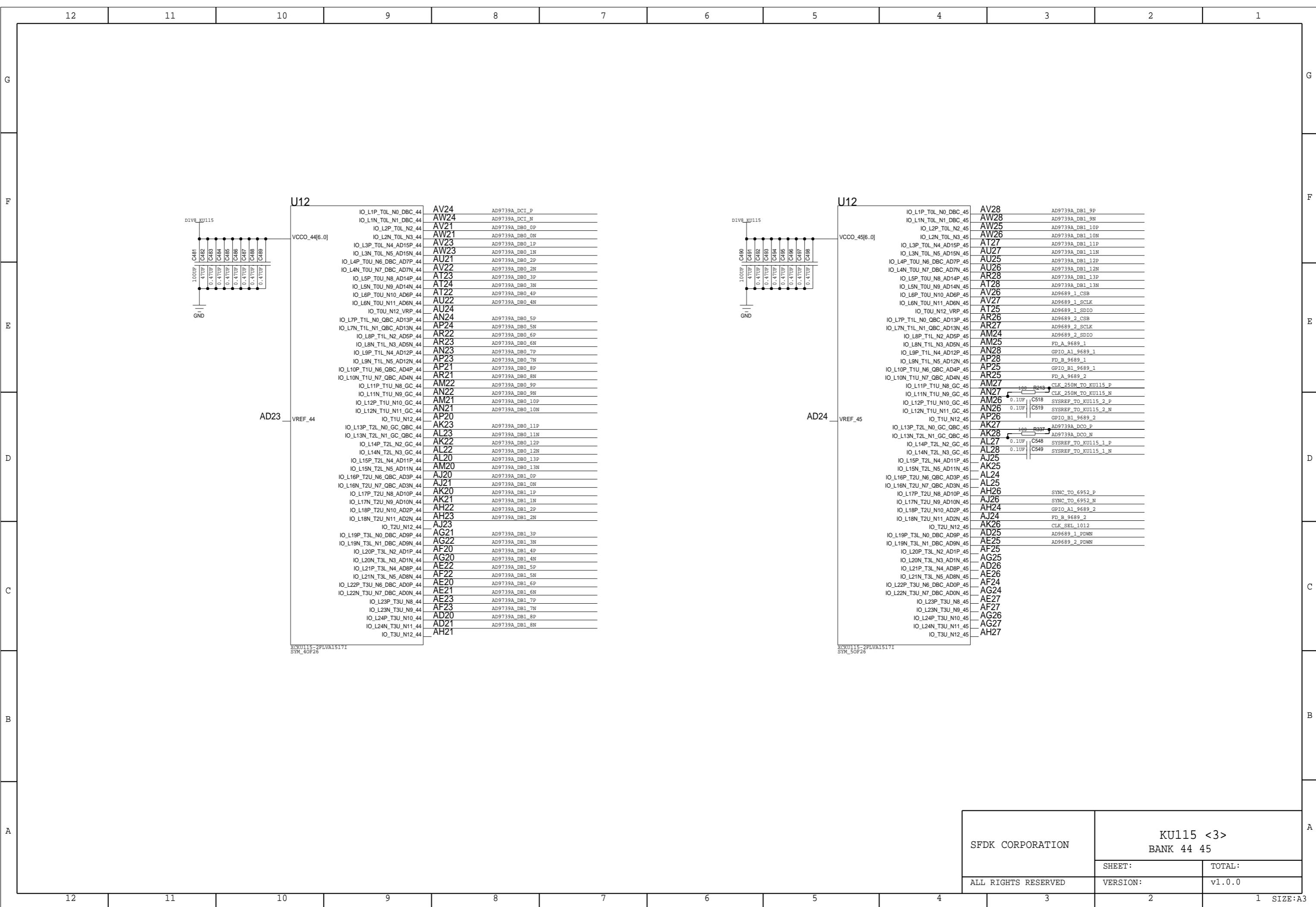
B

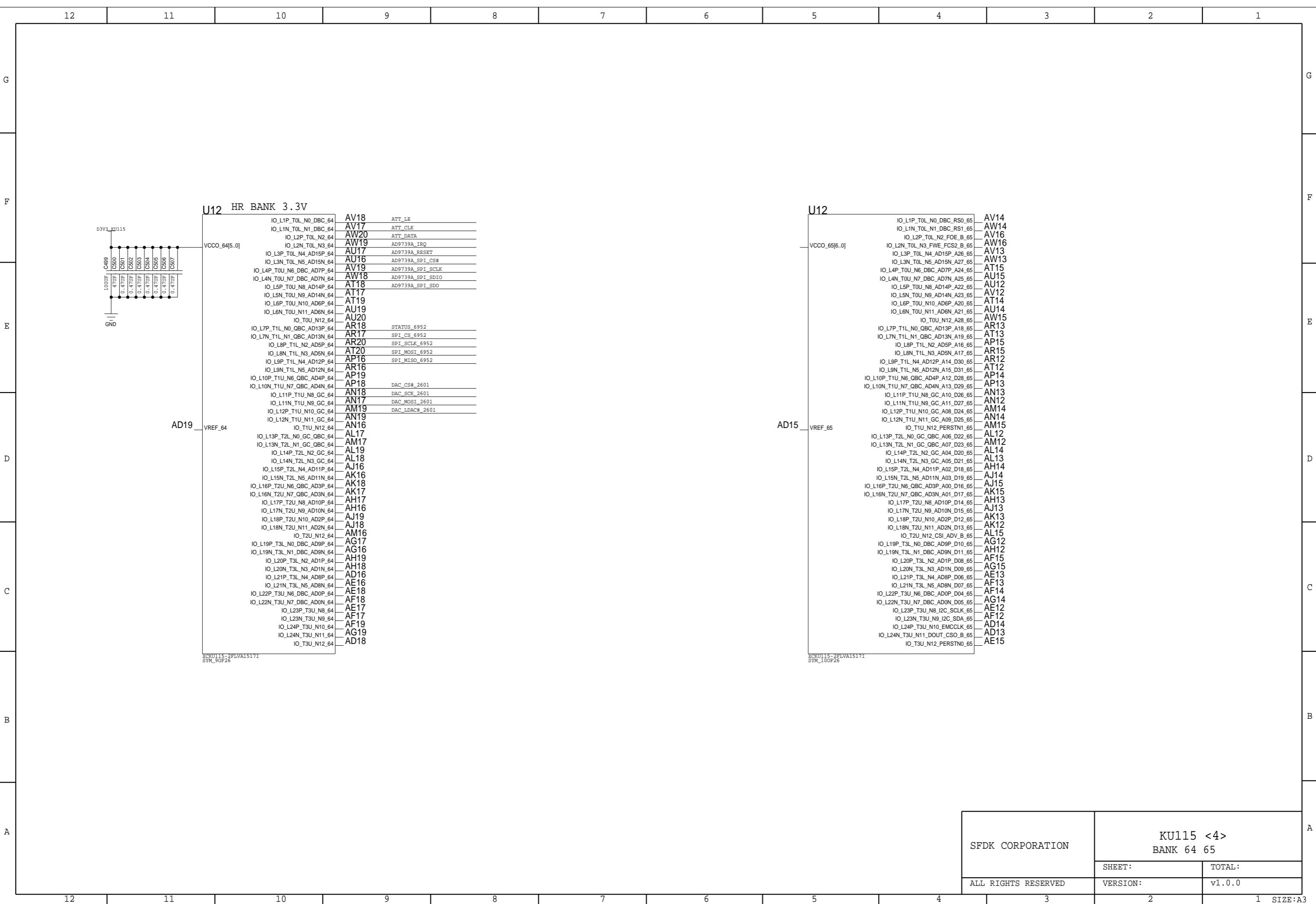
B

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A

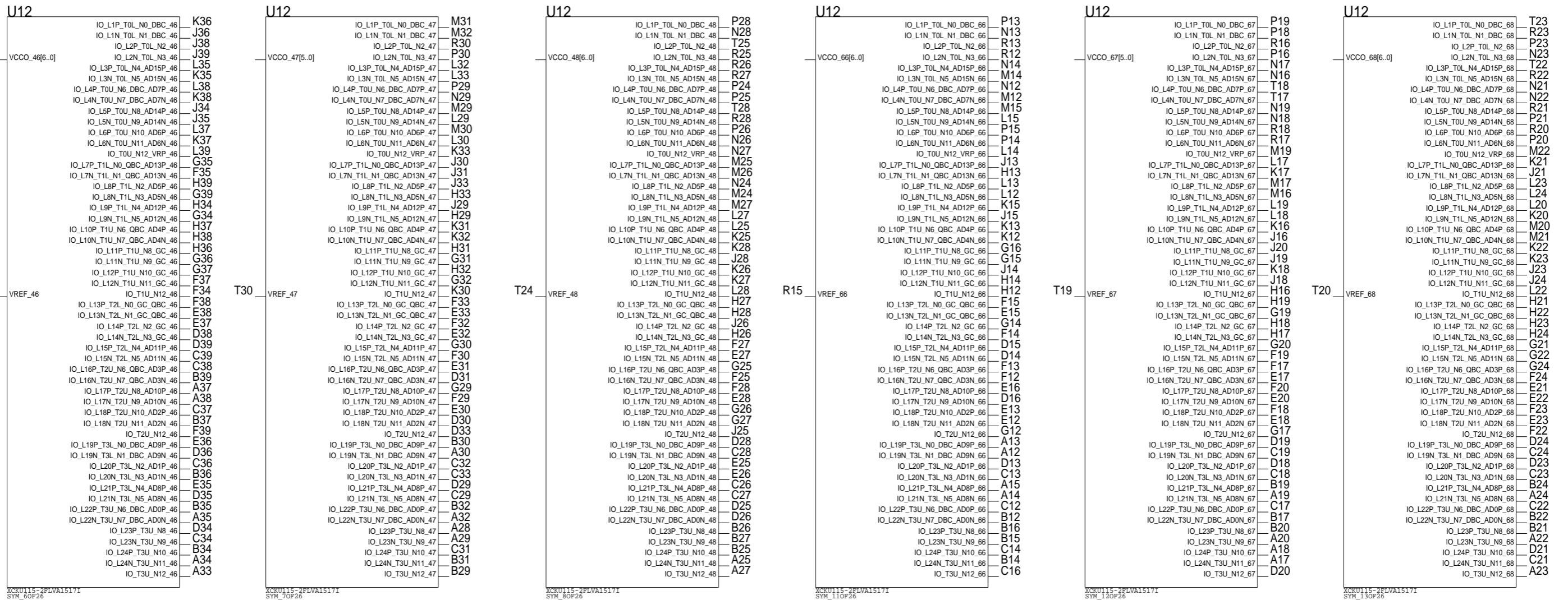






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XCKU115-2FLVA1517  
SYM\_60F26

XCKU115-2FLVA151  
SYM\_70F26

1517I

XCKU115-2FLVA1  
SYM\_110F26

XCKU115  
SYM\_120

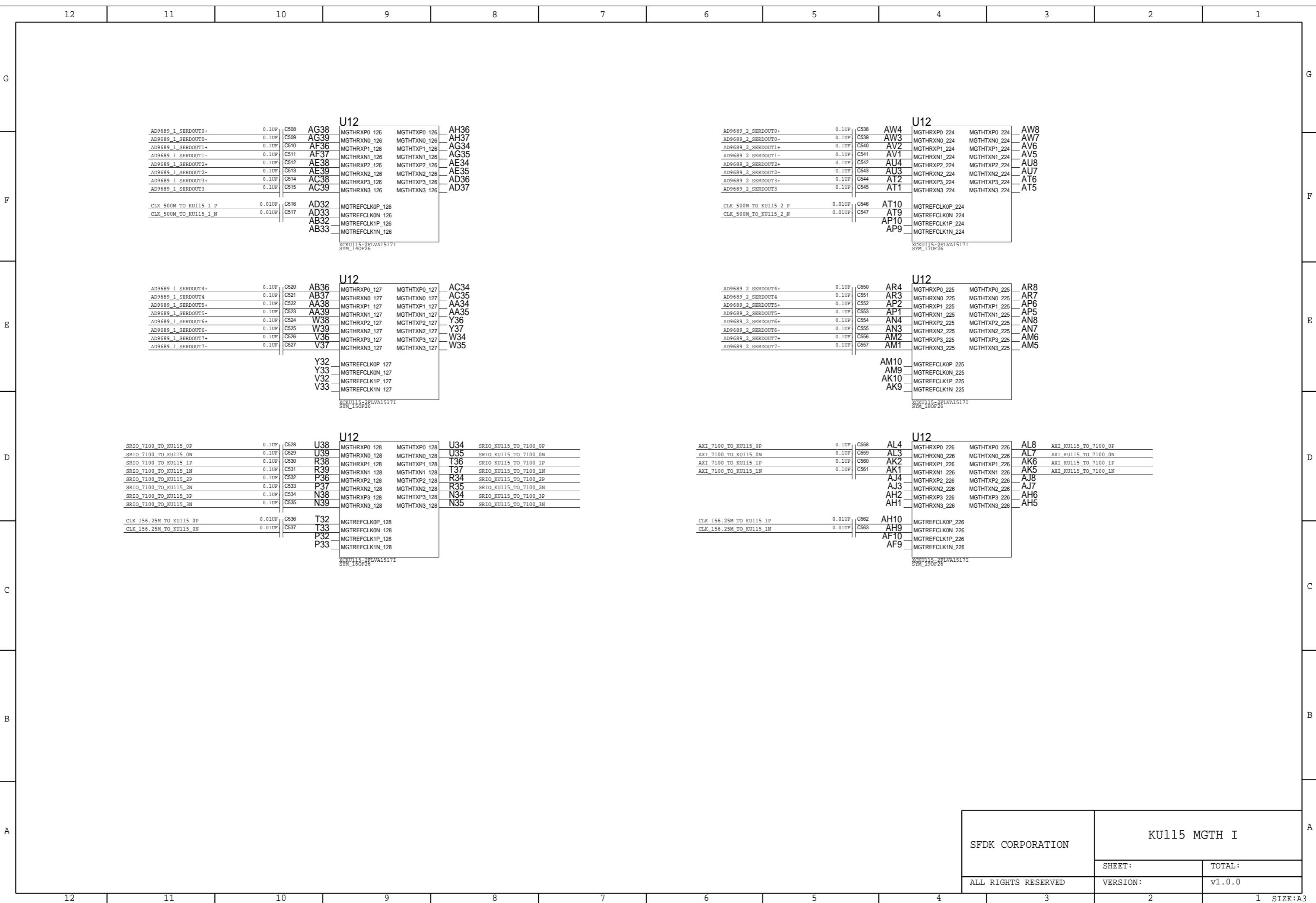
J115-2FLVA1517I  
\_130F26

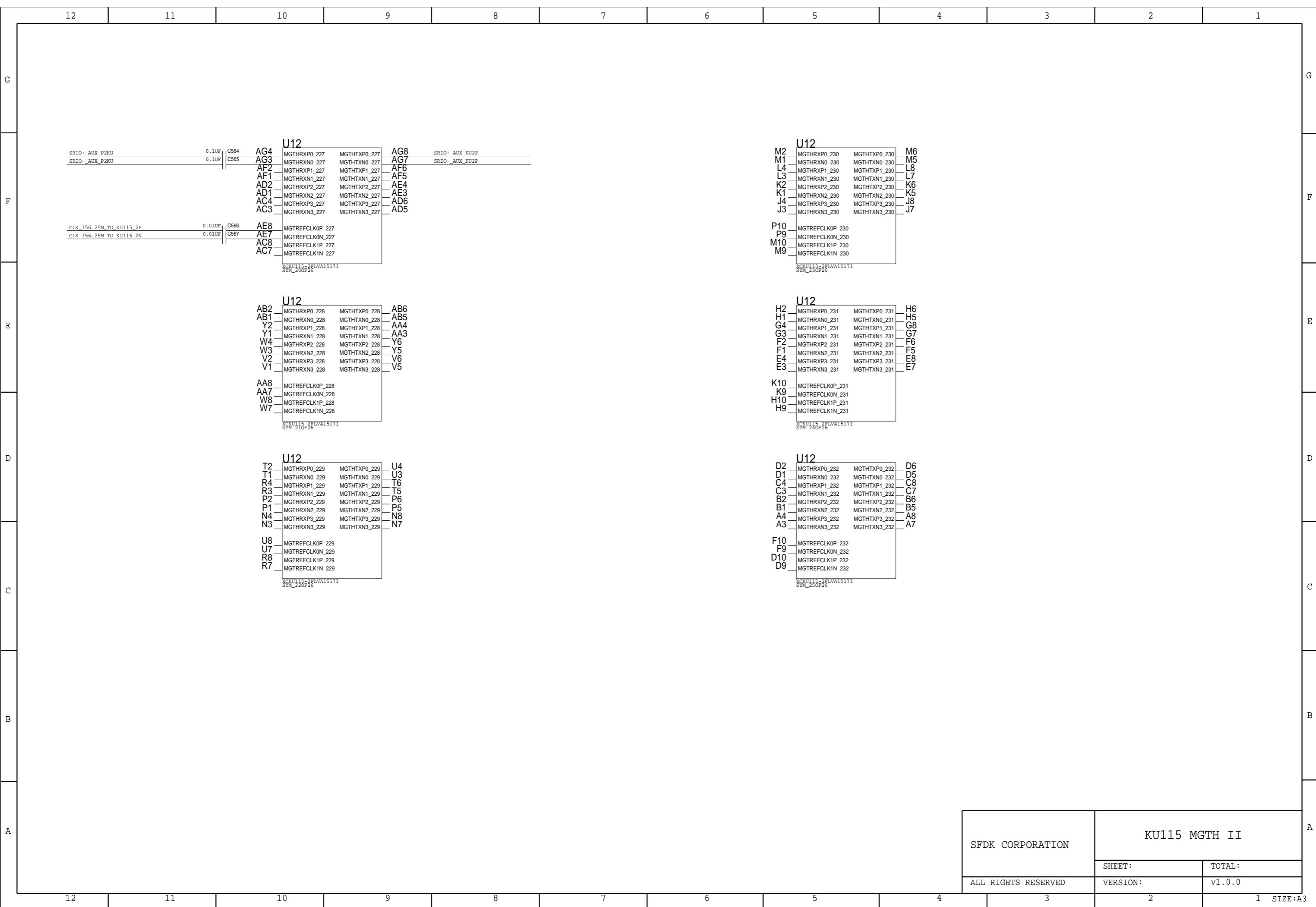
1

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SFDK CORPORATION	KU115 <5>	
	SHEET:	TOTAL:
ALL RIGHTS RESERVED	VERSION:	v1.0.0





SFDK CORPORATION	KU115 MGTH II	
SHEET:	TOTAL:	
ALL RIGHTS RESERVED		VERSION: v1.0.0

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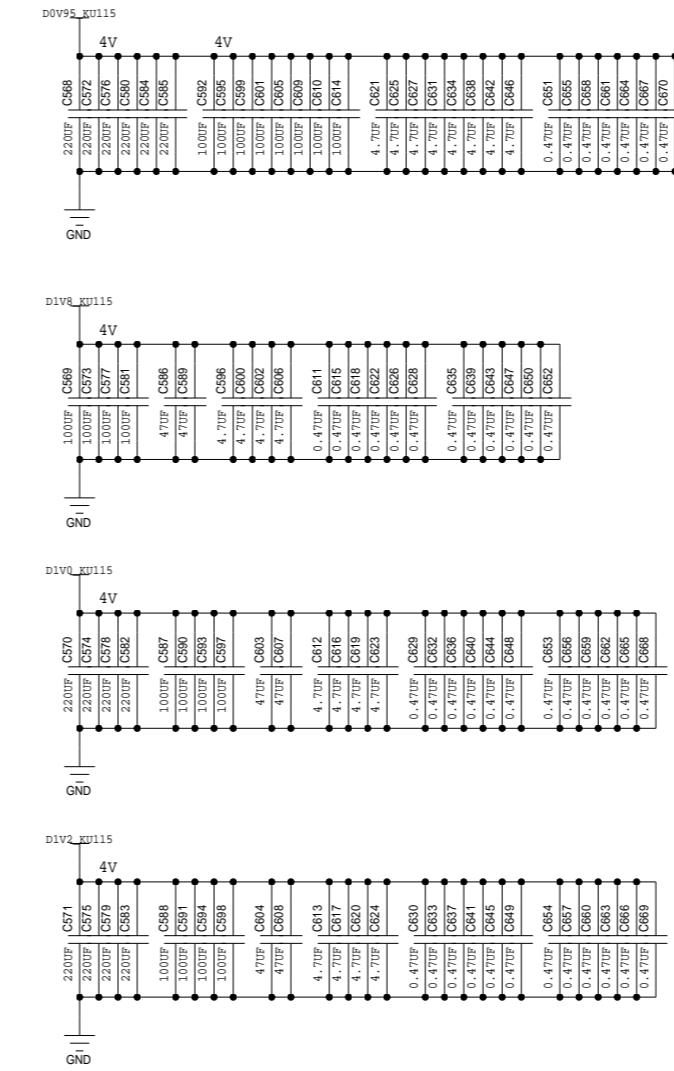
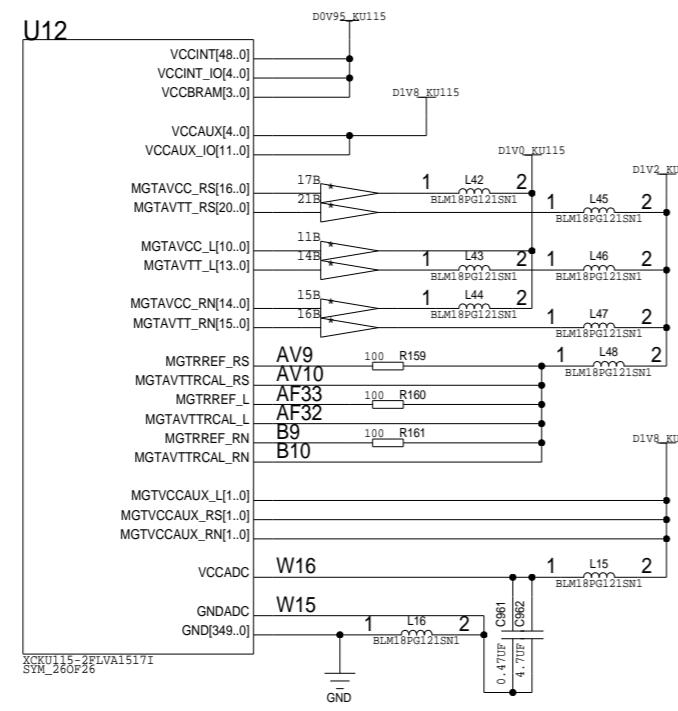
C

B

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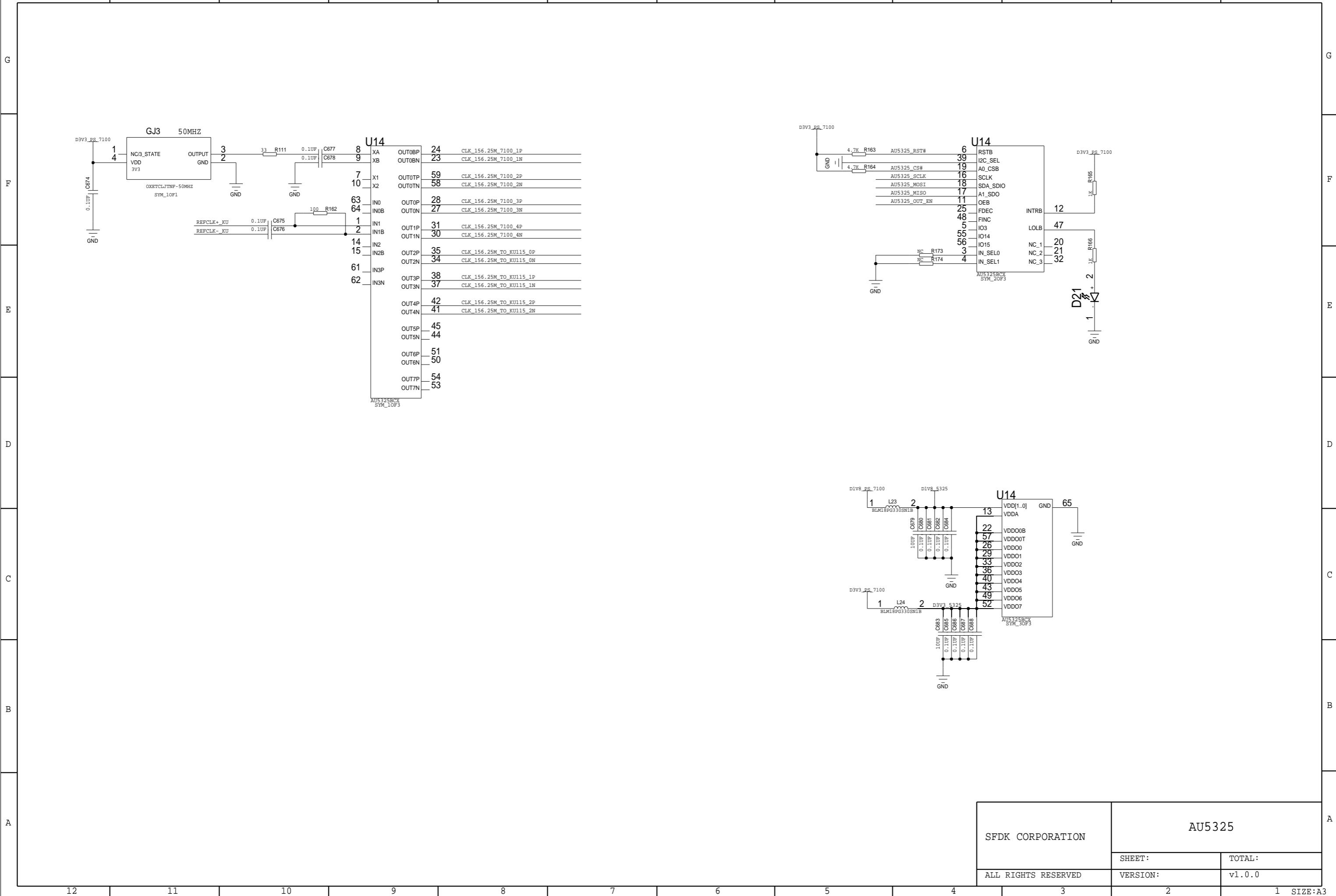
A

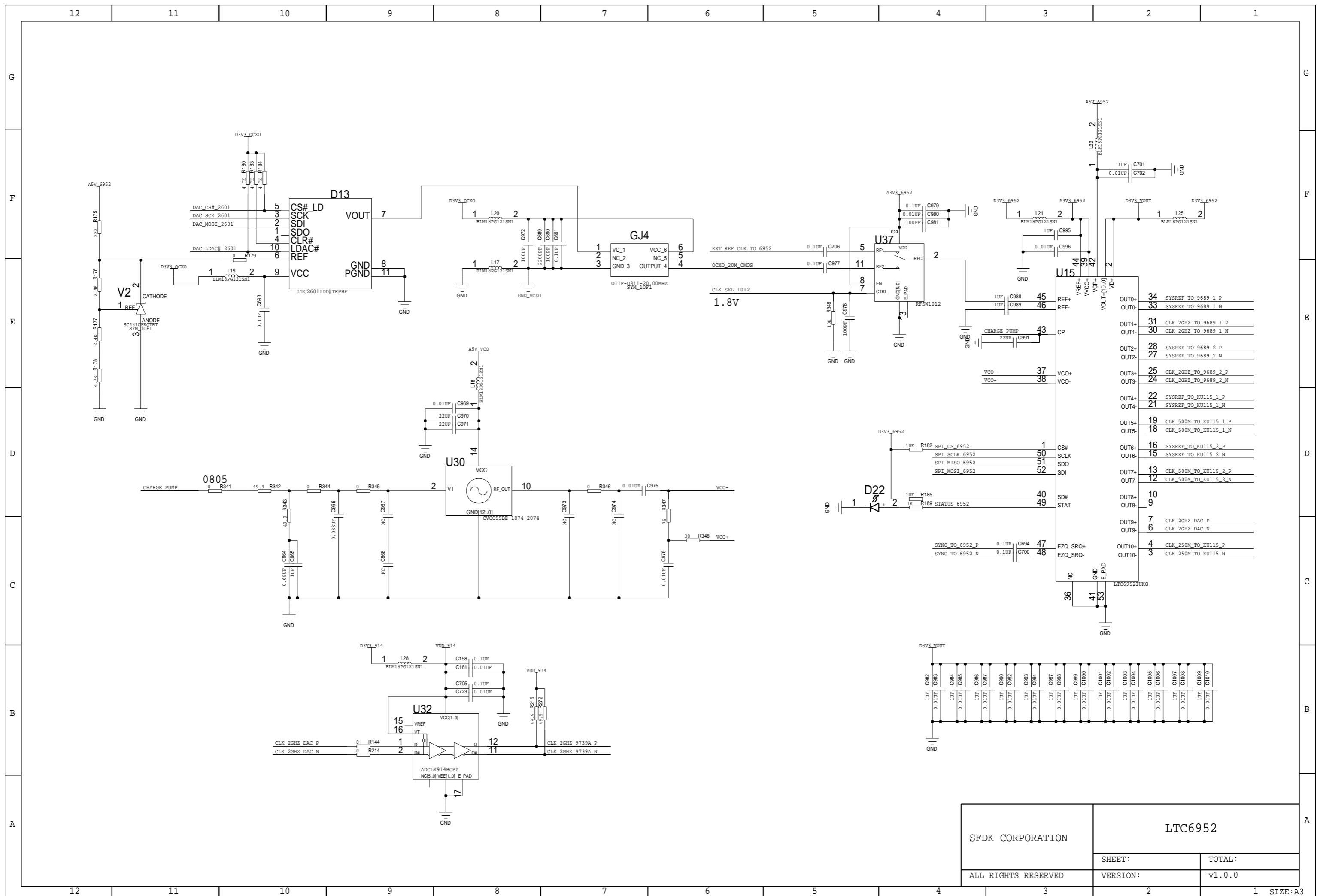
A



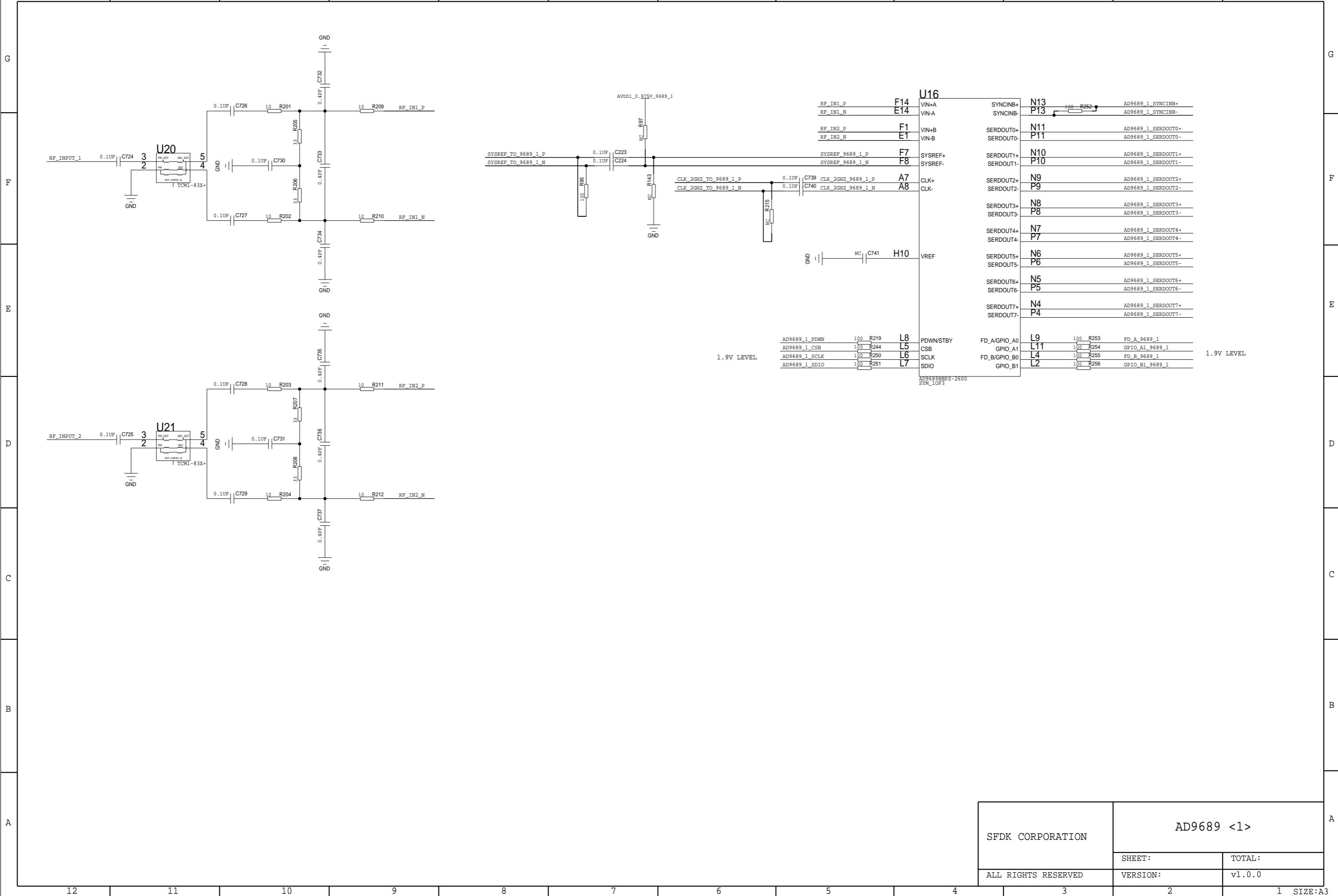
SFDK CORPORATION	KU115 PWR BANK	
SHEET:	TOTAL:	
ALL RIGHTS RESERVED		VERSION: v1.0.0

12 11 10 9 8 7 6 5 4 3 2 1

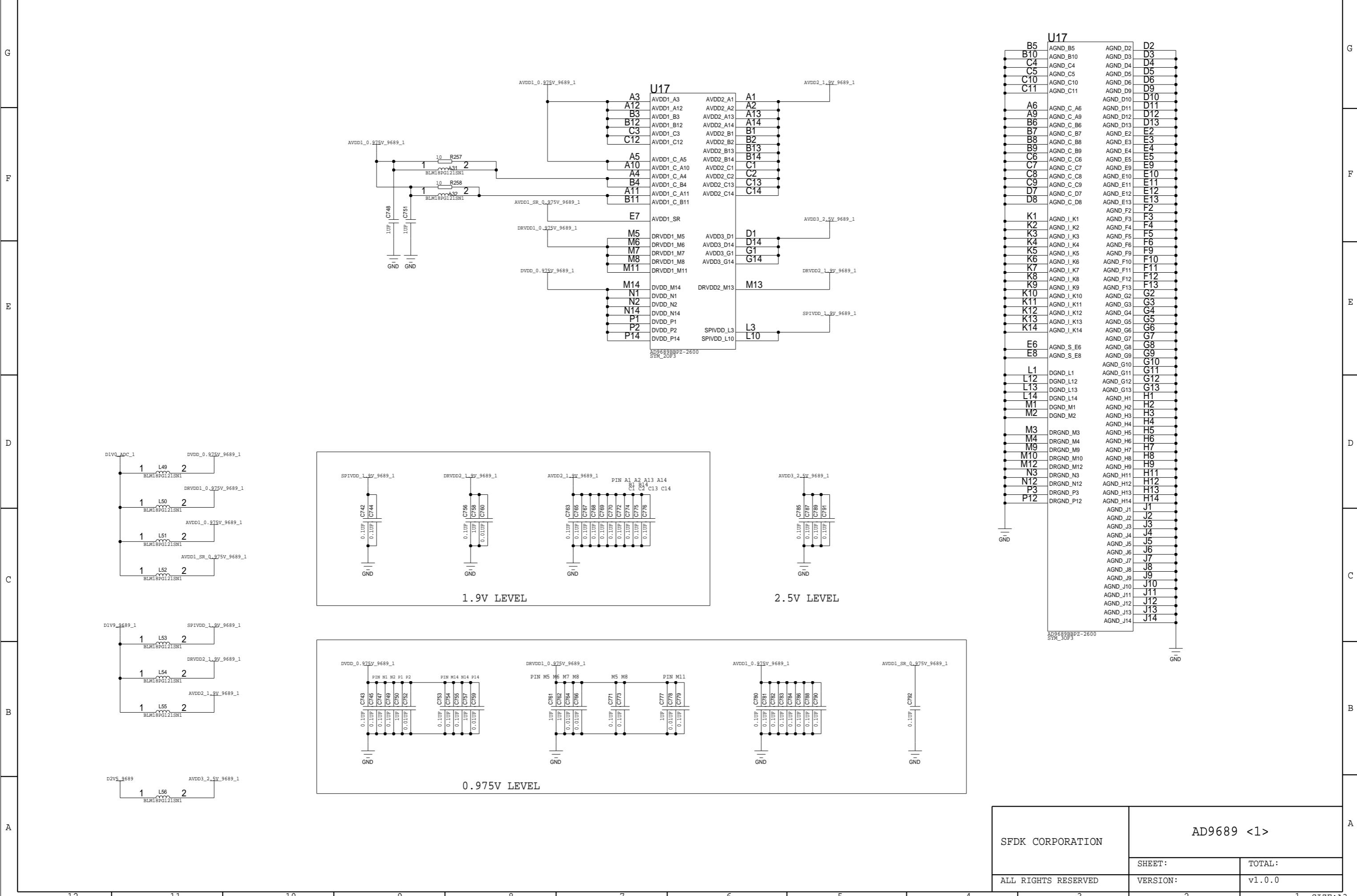




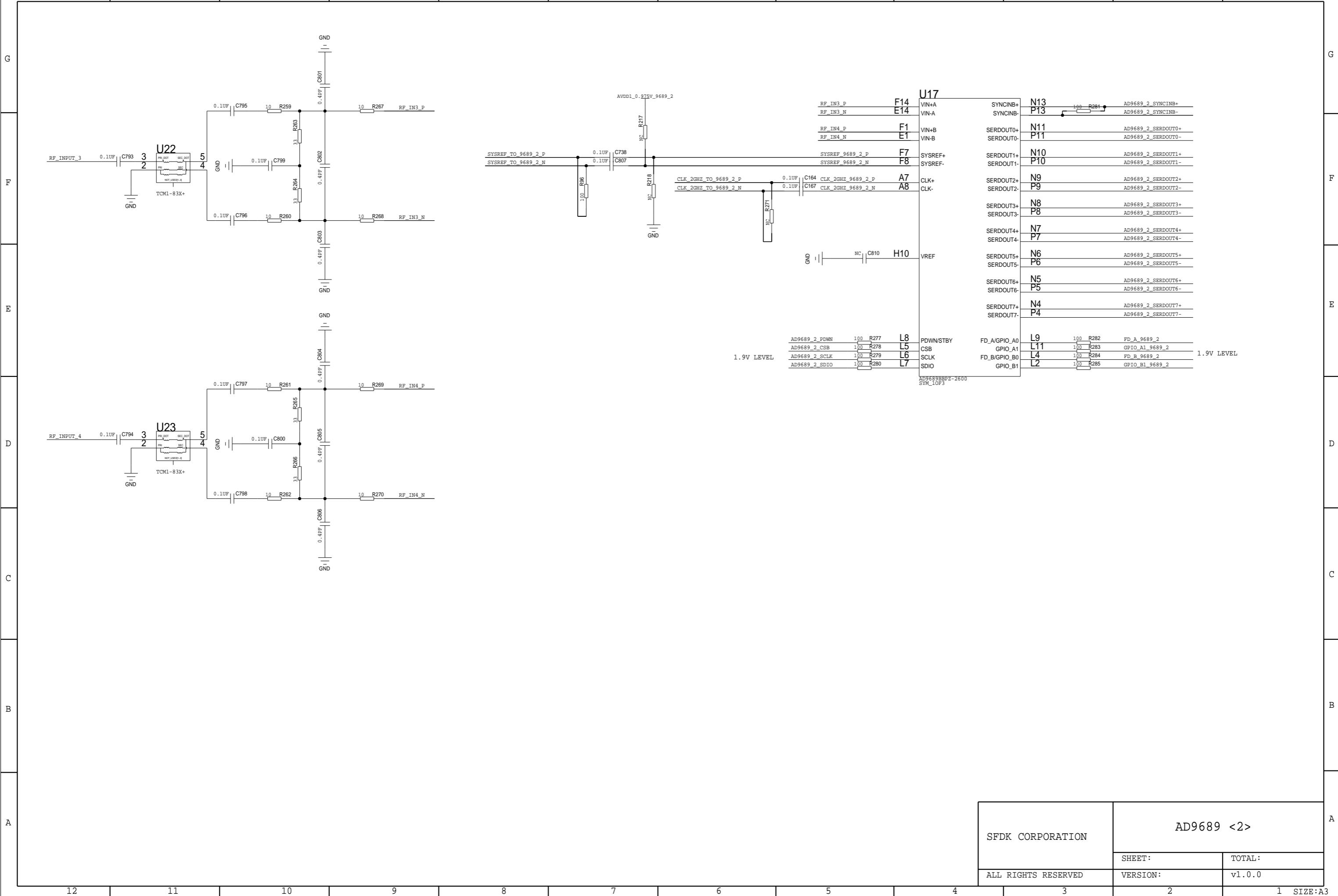
12 11 10 9 8 7 6 5 4 3 2 1



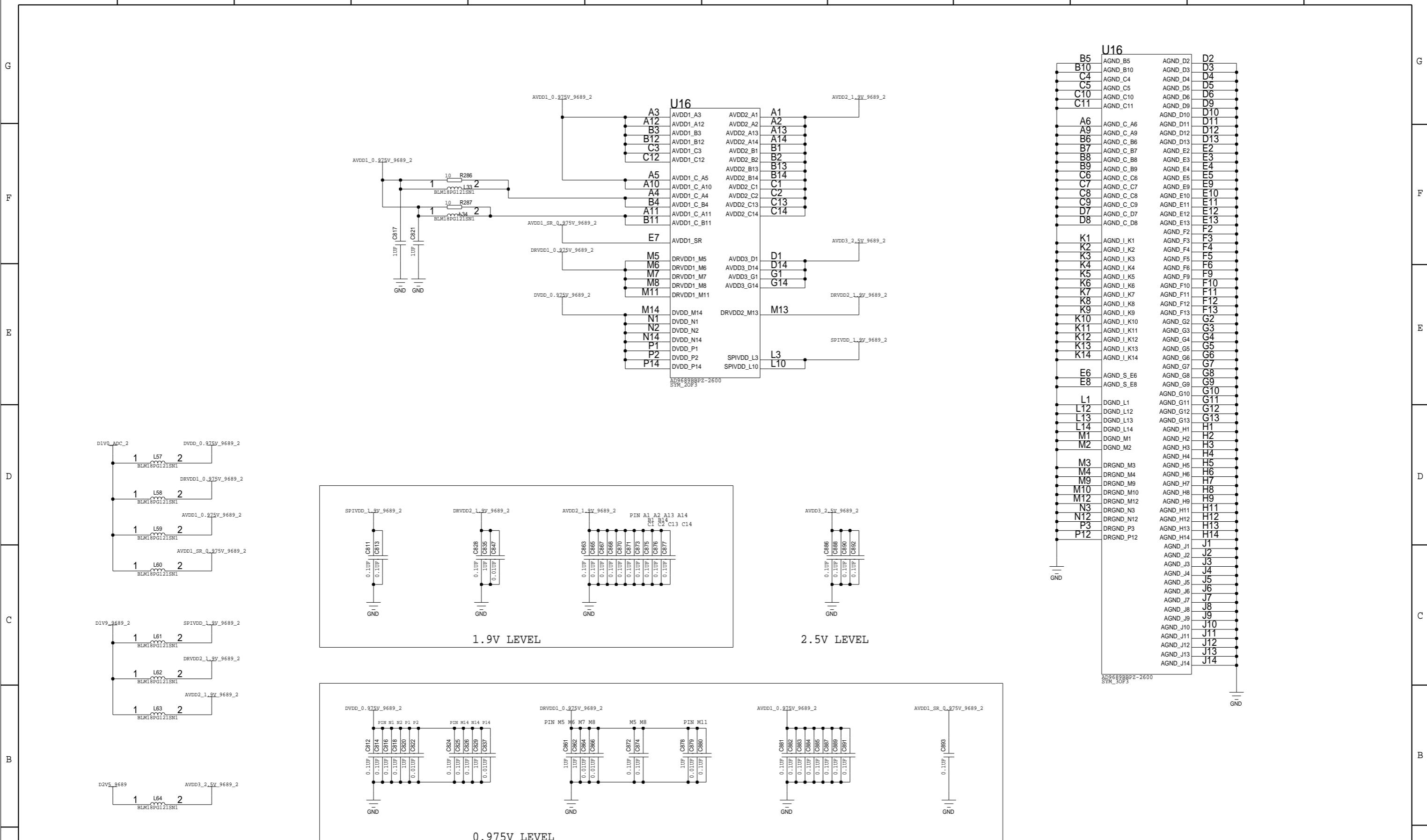
12 11 10 9 8 7 6 5 4 3 2 1 SIZE:A3



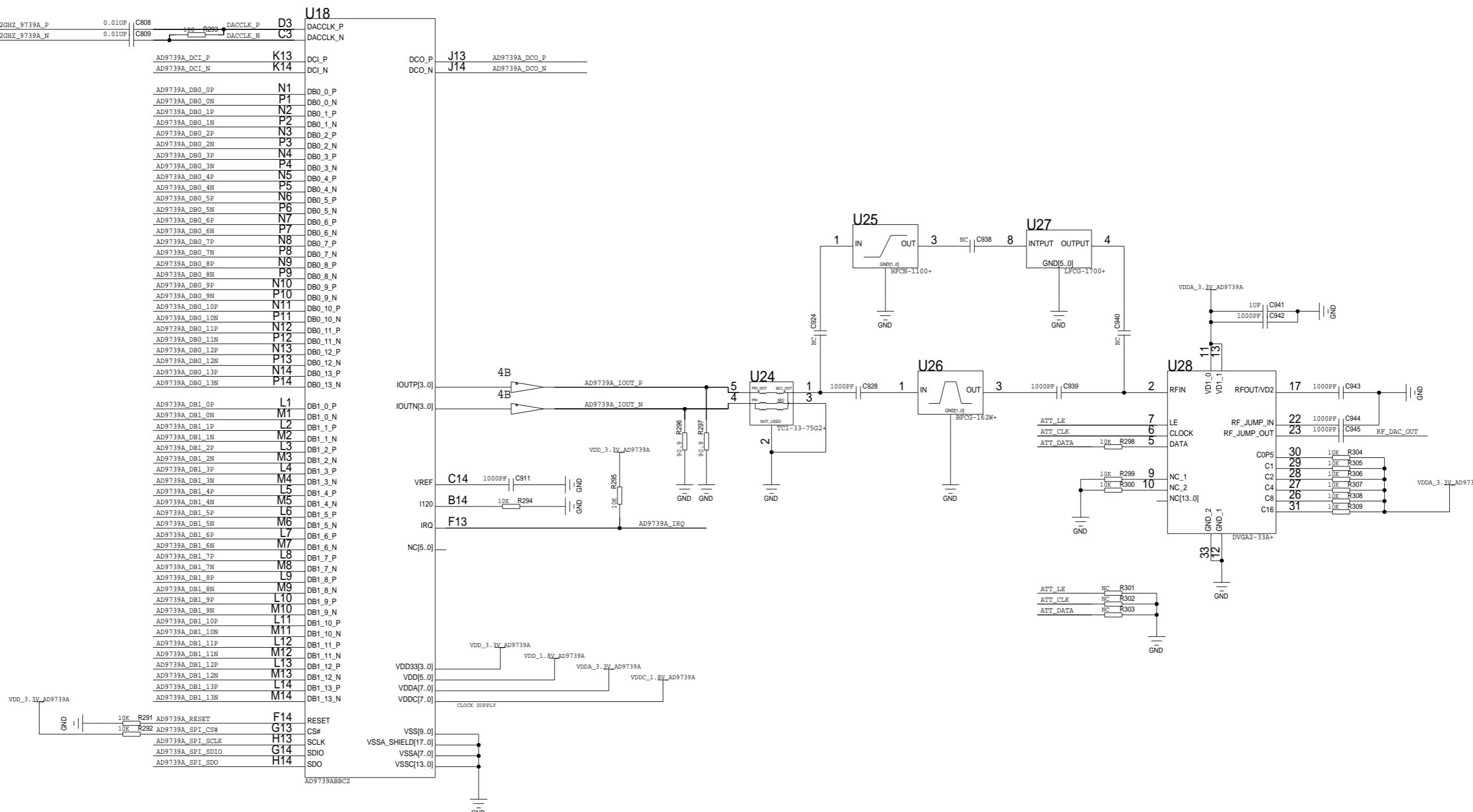
12 11 10 9 8 7 6 5 4 3 2 1



12 11 10 9 8 7 6 5 4 3 2 1 SIZE:A3



SFDK CORPORATION	AD9689 <2>	
SHEET:	TOTAL:	
ALL RIGHTS RESERVED		v1.0.0



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AD9739A

SHEET: \_\_\_\_\_ TOTAL: \_\_\_\_\_

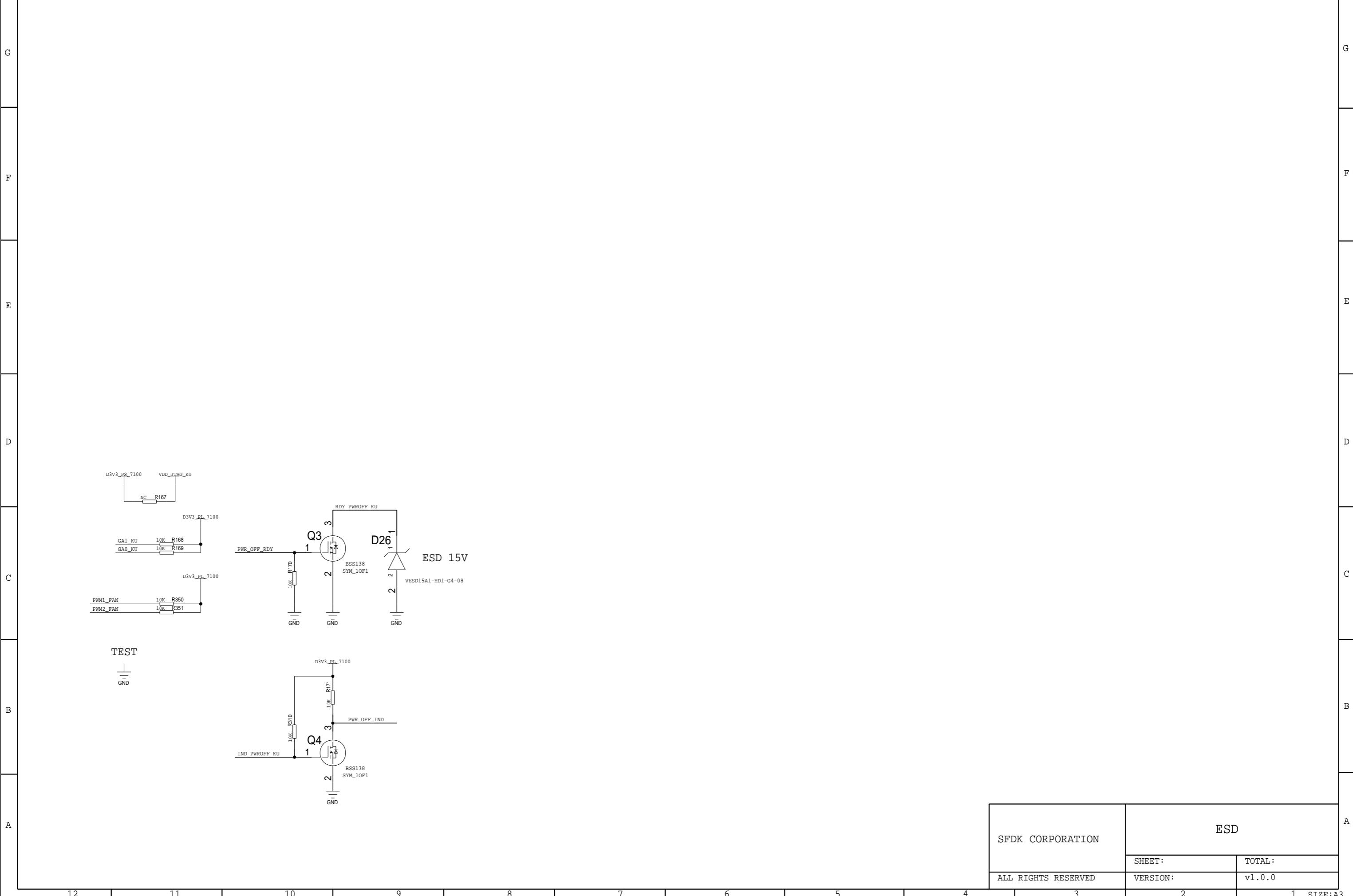
ALL RIGHTS RESERVED

VERSION: v1.0.0

3

VERSION 2

12	11	10	9	8	7	6	5	4	3	2	1
----	----	----	---	---	---	---	---	---	---	---	---



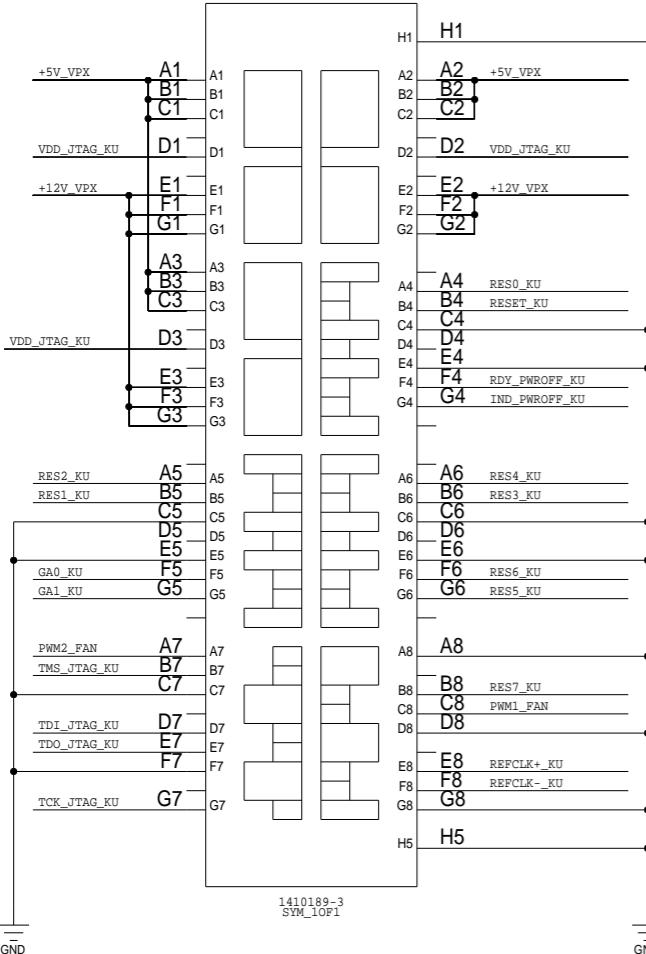
SFDK CORPORATION	ESD	
SHEET:	TOTAL:	
ALL RIGHTS RESERVED	VERSION:	v1.0.0

12	11	10	9	8	7	6	5	4	3	2	1 SIZE:A3
----	----	----	---	---	---	---	---	---	---	---	-----------

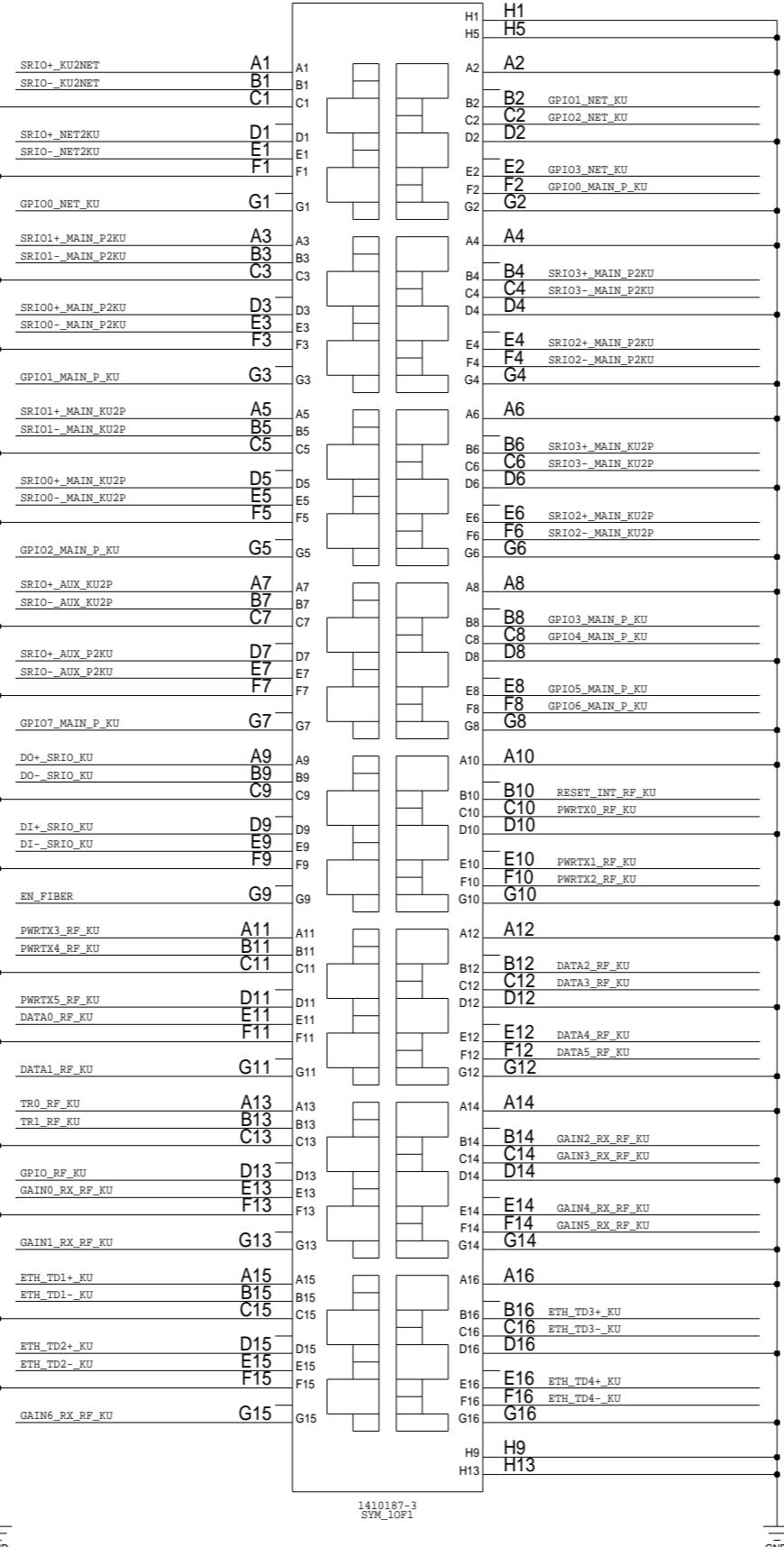
12 11 10 9 8 7 6 5 4 3 2 1

5V 8A

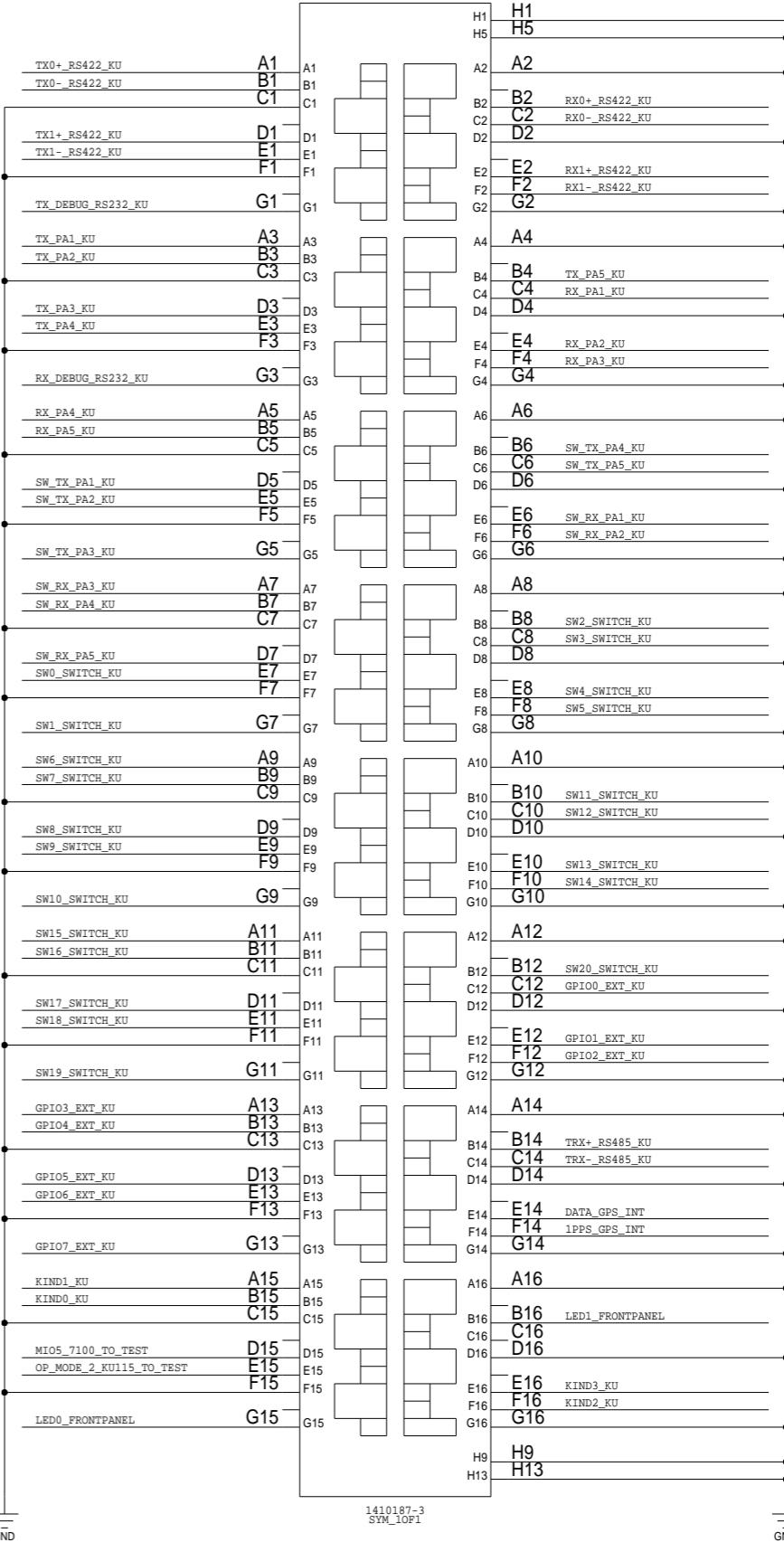
J9



J10



J11



SFDK CORPORATION  
VPX\_CONN

SHEET: TOTAL:  
ALL RIGHTS RESERVED VERSION: v1.0.0

12 11 10 9 8 7 6 5 4 3 2 1 SIZE:A3