



Silicon Motion, Inc.

SM3257ENLT Test Program and ISP Release Note

**SM3257ENLT Test Program and ISP Release Note:**

Release Date	ISP Version	ISP Check Sum	Test AP Version	Description
2012/10/31	121024-LT	SM3257ENLTISP-ISPSA21nmTLC.bin 00696BFC SM3257ENLTISP-ISPSD19nm4P.bin 006CE48E SM3257ENLTISP-ISPT19nm4P.bin 006D35F0	V 2.3.98 v4 12/10/25 Build	<ol style="list-style-type: none"> 1. First SM3257ENLT MP tool / FW formal release 2. Support Samsung 21nm TLC ACG-A die: K9ACGD8U0A (x1 mode) 3. Support SanDisk 19nm TLC 128Gb: SDTNQCAMA-016G 4. Support Toshiba 19nm TLC 128Gb: TC58TEG7T2JTA00
2012/11/09	121106-LT	SM3257ENLTISP-ISPSA21nmTLC.bin SM3257ENLTISP-ISPSD19nm.bin SM3257ENLTISP-ISPT19nm.bin	V 2.3.98 v4 12/10/25 Build	<ol style="list-style-type: none"> 1. Enable cache read for 19 / 21nm TLC (8K 2Plane Flash). 2. Support Samsung 21nm TLC ACG-A die: K9ACGD8U0A (x2 mode)
2012/11/21	121112-LT	SM3257ENLTISP-ISPSA21nmTLC.bin 00696BAE SM3257ENLTISP-ISPSD19nm4P.bin 006CE014 SM3257ENLTISP-ISPSD19nmDDR4P.bin 006D2359 SM3257ENLTISP-ISPT19nm4P.bin 006D3176	V 2.5.02 v3 12/11/16 Build	<ol style="list-style-type: none"> 1. Enable pre-read to improve read performance for 21nm TLC (8K 2Plane Flash) 2. Support Micron 20nm MLC L84 DDR / 25nm TLC B74 SDR: MT29F64G08CBABB / MT29F64G08EBAAA 3. Support Samsung 21nm TLC ABG-C die: K9ABGD8U0C
2013/01/02	121221-LT	SM3257ENLTPTTEST.BIN SM3257ENLTISP.BIN	V 2.5.06 v7 12/12/21 Build	<ol style="list-style-type: none"> 1. Support Samsung 21nm TLC 1CE / 2Die: K9BDGD8U0A / K9CFGD8U1A 2. Support Samsung 27nm TLC: K9ABGD8U0B 3. Support Micron 25nm TLC B74 SDR 1CE / 2Die:



				<p>MT29F256G08EJAAA</p> <p>4. Support Toshiba / SanDisk 19nm 32Gb / 64Gb TLC: TC58TEG5T2JTA00 / TC58TEG6T2JTA00 / SDTNQCAMA-004G / SDTNQCAMA-008G</p> <p>5. Support SanDisk 19nm 128Gb TLC 1CE / 2Die: SDTNQCBMA-032G</p> <p>6. Enhance 19nm 8K 2Plane write performance.</p> <p>7. Modify USB change state for Mac compatibility.</p> <p>8. Improve SPOR</p>
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