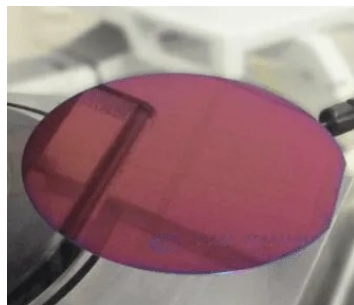


## Positive LED Wafer Based on GaAs Substrate

GaAs-based AlGaInP red LED epi-wafer is a common visible light LED material that has been widely developed in recent years. AlGaInP quaternary red LED has many advantages such as strong current bearing capacity, high luminous efficiency and high temperature resistance. It has an irreplaceable position in the application of lighting, display and indicator lights, and is widely used in various fields of lighting. PAM-XIAMEN (<https://www.powerwaywafer.com/>) supplies positive polarity AlGaInP / GaInP LED wafer at 620nm wavelength. The specific parameters of positive LED wafer for sale are listed in the table as follows:



### 1. Positive LED Wafer Spec.

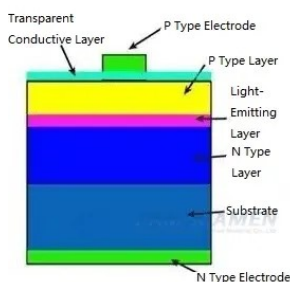
#### PAMP19226-620LED

| Positive Polarity Red LED Structure (W <sub>0</sub> =620nm) |                |               |                                    |
|---|----------------|---------------|------------------------------------|
|   | Layers         | Thickness(nm) | Carrier Density(cm <sup>-3</sup> ) |
| P Contact   | p+ GaAs        | 20            | —                                  |
|   | p- AlGaInP     | —             | 1.00E+18                           |
|   | p- AlInP       | —             | —                                  |
| WG  | u- AlGaInP     | —             | —                                  |
| MQW   | AlGaInP/InGaP  | —             | —                                  |
| WG  | u- AlGaInP     | 100           | —                                  |
|   | n- AlInP       | —             | —                                  |
|   | n- AlGaInP     | —             | —                                  |
| ESL   | n- InGaP       | —             | 3.00E+18                           |
| N Contact   | n+ GaAs        | 30            | —                                  |
|   | u- AlGaInP     | —             | —                                  |
|   | AIAs           | —             | —                                  |
|   | GaAs substrate | —             | —                                  |

### 2. What Is the Positive Polarity of LED Wafer?

The polarity of LED wafer is divided into N/P and P/N type (P: positive; N: negative) according to the polarity of LED chips. Therein, P/N stands for the positive polarity of LED wafer, which means the positive electrode is on the epitaxial film and the negative electrode is under the substrate.

For the AlGaInP light-emitting diode epitaxial material from us, it is directly grown on the GaAs substrate, and then the N electrode is directly prepared on the back of the GaAs substrate, and the P electrode is prepared on the upper surface of the epi thin film, which is shown as figure 1. This is the positive LED wafer technology. The production process of positive led wafer fabrication is relatively mature, and the production efficiency is high.



(https://www.powerwaywafer.com/) If the positive LED wafer is used as a substrate, there is a transparent conductive film placed over the positive LED wafer epi. This is because the conductivity of P-type material layer is lower than that of N-type material layer, and conductive film needs to be deposited to diffuse electrons and improve the luminescence efficiency of positive LED wafer chip.

#### Remark :

The Chinese government has announced new limits on the exportation of Gallium materials (such as GaAs, GaN, Ga<sub>2</sub>O<sub>3</sub>, GaP, InGaAs, and GaSb) and Germanium materials used to make semiconductor chips. Starting from August 1, 2023, exporting these materials is only allowed if we obtains a license from the Chinese Ministry of Commerce. Hope for your understanding and cooperation!



**PAM-XIAMEN**  
Xiamen Powerway Advanced Material Co.,Ltd.

*A Reliable Partner*



**BUY NOW!**

(https://www.powerwaywafer.com/)

Semiconductor Wafer for Researchers

For more information, please contact us email at victorchan@powerwaywafer.com (mailto:victorchan@powerwaywafer.com) and powerwaymaterial@gmail.com (mailto:powerwaymaterial@gmail.com).

2022-10-09

Share this post

#### RELATED POSTS

**What Are Semiconductors ?** (https://www.powerwaywafer.com/semiconductor-definition.html)

Most of today's electronic products, such as computers, mobile phones, or digital tape recorders, have a very close connection with semiconductors. So, what is a semiconductor? The semiconductor

2021-05-07 meta-author

**Test grade silicon wafers-9** (https://www.powerwaywafer.com/test-grade-silicon-wafers-9.html)

PAM XIAMEN offers test grade silicon wafers Below is just a short list of the test grade silicon substrates! Inches Cust class Dopant Type Orientation PFL length PFL direction SFL Off orientation Resistivity

2019-02-25 meta-author

**(110) Silicon Wafers** (https://www.powerwaywafer.com/wafers.html)

PAM XIAMEN offers (110) Silicon Substrates. If you don't need then please email at sales@powerwaywafer.com. Di Material Dopant Orient. Thck (μm) Surf. Resistivity Ωcm C

2019-02-22 meta-author

## Contact Us

If you would like a quotation or more information about our products, please leave us a message, will reply you as soon as possible.

Your Email Address \*

Subject \*

Your Message

+ 9 = 11

Send Message

#### Contact Information

Powerway is a manufacturer offering Semiconductor Wafer, Wafer Substrate and Epitaxial wafer, please do not hesitate to contact us for technology support.

**Wafer Foundry:** 26-32#, Liamei Rd. Lianhua Industrial Area, Tong an, Xiamen 361100, China

**Phone:** +86-592-5601 404

**Email:** sales@powerwaywafer.com (mailto:sales@powerwaywafer.com) tech@powerwaywafer.com (mailto:tech@powerwaywafer.com)

#### Product Line

► GaN Wafer (https://www.powerwaywafer.com/gan-wafer)

[\(https://www.powerwaywafer.com/\)](https://www.powerwaywafer.com/)  
[GaAs wafer \(https://www.powerwaywafer.com/gaas-wafers\)](https://www.powerwaywafer.com/gaas-wafers)

› [Compound Semiconductor \(https://www.powerwaywafer.com/compound-semiconductor\)](https://www.powerwaywafer.com/compound-semiconductor)

› [Germanium Wafer \(https://www.powerwaywafer.com/germanium-wafer\)](https://www.powerwaywafer.com/germanium-wafer)

› [CdZnTe Wafer \(https://www.powerwaywafer.com/cdznte-czt-wafer\)](https://www.powerwaywafer.com/cdznte-czt-wafer)

› [Silicon Wafer \(https://www.powerwaywafer.com/silicon-wafer\)](https://www.powerwaywafer.com/silicon-wafer)

› [Wafer Fabrication \(https://www.powerwaywafer.com/wafer-fabrication\)](https://www.powerwaywafer.com/wafer-fabrication)

## Keywords Tags

[inp file \(https://www.powerwaywafer.com/tag/inp-file\)](https://www.powerwaywafer.com/tag/inp-file) [Ge Single Crystals and Wafers \(https://www.powerwaywafer.com/tag/ge-single-crystals-and-wafers\)](https://www.powerwaywafer.com/tag/ge-single-crystals-and-wafers) [gaas wafer \(https://www.powerwaywafer.com/tag/gaas-wafer\)](https://www.powerwaywafer.com/tag/gaas-wafer) [GaN \(https://www.powerwaywafer.com/tag/gan\)](https://www.powerwaywafer.com/tag/gan) [semiconductor wafer manufacturers \(https://www.powerwaywafer.com/tag/semiconductor-wafer-manufacturers\)](https://www.powerwaywafer.com/tag/semiconductor-wafer-manufacturers) [Silicon Wafer \(https://www.powerwaywafer.com/tag/silicon-wafer\)](https://www.powerwaywafer.com/tag/silicon-wafer) [Epitaxial wafer \(https://www.powerwaywafer.com/tag/epitaxial-wafer\)](https://www.powerwaywafer.com/tag/epitaxial-wafer) [inp toulouse \(https://www.powerwaywafer.com/tag/inp-toulouse\)](https://www.powerwaywafer.com/tag/inp-toulouse) [GaN HEMT epitaxial wafer \(https://www.powerwaywafer.com/tag/gan-hemt-epitaxial-wafer\)](https://www.powerwaywafer.com/tag/gan-hemt-epitaxial-wafer) [silicon wafer thickness \(https://www.powerwaywafer.com/tag/silicon-wafer-thickness\)](https://www.powerwaywafer.com/tag/silicon-wafer-thickness) [InGaAs Structure Wafer \(https://www.powerwaywafer.com/tag/ingaas-structure-wafer\)](https://www.powerwaywafer.com/tag/ingaas-structure-wafer) [sic wafer \(https://www.powerwaywafer.com/tag/sic-wafer\)](https://www.powerwaywafer.com/tag/sic-wafer) [semico \(https://www.powerwaywafer.com/tag/semico\)](https://www.powerwaywafer.com/tag/semico) [μ-PCD \(https://www.powerwaywafer.com/tag/%sbc-pcd\)](https://www.powerwaywafer.com/tag/%sbc-pcd) [Minority carrier lifetime \(https://www.powerwaywafer.com/tag/minority-carrier-lifetime\)](https://www.powerwaywafer.com/tag/minority-carrier-lifetime) [Phase diagrams \(https://www.powerwaywafer.com/tag/phase-diagrams\)](https://www.powerwaywafer.com/tag/phase-diagrams) [Gallium arsenide \(https://www.powerwaywafer.com/tag/gallium-arsenide\)](https://www.powerwaywafer.com/tag/gallium-arsenide) [wafer sawing \(https://www.powerwaywafer.com/tag/wafer-sawing\)](https://www.powerwaywafer.com/tag/wafer-sawing) [CdZnTe detector \(https://www.powerwaywafer.com/tag/cdznte-detector\)](https://www.powerwaywafer.com/tag/cdznte-detector) [Diffusion \(https://www.powerwaywafer.com/tag/diffusion\)](https://www.powerwaywafer.com/tag/diffusion) [Ge solar cells \(https://www.powerwaywafer.com/tag/ge-solar-cells\)](https://www.powerwaywafer.com/tag/ge-solar-cells) [inp mozambique \(https://www.powerwaywafer.com/tag/inp-mozambique\)](https://www.powerwaywafer.com/tag/inp-mozambique) [GaN on Si \(https://www.powerwaywafer.com/tag/gan-on-si\)](https://www.powerwaywafer.com/tag/gan-on-si) [sili \(https://www.powerwaywafer.com/tag/sili\)](https://www.powerwaywafer.com/tag/sili) [semiconductor wafer \(https://www.powerwaywafer.com/tag/semiconductor-wafer\)](https://www.powerwaywafer.com/tag/semiconductor-wafer) [GaN Wafer \(https://www.powerwaywafer.com/tag/gan-wafer\)](https://www.powerwaywafer.com/tag/gan-wafer) [inp grenoble \(https://www.powerwaywafer.com/tag/inp-grenoble\)](https://www.powerwaywafer.com/tag/inp-grenoble) [Te inclusions \(https://www.powerwaywafer.com/tag/te-inclusions\)](https://www.powerwaywafer.com/tag/te-inclusions) [Defects \(https://www.powerwaywafer.com/tag/defects\)](https://www.powerwaywafer.com/tag/defects) [silicon wafer uk \(https://www.powerwaywafer.com/tag/silicon-wafer-uk\)](https://www.powerwaywafer.com/tag/silicon-wafer-uk) [Spatial resolution \(https://www.powerwaywafer.com/tag/spatial-resolution\)](https://www.powerwaywafer.com/tag/spatial-resolution) [Charge sharing \(https://www.powerwaywafer.com/tag/charge-sharing\)](https://www.powerwaywafer.com/tag/charge-sharing) [Interface \(https://www.powerwaywafer.com/tag/interface\)](https://www.powerwaywafer.com/tag/interface) [SiC Substrate \(https://www.powerwaywafer.com/tag/sic-substrate\)](https://www.powerwaywafer.com/tag/sic-substrate) [CdZnTe \(https://www.powerwaywafer.com/tag/cdznte\)](https://www.powerwaywafer.com/tag/lt-gaas) [LT-GaAs \(https://www.powerwaywafer.com/tag/lt-gaas\)](https://www.powerwaywafer.com/tag/lt-gaas) [InAlN thin films \(https://www.powerwaywafer.com/tag/inaln-thin-films\)](https://www.powerwaywafer.com/tag/inaln-thin-films) [InGaN \(https://www.powerwaywafer.com/tag/ingan\)](https://www.powerwaywafer.com/tag/ingan) [Nucleation \(https://www.powerwaywafer.com/tag/nucleation\)](https://www.powerwaywafer.com/tag/nucleation) [GaN substrate \(https://www.powerwaywafer.com/tag/gan-substrate\)](https://www.powerwaywafer.com/tag/gan-substrate)

## Follow Us



Scan square code to browse



on Pad or Smart-Phone



Copyright © 1990 Xiamen Powerway Advanced Material Co., Ltd. All Copy Right Reserved.

[Sitemap \(https://www.powerwaywafer.com/sitemap.html\)](https://www.powerwaywafer.com/sitemap.html) | [XML \(/sitemap\\_index.xml\)](#)