Notification about the transfer of the semiconductor business

The semiconductor business of Panasonic Corporation was transferred on September 1, 2020 to Nuvoton Technology Corporation (hereinafter referred to as "Nuvoton"). Accordingly, Panasonic Semiconductor Solutions Co., Ltd. became under the umbrella of the Nuvoton Group, with the new name of Nuvoton Technology Corporation Japan (hereinafter referred to as "NTCJ").

In accordance with this transfer, semiconductor products will be handled as NTCJ-made products after September 1, 2020. However, such products will be continuously sold through Panasonic Corporation.

Publisher of this Document is NTCJ.

If you would find description "Panasonic" or "Panasonic semiconductor solutions", please replace it with NTCJ.

Except below description page
 "Request for your special attention and precautions in using the technical information and semiconductors described in this book"

Nuvoton Technology Corporation Japan

MN101C78 Series

Туре	MN101C78A	MN101CF78A				
Internal ROM type	Mask ROM	FLASH				
ROM (byte)	32K					
RAM (byte)	1.5K					
Package (Lead-free)	TQFP048-P-0707B					
Minimum Instruction Execution Time	0.100 μs (at 3.0 V to 3.6 V, 10 MHz) 0.118 μs (at 2.7 V to 3.6 V, 8.5 MHz) 0.235 μs (at 1.8 V to 3.6 V, 4.25 MHz)* 62.5 μs (at 1.8 V to 3.6 V, 32 kHz)* *: The lower limit for operation guarantee for flash memory built-in type is 2.2 V.					

■ Interrupts

RESET. Watchdog. External 0 to 2. External 4 (key interrupt dedicated). Timer 0 to 3. Timer 6. Timer 7 (2 systems). Timer 8 (2 systems). Time base. Serial 0 (2 systems). Serial 1 (2 systems). Serial 3. Serial 4. A/D conversion finish

■ Timer Counter

Timer Counter
8-bit timer \times 5
Timer 0
Timer 1Square-wave output. Event count. Synchronous output event
Timer 2
Timer 3Square-wave output. Event count. Remote control carrier output. Serial 0 baud rate timer
Timer 68-bit freerun timer
Timer 0, 1 can be cascade-connected
Timer 2, 3 can be cascade-connected
16-bit timer \times 2
Timer 7
Timer 8Square-wave/16-bit PWM output (duty continuous variable). Event count. Pulse width measurement. Input capture. Square-wave/PWM output to large current terminal P53 possible
Timer 7, 8 can be cascade-connected: Square-wave output, PWM, input capture, pulse width measurement is possible as a 32-bit timer

Time base timer: One-minute count setting

Watchdog timer × 1

■ Serial interface

Synchronous type/UART (full-duplex) \times 2: Serial 0, 1 Synchronous type/Single-master $I^2C \times 1$: Serial 3 I^2C slave \times 1: Serial 4

Serial 4......I²C high-speed transfer mode. 7-bit/10-bit address setting. General call

■ I/O Pins I/O

39: Common use. Specified pull-up resistor available. Input/output selectable (bit unit)

■ A/D converter

10-bit \times 7 channels (with S/H)

■ Display control function

LCD: 12 segments \times 4 commons (Static, 1/2, 1/3, or 1/4 duty) Usable if VLCD \leq VDD

■ Special Ports

Buzzer output. Inverted buzzer output. Remote control carrier output. High-current drive port

Panasonic MAD00039GEM

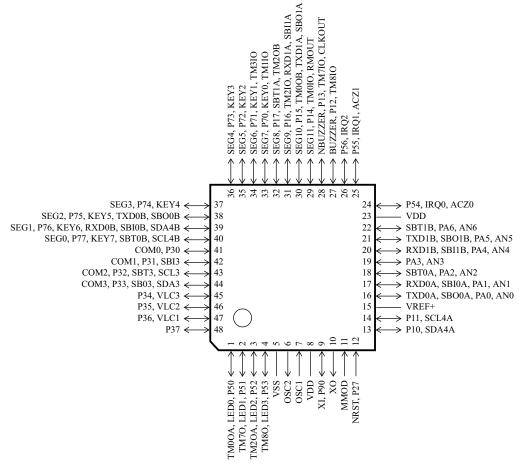
■ Electrical Charactreistics (Supply current)

Parameter Symbol Condition	Symbol	Condition	Limit			Unit
	Condition	min	typ	max	UIIIL	
Operating supply current	IDD1	fosc = 4.25 MHz (fs = fosc/2). VDD = 3 V		0.6(1.3)	1.1(2.2)	mA
	IDD2	fx = 32 kHz (fs = fx/2). VDD = 3 V		4(46)	15(90)	μΑ
Supply current at HALT	IDD3	$fx = 32 \text{ kHz. VDD} = 3 \text{ V. Ta} = 25 ^{\circ}\text{C}$		2(3)	5(13)	μΑ
	IDD4	$fx = 32 \text{ kHz. VDD} = 3 \text{ V. Ta} = -40 ^{\circ}\text{C to} +85 ^{\circ}\text{C}$			10(40)	μΑ
Supply ourrant at STOR	IDD5	$VDD = 3 \text{ V. } Ta = 25 ^{\circ}\text{C}$			2(3)	μΑ
Supply current at STOP	IDD6	VDD = 3 V. Ta = -40 °C to +85 °C			8(30)	μΑ

Note) (): Flash memory built-in type

■ Pin Assignment

TQFP048-P-0707B



MAD00039GEM Panasonic

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