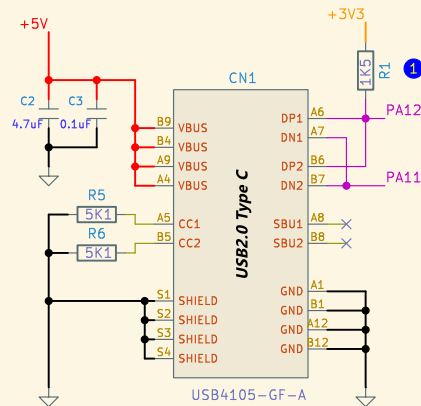
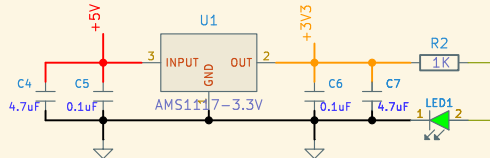


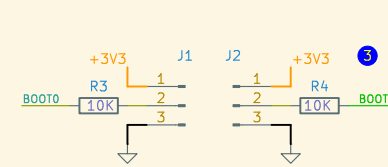
USB Slave



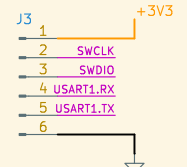
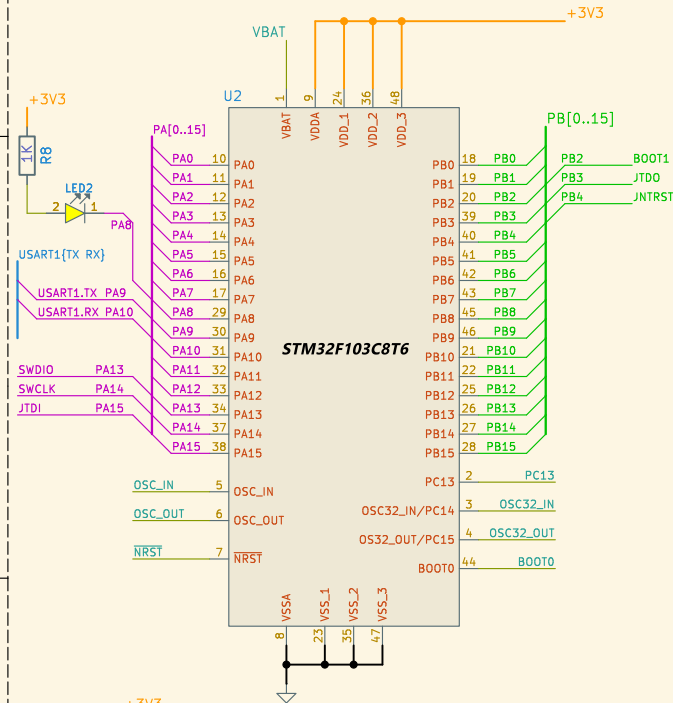
3V3 LDO



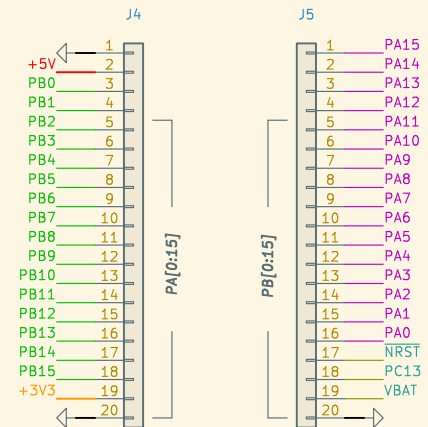
BOOT



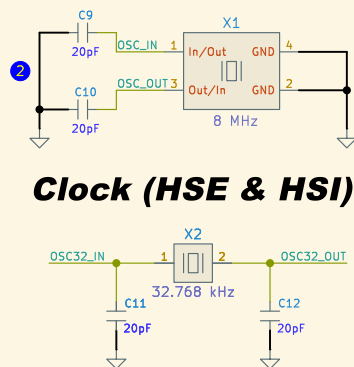
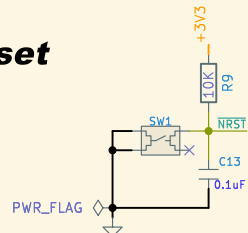
Debug

**MCU**

Expansion Header



Clock (HSE & HSI)

**Reset**

Notes

- 1 To be compliant with the USB 2.0 full-speed electrical specification, the USB_DP (D+) pin must be pulled up to a voltage in the 3.0 to 3.6 V range with a 1.5 kΩ resistor. (see AN4789)
- 2 $C_{\text{shunt}} = 2 * (C_{\text{L}} - C_{\text{stray}})$, where C_{stray} is typically 2-5 pF

BOOT0	BOOT1	Boot Mode
0	X	Boot from user Flash
1	0	Boot from System memory
1	1	Boot from SRAM

STM32F103C8T6 Core Board

