Chapter 5: Intermediate-Code Generation

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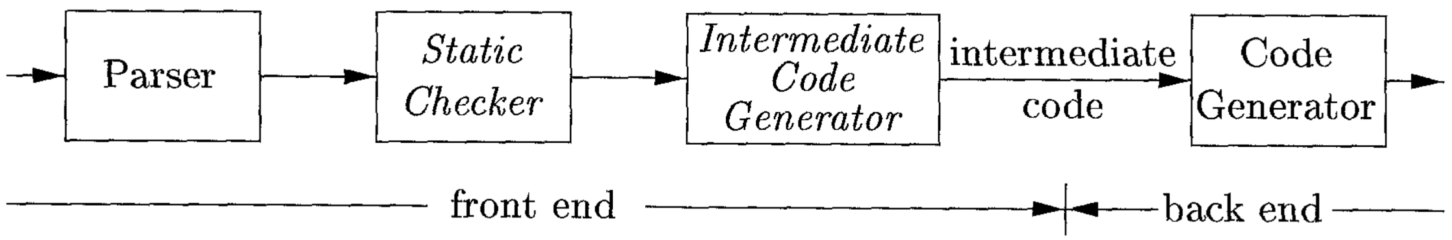
Outline

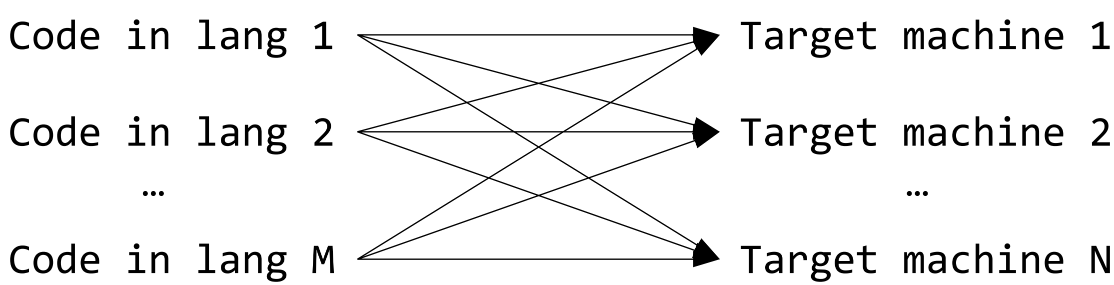
- Intermediate Representation
- Type and Declarations
- Type Checking
- Translation of Expressions
- Control Flow
- Backpatching

Compiler Front End

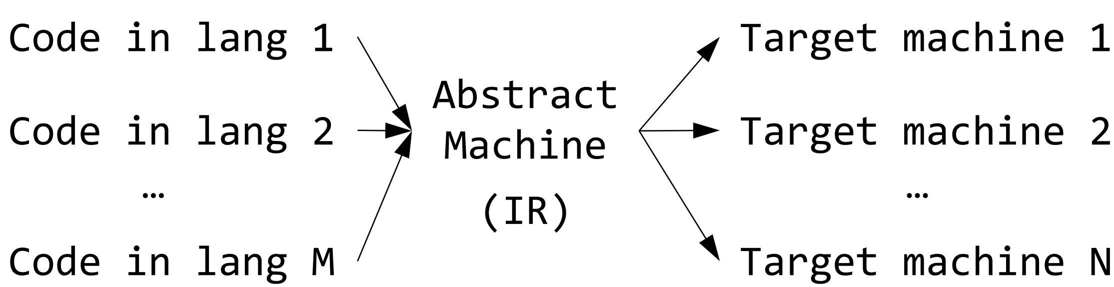
- The front end of a compiler analyzes a source program and creates an intermediate representation (IR, 中间表示), from which the back end generates target code
  - Details of the source language are confined to the front end, and details of the target machine to the back end



The Benefits of A Common IR



M * N compilers without a common IR



M + N compilers with a common IR

Different Levels of IRs

Source Program $\rightarrow$ High Level Intermediate Representation $\rightarrow \cdots \rightarrow$ Low Level Intermediate Representation $\rightarrow$ Target Code

- A compiler may construct a sequence of IR's
  - High-level IR's like syntax trees are close to the source language
    - They are suitable for machine-independent tasks like static type checking
  - Low-level IR's are close to the target machines
    - They are suitable for machine-dependent tasks like <u>register allocation</u> and <u>instruction selection</u>
- Interesting fact: C is often used as an intermediate form. The first C++ compiler has a front end that generates C and a C compiler as a backend

Outline

- Intermediate Representation
  - DAG's for Expressions
  - Three-Address Code
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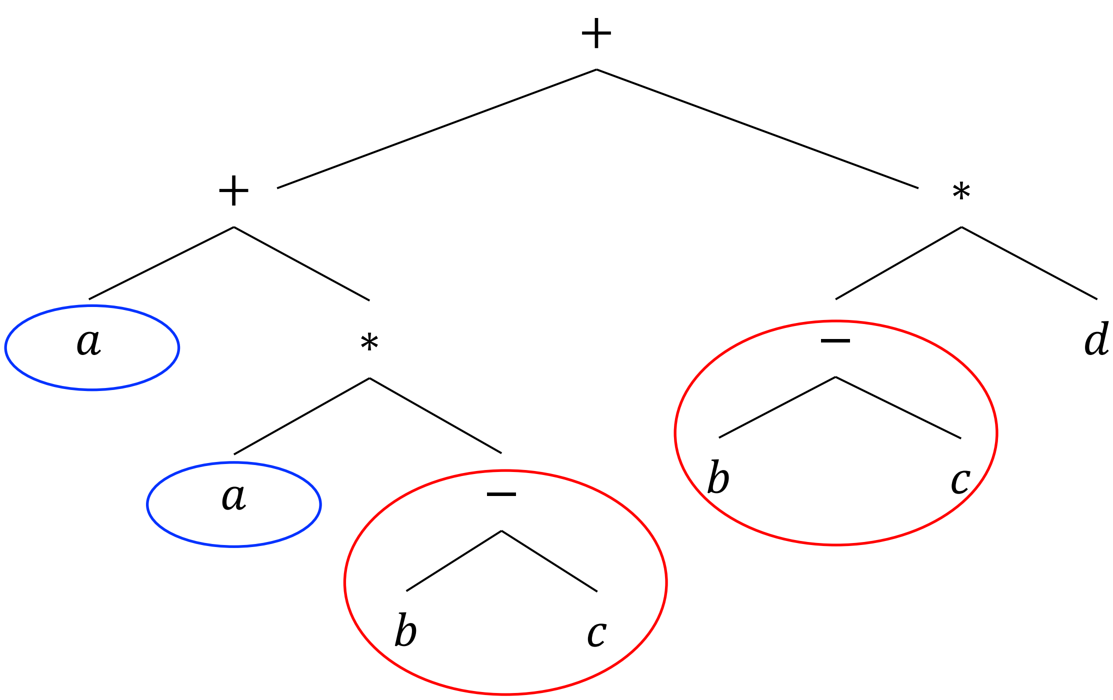
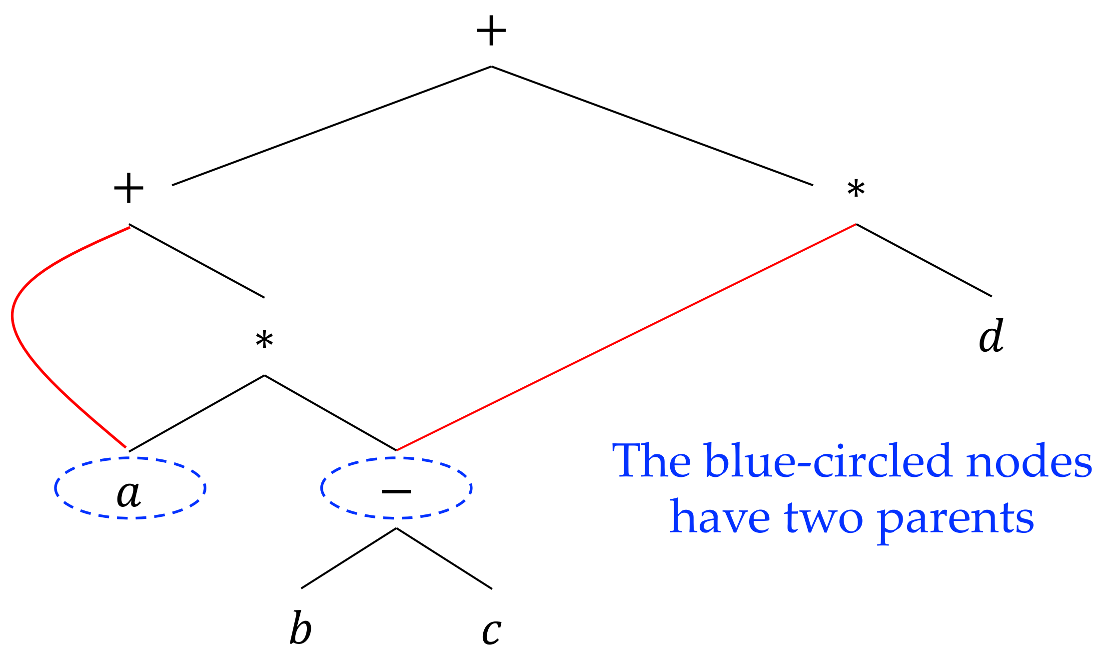
DAG's for Expressions

- In a syntax tree, the tree for a common subexpression would be replicated as many times as the subexpression appears
  - Example: $a + a * (b - c) + (b - c) * d$



DAG's for Expressions Cont.

- A *directed acyclic graph* (DAG, 有向无环图) identifies the common subexpressions and represents expressions succinctly
  - Example: $a + a * (b - c) + (b - c) * d$



Constructing DAG's

- DAG's can be constructed by the same SDD that constructs syntax trees
- **The difference:** When constructing DAG's, a new node is created if and only if there is no existing identical node

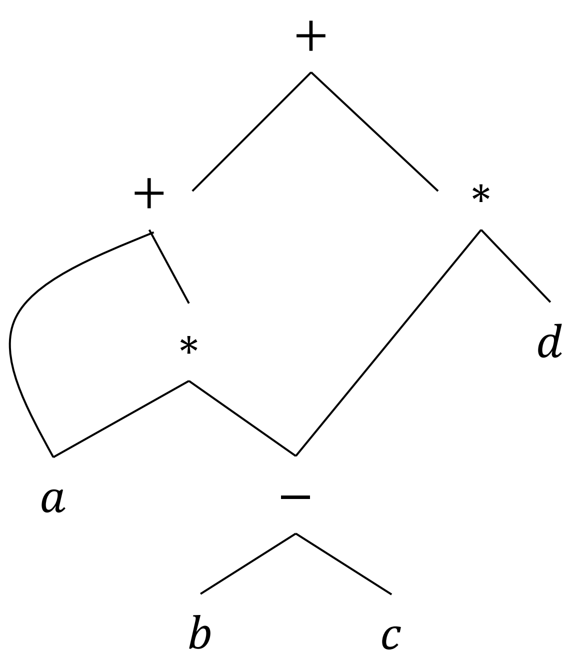
	Production	Semantic Rules
1)	$E \rightarrow E_1 + T$	$E.node = \boxed{\textbf{new}}\ Node('+', E_1.node, T.node)$
2)	$E \rightarrow E_1 - T$	$E.node = \boxed{\textbf{new}}\ Node('-', E_1.node, T.node)$
3)	$E \rightarrow T$	$E.node = T.node$
4)	$T \rightarrow (\ E\ )$	$T.node = E.node$
5)	$T \rightarrow \textbf{id}$	$T.node = \boxed{\textbf{new}}\ Leaf(\textbf{id}, \textbf{id}.entry)$
6)	$T \rightarrow \textbf{num}$	$T.node = \boxed{\textbf{new}}\ Leaf(\textbf{num}, \textbf{num}.val)$

Special "new":

Reuse existing nodes when possible

Constructing DAG's Cont.

- The construction steps



1) $p_1 = Leaf(\textbf{id}, entry\text{-}a)$
2) $p_2 = Leaf(\textbf{id}, entry\text{-}a) = p_1$
3) $p_3 = Leaf(\textbf{id}, entry\text{-}b)$
4) $p_4 = Leaf(\textbf{id}, entry\text{-}c)$
5) $p_5 = Node('-', p_3, p_4)$ Node reuse
6) $p_6 = Node('*', p_1, p_5)$
7) $p_7 = Node('+', p_1, p_6)$
8) $p_8 = Leaf(\textbf{id}, entry\text{-}b) = p_3$
9) $p_9 = Leaf(\textbf{id}, entry\text{-}c) = p_4$
10) $p_{10} = Node('-', p_3, p_4) = p_5$
11) $p_{11} = Leaf(\textbf{id}, entry\text{-}d)$
12) $p_{12} = Node('*', p_5, p_{11})$
13) $p_{13} = Node('+', p_7, p_{12})$

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Three-Address Code (三地址代码)

- In three-address code, there is at most one operator on the right side of an instruction
  - Instructions are often in the form $x = y\ op\ z$

- Operands (or addresses) can be:
  - Names in the source programs
  - Constants: a compiler must deal with many types of constants
  - Temporary names generated by a compiler

Instructions (1)

1. Assignment instructions:
   - $x = y \; op \; z$, where op is a binary arithmetic/logical operation
   - $x = op \; y$, where op is a unary operation
2. Copy instructions: $x = y$
3. Unconditional jump instructions: goto L, where L is a label of the jump target
4. Conditional jump instructions:
   - if x goto L
   - ifFlase x goto L
   - if $x \; relop \; y$ goto L

Instructions (2)

5. Procedural calls and returns
   - param x_1
   - …
   - param x_n
   - call p, n (procedure call)
   - y = call p, n (function call)
   - return y

6. Indexed copy instructions: $x = y[i] \quad x[i] = y$
   - Here, $y[i]$ means the value in the location i memory units beyond location y

Instructions (3)

7. Address and pointer assignment instructions:
   - $x = \&y$ (set the r-value of x to be the l-value of y)
   - $x =* y$ (set the r-value of x to be the content stored at the location pointed to by y; y is a pointer whose r-value is a location)
   - $* x = y$ (set the r-value of the object pointed to by x to the r-value of y)

A variable has l-value and r-value:

- **L-value (location)** refers to the memory location, which identifies an object.
- **R-value (content)** refers to data value stored at some address in memory.

Example

- Source code: `do i = i + 1; while (a[i] < v);`

```
L:  t1 = i + 1
    i = t1
    t2 = i * 8
    t3 = a [ t2 ]
    if t3 < v goto L
```

(a) Symbolic labels.

```
100:  t1 = i + 1
101:  i = t1
102:  t2 = i * 8
103:  t3 = a [ t2 ]
104:  if t3 < v goto 100
```

(b) Position numbers.

Assuming each array element takes 8 units of space

Representation of Instructions

- In a compiler, three-address instructions can be implemented as objects/records with fields for the operator and the operands
- Three typical representations:
  - Quadruples (四元式表示方法)
  - Triples (三元式表示方法)
  - Indirect triples (间接三元式表示方法)

Quadruples (四元式)

- A *quadruple* has four fields
  - General form: *op* arg_1 arg_2 *result*
  - *op* contains an internal code for the operator
  - arg_1, arg_2, *result* are addresses (operands)
  - Example: $x = y + z$ ➔ $+ \quad y \quad z \quad x$

- Some exceptions:
  - Unary operators like $x = minus\ y$ or $x = y$ do not use arg_2
  - *param* operators use neither arg_2 nor *result*
  - Conditional/unconditional jumps put the target label in *result*

Quadruples Example

- Assignment statement: $a = b * -c + b * -c$

```
t1 = minus c
t2 = b * t1
t3 = minus c
t4 = b * t3
t5 = t2 + t4
 a = t5
```

(a) Three-address code

	op	arg_1	arg_2	*result*
0	minus	c		t_1
1	*	b	t_1	t_2
2	minus	c		t_3
3	*	b	t_3	t_4
4	+	t_2	t_4	t_5
5	=	t_5		a
		...		

Temporaries

(b) Quadruples

The result field is used primarily for temporary names
Temporary names waste space (symbol table entries)

Triples (三元式)

- A *triple* has only three fields: *op*, arg_1, arg_2
- We refer to the result of an operation *x op y* by its position without generating temporary names (an optimization over quadruples)

```
t1 = minus c
t2 = b * t1
t3 = minus c
t4 = b * t3
t5 = t2 + t4
 a = t5
```

Three-address code

	op	arg_1	arg_2	result
0	minus	c		t_1
1	*	b	t_1	t_2
2	minus	c		t_3
3	*	b	t_3	t_4
4	+	t_2	t_4	t_5
5	=	t_5		a
		...		

Quadruples

	op	arg_1	arg_2
0	minus	c	
1	*	b	(0)
2	minus	c	
3	*	b	(2)
4	+	(1)	(3)
5	=	a	(4)
		...	

Triples

Quadruples vs. Triples

- In optimizing compilers, instructions are often moved around

	op	arg_1	arg_2	result
0	minus	c		t_1
1	*	b	t_1	t_2
2	minus	c		t_3
3	*	b	t_3	t_4
4	+	t_2	t_4	t_5
5	=	t_5		a
	. . .			

Swap 1 and 2

	op	arg_1	arg_2	result
0	minus	c		t_1
1	minus	c		t_3
2	*	b	t_1	t_2
3	*	b	t_3	t_4
4	+	t_2	t_4	t_5
5	=	t_5		a
	. . .			

Quadruples' advantage

The instructions that use t_1 and t_3 are not affected

Quadruples vs. Triples

- In optimizing compilers, instructions are often moved around

	op	arg_1	arg_2
0	minus	c	
1	*	b	(0)
2	minus	c	
3	*	b	(2)
4	+	(1)	(3)
5	=	a	(4)
		...	

Swap 1 and 2

	op	arg_1	arg_2
0	minus	c	
1	minus	c	
2	*	b	(0)
3	*	b	(2)
4	+	(1)	(3)
5	=	a	(4)
		...	

Are they still correct after swapping?

Triples' problem

The instructions now refer to wrong results; The positions need to be updated.

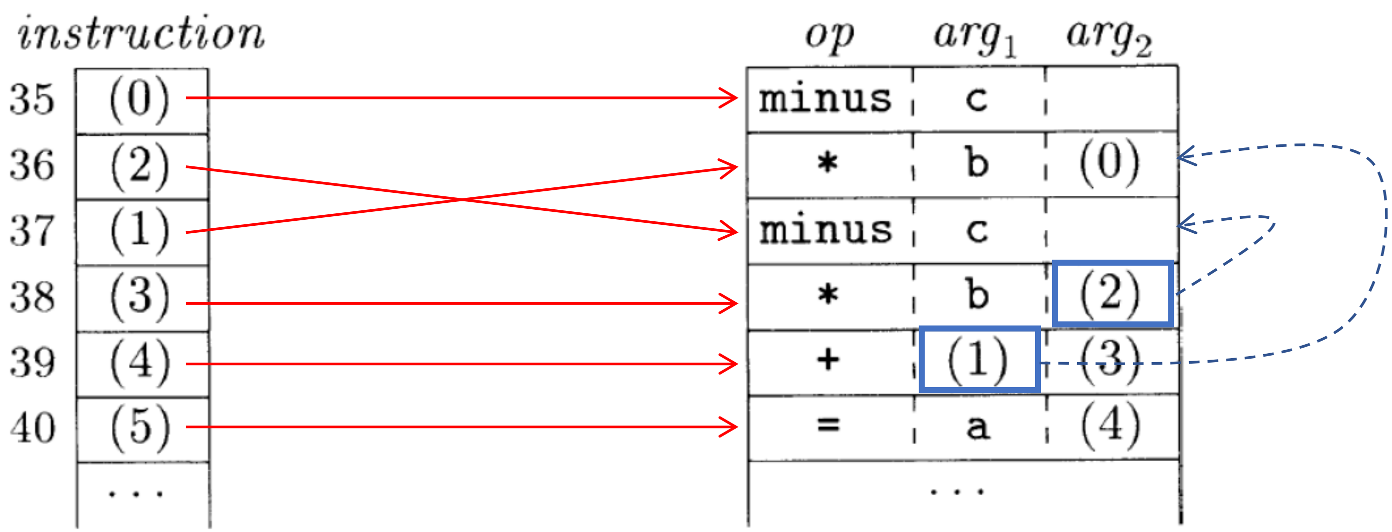
Indirect Triples (间接三元式)

- *Indirect triples* consist of a list of pointers to triples

	instruction	Points to	*op*	arg_1	arg_2
35	(0)	→	`minus`	`c`	
36	(1)	→	`*`	`b`	(0)
37	(2)	→	`minus`	`c`	
38	(3)	→	`*`	`b`	(2)
39	(4)	→	`+`	(1)	(3)
40	(5)	→	`=`	`a`	(4)
	...			...	

Indirect Triples (间接三元式)

- An optimization can move an instruction by reordering the *instruction* list



Swapping pointers!

The triples are not affected.

Static Single-Assignment Form

- Static single-assignment form (SSA, 静态单赋值形式) is an IR that facilitates certain code optimizations
- In SSA, each name receives a single assignment

$$
\begin{array}{l}
\boxed{p} = a + b \\
q = p - c \\
\boxed{p} = q * d \\
p = e - p \\
q = p + q
\end{array}
\qquad\qquad
\begin{array}{l}
\boxed{p_1} = a + b \\
q_1 = p_1 - c \\
\boxed{p_2} = q_1 * d \\
p_3 = e - p_2 \\
q_2 = p_3 + q_1
\end{array}
$$

(a) Three-address code. (b) Static single-assignment form.

Static Single-Assignment Form

- The same variable may be defined in two control-flow paths

```
if ( flag ) x = -1; else x = 1;
y = x * a;
```

X_1 X_2

Which name should we use in y = x * a?

Static Single-Assignment Form

- The same variable may be defined in two control-flow paths

```
if ( flag ) x = -1; else x = 1;
y = x * a;
```

- SSA uses a notational convention called ϕ-function to combine the two definitions of x

$$\texttt{if (flag) } x_1 \texttt{ = -1; else } x_2 \texttt{ = 1;}$$
$$x_3 = \phi(x_1, x_2); \quad \text{// x1 if control flow passes through the true path; otherwise x2}$$
$$\texttt{y = } x_3 \texttt{ * a;}$$

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Types and Type Checking

- *Data type* or simply *type* tells a compiler or interpreter how the programmers intend to use the data

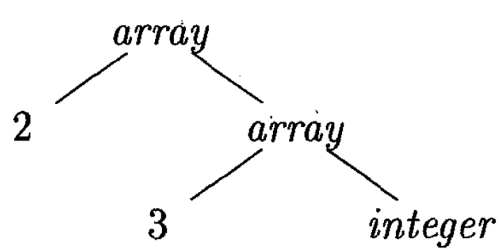
- The usefulness of type information
  - Find faults in the source code
  - Determine the storage needed for a name at runtime
  - Calculate the address of an array element
  - Insert type conversions
  - Choose the right version of some arithmetic operator (e.g., `fadd`, `iadd`)

- *Type checking* (类型检查) uses logical rules to make sure that the types of the operands match the type expectation by an operator

Type Expressions (类型表达式)

- Types have structure, which can be represented by *type expressions*
  - A type expression is either a basic type, or
  - Formed by applying a *type constructor* (类型构造算子) to a type expression

- $array(2, array(3, integer))$ is the type expression for `int[2][3]`
  - *array* is a type constructor with two arguments: a number, a type expression



The Definition of Type Expression

- A basic type is a type expression
  - $boolean$, $char$, $integer$, $float$, and $void$, $\ldots$
- A type name (e.g., name of a class) is a type expression
- A type expression can be formed
  - By applying the $array$ type constructor to a number and a type expression
  - By applying the $record$ type constructor to the field names and their types
  - By applying the $\rightarrow$ type constructor for function types

- If s and t are type expressions, then their Cartesian product $s \times t$ is a type expression (this is introduced for completeness, can be used to represent a list of types such as function parameters)
- Type expressions may contain type variables (e.g., those generated by compilers) whose values are type expressions

Type Equivalence

- Type checking rules usually have the following form

If two type expressions are equivalent
then return a given type
else return **type_error**

Code under analysis:
`a + b`

- The key is to define when two type expressions are equivalent
  - The main difficulty arises from the fact that most modern languages allow the naming of user-defined types
    - In C/C++, type naming is achieved by the `typedef` statement

Name Equivalence (名等价)

- Treat named types as basic types; names in type expressions are not replaced by the exact type expressions they define
- Two type expressions are name equivalent if and only if they are identical (represented by the same syntax tree, with the same labels)

```
typedef struct {
    int data[100];
    int count;
} Stack;
```

```
typedef struct {
    int data[100];
    int count;
} Set;
```

```
Code under analysis:

Stack x, y;

Set r, s;

x = y;   ✓

r = s;   ✓

x = r;   ✗
```

http://web.eecs.utk.edu/~bvanderz/teaching/cs365Sp14/notes/types.html

Structural Equivalence (结构等价)

- For named types, replace the names by the type expressions and recursively check the substituted trees

```
typedef struct {
    int data[100];
    int count;
} Stack;
```

```
typedef struct {
    int data[100];
    int count;
} Set;
```

```
Code under analysis:

Stack x, y;

Set r, s;

x = y;

r = s;

x = r;
```

Declarations (变量声明)

- The grammar below deals with basic, array, and record types
  - Nonterminal D generates a sequence of declarations
  - T generates basic, array, or record types
  - A record type is a sequence of declarations for the fields of the record, surrounded by curly braces
  - B generates one of the basic types: `int` and `float`
  - C generates sequences of one or more integers, each surrounded by brackets

$$
\begin{array}{lcl}
D & \rightarrow & T \ \textbf{id} \ ; \ D \ | \ \epsilon \\
T & \rightarrow & B \ C \ | \ \textbf{record} \ '\{' \ D \ '\}' \\
B & \rightarrow & \textbf{int} \ | \ \textbf{float} \\
C & \rightarrow & \epsilon \ | \ [\ \textbf{num} \ ] \ C
\end{array}
$$

Storage Layout for Local Names (局部变量的存储布局)

- From the type of a name, we can decide the amount of memory needed for the name at run time
  - The *width* (宽度) of a type: # memory units needed for an object of the type
  - For data of varying lengths, such as strings, or whose size cannot be determined until run time, such as dynamic arrays, we only reserve a fixed amount of memory for a pointer to the data
- For local names of a function, we always assign contiguous bytes*
  - For each such name, at compile time, we can compute a relative address
  - Type information and relative addresses are stored in symbol table

* This follows the principle of proximity and is mainly for performance considerations.

An SDT for Computing Types and Their Widths

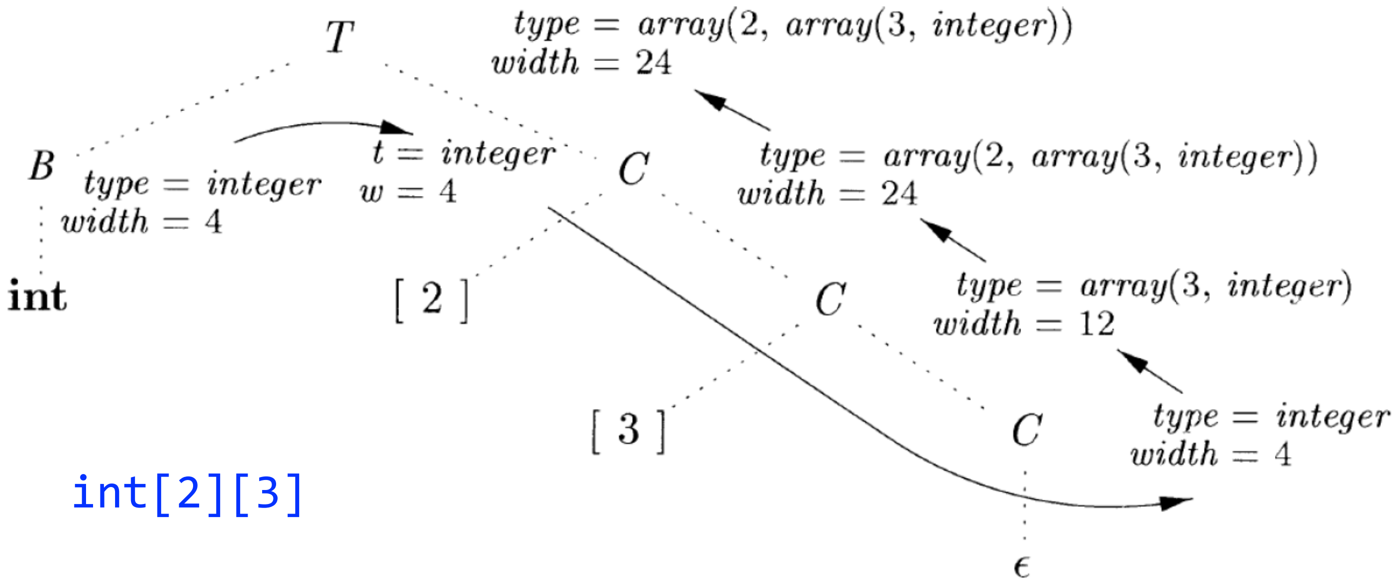
- Synthesized attributes: *type, width*
- Global variables t and w pass type and width information from a B node in a parse tree to the node for the production $C \to \epsilon$
  - In an SDD, t and w would be C's inherited attributes (the SDD is L-attributed)*

$T \to B$	$\{ t = B.type;\ w = B.width;\ \}$
$\quad\quad\ C$	$\{ T.type = C.type;\ T.width = C.width;\ \}$
$B \to$ **int**	$\{\ B.type = integer;\ B.width = 4;\ \}$
$B \to$ **float**	$\{\ B.type = float;\ B.width = 8;\ \}$
$C \to \epsilon$	$\{\ \boxed{C.type = t;}\ \boxed{C.width = w;}\ \}$
$C \to$ [**num**] C_1	$\{\ C.type = array(\mathbf{num}.value,\ C_1.type);\ C.width = \mathbf{num}.value \times C_1.width;\ \}$

This SDT can be implemented during recursive-descent parsing

Translation Process Example

- **Recall the translation during recursive-descent parsing**
  - Use the arguments of function *A*() to pass nonterminal *A*'s inherited attributes*
  - Evaluate and Return the synthesized attributes of *A* when the *A*() completes

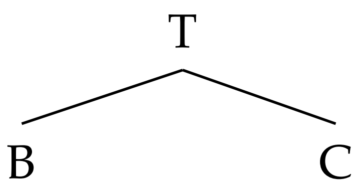


* In our example, we use global variables *t* and *w*

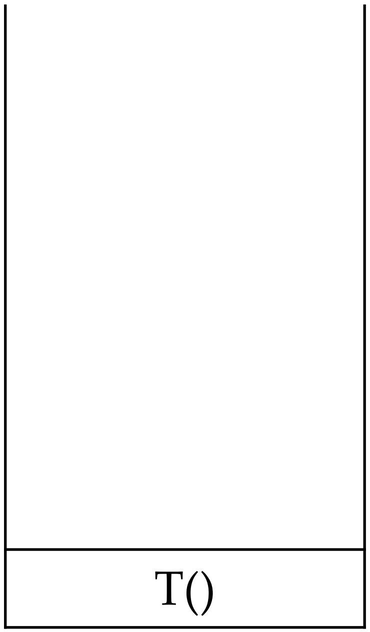
Translation Process Example

$$
\begin{array}{lcl}
T & \rightarrow & B\ C \mid \textbf{record}\ '\{'\ D\ '\}' \\
B & \rightarrow & \textbf{int} \mid \textbf{float} \\
C & \rightarrow & \epsilon \mid [\ \textbf{num}\ ]\ C
\end{array}
$$

Input string: `int[2][3]`



Step 1: Rewrite T using $T \rightarrow BC$

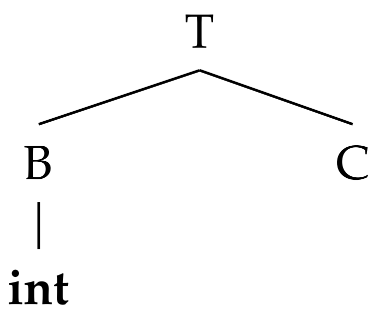


Call stack

Translation Process Example

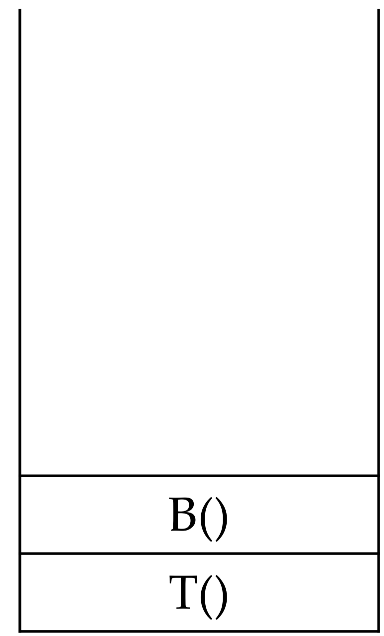
$$
\begin{array}{lcl}
T & \rightarrow & B\ C \mid \textbf{record}\ '\{'\ D\ '\}' \\
B & \rightarrow & \textbf{int} \mid \textbf{float} \\
C & \rightarrow & \epsilon \mid [\ \textbf{num}\ ]\ C
\end{array}
$$

Input string: `int[2][3]`



Step 2:

- Rewrite B using $B \rightarrow$ **int**
- Match input

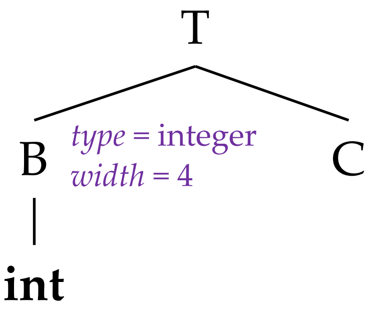


Call stack

Translation Process Example

$$
\begin{aligned}
T &\rightarrow B\ C \mid \textbf{record}\ '\{'\ D\ '\}' \\
B &\rightarrow \textbf{int} \mid \textbf{float} \\
C &\rightarrow \epsilon \mid [\ \textbf{num}\ ]\ C
\end{aligned}
$$

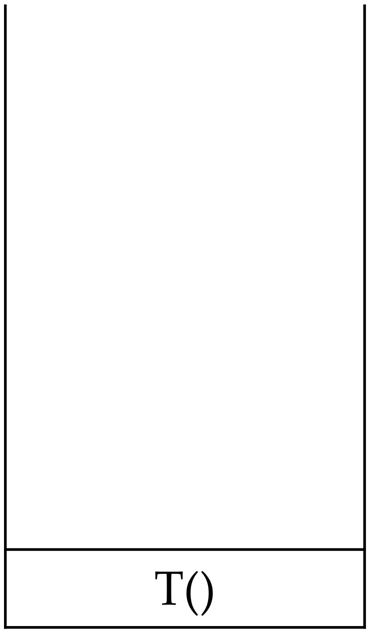
Input string: `int[2][3]`



Step 3:

- B() returns
- Execute semantic action

$B \rightarrow \textbf{int} \qquad \{\ B.type = integer;\ B.width = 4;\ \}$



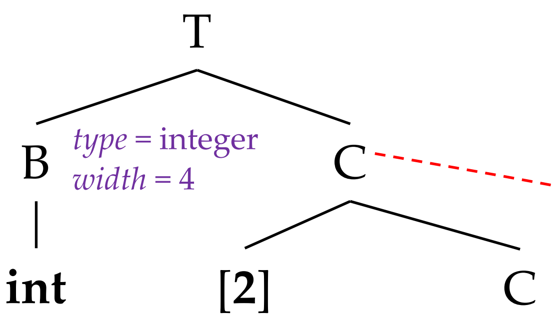
Call stack

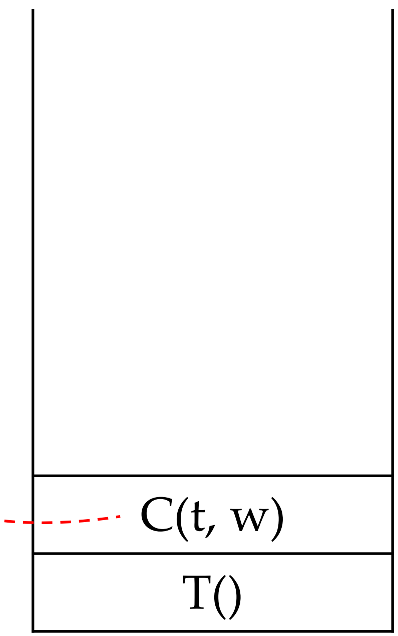
Translation Process Example

$$\begin{array}{lcl} T & \rightarrow & B\ C \mid \textbf{record}\ '\{'\ D\ '\}' \\ B & \rightarrow & \textbf{int} \mid \textbf{float} \\ C & \rightarrow & \epsilon \mid [\ \textbf{num}\ ]\ C \end{array}$$

Input string: `int[2][3]`

t = integer
w = 4





Call stack

Step 4:

- Execute semantic action
- Rewrite C using $C \rightarrow [\textbf{num}]C$
- Match input

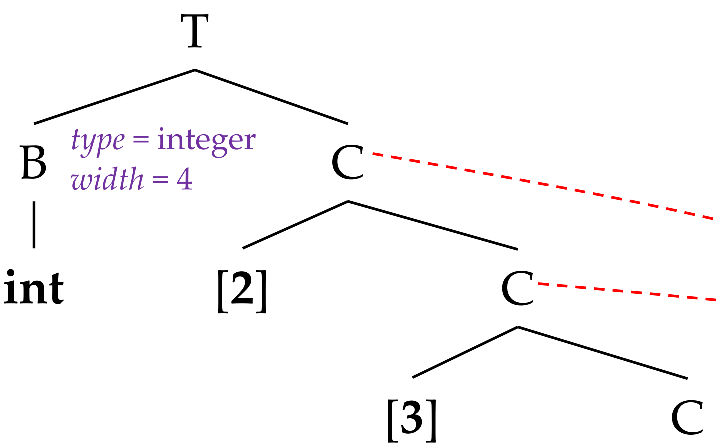
$$\begin{array}{lcll} T & \rightarrow & B & \boxed{\{\ t = B.type;\ w = B.width;\ \}} \\ & & C & \{T.type = C.type;\ T.width = C.width;\ \} \end{array}$$

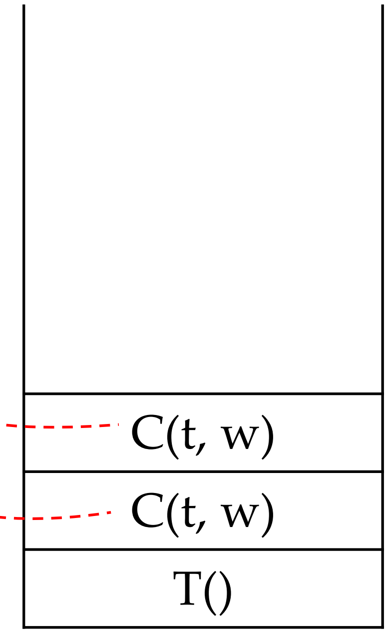
Translation Process Example

$$
\begin{array}{lcl}
T & \rightarrow & B\ C \mid \textbf{record}\ '\{'\ D\ '\}' \\
B & \rightarrow & \textbf{int} \mid \textbf{float} \\
C & \rightarrow & \epsilon \mid [\ \textbf{num}\ ]\ C
\end{array}
$$

Input string: `int[2][3]`

t = integer
w = 4





Call stack

Step 5:

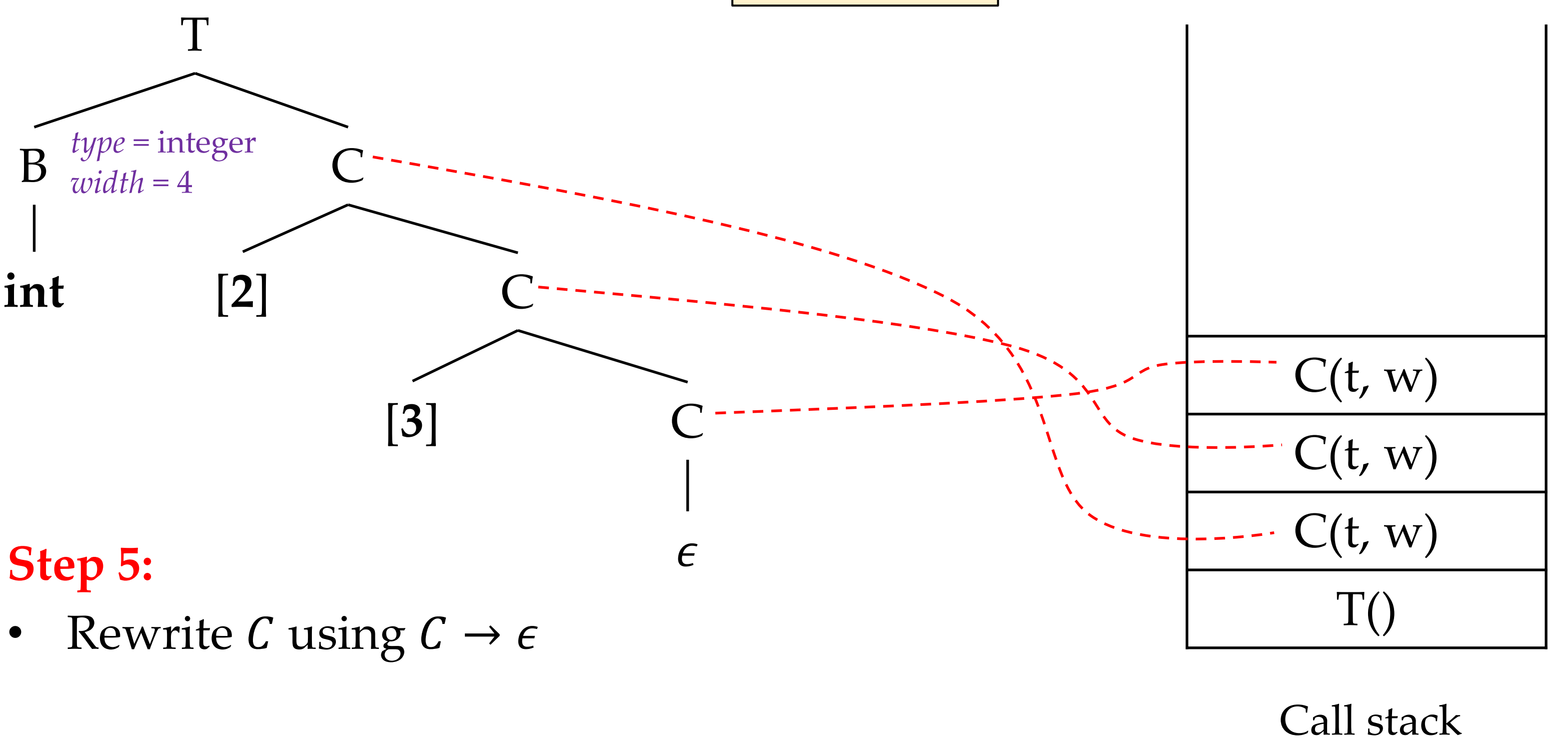
- Rewrite C using $C \rightarrow [\textbf{num}]C$
- Match input

Translation Process Example

$$\begin{array}{lll} T & \rightarrow & B\ C \mid \textbf{record}\ '\{'\ D\ '\}' \\ B & \rightarrow & \textbf{int} \mid \textbf{float} \\ C & \rightarrow & \epsilon \mid [\ \textbf{num}\ ]\ C \end{array}$$

Input string: `int[2][3]`

t = integer
$w = 4$



Step 5:

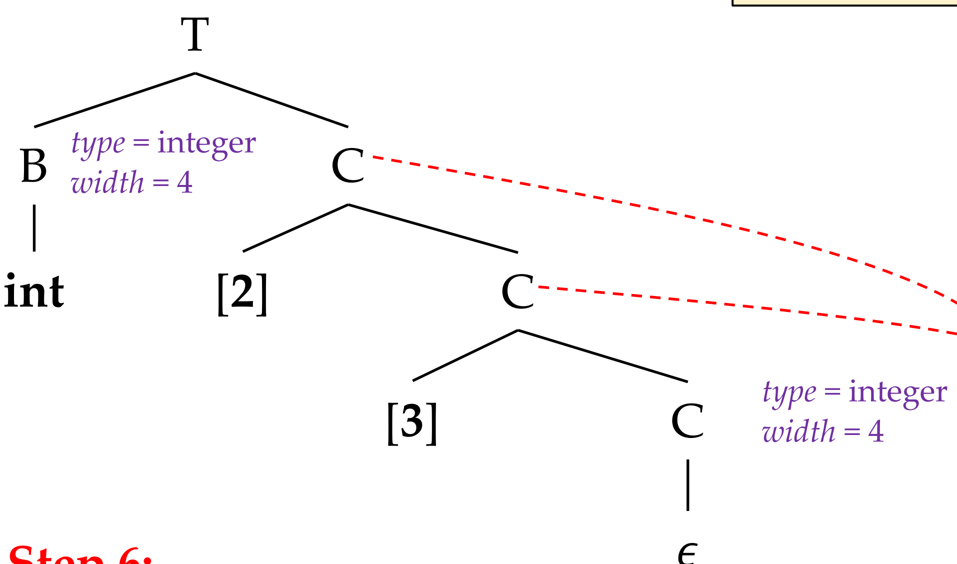
- Rewrite C using $C \rightarrow \epsilon$

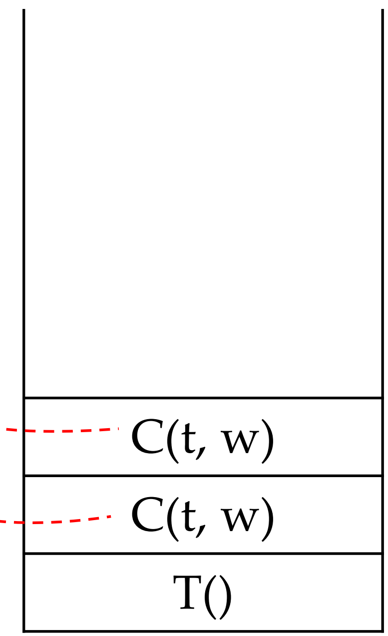
Translation Process Example

$$
\begin{aligned}
T &\rightarrow B\ C \mid \textbf{record}\ '\{'\ D\ '\}' \\
B &\rightarrow \textbf{int} \mid \textbf{float} \\
C &\rightarrow \epsilon \mid [\ \textbf{num}\ ]\ C
\end{aligned}
$$

Input string: `int[2][3]`

t = integer
w = 4





Call stack

Step 6:

- C() returns
- Execute semantic action

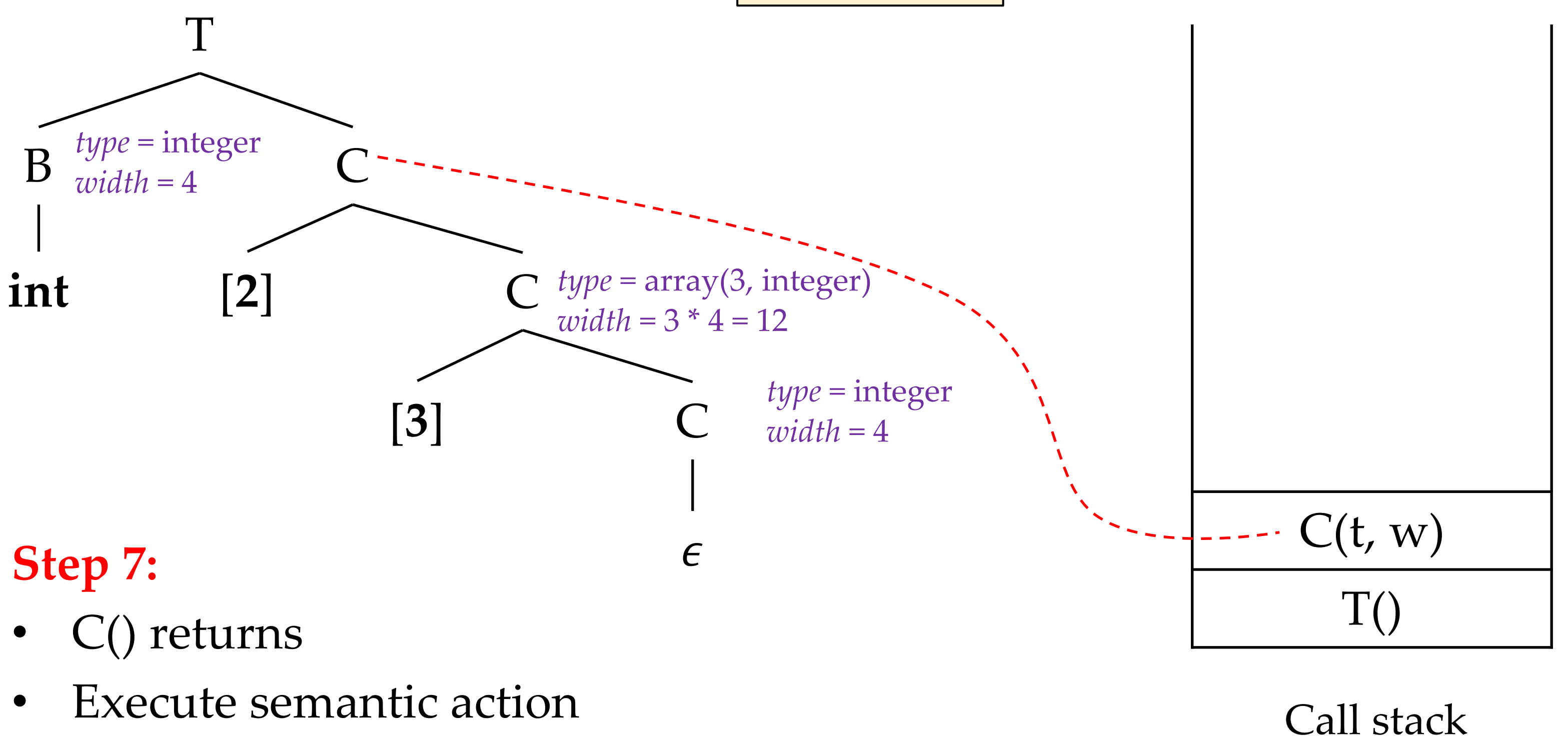
$C \rightarrow \epsilon$ $\quad$ $\boxed{\{\ C.type = t;\ C.width = w;\ \}}$

Translation Process Example

$$
\begin{array}{lcl}
T & \rightarrow & B\ C \mid \textbf{record}\ '\{'\ D\ '\}' \\
B & \rightarrow & \textbf{int} \mid \textbf{float} \\
C & \rightarrow & \epsilon \mid [\ \textbf{num}\ ]\ C
\end{array}
$$

Input string: `int[2][3]`

t = integer
w = 4



Step 7:

- C() returns
- Execute semantic action

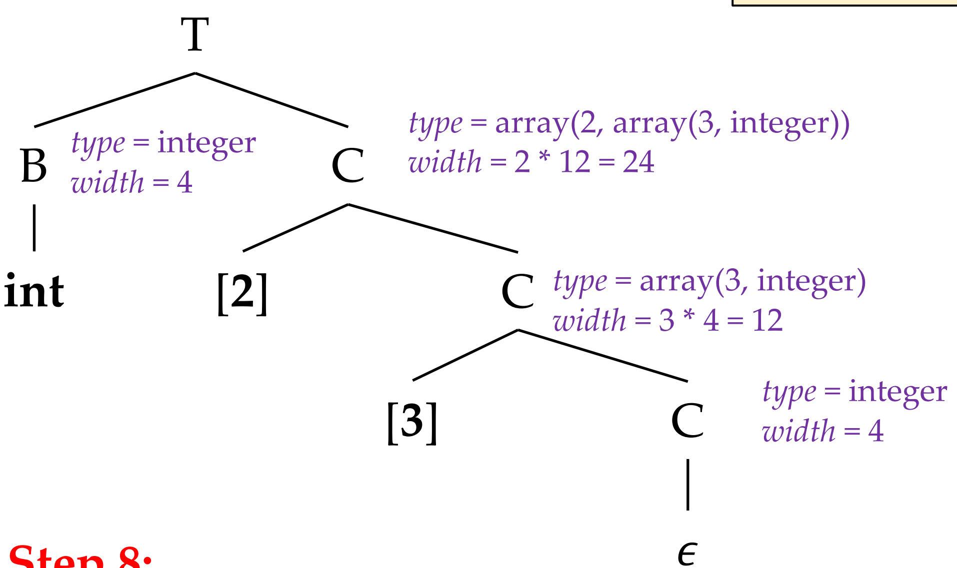
$$C \rightarrow [\ \textbf{num}\ ]\ C_1 \quad \boxed{\begin{array}{l}\{\ \ C.type = \ array(\textbf{num}.value,\ C_1.type); \\ \ \ C.width = \textbf{num}.value \times C_1.width;\ \}\end{array}}$$

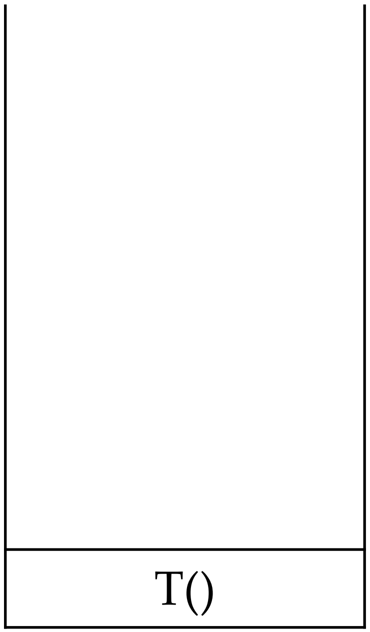
Translation Process Example

$$
\begin{array}{lcl}
T & \rightarrow & B\ C \mid \textbf{record}\ '\{'\ D\ '\}' \\
B & \rightarrow & \textbf{int} \mid \textbf{float} \\
C & \rightarrow & \epsilon \mid [\ \textbf{num}\ ]\ C
\end{array}
$$

Input string: `int[2][3]`

t = integer
w = 4





Call stack

Step 8:

- C() returns
- Execute semantic action

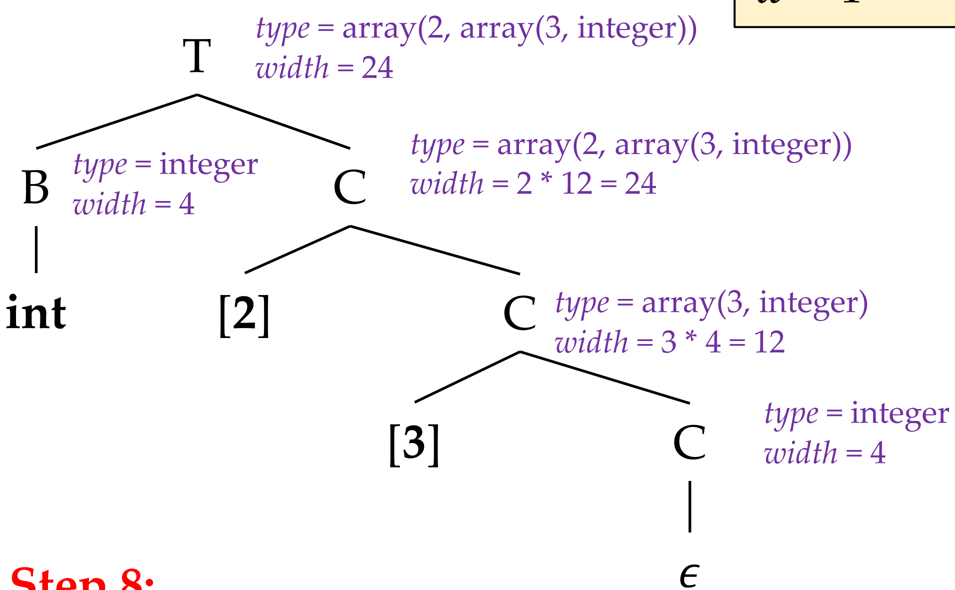
$$C \rightarrow [\ \textbf{num}\ ]\ C_1 \qquad \boxed{\{\ C.type = array(\textbf{num}.value,\ C_1.type); \quad C.width = \textbf{num}.value \times C_1.width;\ \}}$$

Translation Process Example

$$
\begin{array}{lcl}
T & \rightarrow & B\ C \mid \textbf{record}\ '\{'\ D\ '\}' \\
B & \rightarrow & \textbf{int} \mid \textbf{float} \\
C & \rightarrow & \epsilon \mid [\ \textbf{num}\ ]\ C
\end{array}
$$

Input string: `int[2][3]`

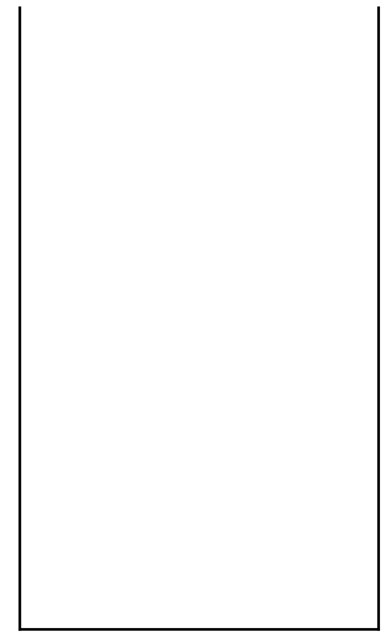
t = integer
w = 4



Step 8:

- T() returns
- Execute semantic action

$$
\begin{array}{lcll}
T & \rightarrow & B & \{\ t = B.type;\ w = B.width;\ \} \\
 & & C & \boxed{\{T.type = C.type;\ T.width = C.width;\}}
\end{array}
$$



Call stack

Sequences of Declarations

- When dealing with a procedure, local variables should be put in a separate symbol table; their declarations can be processed as a group
  - Name, type, and relative address of each variable should be stored
- The translation scheme below handles a sequence of declarations
  - $offset$: the next available relative address; top: the current symbol table

P	$\rightarrow$		$\{\ offset = 0;\ \}$
		D	
D	$\rightarrow$	T **id** ;	$\{\ top.put(\mathbf{id}.lexeme,\ T.type,\ offset);$ $offset = offset + T.width;\ \}$
		D_1	
D	$\rightarrow$	ϵ	

Computing relative addresses of declared names

Fields in Records and Classes*

- Two assumptions:
  - The field names within a record must be distinct
  - The offset for a field name is relative to the data area (数据区) for that record
- For convenience, we use a symbol table for each record type
  - Store both type and relative address of fields
- A record type has the form $record(t)$
  - $record$ is the type constructor
  - t is a symbol table object, holding info about the fields of this record type

* Self-study materials

Fields in Records and Classes

$$
\begin{array}{llll}
T & \rightarrow & \textbf{record}\ '\{' & \{\ Env.push(top);\ top = \textbf{new}\ Env(); \\
 & & & \quad Stack.push(offset);\ offset = 0;\ \} \\
 & & D\ '\}' & \{\ T.type = record(top);\ T.width = offset; \\
 & & & \quad top = Env.pop();\ offset = Stack.pop();\ \}
\end{array}
$$

- The class Env implements symbol tables
- $Env.push(top)$ and $Stack.push(offset)$ save the current symbol table and offset; later, they will be popped to continue with other translation
- The translation scheme can be adapted to deal with classes

Outline

- Intermediate Representation
- Type and Declarations
- Type Checking
- Translation of Expressions
- Control Flow
- Backpatching

Type Checking

- To do type checking, a compiler needs to assign a type expression to each component of the source program

- The compiler then determines whether the type expressions conform to a collection of logical rules (i.e., the *type system*)
  - A *sound* type system allows us to determine statically that type errors cannot occur at run time

- A language is *strongly typed* if the compiler guarantees that the programs it accepts will run without type errors (sound type system)
  - **Strongly typed:** Java (`double a;` ~~`int b = a`~~`; //cannot compile`)
  - **Weakly typed:** C/C++ (`double a; int b = a; //implicit conversion`)

Rules for Type Checking

- **Type synthesis (类型合成)**
  - Build up the type of an expression from the types of subexpressions
    - **Typical form:** **if** f has type $s \rightarrow t$ **and** x has type s, **then** expression $f(x)$ has type t
    - **Example:** $f(x) = -x$ (can be generalized to multi-argument cases)

- **Type inference (类型推导)**
  - Determine the type of a language construct from <u>the way it is used</u>
    - **Typical form:** **if** $f(x)$ is an expression, **then:** as f has type $\alpha \rightarrow \beta$ (α, β represent two types), x has type α
    - **Example:** let *null* be a function that tests whether a list is empty, then from the usage $null(x)$, we can tell that x must be a list

Type Conversions

- Consider an expression $x * i$, where x is a float and i is an integer
  - The representation (the way of organizing 0/1 bits) of integers and floating-point numbers is different
  - Different machine instructions are used for operations on integers an floats
  - Convert integers to floats: $\mathrm{t}_1 = \text{(float) i}$ $\quad \mathrm{t}_2 = \text{x fmul } \mathrm{t}_1$

- Type conversion SDT for a simple case (using type synthesis)
  - $E \rightarrow E_1 + E_2$

$\{ \quad$ **if** $(E_1.type = integer$ **and** $E_2.type = integer)\ E.type = integer;$

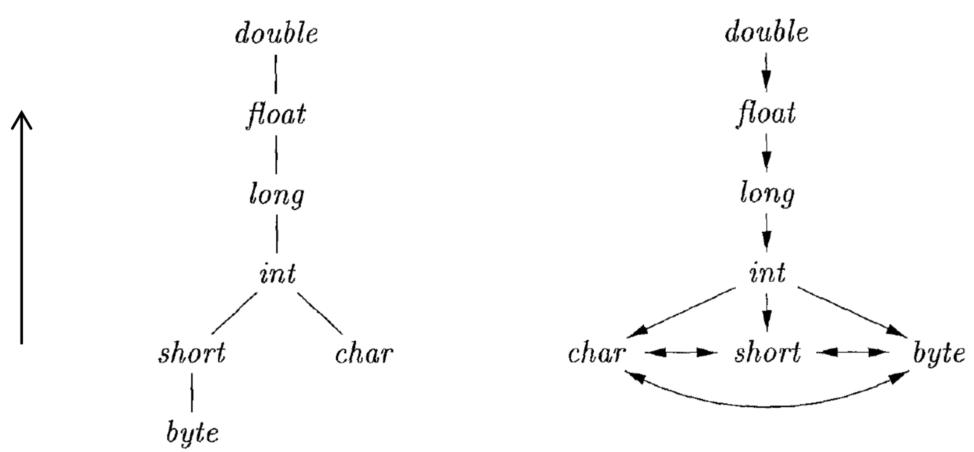
$\quad$ **else if** $(E_1.type = float$ **and** $E_2.type = integer)\ E.type = float;$

$\quad \ldots$

$\}$

Widening and Narrowing (1)

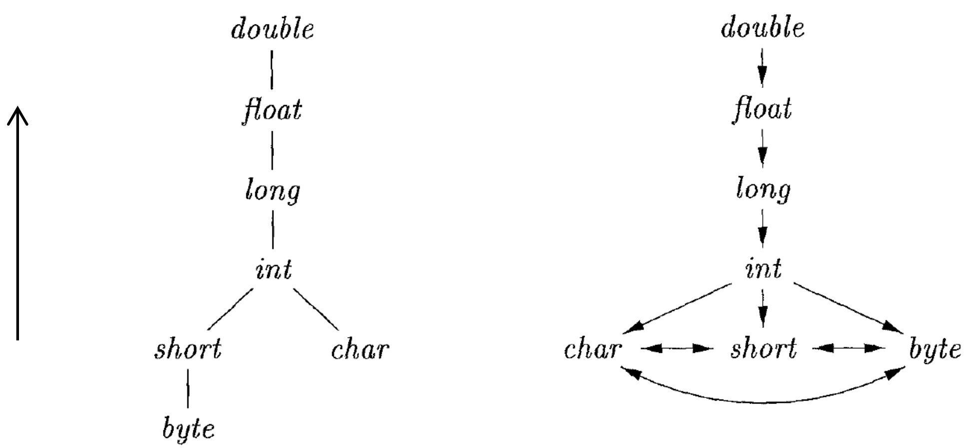
- Type conversion rules vary from language to language
- Java distinguishes between *widening* conversions (类型拓宽) and *narrowing* conversions (类型窄化)



(a) Widening conversions (b) Narrowing conversions

Widening and Narrowing (2)

- *Widening* conversions preserve information and can be done automatically by the compiler (*implicit* type conversions, or *coercions*)
- *Narrowing* conversions lose information and require programmers to write code to cause the conversion (*explicit* type conversions, or *casts*)



(a) Widening conversions (b) Narrowing conversions

SDT for Type Conversion

- $\max(t_1, t_2)$ takes two types t_1 and t_2 and returns the maximum (or least upper bound) of the two types in the widening hierarchy

- $widen(a, t, w)$ generates type conversions if needed to widen an address a of type t into a value of type w

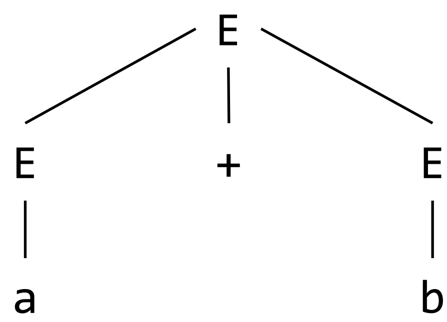
```
Addr widen(Addr a, Type t, Type w)
        if ( t = w ) return a;
        else if ( t = integer and w = float ) {
                temp = new Temp();
                gen(temp '=' '(float)' a);
                return temp;
        }
        else error;
}
```

```
E → E1 + E2   { E.type = max(E1.type, E2.type);
                a1 = widen(E1.addr, E1.type, E.type);
                a2 = widen(E2.addr, E2.type, E.type);
                E.addr = new Temp ();
                gen(E.addr '=' a1 '+' a2); }
```

Example

- a + b (suppose a is of *int* type and b is of *float* type)

$$
\begin{array}{l}
\mathit{Addr}\ \mathit{widen}(\mathit{Addr}\ a,\ \mathit{Type}\ t,\ \mathit{Type}\ w)\\
\qquad \textbf{if}\ (\ t = w\ )\ \textbf{return}\ a; \quad {\color{red}3}\\
\qquad \textbf{else if}\ (\ t = \mathit{integer}\ \textbf{and}\ w = \mathit{float}\ )\ \{\\
\qquad\qquad \mathit{temp}\ =\ \textbf{new}\ \mathit{Temp}();\\
\qquad\qquad \mathit{gen}(\mathit{temp}\ '='\ '(\textbf{float})'\ a); \quad {\color{red}2}\\
\qquad\qquad \textbf{return}\ \mathit{temp};\\
\qquad \}\\
\qquad \textbf{else}\ \ \textbf{error};\\
\}
\end{array}
$$



Generated code:

```
temp = (float) a        2
temp2 = temp + b        5
```

$$
\begin{array}{lll}
E \rightarrow E_1 + E_2 & \{\ E.\mathit{type} = \mathit{max}(E_1.\mathit{type}, E_2.\mathit{type});\\
& \quad a_1 = \mathit{widen}(E_1.\mathit{addr}, E_1.\mathit{type}, E.\mathit{type});\\
& \quad a_2 = \mathit{widen}(E_2.\mathit{addr}, E_2.\mathit{type}, E.\mathit{type});\\
& \quad E.\mathit{addr} = \textbf{new}\ \mathit{Temp}\ ();\\
& \quad \mathit{gen}(E.\mathit{addr}\ '='\ a_1\ '+'\ a_2);\ \}
\end{array}
$$

$E.type = max($`int`, `float`$) =$ `float` 1

$a_1 = widen($`a`, `int`, `float`$) =$ `temp` 2

$a_2 = widen($`b`, `float`, `float`$) =$ `b` 3

$E.addr =$ `new` $Temp() =$ `temp2` 4

5

Outline

- Intermediate Representation
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Expressions and Arrays

- An expression with more than one operator: $a + b * c$
  - Translate into multiple instructions with at most one operator per instruction

$$a + b * c \Rightarrow \begin{array}{c} t_1 = b * c \\ t_2 = a + t_1 \end{array}$$

- An array reference $A[i][j]$ will expand into a sequence of three-address instructions that calculate an address for the reference

SDD for Expressions (1)

PRODUCTION	SEMANTIC RULES
$S \rightarrow \textbf{id} = E\ ;$	$S.code = E.code \ \Vert$ $gen(top.get(\textbf{id}.lexeme)\ '='\ E.addr)$
$E \rightarrow E_1 + E_2$	$E.addr = \textbf{new}\ Temp\,()$ $E.code = E_1.code \ \Vert\ E_2.code \ \Vert$ $gen(E.addr\ '='\ E_1.addr\ '+'\ E_2.addr)$
$\mid\ -\ E_1$	$E.addr = \textbf{new}\ Temp\,()$ $E.code = E_1.code \ \Vert$ $gen(E.addr\ '='\ '\textbf{minus}'\ E_1.addr)$
$\mid\ (\ E_1\ )$	$E.addr = E_1.addr$ $E.code = E_1.code$
$\mid\ \textbf{id}$	$E.addr = top.get(\textbf{id}.lexeme)$ $E.code =\ ''$

$S.code$ and $E.code$ denote three-address code

$E.addr$ denotes the address that will hold the value of E

top denotes the current symbol table; *get* returns the address of **id** (a variable)

gen generates three-address instructions

All attributes are synthesized. This S-attributed SDD can be implemented during bottom-up parsing.

SDD for Expressions (2)

PRODUCTION	SEMANTIC RULES
$S \rightarrow \textbf{id} = E\ ;$	$S.code = E.code \parallel gen(top.get(\textbf{id}.lexeme)\ '='\ E.addr)$
$E \rightarrow E_1 + E_2$	$E.addr = \textbf{new}\ Temp\,()$ $E.code = E_1.code \parallel E_2.code \parallel gen(E.addr\ '='\ E_1.addr\ '+'\ E_2.addr)$
$\mid\ -E_1$	$\boxed{E.addr = \textbf{new}\ Temp\,()}$ → Temporary name generated by compiler $E.code = E_1.code \parallel gen(E.addr\ '='\ '\textbf{minus}'\ E_1.addr)$
$\mid\ (\ E_1\ )$	$E.addr = E_1.addr$ $E.code = E_1.code$
$\mid\ \textbf{id}$	$\boxed{E.addr = top.get(\textbf{id}.lexeme)}$ → Check the symbol-table entry for **id** and save its address in *E.addr* $E.code = ''$

SDD for Expressions (3)

PRODUCTION	SEMANTIC RULES
$S \rightarrow \textbf{id} = E\ ;$	$\boxed{S.code = E.code \ \Vert \ gen(top.get(\textbf{id}.lexeme)\ '='\ E.addr)}$
$E \rightarrow E_1 + E_2$	$E.addr = \textbf{new}\ Temp\,()$ $\boxed{E.code = E_1.code \ \Vert \ E_2.code \ \Vert \ gen(E.addr\ '='\ E_1.addr\ '+'\ E_2.addr)}$
$\mid\ -\ E_1$	$E.addr = \textbf{new}\ Temp\,()$ $\boxed{E.code = E_1.code \ \Vert \ gen(E.addr\ '='\ '\textbf{minus}'\ E_1.addr)}$
$\mid\ (\ E_1\ )$	$E.addr = E_1.addr$ $E.code = E_1.code$
$\mid\ \textbf{id}$	$E.addr = top.get(\textbf{id}.lexeme)$ $E.code = ''$

- Generate instructions when seeing operations.
- Then concatenate instructions.

Inefficiency in the SDD

PRODUCTION	SEMANTIC RULES
$S \rightarrow \mathbf{id} = E\ ;$	$\boxed{S.code} = \boxed{E.code} \,\|\|$ $gen(top.get(\mathbf{id}.lexeme)\ '='\ E.addr)$
$E \rightarrow E_1 + E_2$	$E.addr = \mathbf{new}\ Temp\,()$ $\boxed{E.code} = \boxed{E_1.code} \,\|\|\, \boxed{E_2.code} \,\|\|$ $gen(E.addr\ '='\ E_1.addr\ '+'\ E_2.addr)$
$\quad\mid\ - E_1$	$E.addr = \mathbf{new}\ Temp\,()$ $\boxed{E.code} = \boxed{E_1.code} \,\|\|$ $gen(E.addr\ '='\ '\mathbf{minus}'\ E_1.addr)$
$\quad\mid\ (\ E_1\ )$	$E.addr = E_1.addr$ $\boxed{E.code} = \boxed{E_1.code}$
$\quad\mid\ \mathbf{id}$	$E.addr = top.get(\mathbf{id}.lexeme)$ $E.code = ''$

Code attributes can be very long strings (as the expressions can be arbitrarily complex)

Redundant parts waste memory!

Incremental Translation Scheme

- In the SDT below, *gen* not only generates a three-address instruction, but also appends it to the sequence of instructions generated so far
  - In comparison, in the previous SDD, the *code* attribute can be long strings after concatenations

$$
\begin{array}{lll}
S & \rightarrow \ \textbf{id} = E \ ; & \{ \ gen(\ top.get(\textbf{id}.lexeme) \ '=' \ E.addr); \ \} \\
E & \rightarrow \ E_1 + E_2 & \{ \ E.addr = \textbf{new} \ Temp\,(); \\
 & & \quad gen(E.addr \ '=' \ E_1.addr \ '+' \ E_2.addr); \ \} \\
 & | \ \ - E_1 & \{ \ E.addr = \textbf{new} \ Temp\,(); \\
 & & \quad gen(E.addr \ '=' \ '\textbf{minus}' \ E_1.addr); \ \} \\
 & | \ \ (\ E_1\ ) & \{ \ E.addr = E_1.addr; \ \} \\
 & | \ \ \textbf{id} & \{ \ E.addr = top.get(\textbf{id}.lexeme); \ \}
\end{array}
$$

Why this incremental approach can guarantee the correct order of instructions?

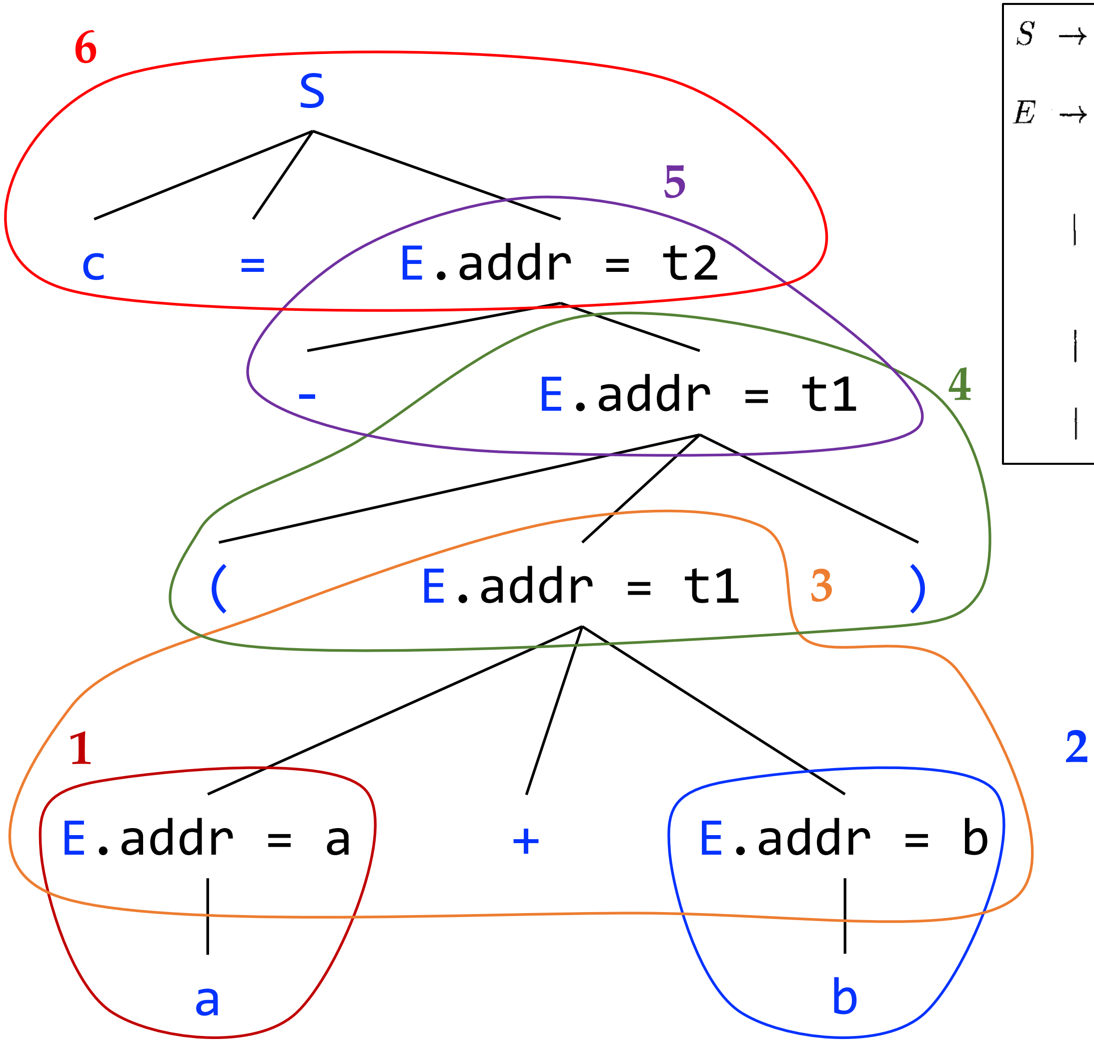
Incremental Translation Scheme

- In the SDT below, *gen* not only generates a three-address instruction, but also appends it to the sequence of instructions generated so far
  - In comparison, in the previous SDD, the *code* attribute can be long strings after concatenations

S	$\rightarrow$	**id** = E ;	$\{$ $gen($ $top.get(\textbf{id}.lexeme)$ $'='$ $E.addr);$ $\}$
E	$\rightarrow$	E_1 + E_2	$\{$ $E.addr = \textbf{new}\ Temp\,();$ $gen(E.addr\ '='\ E_1.addr\ '+'\ E_2.addr);$ $\}$
	$\mid$	- E_1	$\{$ $E.addr = \textbf{new}\ Temp\,();$ $gen(E.addr\ '='\ '\textbf{minus}'\ E_1.addr);$ $\}$
	$\mid$	(E_1)	$\{$ $E.addr = E_1.addr;$ $\}$
	$\mid$	**id**	$\{$ $E.addr = top.get(\textbf{id}.lexeme);$ $\}$

This postfix SDT can be implemented in bottom-up parsing where subexpressions are always handled first (e.g., the code of E_1 and E_2 is generated before E)

Example: Translating c = - (a + b)



$S \rightarrow$	$\textbf{id} = E\ ;$	$\{\ gen(\ top.get(\textbf{id}.lexeme)\ '='\ E.addr);\ \}$	6
$E \rightarrow$	$E_1 + E_2$	$\{\ E.addr = \textbf{new}\ Temp\,();\ gen(E.addr\ '='\ E_1.addr\ '+'\ E_2.addr);\ \}$	3
$\mid$	$-\ E_1$	$\{\ E.addr = \textbf{new}\ Temp\,();\ gen(E.addr\ '='\ '\textbf{minus}'\ E_1.addr);\ \}$	5
$\mid$	$(\ E_1\ )$	$\{\ E.addr = E_1.addr;\ \}$	4
$\mid$	$\textbf{id}$	$\{\ E.addr = top.get(\textbf{id}.lexeme);\ \}$	1 2

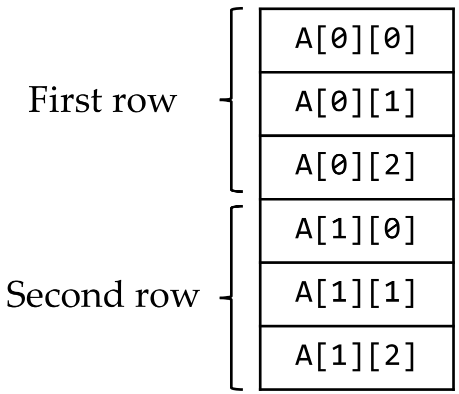
Generated code:

```
t1 = a + b
t2 = - t1
c = t2
```

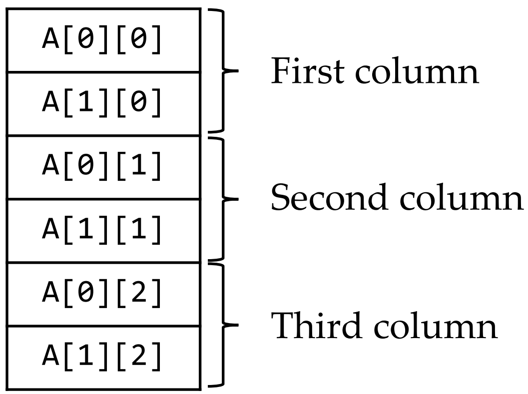
3
5
6

Addressing Array Elements

- Array elements can be accessed quickly if they are stored consecutively
- For an array A with n elements, the relative address of $A[i]$ is:
  - $\boldsymbol{base + i * w}$ ($base$ is the relative address of $A[0]$, w is the width of an element)
- For a 2D array A (row-major layout), the relative address of $A[i_1][i_2]$ is:
  - $\boldsymbol{base + i_1 * w_1 + i_2 * w_2}$ (w_1 is the width of a row, w_2 is the width of an element)



Row-major (C)



Column-major (Fortran)

Addressing Array Elements

- Array elements can be accessed quickly if they are stored consecutively
- For an array A with n elements, the relative address of $A[i]$ is:
  - $\boldsymbol{base} + \boldsymbol{i} * \boldsymbol{w}$ ($base$ is the relative address of $A[0]$, w is the width of an element)
- For a 2D array A (row-major layout), the relative address of $A[i_1][i_2]$ is:
  - $\boldsymbol{base} + \boldsymbol{i_1} * \boldsymbol{w_1} + \boldsymbol{i_2} * \boldsymbol{w_2}$ (w_1 is the width of a row, w_2 is the width of an element)
- Further generalize to k-dimensional array A (row-major layout), the relative address of $A[i_1][i_2] \dots [i_k]$ is:
  - $\boldsymbol{base} + \boldsymbol{i_1} * \boldsymbol{w_1} + \boldsymbol{i_2} * \boldsymbol{w_2} + \cdots + \boldsymbol{i_k} * \boldsymbol{w_k}$ (w's can be generalized as above)

Translation of Array References

- The main problem in generating code for array references is to relate the address-calculation formula to the grammar

  - The relative address of $A[i_1][i_2] \ldots [i_k]$ is $base\ +\ i_1 * w_1 + i_2 * w_2 + \cdots + i_k * w_k$

  - Productions for generating array references: $L \rightarrow L\ [\ E\ ]\ |\ \mathbf{id}\ [\ E\ ]$

SDT for Array References (1)

$$
\begin{array}{lll}
L & \rightarrow \ \mathbf{id}\ [\ E\ ] & \{\ L.array = top.get(\mathbf{id}.lexeme); \\
 & & \quad L.type = L.array.type.elem; \\
 & & \quad L.addr = \mathbf{new}\ Temp\,(); \\
 & & \quad gen(L.addr\ '='\ E.addr\ '*'\ L.type.width);\ \} \\
 & \mid \ L_1\ [\ E\ ] & \{\ L.array = L_1.array; \\
 & & \quad L.type = L_1.type.elem; \\
 & & \quad t = \mathbf{new}\ Temp\,(); \\
 & & \quad L.addr = \mathbf{new}\ Temp\,(); \\
 & & \quad gen(t\ '='\ E.addr\ '*'\ L.type.width); \\
 & & \quad gen(L.addr\ '='\ L_1.addr\ '+'\ t);\ \}
\end{array}
$$

A is a 2*3 array of integers
Translate A[i][j]

L.type is the type of A's element:
array(3, int)

A[i]
↓ Reduce using prod. #1
L

L[j]
↓ Reduce using prod. #2
L

L.type is the type of A[i]'s element:
int

$L.array$: a pointer to the symbol-table entry for the array name

$L.array.base$: the base address of the array

$L.addr$: a temporary for computing the <u>offset</u> for the array reference

$L.type$: the type of the subarray generated by L

$t.elem$: for any array type t, $t.elem$ gives the element type

SDT for Array References (2)

- The semantic actions of L-productions compute offsets
- The address of an array element is $base + offset$

$$
\begin{array}{lll}
E & \rightarrow E_1 + E_2 & \{ \; E.addr = \mathbf{new} \; Temp\,(); \\
& & \quad gen(E.addr \; '=' \; E_1.addr \; '+' \; E_2.addr); \; \} \\
& \mid \; \mathbf{id} & \{ \; E.addr = top.get(\mathbf{id}.lexeme); \; \} \\
& \mid \; L & \{ \; E.addr = \mathbf{new} \; Temp\,(); \\
& & \quad gen(\underline{E.addr \; '=' \; L.array.base \; '[' \; L.addr \; ']'}); \; \}
\end{array}
$$

Instruction of the form $x = a[i]$

Array references can be part of an expression

SDT for Array References (3)

$$
\begin{array}{llll}
S & \rightarrow & \textbf{id} = E \; ; & \{ \; gen(\; top.get(\textbf{id}.lexeme) \; '=' \; E.addr); \; \} \\
 & | & L = E \; ; & \{ \; gen(L.addr.base \; '[' \; L.addr \; ']' \; '=' \; E.addr); \; \}
\end{array}
$$

Instruction of form $a[i] = x$

Array references can appear at the LHS of an assignment statement

$E \rightarrow E_1 + E_2$ $\quad \{ E.addr = \textbf{new}\ Temp();$ $\quad$ 6
$\quad gen(E.addr\ '='\ E_1.addr\ '+'\ E_2.addr); \}$

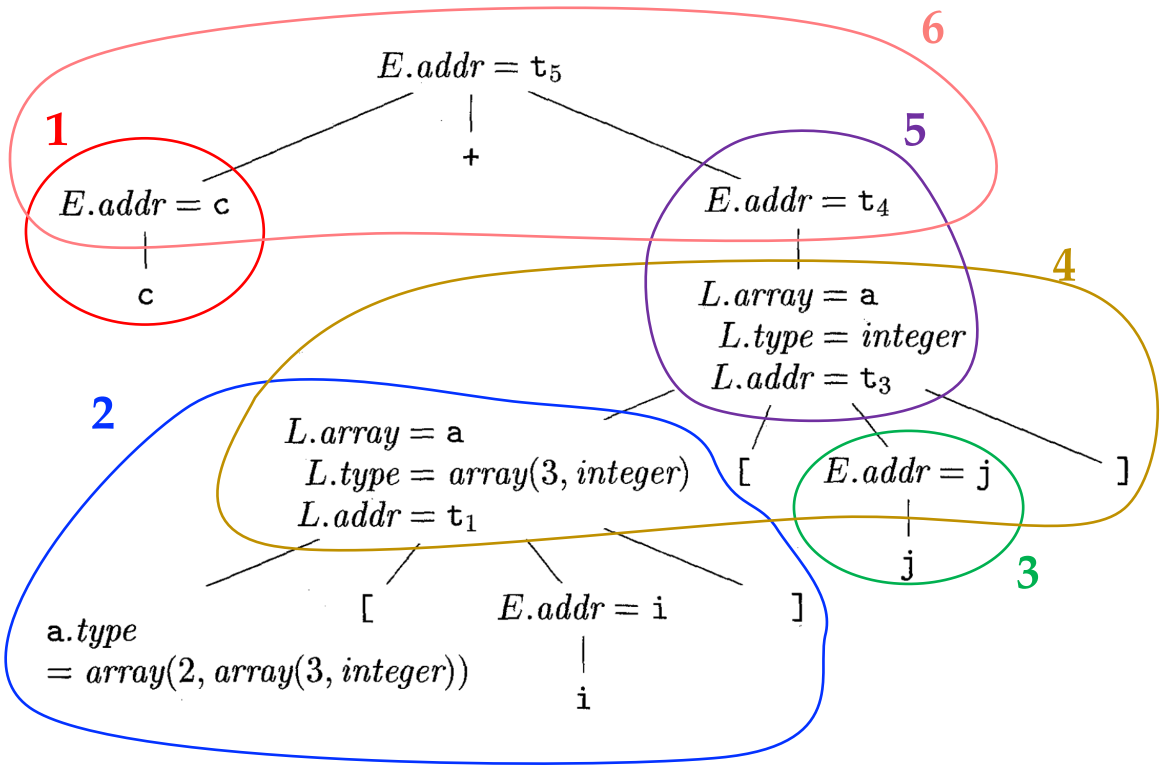
$\quad |\ \textbf{id}$ $\quad \{ E.addr = top.get(\textbf{id}.lexeme); \}$ $\quad$ 1 3

$\quad |\ L$ $\quad \{ E.addr = \textbf{new}\ Temp();$ $\quad$ 5
$\quad gen(E.addr\ '='\ L.array.base\ '['\ L.addr\ ']'); \}$

$L \rightarrow \textbf{id}\ [\ E\ ]$ $\quad \{ L.array = top.get(\textbf{id}.lexeme);$
$\quad L.type = L.array.type.elem;$ $\quad$ 2
$\quad L.addr = \textbf{new}\ Temp();$
$\quad gen(L.addr\ '='\ E.addr\ '*'\ L.type.width); \}$

$\quad |\ L_1\ [\ E\ ]$ $\quad \{ L.array = L_1.array;$
$\quad L.type = L_1.type.elem;$
$\quad t = \textbf{new}\ Temp();$ $\quad$ 4
$\quad L.addr = \textbf{new}\ Temp();$
$\quad gen(t\ '='\ E.addr\ '*'\ L.type.width);$
$\quad gen(L.addr\ '='\ L_1.addr\ '+'\ t); \}$

Translating c + a[i][j]



Generated code:

```
t1 = i * 12      2
t2 = j * 4
t3 = t1 + t2     4
t4 = a[t3]       5
t5 = c + t4      6
```

Outline

- Intermediate Representation
- Type and Declarations
- Type Checking
- Translation of Expressions
- Control Flow
- Backpatching

Control Flow

- Boolean expressions are often used to alter the flow of control or compute logical values

- Grammar: $B \rightarrow B \parallel B \mid B \;\&\&\; B \mid\, !B \mid (B) \mid E$ **rel** $E \mid$ **true** $\mid$ **false**

- Given the expression $B_1 \parallel B_2$, if B_1 is true, then the expression is true without having to evaluate B_2. In other words, B_1 or B_2 may not need to be evaluated fully.*

- In *short-circuit* code, the boolean operators &&, ‖, ! translate into jumps. The operators do not appear in the code.

* If B_1 or B_2 has side effect (e.g., changing the value of a global variable), then the effect may not occur

Short-Circuit Code Example

- `if ( x < 100 || x > 200 && x != y ) x = 0;`

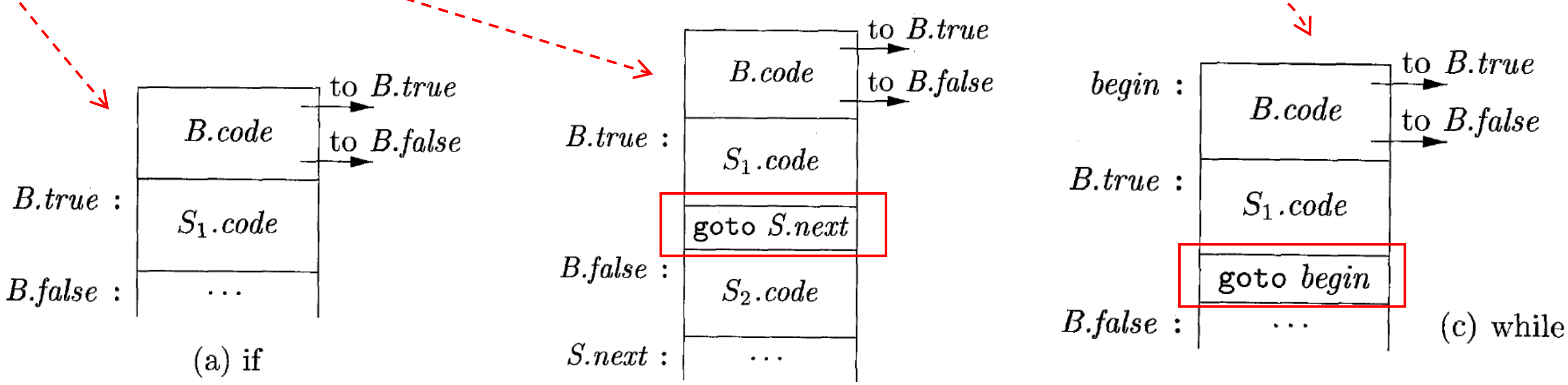
```
        if x < 100 goto L2
        ifFalse x > 200 goto L1
        ifFalse x != y goto L1
L2:     x = 0
L1:
```

Flow-of-Control Statements

- Grammar:
  - $S \rightarrow$ **if** $(\ B\ )\ S_1$
  - $S \rightarrow$ **if** $(\ B\ )\ S_1$ **else** S_2
  - $S \rightarrow$ **while** $(\ B\ )\ S_1$

Inherited attributes:

- $B.true$: the label to which control flows if B is true
- $B.false$: the label to which control flows if B is false
- $S.next$: the label for the instruction immediately after the code for S



(a) if

(b) if-else

(c) while

$S.next$ is not needed

$S.next$ is not needed

SDD for Flow-of-Control Statements (1)

Production	Semantic Rules
$P \rightarrow S$	$S.next = newlabel()$ $P.code = S.code \;\|\|\; label(S.next)$
$S \rightarrow \textbf{assign}$	$S.code = \textbf{assign}.code$
$S \rightarrow \textbf{if}\ (\ B\ )\ S_1$	$B.true = newlabel()$ $B.false = S_1.next = S.next$ $S.code = B.code \;\|\|\; label(B.true) \;\|\|\; S_1.code$
$S \rightarrow \textbf{if}\ (\ B\ )\ S_1\ \textbf{else}\ S_2$	$B.true = newlabel()$ $B.false = newlabel()$ $S_1.next = S_2.next = S.next$ $S.code = B.code$ $\quad \|\| \; label(B.true) \;\|\|\; S_1.code$ $\quad \|\| \; gen(\texttt{'goto'}\ S.next)$ $\quad \|\| \; label(B.false) \;\|\|\; S_2.code$

Illustrated by previous figures

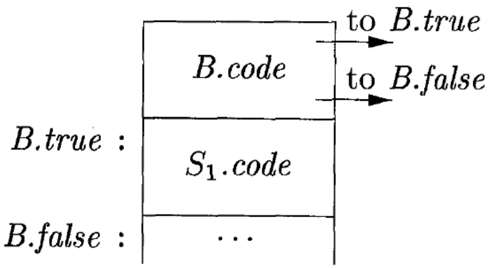
SDD for Flow-of-Control Statements (2)

Illustrated by previous figure

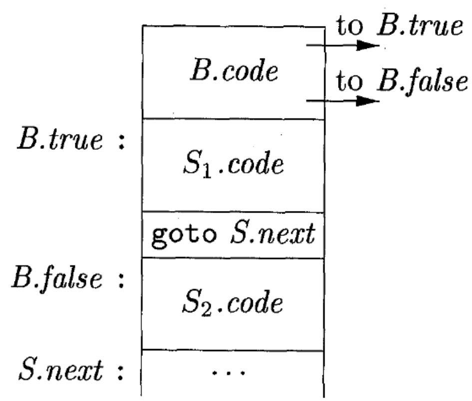
$S \rightarrow \textbf{while} \ (\ B \ ) \ S_1$	$begin = newlabel()$ $B.true = newlabel()$ $B.false = S.next$ $S_1.next = begin$ $S.code = label(begin) \ \Vert \ B.code \ \Vert \ label(B.true) \ \Vert \ S_1.code \ \Vert \ gen('\texttt{goto}' \ begin)$
$S \rightarrow S_1 \ S_2$	$S_1.next = newlabel()$ $S_2.next = S.next$ $S.code = S_1.code \ \Vert \ label(S_1.next) \ \Vert \ S_2.code$

Translating Boolean Expressions in Flow-of-Control Statements

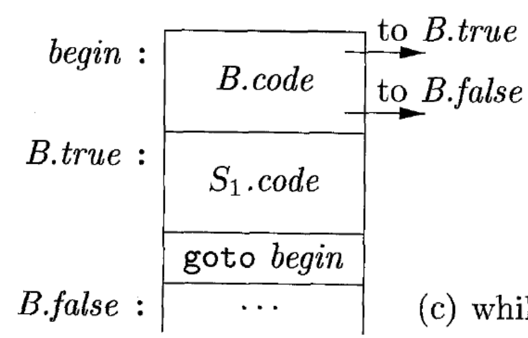
- A boolean expression B is translated into three-address instructions that evaluate B using conditional and unconditional jumps to one of two labels: $B.true$ and $B.false$
  - $B.true$ and $B.false$ are two inherited attributes. Their value depends on the context of B (e.g., if statement, if-$else$ statement, $while$ statement)



(a) if



(b) if-else



(c) while

Generating Three-Address Code for Booleans (1)

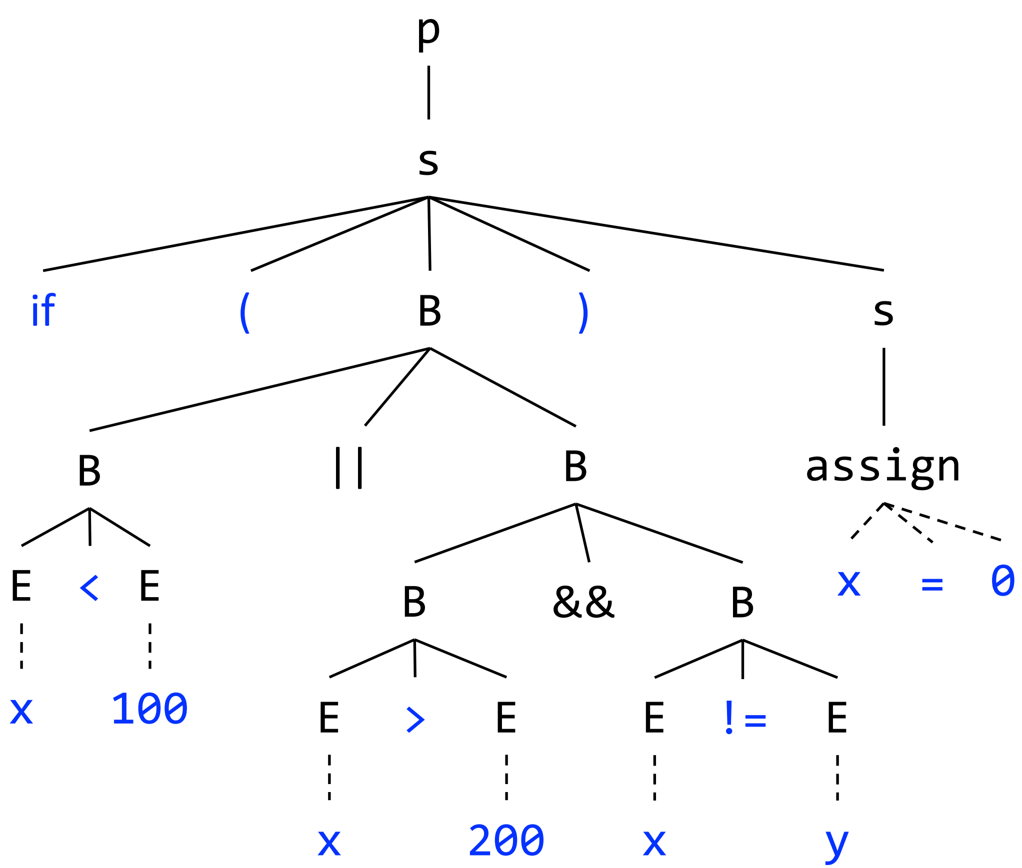
Production	Semantic Rules
$B \rightarrow B_1 \mid\mid B_2$	$B_1.true = B.true$ // short-circuiting $B_1.false = newlabel()$ $B_2.true = B.true$ $B_2.false = B.false$ $B.code = B_1.code \mid\mid label(B_1.false) \mid\mid B_2.code$
$B \rightarrow B_1 \,\&\&\, B_2$	$B_1.true = newlabel()$ $B_1.false = B.false$ // short-circuiting $B_2.true = B.true$ $B_2.false = B.false$ $B.code = B_1.code \mid\mid label(B_1.true) \mid\mid B_2.code$
$B \rightarrow \,!\, B_1$	$B_1.true = B.false$ $B_1.false = B.true$ $B.code = B_1.code$

Generating Three-Address Code for Booleans (2)

$B \rightarrow E_1 \textbf{ rel } E_2$	$B.code = E_1.code \mid\mid E_2.code$ $\mid\mid gen(\texttt{'if'}\ E_1.addr\ \textbf{rel}.op\ E_2.addr\ \texttt{'goto'}\ B.true)$ $\mid\mid gen(\texttt{'goto'}\ B.false)$
$B \rightarrow \textbf{true}$	$B.code = gen(\texttt{'goto'}\ B.true)$
$B \rightarrow \textbf{false}$	$B.code = gen(\texttt{'goto'}\ B.false)$

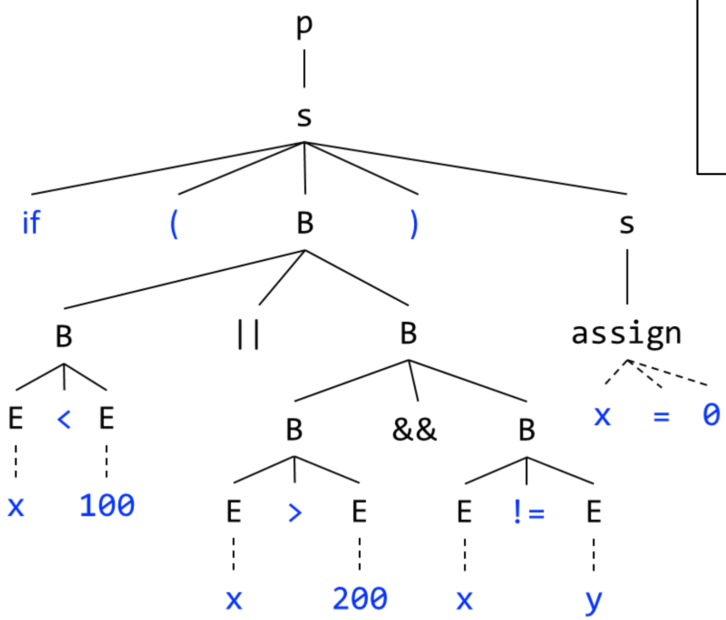
Example

- `if ( x < 100 || x > 200 && x != y ) x = 0;`



Dashed lines mean that the reduction may consist of multiple steps

Example



PRODUCTION	SEMANTIC RULES
$P \rightarrow S$	$S.next = newlabel()$ $P.code = S.code \mid\mid label(S.next)$
$S \rightarrow \textbf{assign}$	$S.code = \textbf{assign}.code$
$S \rightarrow \textbf{if}\ (\ B\ )\ S_1$	$B.true = newlabel()$ $B.false = S_1.next = S.next$ $S.code = B.code \mid\mid label(B.true) \mid\mid S_1.code$

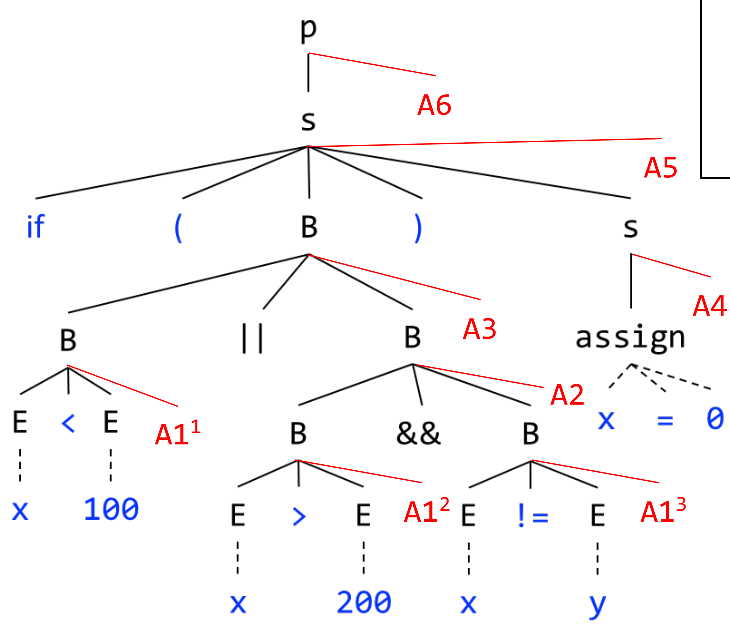
$B \rightarrow B_1 \mid\mid B_2$	$B_1.true = B.true$ $B_1.false = newlabel()$ $B_2.true = B.true$ $B_2.false = B.false$ $B.code = B_1.code \mid\mid label(B_1.false) \mid\mid B_2.code$
$B \rightarrow B_1\ \&\&\ B_2$	$B_1.true = newlabel()$ $B_1.false = B.false$ $B_2.true = B.true$ $B_2.false = B.false$ $B.code = B_1.code \mid\mid label(B_1.true) \mid\mid B_2.code$
$B \rightarrow\ !\ B_1$	$B_1.true = B.false$ $B_1.false = B.true$ $B.code = B_1.code$

$B \rightarrow E_1\ \textbf{rel}\ E_2$	$B.code = E_1.code \mid\mid E_2.code$ $\mid\mid gen('\texttt{if}'\ E_1.addr\ \textbf{rel}.op\ E_2.addr\ '\texttt{goto}'\ B.true)$ $\mid\mid gen('\texttt{goto}'\ B.false)$

This SDD is L-attributed, not S-attributed. The grammar is not LL. There is no way to implement the SDD directly during parsing.

Traversing the parse tree to evaluate the attributes helps generate intermediate code

Example



Virtual nodes are in red color

Application order of actions (preorder traversal of the tree):

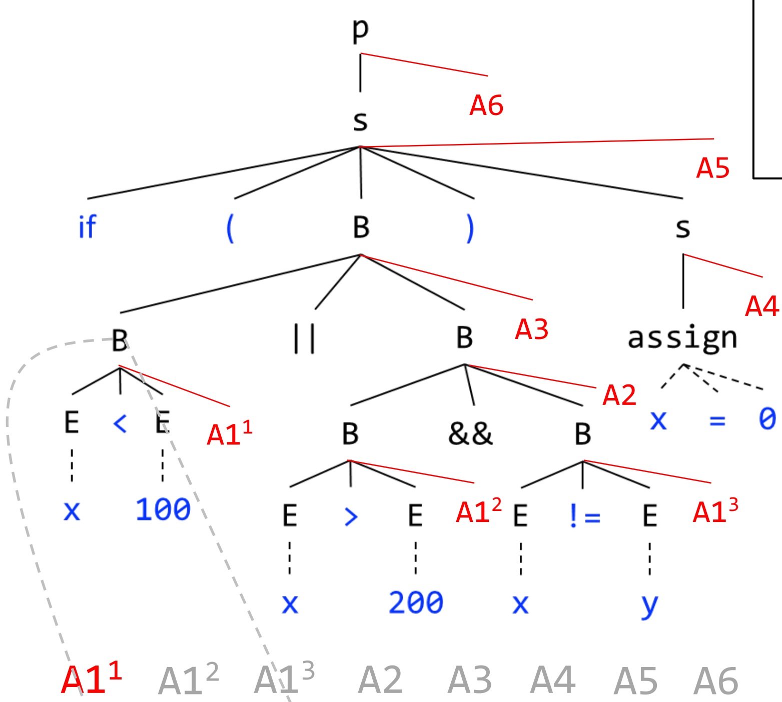
A1[1] A1[2] A1[3] A2 A3 A4 A5 A6

PRODUCTION	SEMANTIC RULES
$P \rightarrow S$	$S.next = newlabel()$ $P.code = S.code \mid\mid label(S.next)$ A6
$S \rightarrow \textbf{assign}$	$S.code = \textbf{assign}.code$ A4
$S \rightarrow \textbf{if} (B) S_1$	$B.true = newlabel()$ $B.false = S_1.next = S.next$ A5 $S.code = B.code \mid\mid label(B.true) \mid\mid S_1.code$

$B \rightarrow B_1 \mid\mid B_2$	$B_1.true = B.true$ $B_1.false = newlabel()$ $B_2.true = B.true$ A3 $B_2.false = B.false$ $B.code = B_1.code \mid\mid label(B_1.false) \mid\mid B_2.code$
$B \rightarrow B_1 \&\& B_2$	$B_1.true = newlabel()$ $B_1.false = B.false$ $B_2.true = B.true$ A2 $B_2.false = B.false$ $B.code = B_1.code \mid\mid label(B_1.true) \mid\mid B_2.code$
$B \rightarrow ! B_1$	$B_1.true = B.false$ $B_1.false = B.true$ $B.code = B_1.code$

$B \rightarrow E_1 \textbf{ rel } E_2$	$B.code = E_1.code \mid\mid E_2.code$ A1 $\mid\mid gen('\texttt{if}'\ E_1.addr\ \textbf{rel}.op\ E_2.addr\ '\texttt{goto}'\ B.true)$ $\mid\mid gen('\texttt{goto}'\ B.false)$

Example



Generated code:

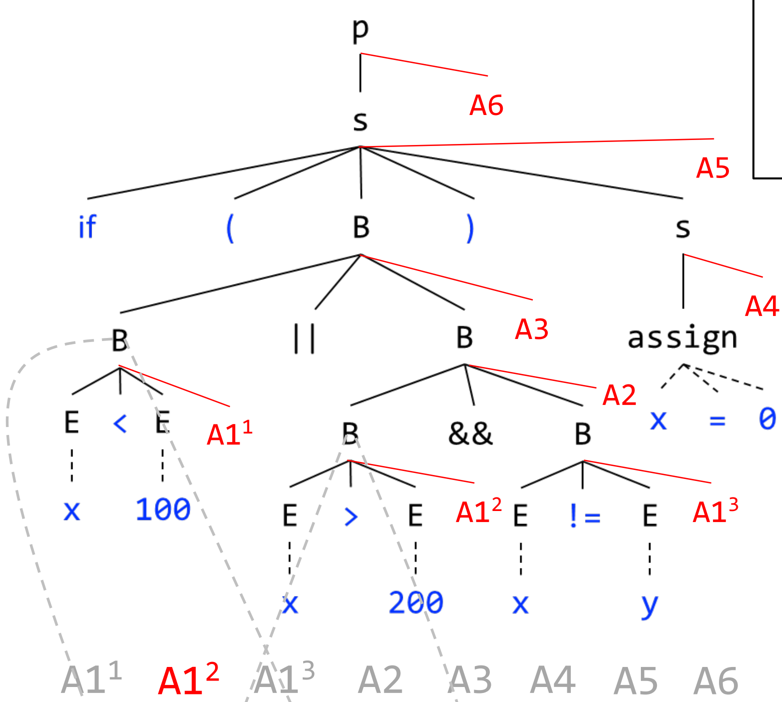
```
if x < 100 goto B.true
goto B.false
```

Production	Semantic Rules
$P \rightarrow S$	$S.next = newlabel()$ $P.code = S.code \mid\mid label(S.next)$ A6
$S \rightarrow \textbf{assign}$	$S.code = \textbf{assign}.code$ A4
$S \rightarrow \textbf{if}\ (\ B\ )\ S_1$	$B.true = newlabel()$ $B.false = S_1.next = S.next$ A5 $S.code = B.code \mid\mid label(B.true) \mid\mid S_1.code$

$B \rightarrow B_1 \mid\mid B_2$	$B_1.true = B.true$ $B_1.false = newlabel()$ A3 $B_2.true = B.true$ $B_2.false = B.false$ $B.code = B_1.code \mid\mid label(B_1.false) \mid\mid B_2.code$
$B \rightarrow B_1\ \&\&\ B_2$	$B_1.true = newlabel()$ $B_1.false = B.false$ $B_2.true = B.true$ A2 $B_2.false = B.false$ $B.code = B_1.code \mid\mid label(B_1.true) \mid\mid B_2.code$
$B \rightarrow !\ B_1$	$B_1.true = B.false$ $B_1.false = B.true$ $B.code = B_1.code$

$B \rightarrow E_1\ \textbf{rel}\ E_2$	$B.code = E_1.code \mid\mid E_2.code$ A1 $\mid\mid gen('\texttt{if}'\ E_1.addr\ \textbf{rel}.op\ E_2.addr\ '\texttt{goto}'\ B.true)$ $\mid\mid gen('\texttt{goto}'\ B.false)$

Example



Generated code:

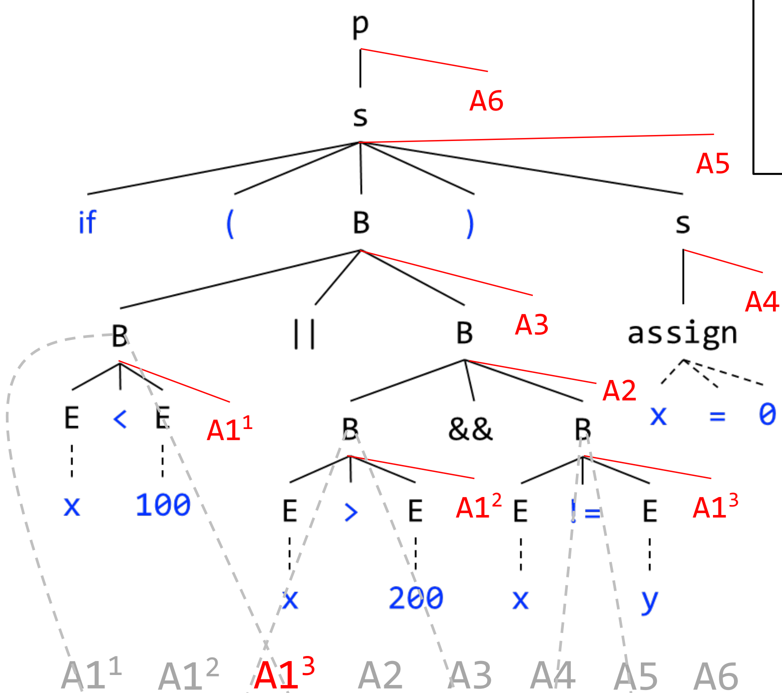
```
if x < 100 goto B.true
goto B.false
if x > 200 goto B.true
goto B.false
```

PRODUCTION	SEMANTIC RULES	
$P \rightarrow S$	$S.next = newlabel()$ $P.code = S.code \mid\mid label(S.next)$	A6
$S \rightarrow \textbf{assign}$	$S.code = \textbf{assign}.code$	A4
$S \rightarrow \textbf{if}\ (\ B\ )\ S_1$	$B.true = newlabel()$ $B.false = S_1.next = S.next$ $S.code = B.code \mid\mid label(B.true) \mid\mid S_1.code$	A5

$B \rightarrow B_1 \mid\mid B_2$	$B_1.true = B.true$ $B_1.false = newlabel()$ $B_2.true = B.true$ $B_2.false = B.false$ $B.code = B_1.code \mid\mid label(B_1.false) \mid\mid B_2.code$	A3
$B \rightarrow B_1\ \&\&\ B_2$	$B_1.true = newlabel()$ $B_1.false = B.false$ $B_2.true = B.true$ $B_2.false = B.false$ $B.code = B_1.code \mid\mid label(B_1.true) \mid\mid B_2.code$	A2
$B \rightarrow\ !\ B_1$	$B_1.true = B.false$ $B_1.false = B.true$ $B.code = B_1.code$	

$B \rightarrow E_1\ \textbf{rel}\ E_2$	A1	$B.code = E_1.code \mid\mid E_2.code$ $\mid\mid gen('\texttt{if}'\ E_1.addr\ \textbf{rel}.op\ E_2.addr\ '\texttt{goto}'\ B.true)$ $\mid\mid gen('\texttt{goto}'\ B.false)$

Example



Generated code:

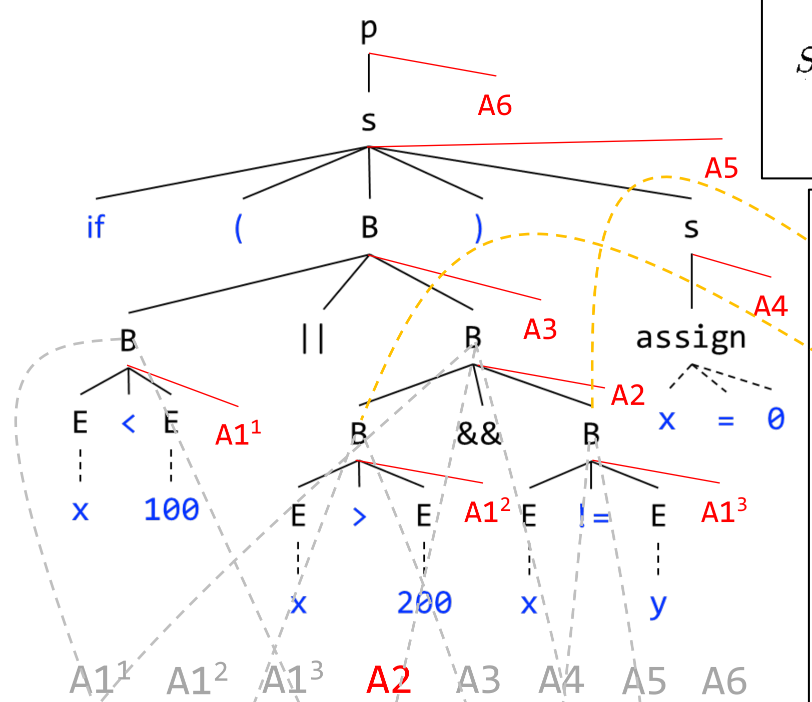
```
if x < 100 goto B.true
goto B.false
if x > 200 goto B.true
goto B.false
if x != y goto B.true
goto B.false
```

PRODUCTION	SEMANTIC RULES	
$P \rightarrow S$	$S.next = newlabel()$ $P.code = S.code \mid\mid label(S.next)$	A6
$S \rightarrow \textbf{assign}$	$S.code = \textbf{assign}.code$	A4
$S \rightarrow \textbf{if}\ (\ B\ )\ S_1$	$B.true = newlabel()$ $B.false = S_1.next = S.next$ $S.code = B.code \mid\mid label(B.true) \mid\mid S_1.code$	A5

$B \rightarrow B_1 \mid\mid B_2$	$B_1.true = B.true$ $B_1.false = newlabel()$ $B_2.true = B.true$ $B_2.false = B.false$ $B.code = B_1.code \mid\mid label(B_1.false) \mid\mid B_2.code$	A3
$B \rightarrow B_1\ \&\&\ B_2$	$B_1.true = newlabel()$ $B_1.false = B.false$ $B_2.true = B.true$ $B_2.false = B.false$ $B.code = B_1.code \mid\mid label(B_1.true) \mid\mid B_2.code$	A2
$B \rightarrow\ !\ B_1$	$B_1.true = B.false$ $B_1.false = B.true$ $B.code = B_1.code$	

$B \rightarrow E_1\ \textbf{rel}\ E_2$	A1	$B.code = E_1.code \mid\mid E_2.code$ $\mid\mid gen('\texttt{if}'\ E_1.addr\ \textbf{rel}.op\ E_2.addr\ '\texttt{goto}'\ B.true)$ $\mid\mid gen('\texttt{goto}'\ B.false)$

Example



PRODUCTION	SEMANTIC RULES	
$P \rightarrow S$	$S.next = newlabel()$ $P.code = S.code \mid\mid label(S.next)$	A6
$S \rightarrow \textbf{assign}$	$S.code = \textbf{assign}.code$	A4
$S \rightarrow \textbf{if}\ (\ B\ )\ S_1$	$B.true = newlabel()$ $B.false = S_1.next = S.next$ $S.code = B.code \mid\mid label(B.true) \mid\mid S_1.code$	A5

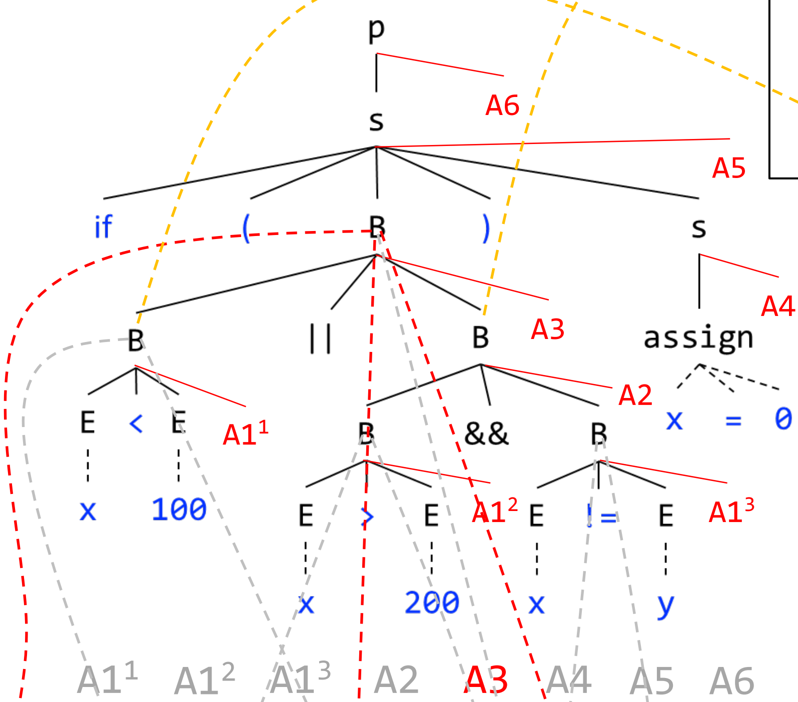
$B \rightarrow B_1 \mid\mid B_2$	$B_1.true = B.true$ $B_1.false = newlabel()$ $B_2.true = B.true$ $B_2.false = B.false$ $B.code = B_1.code \mid\mid label(B_1.false) \mid\mid B_2.code$	A3
$B \rightarrow B_1\ \&\&\ B_2$	$B_1.true = newlabel()$ $B_1.false = B.false$ $B_2.true = B.true$ $B_2.false = B.false$ $B.code = B_1.code \mid\mid label(B_1.true) \mid\mid B_2.code$	A2
$B \rightarrow\ !\ B_1$	$B_1.true = B.false$ $B_1.false = B.true$ $B.code = B_1.code$	

$B \rightarrow E_1\ \textbf{rel}\ E_2$	A1	$B.code = E_1.code \mid\mid E_2.code$ $\mid\mid gen('\texttt{if}'\ E_1.addr\ \textbf{rel}.op\ E_2.addr\ '\texttt{goto}'\ B.true)$ $\mid\mid gen('\texttt{goto}'\ B.false)$

Generated code:

```
    if x < 100 goto B.true
    goto B.false
    if x > 200 goto B.true  = L4
    goto B.false  = B.false
L4: if x != y goto B.true = B.true
    goto B.false = B.false
```

Example



PRODUCTION	SEMANTIC RULES
$P \rightarrow S$	$S.next = newlabel()$ $P.code = S.code \mid\mid label(S.next)$ A6
$S \rightarrow$ **assign**	$S.code =$ **assign**$.code$ A4
$S \rightarrow$ **if** $(B)\ S_1$	$B.true = newlabel()$ $B.false = S_1.next = S.next$ A5 $S.code = B.code \mid\mid label(B.true) \mid\mid S_1.code$

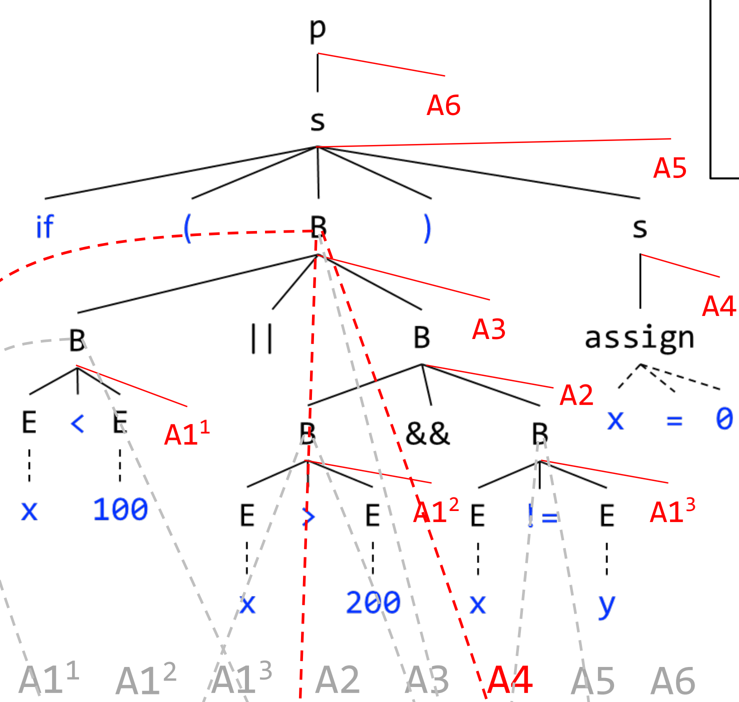
$B \rightarrow B_1 \mid\mid B_2$	$B_1.true = B.true$ $B_1.false = newlabel()$ A3 $B_2.true = B.true$ $B_2.false = B.false$ $B.code = B_1.code \mid\mid label(B_1.false) \mid\mid B_2.code$
$B \rightarrow B_1\ \&\&\ B_2$	$B_1.true = newlabel()$ $B_1.false = B.false$ $B_2.true = B.true$ A2 $B_2.false = B.false$ $B.code = B_1.code \mid\mid label(B_1.true) \mid\mid B_2.code$
$B \rightarrow !\ B_1$	$B_1.true = B.false$ $B_1.false = B.true$ $B.code = B_1.code$

$B \rightarrow E_1$ **rel** E_2	$B.code = E_1.code \mid\mid E_2.code$ A1 $\mid\mid gen($'if' $E_1.addr$ **rel**$.op$ $E_2.addr$ 'goto' $B.true)$ $\mid\mid gen($'goto' $B.false)$

Generated code:

```
    if x < 100 goto B.true  = B.true
    goto B.false  = L3
L3: if x > 200 goto B.true  = L4
    goto B.false  = B.false
L4: if x != y goto B.true  = B.true
    goto B.false = B.false
```

Example



Generated code:

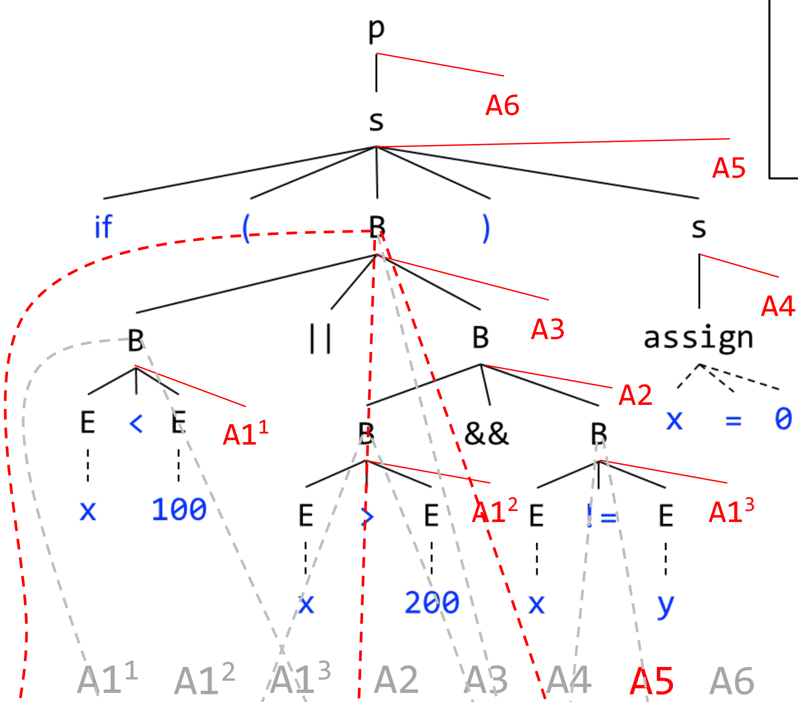
```
    if x < 100 goto B.true  = B.true
    goto B.false = L3
L3: if x > 200 goto B.true  = L4
    goto B.false  = B.false
L4: if x != y goto B.true = B.true
    goto B.false = B.false
    x = 0
```

PRODUCTION	SEMANTIC RULES	
$P \rightarrow S$	$S.next = newlabel()$ $P.code = S.code \mid\mid label(S.next)$	A6
$S \rightarrow \textbf{assign}$	$S.code = \textbf{assign}.code$	A4
$S \rightarrow \textbf{if}\ (\ B\ )\ S_1$	$B.true = newlabel()$ $B.false = S_1.next = S.next$ $S.code = B.code \mid\mid label(B.true) \mid\mid S_1.code$	A5

$B \rightarrow B_1 \mid\mid B_2$	$B_1.true = B.true$ $B_1.false = newlabel()$ $B_2.true = B.true$ $B_2.false = B.false$ $B.code = B_1.code \mid\mid label(B_1.false) \mid\mid B_2.code$	A3
$B \rightarrow B_1\ \&\&\ B_2$	$B_1.true = newlabel()$ $B_1.false = B.false$ $B_2.true = B.true$ $B_2.false = B.false$ $B.code = B_1.code \mid\mid label(B_1.true) \mid\mid B_2.code$	A2
$B \rightarrow\ !\ B_1$	$B_1.true = B.false$ $B_1.false = B.true$ $B.code = B_1.code$	

$B \rightarrow E_1\ \textbf{rel}\ E_2$	$B.code = E_1.code \mid\mid E_2.code$ $\mid\mid gen('\texttt{if}'\ E_1.addr\ \textbf{rel}.op\ E_2.addr\ '\texttt{goto}'\ B.true)$ $\mid\mid gen('\texttt{goto}'\ B.false)$	A1

Example



PRODUCTION	SEMANTIC RULES	
$P \rightarrow S$	$S.next = newlabel()$ $P.code = S.code \mid\mid label(S.next)$	A6
$S \rightarrow$ **assign**	$S.code =$ **assign**$.code$	A4
$S \rightarrow$ **if** $(B) S_1$	$B.true = newlabel()$ $B.false = S_1.next = S.next$ $S.code = B.code \mid\mid label(B.true) \mid\mid S_1.code$	A5

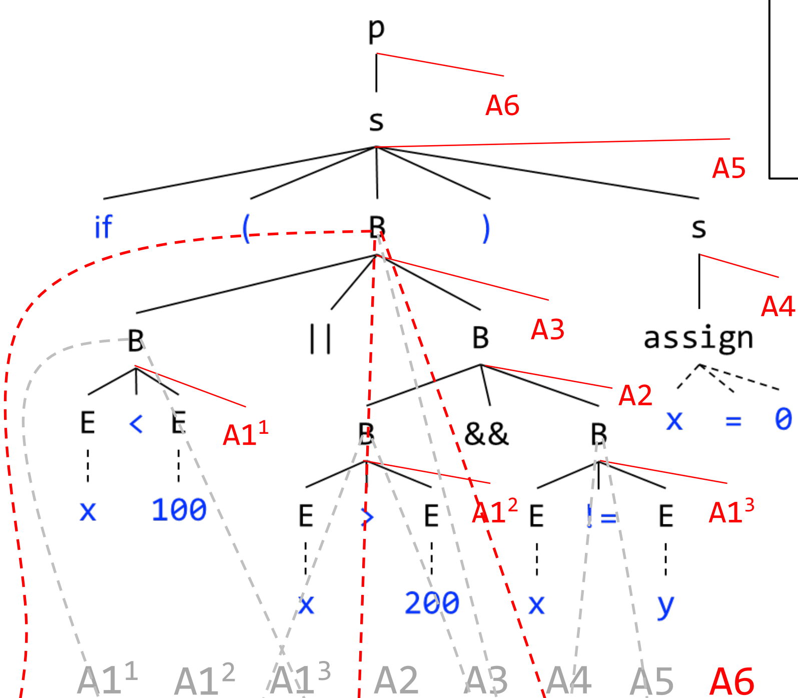
$B \rightarrow B_1 \mid\mid B_2$	$B_1.true = B.true$ $B_1.false = newlabel()$ $B_2.true = B.true$ $B_2.false = B.false$ $B.code = B_1.code \mid\mid label(B_1.false) \mid\mid B_2.code$	A3
$B \rightarrow B_1$ && B_2	$B_1.true = newlabel()$ $B_1.false = B.false$ $B_2.true = B.true$ $B_2.false = B.false$ $B.code = B_1.code \mid\mid label(B_1.true) \mid\mid B_2.code$	A2
$B \rightarrow\ !\ B_1$	$B_1.true = B.false$ $B_1.false = B.true$ $B.code = B_1.code$	

$B \rightarrow E_1$ **rel** E_2	$B.code = E_1.code \mid\mid E_2.code$ $\mid\mid gen($'if' $E_1.addr$ **rel**$.op$ $E_2.addr$ 'goto' $B.true)$ $\mid\mid gen($'goto' $B.false)$	A1

Generated code:

```
    if x < 100 goto B.true  = B.true  = L2
    goto B.false = L3
L3: if x > 200 goto B.true  = L4
    goto B.false  = B.false  = s.next
L4: if x != y goto B.true = B.true  = L2
    goto B.false = B.false  = s.next
L2: x = 0
```

Example



Production	Semantic Rules	
$P \to S$	$S.next = newlabel()$ $P.code = S.code \mid\mid label(S.next)$	A6
$S \to \textbf{assign}$	$S.code = \textbf{assign}.code$	A4
$S \to \textbf{if}\ (\ B\ )\ S_1$	$B.true = newlabel()$ $B.false = S_1.next = S.next$ $S.code = B.code \mid\mid label(B.true) \mid\mid S_1.code$	A5

$B \to B_1 \mid\mid B_2$	$B_1.true = B.true$ $B_1.false = newlabel()$ $B_2.true = B.true$ $B_2.false = B.false$ $B.code = B_1.code \mid\mid label(B_1.false) \mid\mid B_2.code$	A3
$B \to B_1\ \&\&\ B_2$	$B_1.true = newlabel()$ $B_1.false = B.false$ $B_2.true = B.true$ $B_2.false = B.false$ $B.code = B_1.code \mid\mid label(B_1.true) \mid\mid B_2.code$	A2
$B \to\ !\ B_1$	$B_1.true = B.false$ $B_1.false = B.true$ $B.code = B_1.code$	

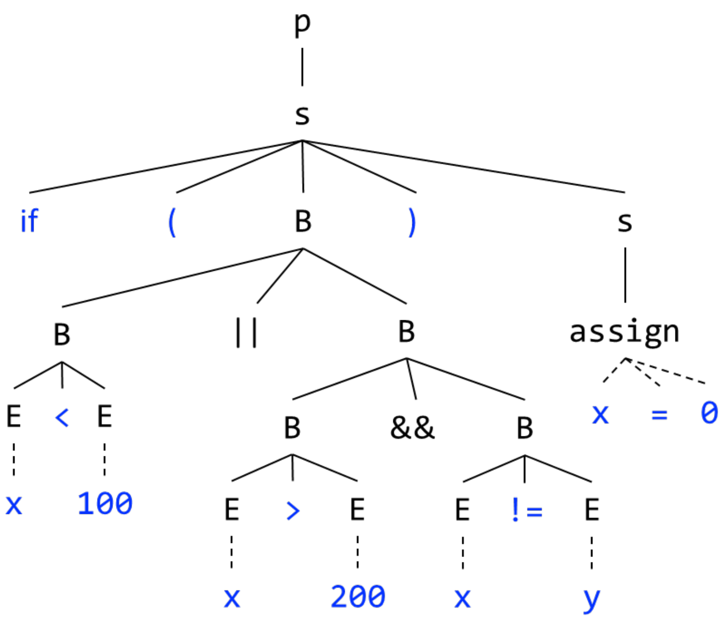
$B \to E_1\ \textbf{rel}\ E_2$	A1	$B.code = E_1.code \mid\mid E_2.code$ $\mid\mid gen('\texttt{if}'\ E_1.addr\ \textbf{rel}.op\ E_2.addr\ '\texttt{goto}'\ B.true)$ $\mid\mid gen('\texttt{goto}'\ B.false)$

Generated code:

```
    if x < 100 goto B.true  = B.true  = L2
    goto B.false = L3
L3: if x > 200 goto B.true  = L4
    goto B.false  = B.false  = S.next
L4: if x != y goto B.true = B.true  = L2
    goto B.false = B.false  = S.next  = L1
L2: x = 0
L1: ...
```

Example

- `if ( x < 100 || x > 200 && x != y ) x = 0;`



Generated code:

$$
\begin{array}{ll}
 & \texttt{if x < 100 goto } L_2 \\
 & \texttt{goto } L_3 \\
L_3: & \texttt{if x > 200 goto } L_4 \\
 & \texttt{goto } L_1 \\
L_4: & \texttt{if x != y goto } L_2 \\
 & \texttt{goto } L_1 \\
L_2: & \texttt{x = 0} \\
L_1: &
\end{array}
$$

Outline

- Intermediate Representation
- Type and Declarations
- Type Checking
- Translation of Expressions
- Control Flow
- Backpatching

Backpatching (回填)

- A **key problem** when generating code for boolean expressions and flow-of-control statements is to match a jump instruction with the jump target
- **Example: if (*B*) *S***
  - According to the short-circuit translation, B's code contains a jump to the instruction following the code for S (executed when B is false)
  - However, B must be translated before S. The jump target is unknown when translating B
  - Earlier, we address the problem by passing labels as inherited attributes ($S.next$), but this requires another separate pass (traversing the parse tree) after parsing

How to address the problem in one pass?

One-Pass Code Generation Using Backpatching

- **Basic idea of backpatching (基本思想):**
  - When a jump is generated, its target is temporarily left unspecified.
  - Incomplete jumps are grouped into lists. All jumps on a list have the same target.
  - Fill in the labels for incomplete jumps when the targets become known.

- **The technique (技术细节):**
  - For a nonterminal B that represents a boolean expression, we define two synthesized attributes: $truelist$ and $falselist$
  - $truelist$: a list of jump instructions whose target is the label to which the control goes when B is true
  - $falselist$: a list of jump instructions whose target is the label to which the control goes when B is false

One-Pass Code Generation Using Backpatching

- **The technique (技术细节) Cont.:**
  - $makelist(i)$: create a new list containing only i, the index of a jump instruction, and return the pointer to the list
  - $merge(p_1, p_2)$: concatenate the lists pointed by p_1 and p_2, and return a pointer to the concatenated list
  - $backpatch(p, i)$: insert i as the target for each of the jump instructions on the list pointed by p

Backpatching for Boolean Expressions (布尔表达式的回填)

- An SDT suitable for generating code for boolean expressions during bottom-up parsing
- Grammar:
  - $B \rightarrow B_1 \parallel MB_2 \mid B_1 \&\& MB_2 \mid !\, B_1 \mid (B_1) \mid E_1\ \mathbf{rel}\ E_2 \mid \mathbf{true} \mid \mathbf{false}$
  - $M \rightarrow \epsilon$

Keep this question in mind: Why do we introduce M before B_2?

1)	$B \rightarrow B_1 \mid\mid M\ B_2$	$\{\ backpatch(B_1.falselist, M.instr);$ $B.truelist = merge(B_1.truelist, B_2.truelist);$ $B.falselist = B_2.falselist;\ \}$
2 2)	$B \rightarrow B_1\ \&\&\ M\ B_2$	$\{\ backpatch(B_1.truelist, M.instr);$ $B.truelist = B_2.truelist;$ $B.falselist = merge(B_1.falselist, B_2.falselist);\ \}$
3)	$B \rightarrow\ !\ B_1$	$\{\ B.truelist = B_1.falselist;$ $B.falselist = B_1.truelist;\ \}$
4)	$B \rightarrow (\ B_1\ )$	$\{\ B.truelist = B_1.truelist;$ $B.falselist = B_1.falselist;\ \}$
1 5)	$B \rightarrow E_1\ \mathbf{rel}\ E_2$	$\{\ B.truelist = makelist(nextinstr);$ $B.falselist = makelist(nextinstr + 1);$ gen('if' $E_1.addr$ **rel**.op $E_2.addr$ 'goto _'); gen('goto _'); }
6)	$B \rightarrow \mathbf{true}$	$\{\ B.truelist = makelist(nextinstr);$ gen('goto _'); }
7)	$B \rightarrow \mathbf{false}$	$\{\ B.falselist = makelist(nextinstr);$ gen('goto _'); }
8)	$M \rightarrow \epsilon$	$\{\ M.instr = nextinstr;\ \}$

Tip: understand 1 and 2 at a high level first and then revisit this slide after you understand the later examples.

Backpatching vs. Non-Backpatching (1)

(1) Non-backpatching SDD
with inherited attributes:

$B \rightarrow E_1$ **rel** E_2	$B.code = E_1.code \mid\mid E_2.code$ $\mid\mid gen($'if' $E_1.addr$ **rel**$.op$ $E_2.addr$ 'goto' $B.true)$ $\mid\mid gen($'goto' $B.false)$

(2) Backpatching scheme:

$B \rightarrow E_1$ **rel** E_2	{ $B.truelist = makelist(nextinstr);$ $B.falselist = makelist(nextinstr + 1);$ $gen($'if' $E_1.addr$ **rel**$.op$ $E_2.addr$ 'goto _'$);$ $gen($'goto _'$);$ }

Comparison:

- In (2), incomplete instructions (指令坯) are added to corresponding lists
- The instruction jumping to $B.true$ in (1) is added to $B.truelist$ in (2)
- The instruction jumping to $B.false$ in (1) is added to $B.falselist$ in (2)

Backpatching vs. Non-Backpatching (2)

(1) Non-backpatching SDD with inherited attributes:

$B \rightarrow B_1 \mid\mid B_2$	$B_1.true = B.true$ $B_1.false = newlabel()$ $B_2.true = B.true$ $B_2.false = B.false$ $B.code = B_1.code \mid\mid label(B_1.false) \mid\mid B_2.code$

(2) Backpatching scheme:

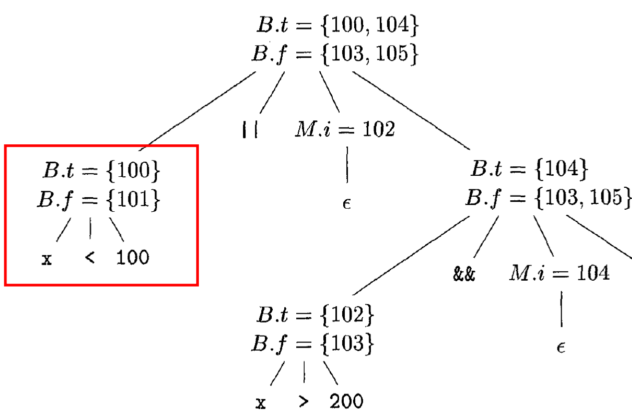
$B \rightarrow B_1 \mid\mid M\ B_2$	$\{$ $backpatch(B_1.falselist, M.instr);$ $B.truelist = merge(B_1.truelist, B_2.truelist);$ $B.falselist = B_2.falselist;$ $\}$

Comparison:

- The assignments to $true/false$ attributes in (1) correspond to the assignments to $truelist/falselist$ or $merge$ in (2)

Example – Boolean Expressions

- The earlier SDT is a postfix SDT. The semantic actions can be performed during a bottom-up parse.
- Boolean expression: $x < 100 \parallel x > 200 \;\&\&\; x \mathrel{!=} y$
- **Step 1:** reduce $x < 100$ to B by production (5)



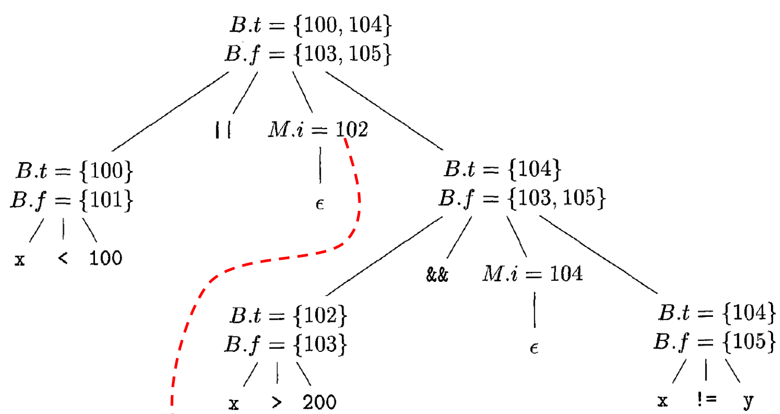
5) $B \rightarrow E_1 \ \mathbf{rel}\ E_2$ $\{$ $B.truelist = makelist(nextinstr);$
$B.falselist = makelist(nextinstr + 1);$
$gen($'if' $E_1.addr\ \mathbf{rel}.op\ E_2.addr$ 'goto _'$);$
$gen($'goto _'$);$ $\}$

Generated code:

```
100:   if x < 100 goto _
101:   goto _
```

Example – Boolean Expressions

- The earlier SDT is a postfix SDT. The semantic actions can be performed during a bottom-up parse.
- Boolean expression: $x < 100 \parallel x > 200 \;\&\&\; x \,!= y$
- **Step 2:** reduce ϵ to M by production (8)

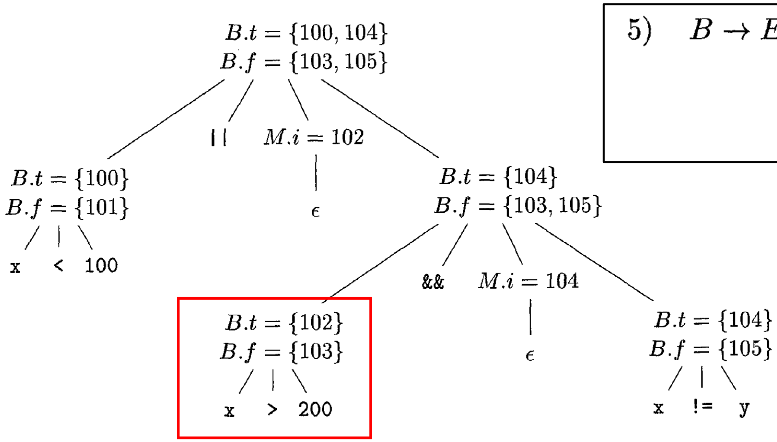


8)	$M \to \epsilon$	$\{\ M.instr = nextinstr;\ \}$

The marker nonterminal records the value of *nextinstr*, 102

Example – Boolean Expressions

- Boolean expression: $x < 100 \parallel x > 200 \; \&\& \; x \,!= y$
- **Step 3:** reduce $x > 200$ to B by production (5)



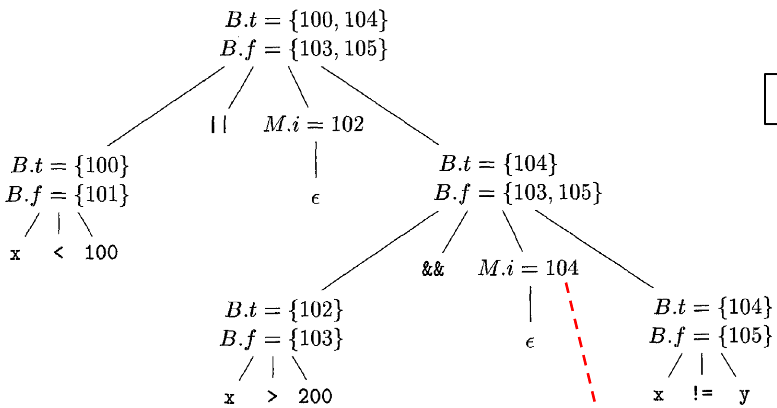
5) $B \rightarrow E_1$ **rel** E_2 { $B.truelist = makelist(nextinstr);$
$B.falselist = makelist(nextinstr + 1);$
gen('`if`' $E_1.addr$ **rel**.op $E_2.addr$ '`goto _`');
gen('`goto _`'); }

Generated code:

```
102:  if x > 200 goto _
103:  goto _
```

Example – Boolean Expressions

- Boolean expression: $x < 100 \parallel x > 200 \; \&\& \; x \mathrel{!}= y$
- **Step 4:** reduce ϵ to M by production (8)

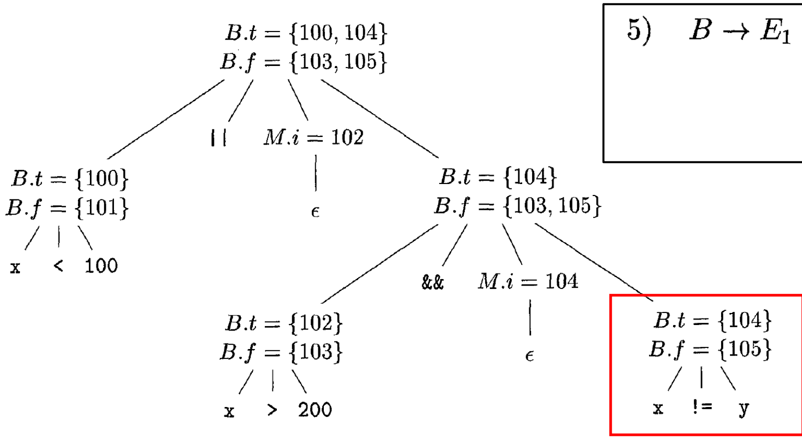


8)	$M \to \epsilon$	$\{ \; M.instr = nextinstr; \; \}$

The marker nonterminal records the value of *nextinstr*, 104

Example – Boolean Expressions

- Boolean expression: $x < 100 \parallel x > 200 \;\&\&\; x \mathrel{!}= y$
- **Step 5:** reduce $x! = y$ to B by production (5)



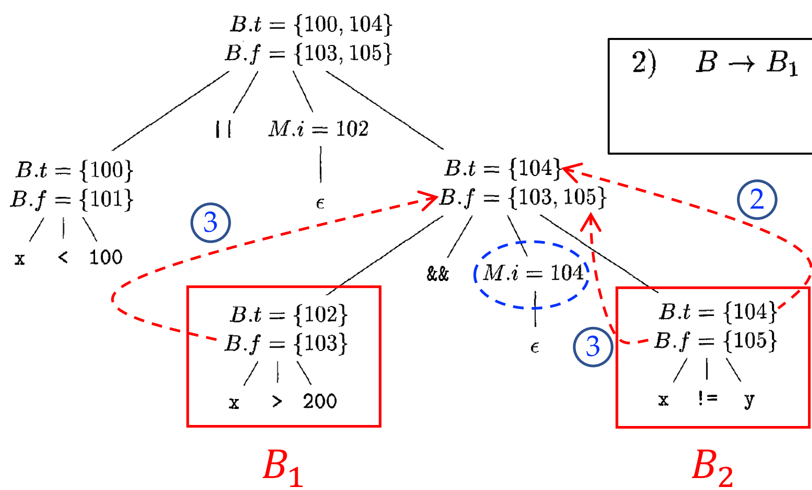
5)	$B \rightarrow E_1$ **rel** E_2	{ $B.truelist = makelist(nextinstr)$; $B.falselist = makelist(nextinstr + 1)$; *gen*('`if`' $E_1.addr$ **rel**.op $E_2.addr$ '`goto _`'); *gen*('`goto _`'); }

Generated code:

```
104:   if x != y goto _
105:   goto _
```

Example – Boolean Expressions

- Boolean expression: $x < 100 \parallel x > 200 \;\&\&\; x\,! = y$
- **Step 6:** reduce B_1 && MB_2 to B by production (2)



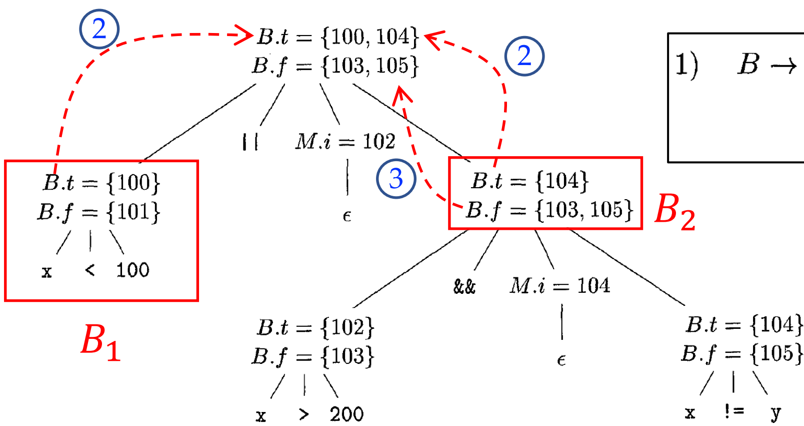
2) $B \rightarrow B_1 \;\&\&\; M\; B_2$ { $backpatch(B_1.truelist, M.instr)$; 1
$B.truelist = B_2.truelist$; 2
$B.falselist = merge(B_1.falselist, B_2.falselist)$; } 3

Backpatch instruction 102: 1

```
100:  if x < 100 goto _
101:  goto _
102:  if x > 200 goto 104
103:  goto _
104:  if x != y goto _
105:  goto _
```

Example – Boolean Expressions

- Boolean expression: $x \ < \ 100 \parallel x > 200 \,\&\&\, x\,! = y$
- **Step 7:** reduce $B_1 \ \parallel \ MB_2$ to B by production (1)



1)	$B \rightarrow B_1 \text{ || } M \ B_2$	$\{$ $backpatch(B_1.falselist, M.instr);$ 1 $B.truelist = merge(B_1.truelist, B_2.truelist);$ 2 $B.falselist = B_2.falselist; \}$ 3

Backpatch instruction 101: 1

```
100:  if x < 100 goto _
101:  goto 102
102:  if x > 200 goto 104
103:  goto _
104:  if x != y goto _
105:  goto _
```

The remaining jump targets will be filled in later parsing steps

Reading Tasks

- Chapter 6 of the dragon book
  - 6.1.1 Directed Acyclic Graphs for Expressions
  - 6.2 Three-Address Code
  - 6.3 Types and Declarations
  - 6.4 Translation of Expressions
  - 6.5 Type Checking (6.5.1 – 6.5.2)
  - 6.6 Control Flow (6.6.1 – 6.6.4)
  - 6.7 Backpatching (6.7.1 – 6.7.3)