Entries

RISCVAsmPrinter

RISCVInstPrinter

RISCVGenAsmWriter.inc

runOnMachineFunction
llvm/include/llvm/CodeGen/AsmPrinter.h

SetupMachineFunction(MF);
emitFunctionBody();

emitFunctionBody
llvm/lib/CodeGen/AsmPrinter/AsmPrinter.cpp

for (auto &MBB : *MF) {
   for (auto &MI : MBB) {
      emitInstruction(&MI);

emitInstruction
llvm/lib/Target/RISCV/RISCVAsmPrinter.cpp

if (!lowerRISCVMachineInstrToMCInst(MI, TmpInst, *this))
   EmitToStreamer(*OutStreamer, TmpInst);

EmitToStreamer
llvm/lib/Target/RISCV/RISCVAsmPrinter.cpp

AsmPrinter::EmitToStreamer(*OutStreamer, Res ? CInst : Inst);

EmitToStreamer
llvm/lib/CodeGen/AsmPrinter/AsmPrinter.cpp

S.emitInstruction(Inst, getSubtargetInfo());

emitInstruction
llvm/lib/MC/MCAsmStreamer.cpp

InstPrinter->printInst(&Inst, 0, "", STI, OS);

printInst
llvm/lib/Target/RISCV/MCTargetDesc/RISCVInstPrinter.cpp

printInstruction(NewMI, Address, STI, O);

printInstruction
llvm/lib/Target/RISCV/RISCGenAsmWriter.inc

printOperand(MI, 0, O);

printOperand
llvm/lib/Target/RISCV/MCTargetDesc/RISCVInstPrinter.cpp

if (MO.isReg()) {
   printRegName(O, MO.getReg());
if (MO.isImm()) {
   O << MO.getImm();

AsmPrinter was handled by MCAsmStreamer to printe each Instruction