

Entries	RISCVAsmPrinter	RISCVInstPrinter	RISCVGenAsmWriter.inc
runOnMachineFunction   Ilvm/include/Ilvm/CodeGen/AsmPrinter.h  SetupMachineFunction(MF); emitFunctionBody();  emitFunctionBody   Ilvm/lib/CodeGen/AsmPrinter/AsmPrinter.cpp  for (auto &MBB : *MF) {     for (auto &MI : MBB) {         emitInstruction(&MI);     }	emitInstruction   Ilvm/lib/Target/RISCV/RISCVAsmPrinter.cpp   if (!lowerRISCVMachineInstrToMCInst(MI, TmpInst, *this))		
EmitToStreamer   Ilvm/lib/CodeGen/AsmPrinter/AsmPrinter.cpp   S.emitInstruction(Inst, getSubtargetInfo());   emitInstruction   Ilvm/lib/MC/MCAsmStreamer.cpp   InstPrinter->printInst(&Inst, 0, "", STI, OS);	EmitToStreamer (*OutStreamer, TmpInst);  EmitToStreamer  Ilvm/lib/Target/RISCV/RISCVAsmPrinter.cpp  AsmPrinter::EmitToStreamer (*OutStreamer, Res ? Clnst : Inst);	printInst   vm/lib/Target/RISCV/MCTargetDesc/RISCVInstPrinter.cpp  printInstruction(NewMI, Address, STI, O);	printInstruction   vm/ ib/Target/RISCV/RISCGenAsmWriter.inc    printOperand(MI, 0, 0);
		<pre>printOperand   Ivm/lib/Target/RISCV/MCTargetDesc/RISCVInstPrinter.cpp if (MO.isReg()) {     printRegName(0, MO.getReg()); if (MO.isImm()) {     O &lt;&lt; MO.getImm(); }</pre>	