

Entries	RISCAsmPrinter	RISCVInstPrinter	RISCVGenAsmWriter.inc
runOnMachineFunction llvm/include/llvm/CodeGen/AsmPrinter.h			
SetupMachineFunction(MF);			
emitFunctionBody();			
emitFunctionBody llvm/lib/CodeGen/AsmPrinter/AsmPrinter.cpp			
for (auto &MBB : "MF) { for (auto &MI : MBB) {			
for (auto &MI : MBB) { emitInstruction(&MI);	emitFunctionBody Ilvm/lib/Target/RISCV/RISCVAsmPrinter.cpp		
	if (!lowerRISCVMachineInstrToMCInst(MI, TmpInst, *this)) EmitToStreamer(*OutStreamer, TmpInst);		
	EmitToStreamer Ilvm/lib/Target/RISCV/RISCVAsmPrinter.cpp		
	AsmPrinter::EmitToStreamer(*OutStreamer, Res ? Clnst : Inst);		
	EmitToStreamer llvm/lib/CodeGen/AsmPrinter/AsmPrinter.cpp		
	S.emitInstruction(Inst, getSubtargetInfo());		
	EmitToStreamer		
	Ilvm/lib/MC/MCAsmStreamer.cpp InstPrinter->printinst(&Inst, 0, ™, STI, OS);	printlnst	
	max max paramodamod of 100%	Ilvm/lib/Target/RISCV/MCTargetDesc/RISCVInstPrinter.cpp	printlnstruction
		printlnstruction(NewMI, Address, STI, O);	llvm/lib/Target/RISCV/RISCGenAsmWriter.inc
			printOperand(MI, 0, O);
		printOperand	
		<pre>Ilvm/lib/Target/RISCV/MCTargetDesc/RISCVInstPrinter.cpp if (MO.isReg()) {</pre>	
		<pre>if (MO.isReg()) { printRegName(O, MO.getReg()); if (MO.isImm()) {</pre>	
		O << MO.getImm();	