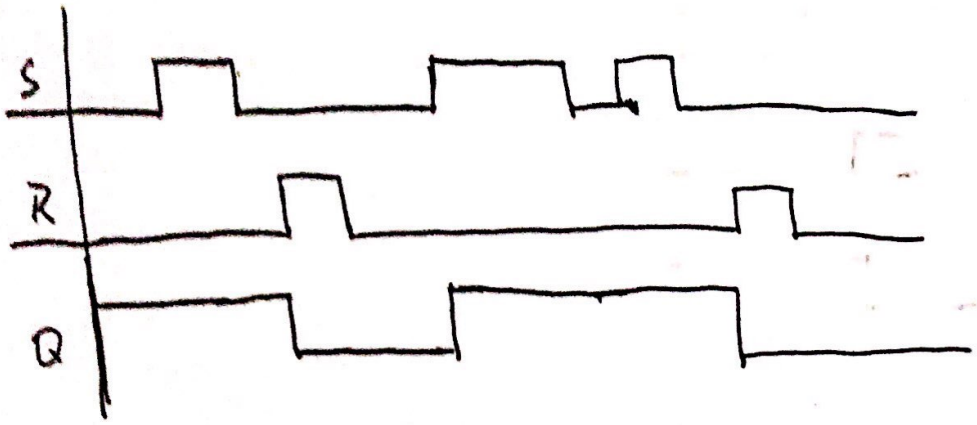
Ve270 HW4 夏陶院 518370910087.

1.



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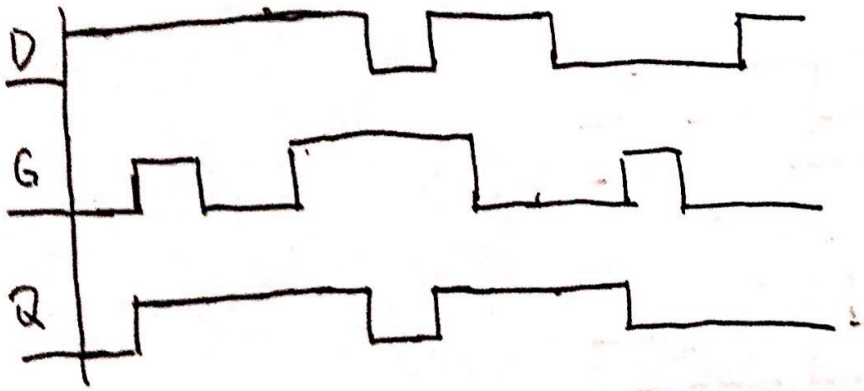

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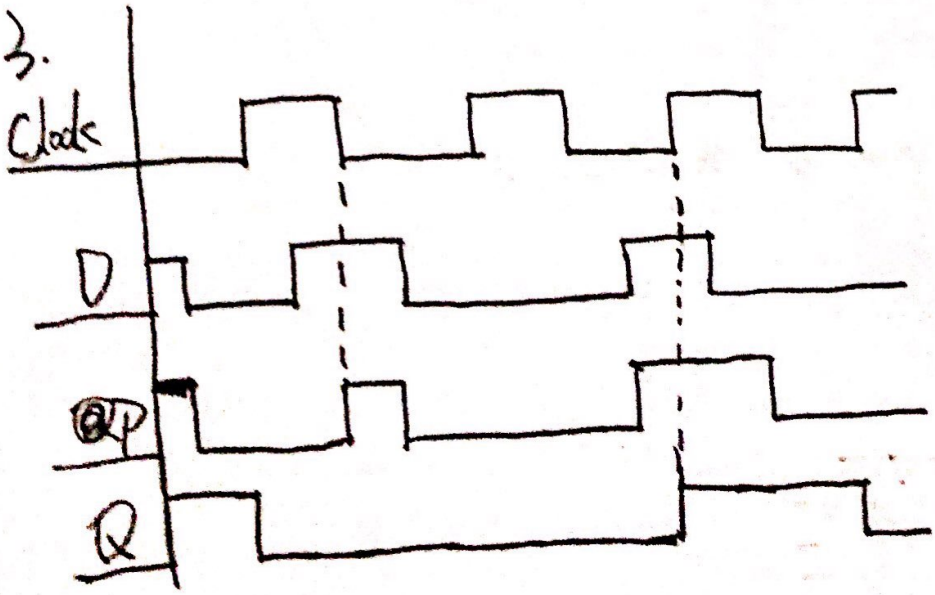

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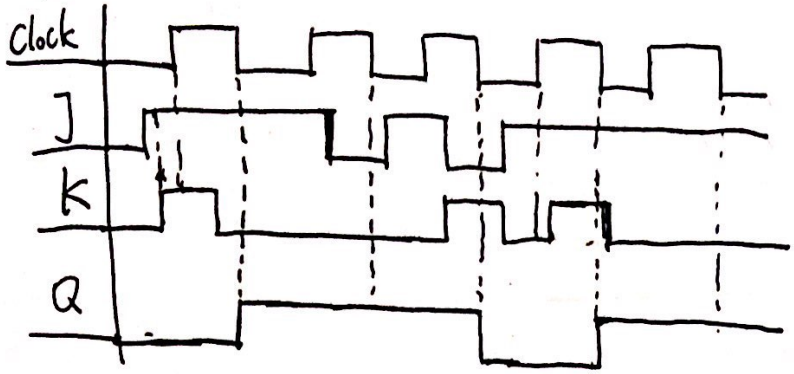

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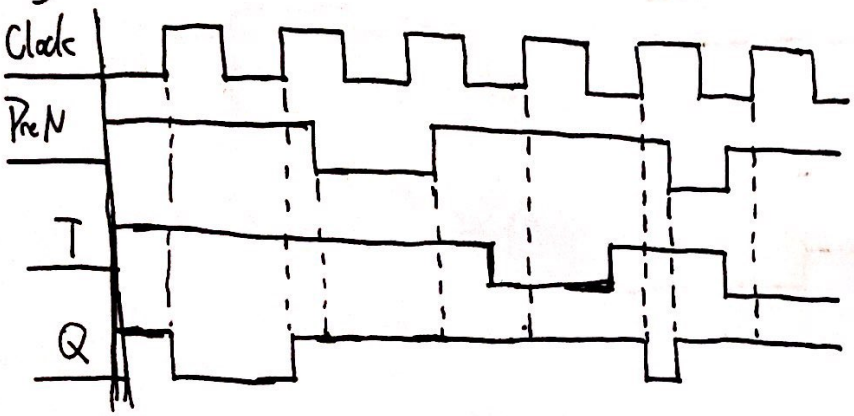
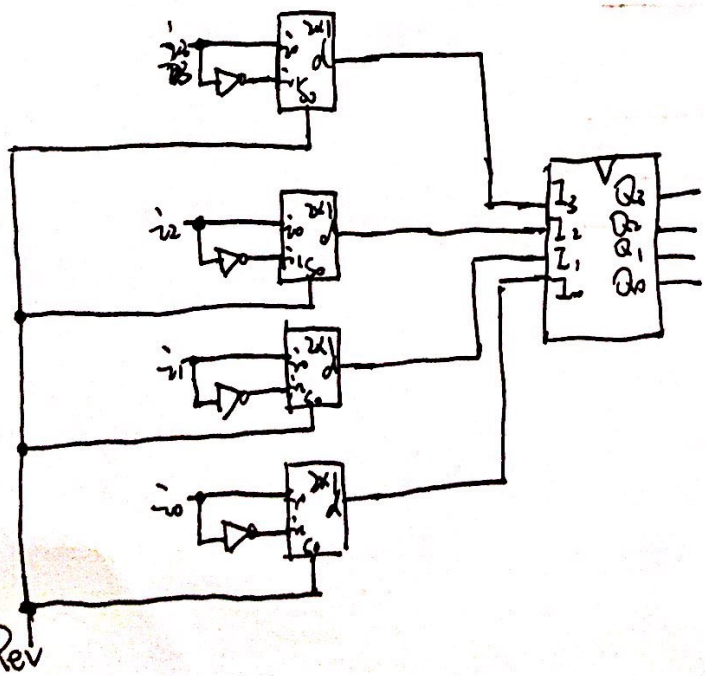


6. Assume the input 4-bit signal is $i_3 i_2 i_1 i_0$.



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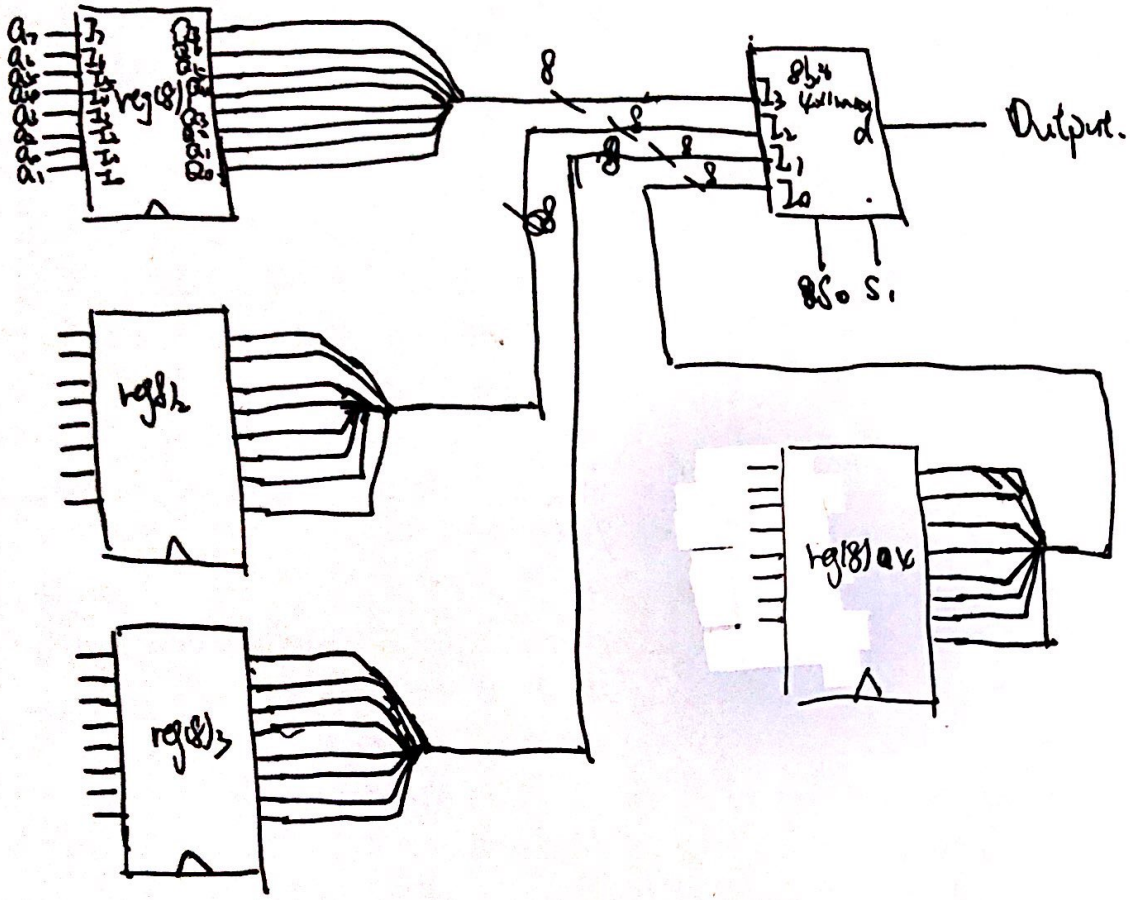
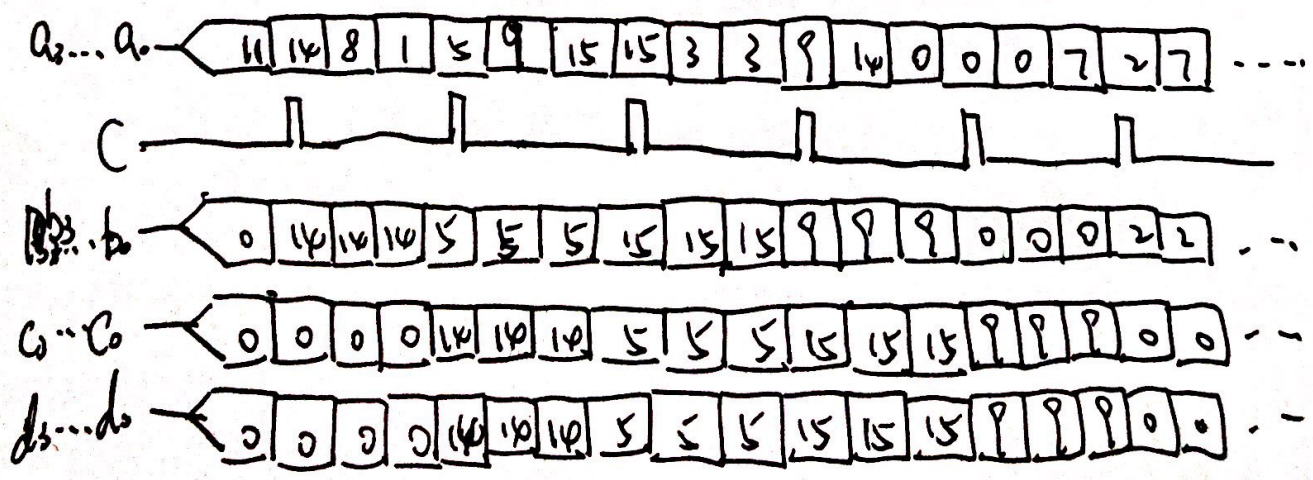
8.