**Ve270 Introduction to Logic Design Homework 6**

**Assigned: June 18, 2020**

**Due: July 2, 2020, 2:00pm.**

**A pop quiz will be given on the due date.**

1. Problem 4.2 (10 points)



1. Problem 4.3, using components to draw schematic (20 points)
2. Problem 4.45. Draw schematic. (20 Points).
3. Problem 4.57. Draw schematic (a), (b). (20 points)
4. Problem 4.59. Draw schematic (10 Points)
5. Model a Universal Shift Register (shown below) with Verilog. Simulate your design. (20 Points)



