Version_Revisions

Rev	Date	Description	Drawn
V1.0	2023_11_19	First Release	XieHaotian

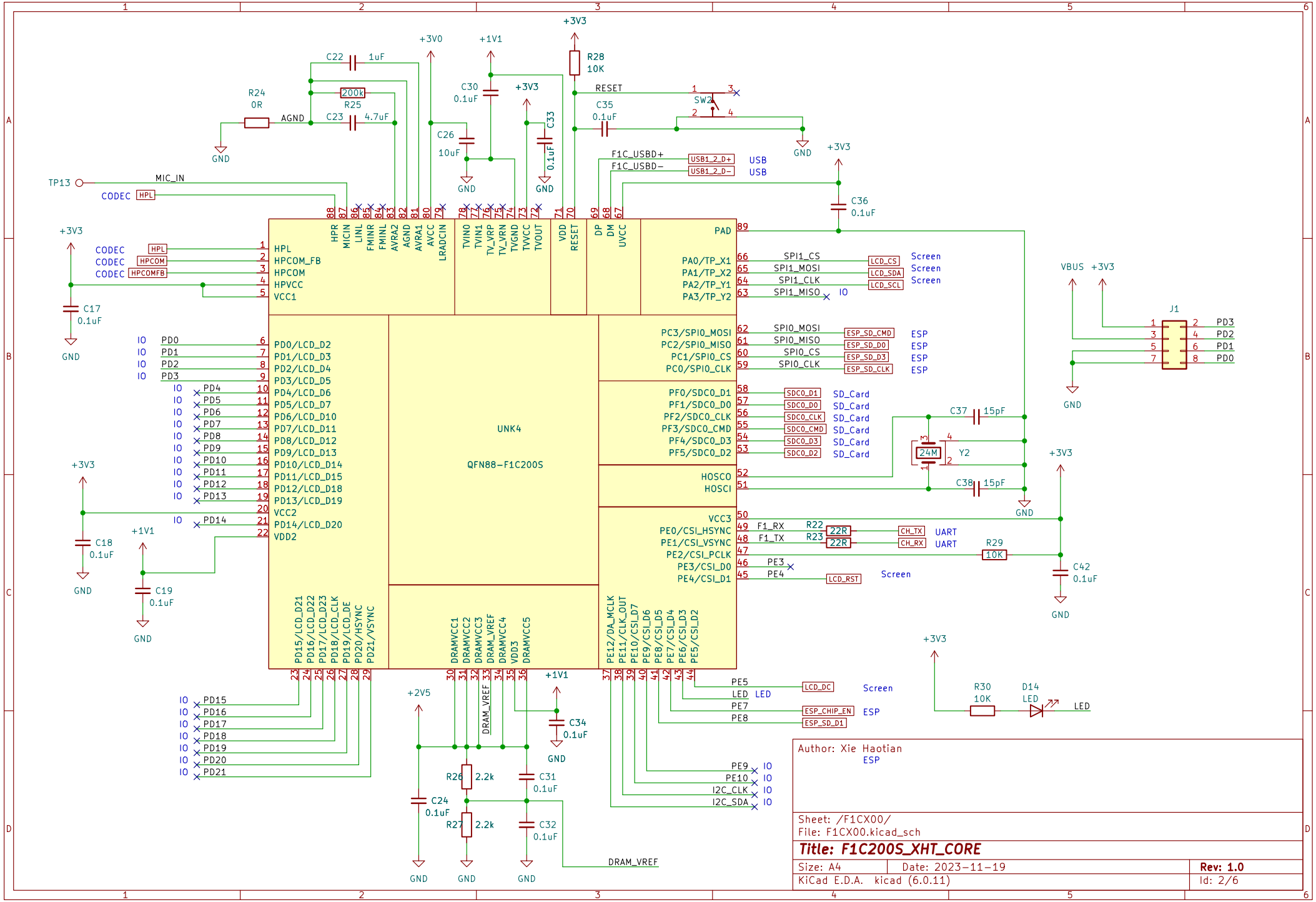
F1C200S_XHTCORE

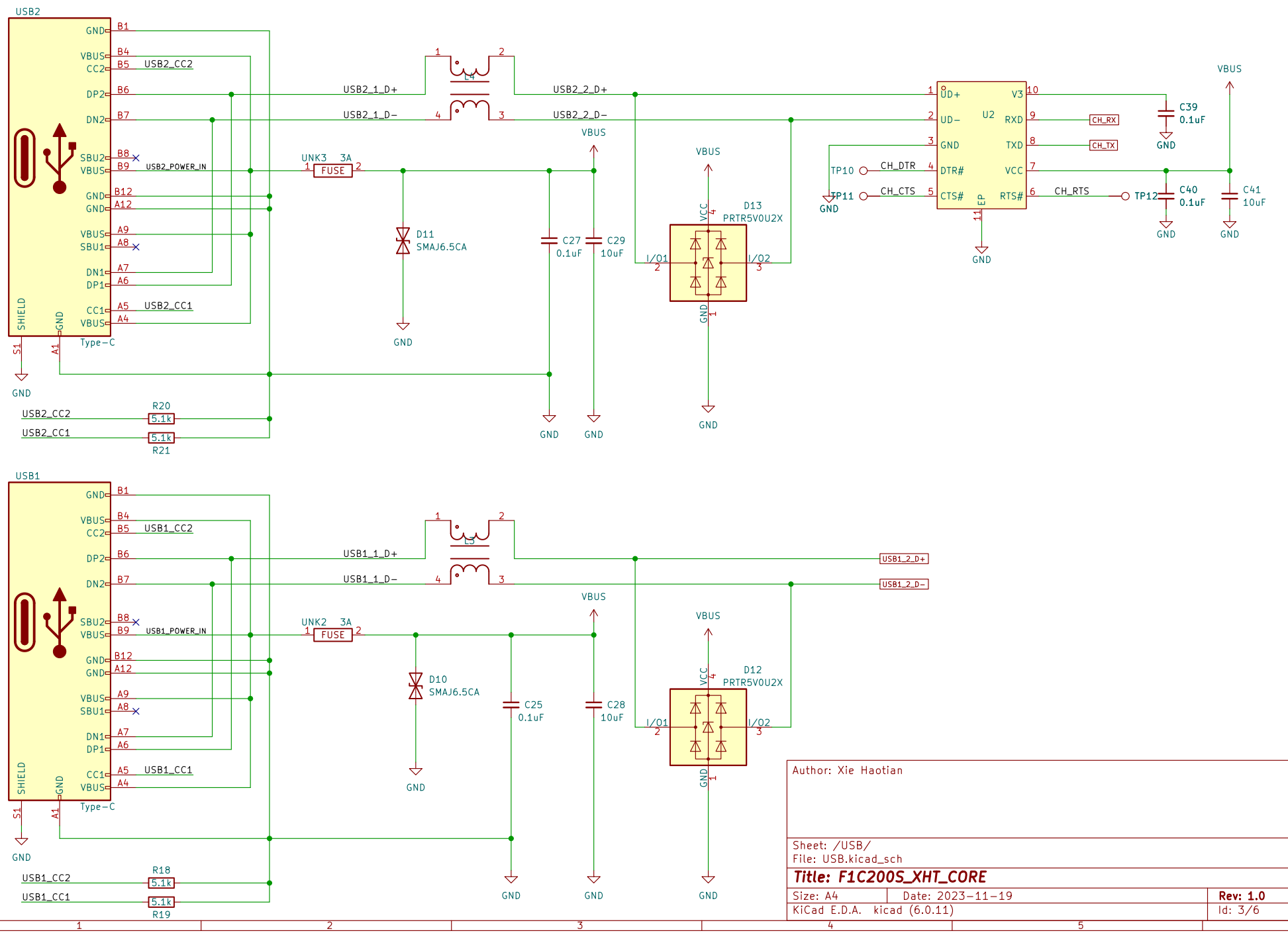
Author: Xie Haotian
https://space.bilibili.com/1855864901
https://xiehaotian.com/linuxpc/f1c200s-xht-core

Sheet: /
File: F1c200s_XhtCore.kicad_sch

Title: F1C200S_XHT_CORE

Size: A4	Date: 2023-11-19	**Rev: 1.0**
KiCad E.D.A. kicad (6.0.11)		Id: 1/6


UNK4
QFN88-F1C200S
Author: Xie Haotian
ESP
Sheet: /F1CX00/
File: F1CX00.kicad_sch
Title: F1C200S_XHT_CORE
Size: A4
Date: 2023-11-19
Rev: 1.0
KiCad E.D.A. kicad (6.0.11)
Id: 2/6



Author: Xie Haotian

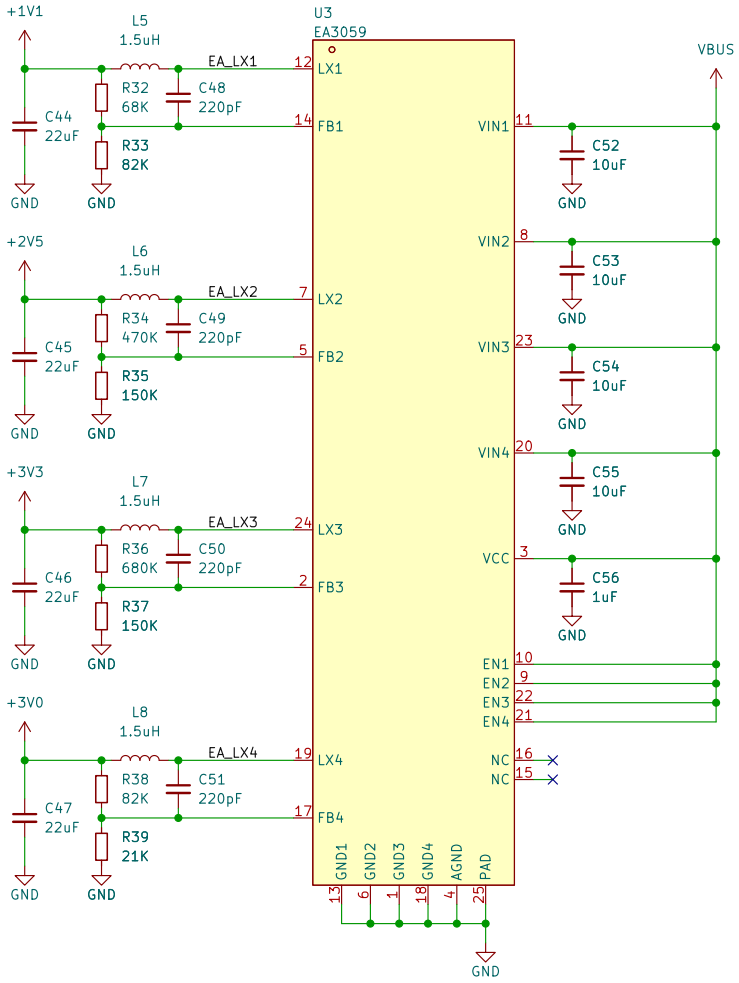
Sheet: /USB/
File: USB.kicad_sch

Title: F1C200S_XHT_CORE

Size: A4	Date: 2023-11-19	**Rev: 1.0**
KiCad E.D.A. kicad (6.0.11)		Id: 3/6

DCDC部分，输出值

DCDC部分，输出值

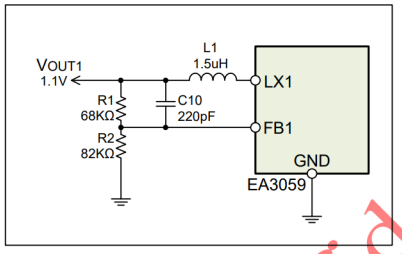


Application Information

Output Voltage Setting

Each of the regulators output voltage can be set via a resistor divider (ex. R1, R2). The output voltage is calculated by following equation:

$$V_{OUT1} = 0.6 \times \frac{R1}{R2} + 0.6\ V$$



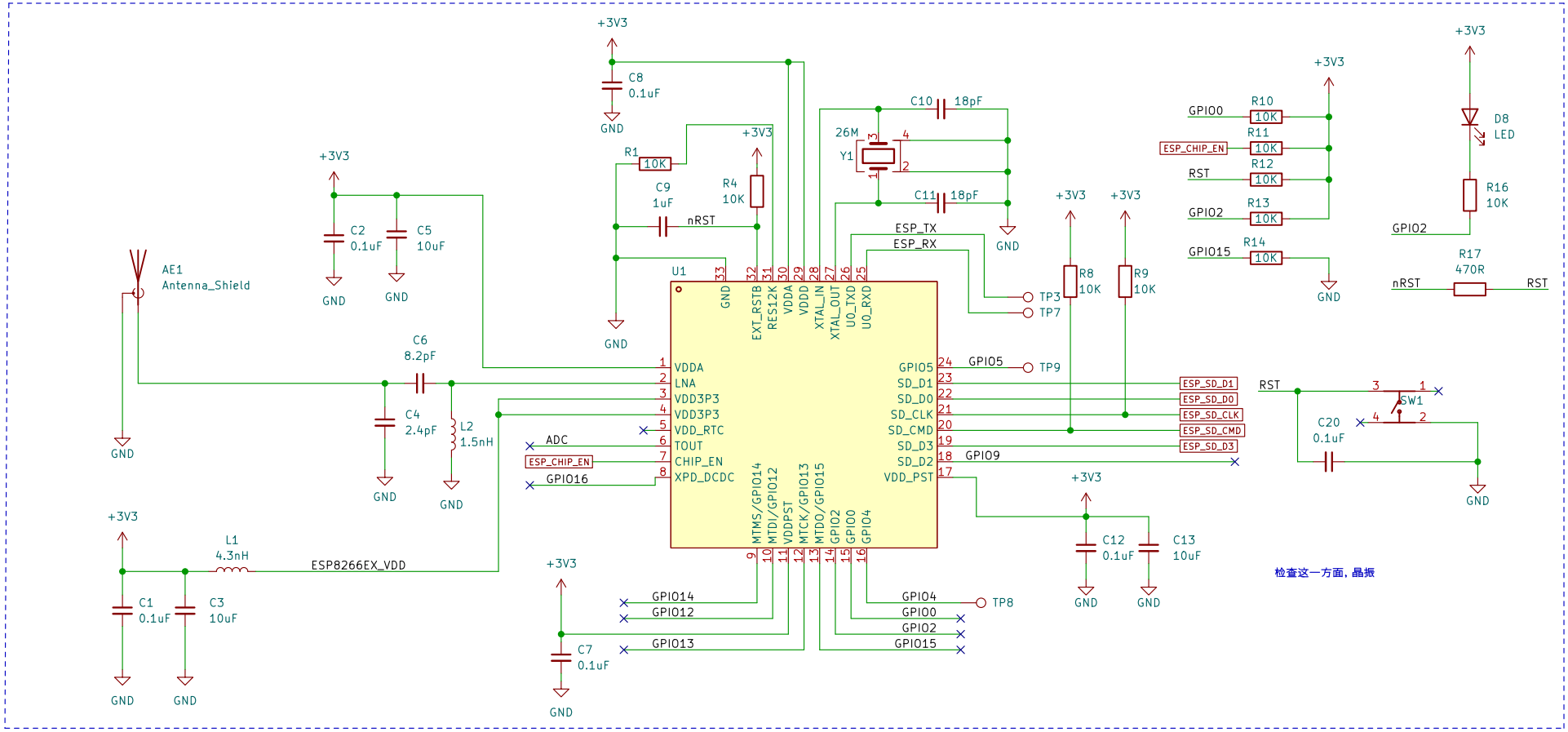
DC/DC1	+1V1	1.1V@2A	VCC 1V1 FOR SOC 100MA
DC/DC2	+2V5	2.5V@2A	VDD 2.5V FOR SOC DRAM 150MA
DC/DC3	+3V3	3.3V@2A	VDD 3.3V FOR SOC、SCREEN、SD 650MA
DC/DC4	+3V0	3.0V@2A	VCC 3.0V FOR SOC 100MA

Author: Xie Haotian

Sheet: /POWER/
File: POWER.kicad_sch

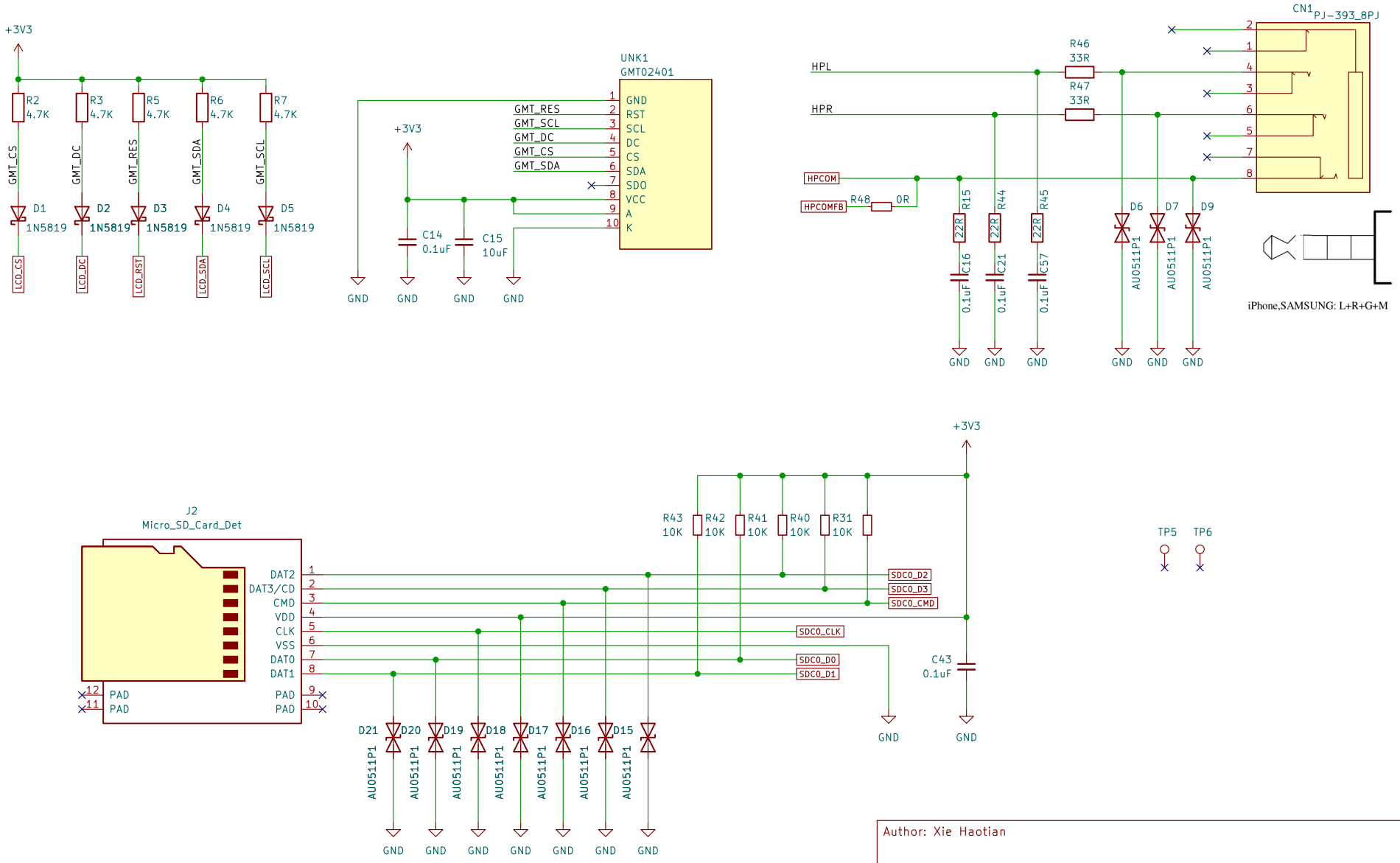
Title: F1C200S_XHT_CORE

Size: A4	Date: 2023-11-19	**Rev: 1.0**
KiCad E.D.A. kicad (6.0.11)		Id: 4/6



Author: Xie Haotian	
Sheet: /WIFI/ File: WIFI.kicad_sch	
Title: F1C200S_XHT_CORE	
Size: A4	Date: 2023-11-19
KiCad E.D.A. kicad (6.0.11)	Id: 5/6

Rev: 1.0



Author: Xie Haotian

Sheet: /AUDIO&SCREEN&SD/
File: AUDIO&SCREEN&SD.kicad_sch

Title: F1C200S_XHT_CORE

Size: A4	Date: 2023-11-19	**Rev: 1.0**
KiCad E.D.A. kicad (6.0.11)		Id: 6/6