位相検出



【周辺機能】

出力

ポート(2pin 選択)

タイマ(1ch,チャネル0 or 1 or 5 選択)

ELOMONIレジスタリード(1bit 固定)

[ELCL]

ELL2SEL T/B

【スペック定義・タイミング対策】

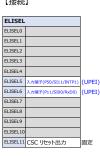
ELL3SEL T/B

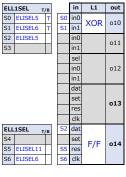
ELCL	他機能と排他使用可&ブロック選択可
	【使用リソース】
	入力: 3ch
	出力: 2ch
	内部:1ブロック

in L2 out

ポート	・外部端子からの内部伝搬遅延を
TAU0	スペックとして定義する
	内部伝搬遅延時間: Max 50ns

ELCLを経由する内部遅延は40ns程度。
768K版まで同じスペックとするため、内部
伝搬遅延時間を50nsとしておく。



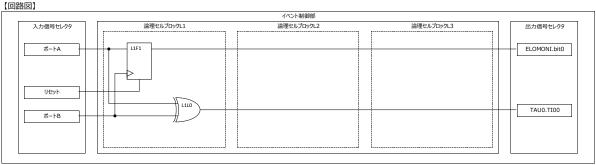


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1	S0			in0	o20
	S1		П	in1	020
	S2			in0	021
	S3			in1	021
				sel	
				in0	o22
				in1	
				dat	
				set	o23
				res	023
				clk	
	ELL	2SEL T	/B	dat	
	S4			set	024
	S5		П	res	024
	S6			clk	

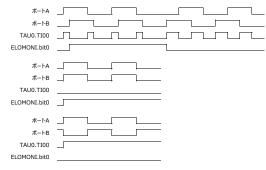
		1/6				L
S0			in0		030	Ī
S1			in1			
S2			in0		o31	Ī
S3			in1			
	•		sel			Ī
			in0		o32	
			in1			
			dat			Ī
			set		o33	
			res		033	
			clk			
	3SEL	T/B	dat			Ī
S4			set		o34	
S5			res		034	
S6			clk			

in L3 out

E	LOSEL T		
	014		固定
	o10		(UPEB,UPEO
			(UPEB,UPEO
			(UPEB,UPEO



【タイミングチャート】



【ELCL経由遅延】 Incr Path Point input external delay 0.000 f P50 (inout) 0.000 & 0.000 f pericore/port5_iobuf/iobuf0/SPAD (P3B20EKASACCTW) 0.000 & 0.000 f pericore/port5_iobuf/iobuf0/DIN (P3B20EKASACCTW) pericore/port5/portbit0/U7/YB (TM7AN21X40) 16 518 & 16 518 f 1. 330 & 17. 848 r pericore/port5/portbit0/U8/YB (TM7NOR2ZHAX40) pericore/mp200/elcl/postRoute0pt1st_setup_I_FE_PDC46_pt_p50elclin/Y (TM7BUFX30) 2 188 % 20 036 f pericore/mp200/elcl/l_url78helcl21_insel_0/1_url78helcl21_selc_5/U30/YB (TM7AM22X05) pericore/mp200/elcl/l_url78helcl21_insel_0/1_url78helcl21_selc_5/U32/YB (TM7ON2BB11X08) 0 632 & 22 192 r 0.356 & 22.547 f per loor*/mpDOV/els//_Lurl*/abels/2_inset_JV_ 0 578 & 23 126 f 0.592 & 23.718 f 0.322 & 24.040 r 0. 260 & 24. 301 f 0.260 & 24.561 r pericore/mp200/elcl/placeOptI_FE_0FC382_insel_0_out_5/YB (TM71NVZHHX100) 0. 239 & 24. 799 f pericore/mp200/elc// ur/78helc/21 elc 11/1 ur/78helc/21 elcblk in12out9 0/1 ur/78helc/21 selc/U20/Y (TM7AND3XO5) 0 404 & 25 203 f pericore/mp200/elcl/1_url78helcl21_elc_11/1_url78helcl21_elcblk_in12out9_0/1_url78helcl21_elcblk_in2out9_0/1_url78helcl21_elcb 0. 472 & 25. 676 r 0 274 & 25 950 f pericore/mp200/elcl/I_url78helcl21_elc_l1/I_url78helcl21_elcblk_in12out9_0/I_url78helcl21_selc/U33/YB (TM70N21ZRX10) 0.366 & 26.315 r pericore/mp200/elcl/I url78helcl21 elc 11/I url78helcl21 elcblk in12out9 0/I url78helcl21 selc/U16/YB (TM7NAND2X10) 0. 258 & 26. 573 f pericore/mp200/elcl/I_url78helcl21_elc_l1/I_url78helcl21_elcblk_in12out9_0/I_url78helcl21_licc/U1/Y (TM7X0R2ZX10) pericore/mp200/elcl/I_url78helcl21_elc_l1/1_url78helcl21_elcblk_in12out9_0/I_url78helcl21_dec4to9/U12/YB (TM7NAND2X10) 0.208 & 27.307 r pericore/mp200/elcl/1_url78helcl21_elc_11/1_url78helcl21_elcblk_in12out9_0/1_url78helcl21_dec4to9/029/78 (TMT71NV2TX10) pericore/mp200/elcl/1_url78helcl21_elc_11/1_url78helcl21_elcblk_in12out9_0/1_url78helcl21_dec4to9/017/78 (TM7NANDX10) 0. 245 & 27. 552 f 0.279 & 27.831 r pericore/mp200/elcl/I_url78helcl21_elc_l1/U5/YB (TM7NAND4X10) 0.541 & 28.372 f pericore/mp200/elcl/I url78helcl21 lchtop 0/I url78helcl21 lch type1 0/U1/Y (TM7X0R27X10) 0 742 & 29 113 r periore/mp200/elcl/1_url78helcl21_lcbtop_0/1_url78helcl21_lcb_type1_0/1_url8helcl21_selc_0/U7/Y8 (TM/TMR22ZHX10) periore/mp200/elcl/1_url78helcl21_lcbtop_0/1_url78helcl21_lcb_type1_0/1_url8helcl21_selc_0/U2/Y8 (TM/TMR2ZHX10) 0. 137 & 29. 250 f 0 393 & 29 643 r pericore/mp200/elcl/placeOptl_FE_OFC440_cbl_out_0/Y (TM7BUFFZHOX30) pericore/mp200/elcl/U40/YB (TM7INVZHX10) 0. 436 & 30. 080 r 0. 280 & 30. 360 f pericore/mp200/elcl/placeOptl_FE_0FC441_n55/Y (TM7BUFFZHOX30) pericore/mp200/elcl/placeOptl_FE_0FC441_n55/Y (TM7BUFFZHOX30) 0. 281 & 30. 641 f 0.179 & 30.819 r pericore/mp200/elcl/placeOptl_FE_0FC442_n56/YB (TM7INVZHHX20) pericore/mp200/elcl/placeOptl_FE_0FC443_n56/YB (TM7INVZHHX70) 0. 208 & 31. 027 f 0. 208 & 31. 236 r pericore/mp200/elc/l_url78helcl21_outsel/1_url78helcl21_outsel/1_url78helcl21_outsel/blk_3/1_url78helcl21_selc_0/U12/YB (TM7NAND2ZHX10) pericore/mp200/elcl/1_url78helcl21_outsel/1_url78helcl21_outsel/blk_3/1_url78helcl21_selc_0/U13/YB (TM7AN31X05) 0. 223 & 31. 458 f 0. 535 & 31. 993 r pericore/mp200/elci/1_ur178helcl21_outsel/1_ur178helcl21_outsel_blk_3/1_ur178helcl21_selc_0/U14/YB_(TNTAN21X05) pericore/mp200/elci/1_ur178helcl21_outsel/1_ur178helcl21_outsel_blk_3/1_ur178helcl21_selc_0/U25/YB_(TNTON21X10) 0. 270 & 32. 263 f 0.558 & 32.821 r pericore/mp200/elcl/l_url78helcl21_outsel/l_url78helcl21_outsel_blk_3/l_url78helcl21_licc/Ul/Y (TM7X0R2ZX05) pericore/mp200/elcl/l_url78helcl21_outsel/l_url78helcl21_outsel_blk_3/postCtsOptI_FE_0FC108_log_inv_out/Y (TM78UFX20) 0. 617 & 33. 438 r 0. 362 & 33. 801 r pericore/mp200/elcl/l_url78helcl21_outsel/l_url78helcl21_outsel_blk_3/placeOptl_FE_0FC8151_log_inv_out/Y (TM78UFFZH0X10) pericore/mp200/elcl/l_url78helcl21_outsel/l_url78helcl21_outsel_blk_3/placeOptl_FE_0FC7119_log_inv_out/Y (TM78UFFZH0X10) 0.241 & 34.041 r 0. 230 & 34. 272 r pericore/mp200/elcl/1_url78helcl21_outsel/1_url78helcl21_outsel_blk_3/placeOptl_FE_0FC470_log_inv_out/Y8 (TM71MVZHHX20) pericore/mp200/elcl/1_url78helcl21_outsel/1_url78helcl21_outsel_blk_3/placeOptl_FE_0FC471_log_inv_out/Y8 (TM71MVZHHX70) 0 157 & 34 428 f 0.098 & 34.527 r pericore/mp200/elol/I_url78helol2I_outsel/l_url78helol2I_outsel_blk_3/I_url78helol2I_outmask/C7/Y (TM7AND2X10) pericore/mp200/elol/placeOptI_FE_0FC7123_elol_out3_n/Y6 (TM7INVZHHX40) 0. 407 & 34. 934 r 0. 199 & 35. 133 f pericore/mp200/elcl/placeOptI_FE_0FC7124_elcl_out3_n/YB (TM7INVZHHX100) 0 173 & 35 305 r pericore/mp200/placeOptI_FE_0FC477_elcl_out3_n/YB (TM71NVZHX40) 0.149 & 35.454 f pericore/mp200/placeOptI_FE_OFC479_elcl_out3_n/YB (TM71NVZHHX160) pericore/mp200/elcl_out3_i/Y (TM7BUFCLX10) 0.205 & 35.659 r 0.865 & 36.524 r pericore/porga/main/tis1sel/U3/Y (TM7A022X09) pericore/mp200/tau_0/tau08_0/TINO (QLKORTAU08R2V1) <-1 208 & 37 732 r 0.000 & 37.732 r 0.004 & 37.736 r pericore/mp200/tau_0/tau08_0/ch0/filter/ti_ff1_reg/DATA (TM7SDFFQRZMAX10)