

OR

AND

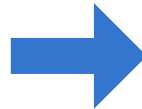
$\text{din}[0][W-1, \dots, 2, 1, 0] \& \{W\{\text{sel}[0]\}\}$

$\text{din}[1][W-1, \dots, 2, 1, 0] \& \{W\{\text{sel}[1]\}\}$

...

...

$\text{din}[S-1][W-1, \dots, 2, 1, 0] \& \{W\{\text{sel}[S-1]\}\}$



$\text{dout}[W-1, \dots, 2, 1, 0]$