CAMEROON GENERAL CERTIFICATE OF EDUCATION BOARD

General Certificate of Education Examination

JUNE 2018

ADVANCED LEVEL

Sub ject Title	Computer Science	•.(0)
Paper No.	2	X
Subject Code No.	0795	

Two and a Half Hours

Answer any SIX questions.

All questions carry 17 marks each. For your guidance, the approximate mark for each part of a question is indicated in brackets.

You are reminded of the necessity for good English and orderly presentation in your answers.

In calculations, you are advised to show all the steps in your working, giving your answer at each stage. Calculators

are allowed.

Where an imperative programming language is required to write program code, either standard (ISO) Pascal or the standard (ANSI C or CII) programming language may be used.

Turn Over

- (ii) LANs are used to help communication around a company. As part of the NATH computer network, explain the purpose of:
 - (a) Routers.
 - (b) Bridges.
 - (c) Moderms. (6 marks)

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(b) Explain two types of infonnation systems.

(2 marks) (2 marks)

- (ii) In a small shop in your quarter, the shopkeeper has two record books; one for his daily sales and the other for his debtors and creditors. He adds up sales per day to know how much money he has made on that day. He equally adds up amounts loaned to him and borrowed out (in articles) to know how much he owes and how much he is owed respectively. He also subtracts money loaned to him from money owed him to know his debt balance. In the end he adds the balance to his sales to know his business state of account. That is whether he runs on surplus or deficit.
 - (a) Has this shop got an information system? Explain! Give the type of information system in case of a yes.

system in case of a yes.

(b) This shopkeeper gets someone to design a database for his shop. The designer brings

(4 marks)

out a table as shown below

BuyerID BorrowerID Creditor ID Amount Date

(c) In what normal form is the table above? Why?

(d) Given your knowledge of the situation in the shop, put the supposed database (the table) in its 3rd normal form (3NF).

(2 marks)

(e) Give the various database entities that result from the normalisation in (c) above, with their various attributes.

(6 marks)
(1 mark)

6. (i) (a) Describe briefly the principle of binary search of an element in an array

(3 marks)

(b) State the nature (iterative or recursive) of the algorithm "binary search" Justify your choice. State the time complexity of this algorithm.

(3 marks)

(ii) What do you understand by algorithmic complexity?

(2 marks)

(iii) Study the algorithm below and answer the questions that follow.

Algorithm INSORT (RealT[], Integer n)

<u>Begin</u> Integer i, k;

Real Aux;

 $\underline{For}(k=2 \text{ to } k=n) \underline{do} \text{ } Aux < -T[k]; i < -k-1;$

While (i)=l and T[i]>aux) do T[i+1]<-

T[i];

I <—*I* -1:

Endwhile

 $T[i+1] < \sim aux;$

<u>Endfor</u>

End

(a) Estimate the time complexity of this algorithm in terms of n, considering the main operation of the algorithm to be "T[i+1] < -T[i]

(3 marks)

(b) Do a dry run of this algorithm with the array having as configuration at the beginning of the process.

(5 marks) (1 mark)

(c) Deduce what the algorithm does, giving your reason.

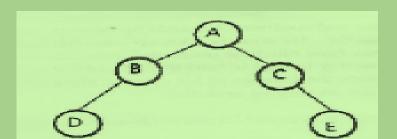
7 (i) Define a pointer. What is a null pointer?

(4 marks)

(ii) (a) What is a complete binary tree?

(3 marks) (4 marks)

(b) Represent the following Binary tree using an array representation.



- (iii)
- (a) What is Object-Oriented Programming (OOP)?

(2 marks)

(b) What are the various elements of OOP?

(4 marks) (5 marks)

8. (i) List IN ORDER the various steps of the software development cycle.

(ii) What is the advantage of using prototype software development model instead of waterfall model? Explain the effect of defining a prototype on the overall cost of the software project?

(6 marks)

(iii)

Define software reliability. What is the difference between hardware & software reliability?

(6 marks)

9. (i) Simplify the following Boolean expressions using Boolean algebra.

(4 marks)

$$ABC+ABC+ABC+ABC+ABC$$

(ii) Use NAND gates, NOR gates, or a combination of both to implement the following

logic expressions:

$$X = ACD + EF + AF$$

(4 marks)

- (iii) A microprocessor uses RAM chips of 1 x 1024 capacity.
 - (a) How many chips will be required and how many address lines will be connected to provide capacity of 1024 bytes.

(6 marks)

(b) How many chips will be required to obtain a memory of capacity, 16 K bytes.

(3 marks)