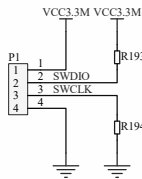
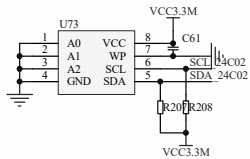


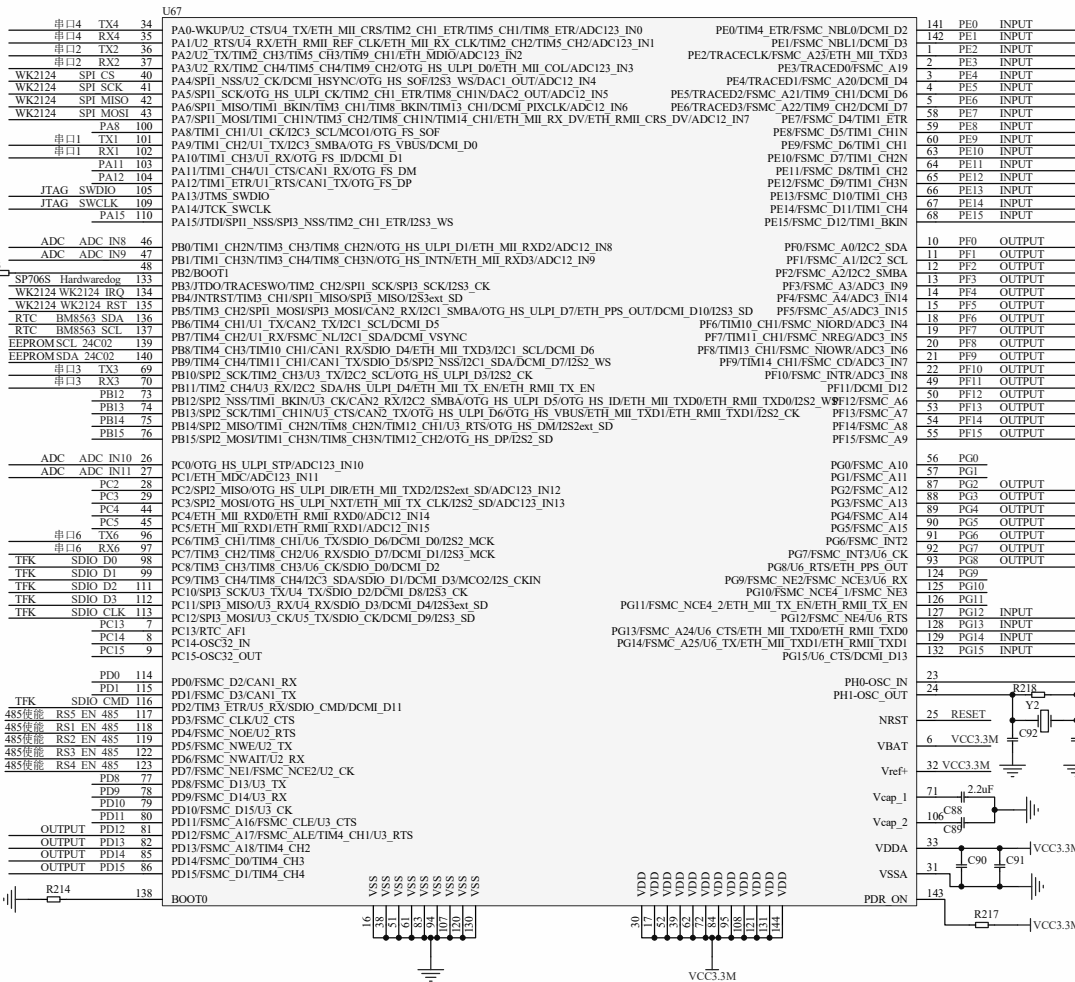
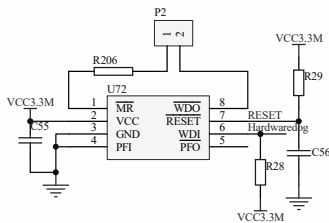
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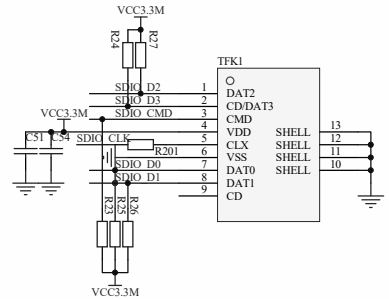
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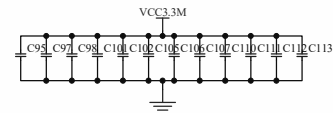
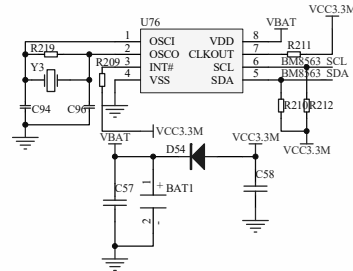
RESET



TF卡



RTC

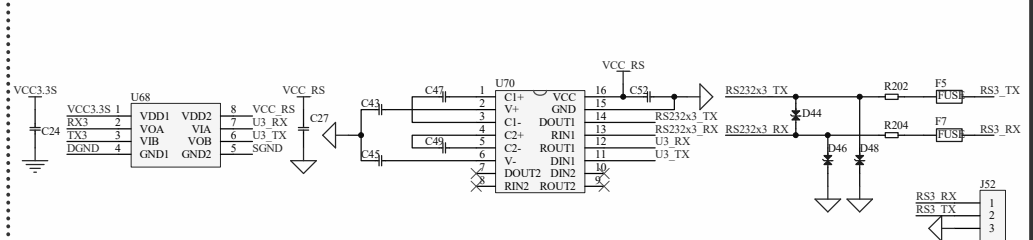
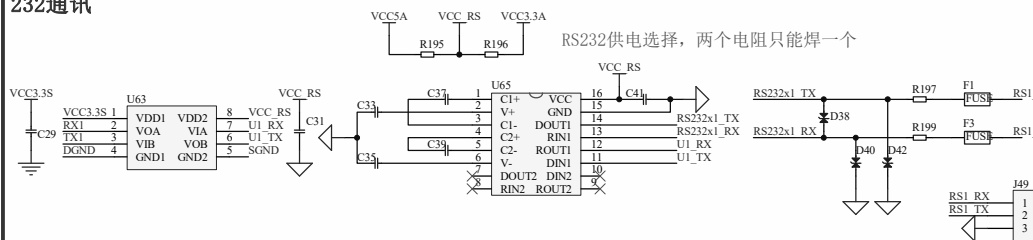


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审核				型号	STD-XXXX
日期	2022/4/13 15:47:45		第1页，共1页		Size E
厦门斯坦道科学仪器股份有限公司					

232通讯

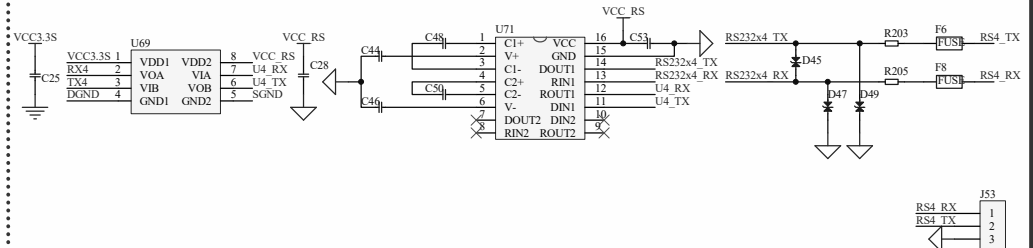
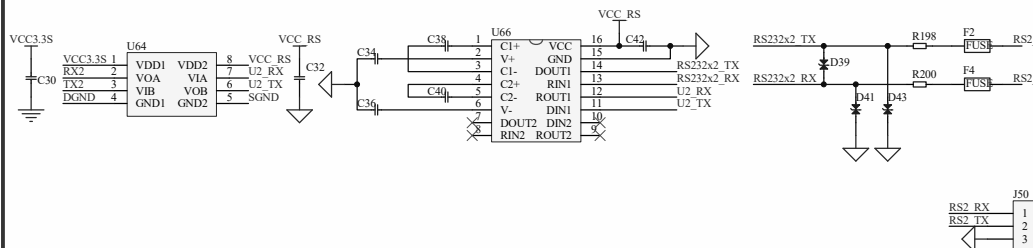
UPS通讯

预留

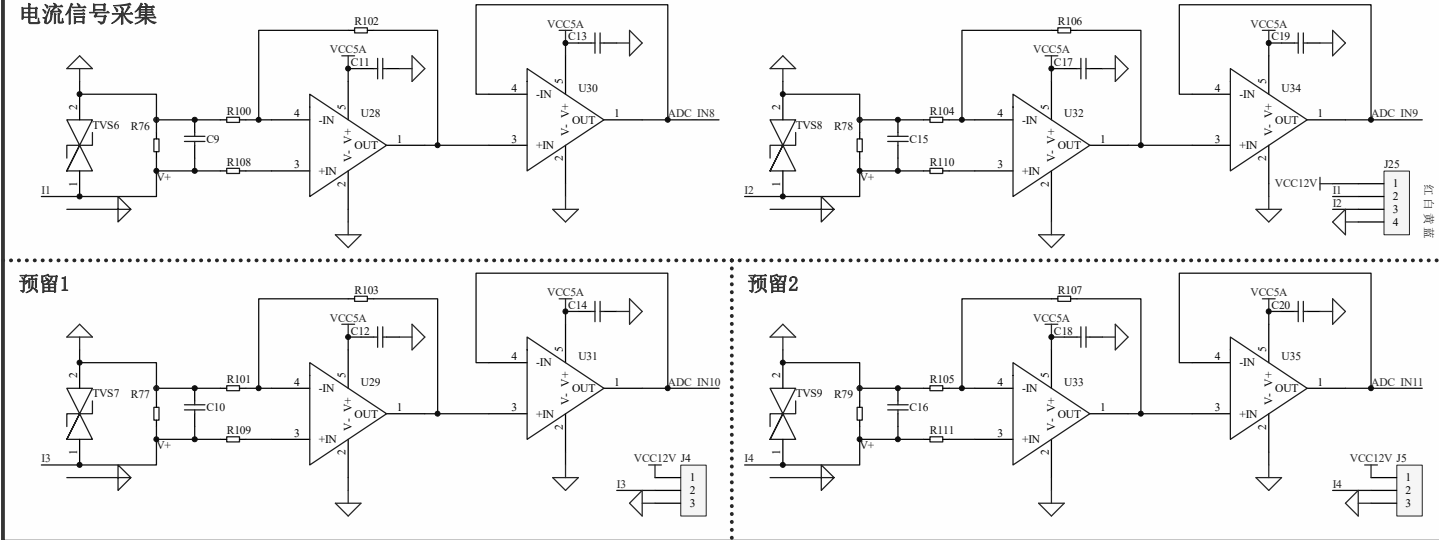


工控机通讯

预留



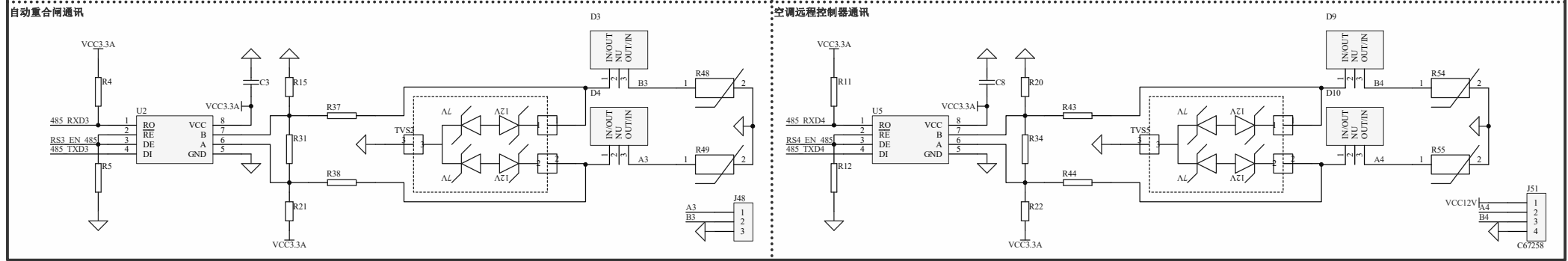
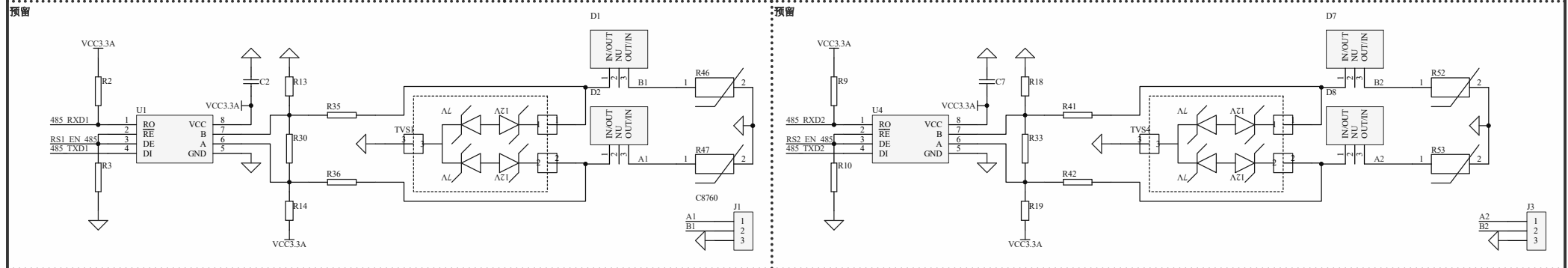
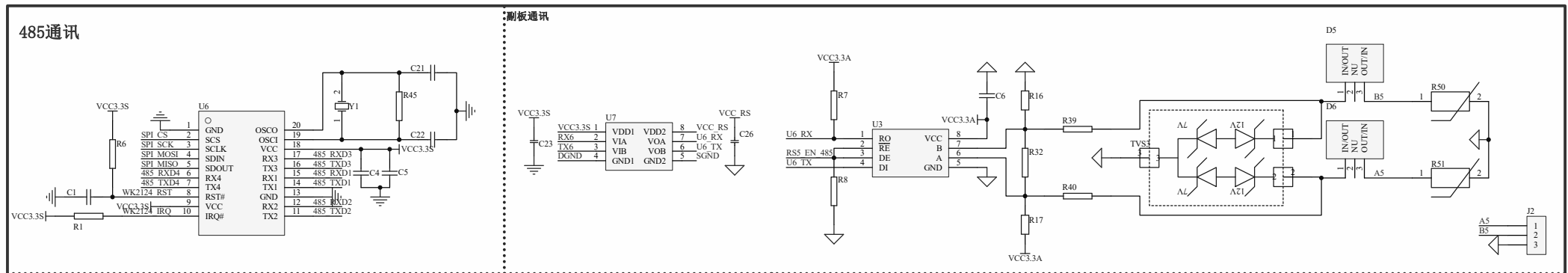
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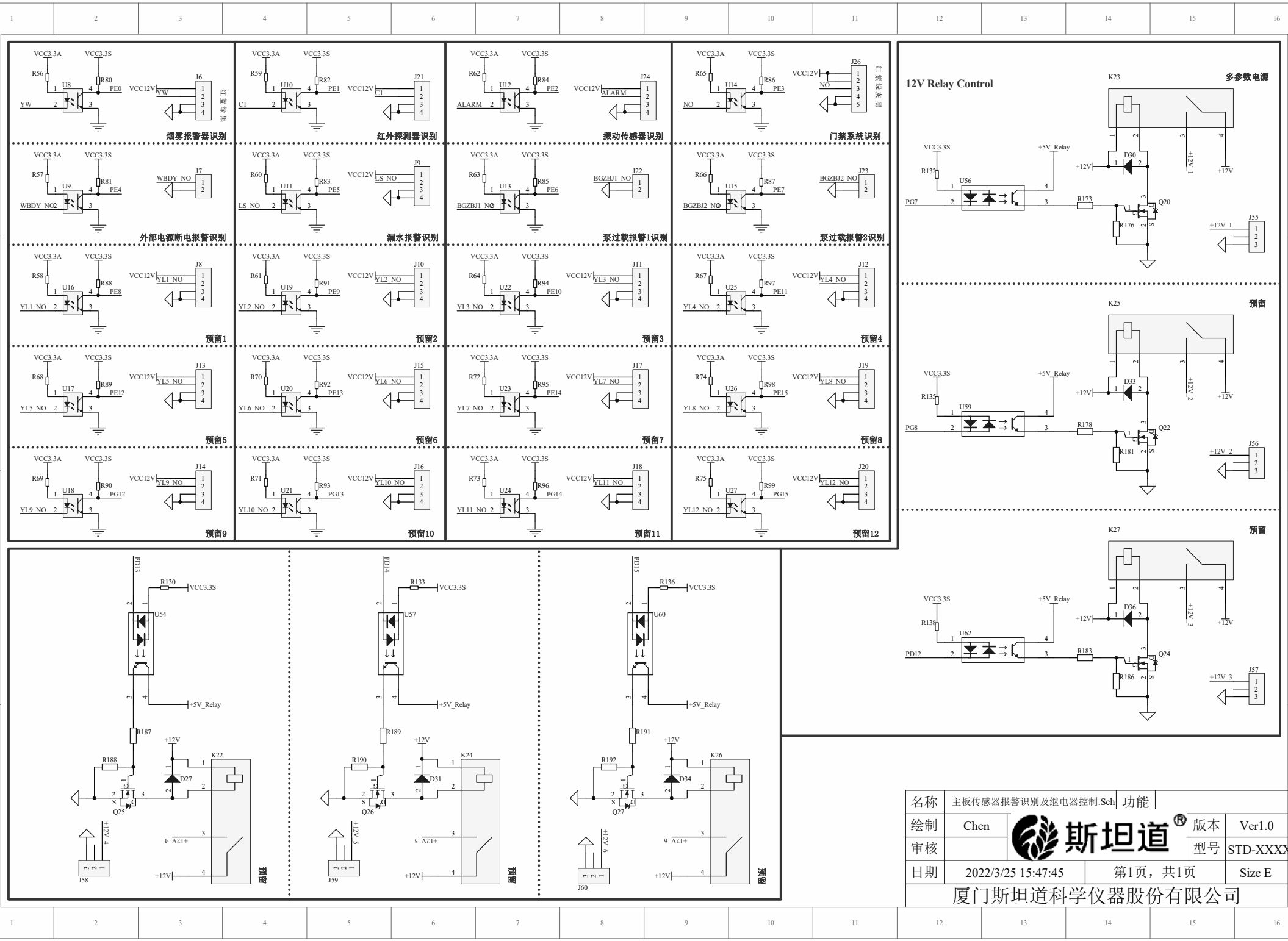
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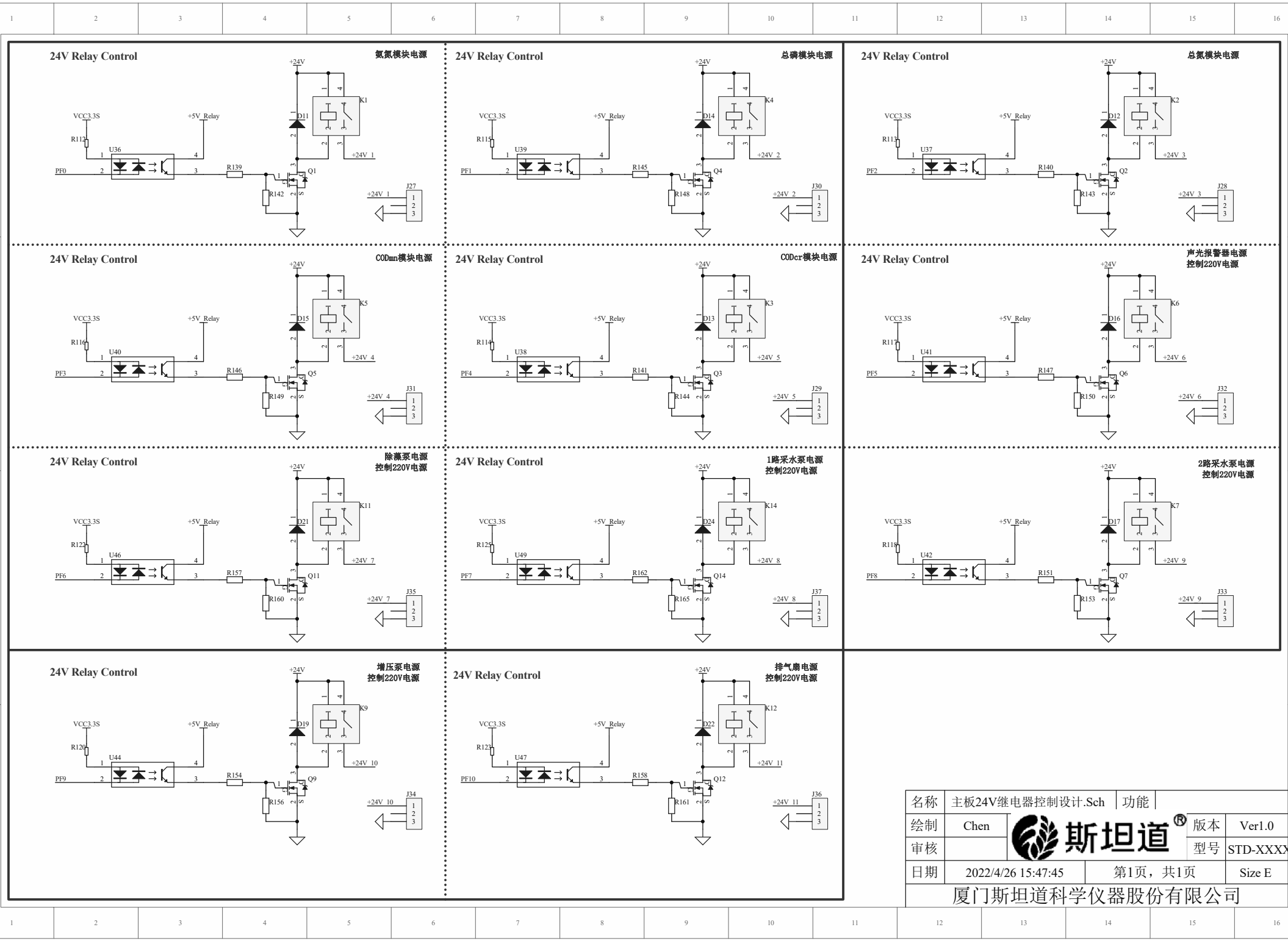
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厦门斯坦道科学仪器股份有限公司					



名称	主板485通讯设计.Sch		功能		
绘制	Chen	 斯坦道 ®	版本	Ver1.0	
审核			型号	STD-XXXX	
日期	2022/3/17 15:47:45		第1页，共1页		Size E
厦门斯坦道科学仪器股份有限公司					



名称	主板传感器报警识别及继电器控制.Sch		功能		
绘制	Chen	 斯坦道®	版本	Ver1.0	
审核			型号	STD-XXXX	
日期	2022/3/25 15:47:45		第1页，共1页		
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厦门斯坦道科学仪器股份有限公司					



24V Relay Control

预留

This circuit diagram shows the control logic for a 24V relay (K15). The input signal from PF11 passes through a pull-up resistor R126 to VCC3.3S. The signal is then processed by a logic gate U50. The output of U50 drives the base of a transistor Q15 through a resistor R163. The emitter of Q15 is grounded, and the collector is connected to the coil of relay K15 through a resistor R166. A diode D25 is connected in parallel with the relay coil for protection. The relay is also connected to a +5V Relay supply and a +24V 12 supply. A 3-pin connector J38 is shown at the bottom right.

24V Relay Control

预留

This circuit diagram shows the control logic for a 24V relay (K8). The input signal from PF12 passes through a pull-up resistor R119 to VCC3.3S. The signal is then processed by a logic gate U43. The output of U43 drives the base of a transistor Q8 through a resistor R152. The emitter of Q8 is grounded, and the collector is connected to the coil of relay K8 through a resistor R169. A diode D18 is connected in parallel with the relay coil for protection. The relay is also connected to a +5V Relay supply and a +24V 13 supply. A 3-pin connector J39 is shown at the bottom right.

24V Relay Control

预留

This circuit diagram shows the control logic for a 24V relay (K10). The input signal from PF13 passes through a pull-up resistor R12 to VCC3.3S. The signal is then processed by a logic gate U45. The output of U45 drives the base of a transistor Q10 through a resistor R155. The emitter of Q10 is grounded, and the collector is connected to the coil of relay K10 through a resistor R174. A diode D20 is connected in parallel with the relay coil for protection. The relay is also connected to a +5V Relay supply and a +24V 14 supply. A 3-pin connector J42 is shown at the bottom right.

24V Relay Control

预留

This circuit diagram shows the control logic for a 24V relay (K13). The input signal from PF14 passes through a pull-up resistor R124 to VCC3.3S. The signal is then processed by a logic gate U48. The output of U48 drives the base of a transistor Q13 through a resistor R159. The emitter of Q13 is grounded, and the collector is connected to the coil of relay K13 through a resistor R179. A diode D23 is connected in parallel with the relay coil for protection. The relay is also connected to a +5V Relay supply and a +24V 15 supply. A 3-pin connector J44 is shown at the bottom right.

24V Relay Control

预留

This circuit diagram shows the control logic for a 24V relay (K16). The input signal from PF15 passes through a pull-up resistor R127 to VCC3.3S. The signal is then processed by a logic gate U51. The output of U51 drives the base of a transistor Q16 through a resistor R164. The emitter of Q16 is grounded, and the collector is connected to the coil of relay K16 through a resistor R184. A diode D26 is connected in parallel with the relay coil for protection. The relay is also connected to a +5V Relay supply and a +24V 16 supply. A 3-pin connector J46 is shown at the bottom right.

24V Relay Control

预留

This circuit diagram shows the control logic for a 24V relay (K17). The input signal from PG2 passes through a pull-up resistor R128 to VCC3.3S. The signal is then processed by a logic gate U52. The output of U52 drives the base of a transistor Q17 through a resistor R167. The emitter of Q17 is grounded, and the collector is connected to the coil of relay K17 through a resistor R170. A diode D28 is connected in parallel with the relay coil for protection. The relay is also connected to a +5V Relay supply and a +24V 17 supply. A 3-pin connector J40 is shown at the bottom right.

24V Relay Control

预留

This circuit diagram shows the control logic for a 24V relay (K19). The input signal from PG3 passes through a pull-up resistor R134 to VCC3.3S. The signal is then processed by a logic gate U55. The output of U55 drives the base of a transistor Q19 through a resistor R172. The emitter of Q19 is grounded, and the collector is connected to the coil of relay K19 through a resistor R175. A diode D32 is connected in parallel with the relay coil for protection. The relay is also connected to a +5V Relay supply and a +24V 18 supply. A 3-pin connector J43 is shown at the bottom right.

24V Relay Control

预留

This circuit diagram shows the control logic for a 24V relay (K20). The input signal from PG4 passes through a pull-up resistor R134 to VCC3.3S. The signal is then processed by a logic gate U58. The output of U58 drives the base of a transistor Q21 through a resistor R177. The emitter of Q21 is grounded, and the collector is connected to the coil of relay K20 through a resistor R180. A diode D35 is connected in parallel with the relay coil for protection. The relay is also connected to a +5V Relay supply and a +24V 19 supply. A 3-pin connector J45 is shown at the bottom right.

24V Relay Control

预留

This circuit diagram shows the control logic for a 24V relay (K21). The input signal from PG5 passes through a pull-up resistor R137 to VCC3.3S. The signal is then processed by a logic gate U61. The output of U61 drives the base of a transistor Q23 through a resistor R182. The emitter of Q23 is grounded, and the collector is connected to the coil of relay K21 through a resistor R185. A diode D37 is connected in parallel with the relay coil for protection. The relay is also connected to a +5V Relay supply and a +24V 20 supply. A 3-pin connector J47 is shown at the bottom right.

24V Relay Control

预留

This circuit diagram shows the control logic for a 24V relay (K18). The input signal from PG6 passes through a pull-up resistor R129 to VCC3.3S. The signal is then processed by a logic gate U53. The output of U53 drives the base of a transistor Q18 through a resistor R168. The emitter of Q18 is grounded, and the collector is connected to the coil of relay K18 through a resistor R171. A diode D29 is connected in parallel with the relay coil for protection. The relay is also connected to a +5V Relay supply and a +24V 21 supply. A 3-pin connector J41 is shown at the bottom right.

名称	主板24V继电器控制设计.Sch		功能		
绘制	Chen		版本	Ver1.0	
审核			型号	STD-XXXX	
日期	2022/4/26 15:47:45		第1页, 共1页		Size E
厦门斯坦道科学仪器股份有限公司					