

Problem 7: [16 points]
 Filename: hw9prob7.asm
 AndrewID: xinyew

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1 ; read 10000 samples from ADC, calculated the most recent
2 ; 8 samples and write the average value to DAC
3
4 .ORG $500
5 Init MV r1, r0 ; r1 counts the elements
6 ADDI r7, r0, $4 ; r7 = 4 for shifting
7 LI r2, $1 ; r2: enable bit
8 SW r0, r2, $0010 ; set ADC enable bit
9
10 Wait LW r2, r0, $0022 ; check data ready bit
11 MV r5, r0 ; r5 = 0 Init for sum
12 ADDI r4, r0, $C ; r4 = 12 Indexing and loop counter
13 BRN Shift ; if set, start Shifting
14 BRA Wait ; otherwise, keep busy-waiting
15
16 Shift SLT r0, r4, r0 ; RdADC if r4 < 0
17 BRN RdADC ; r3 = Recent[6/5/4...]
18 LW r3, r4, Recent ; accumulating valid values
19 ADD r5, r5, r3 ; r4 += 2 Get positions 7/6/5...
20 ADDI r4, r4, $2 ; Recent[7/6/5...] = Recent[6/5/4...]
21 SW r4, r3, Recent ; r4 -= 4 Set r4 to the next value
22 SUB r4, r4, r7
23
24 RdADC LW r3, r0, $0020 ; r3 = ADC
25 ADD r5, r5, r3 ; accumulating valid values in r5
26 SW r0, r3, Recent ; Recent[0] = r3
27
28 WtDAC SRAI r5, r5, $3 ; r5 = r5 // 8
29 LI r6, $8000 ; DAC enable bit
30 SW r0, r6, $0012 ; store the DAC enable bit
31 SW r0, r5, $0014 ; store the calculated value
32
33
34 Branch ADDI r1, r1, $1 ; increment counter
35 SLTI r0, r1, $2710 ; check if r1 < 10000
36 BRN Wait ; if so, wait for next value
37
38 Done SW r0, r0, $0010 ; turn off ADC: clear enable bit
39
40 .ORG $1000
41 Recent .DW $0
42 .DW $0
43 .DW $0
44 .DW $0
45 .DW $0
46 .DW $0
47 .DW $0
48 .DW $0

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