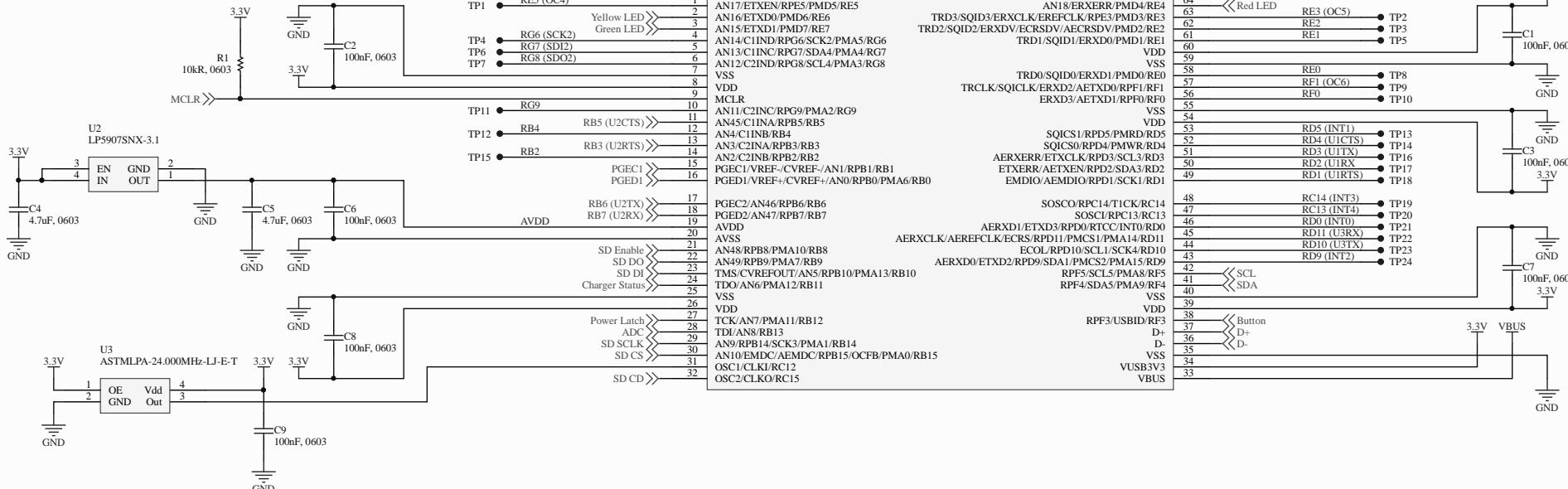
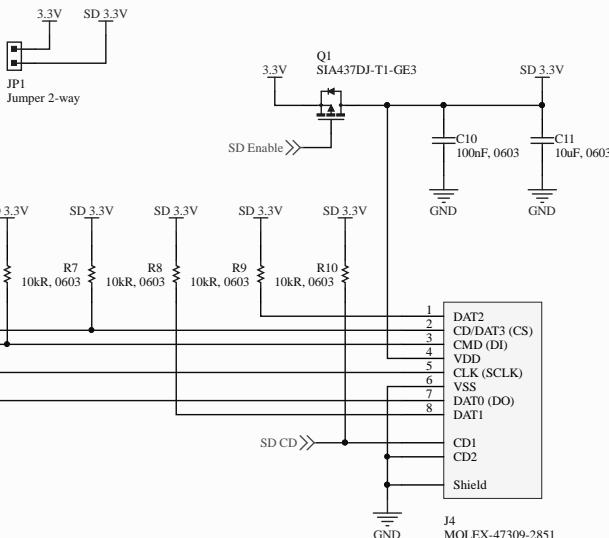


Microcontroller

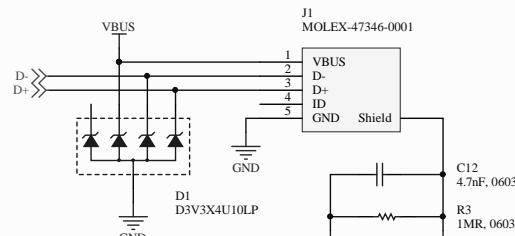


Assumes silicon revision A3 else silicon errata issues may prevent operation

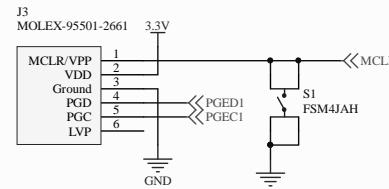
SD Card



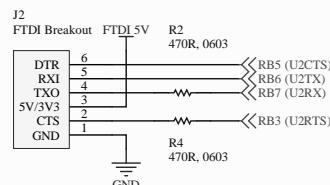
USB



ISCP



FTDI Breakout

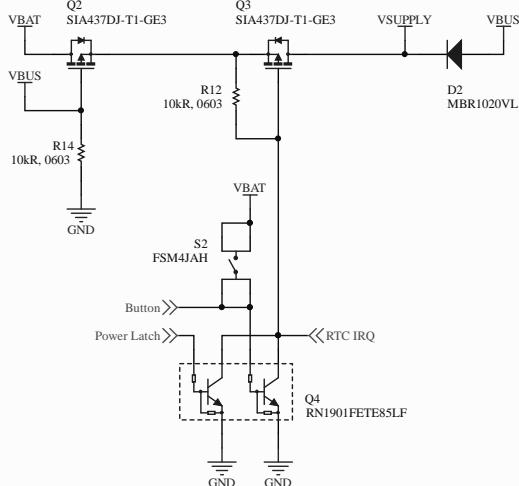


PIC32MZ maximum input clamp current = 5mA
5V clamped to 3.3V through 470R = 3.6mA

Title

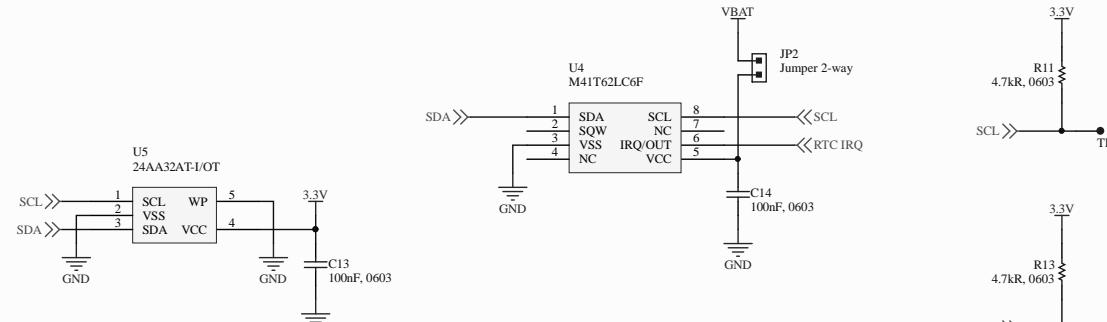
Size	Number	Revision
A3		
Date: 30/09/2018	Sheet of	
File: C:\MyFiles\...\Sheet1.SchDoc		Drawn By:

Power Control

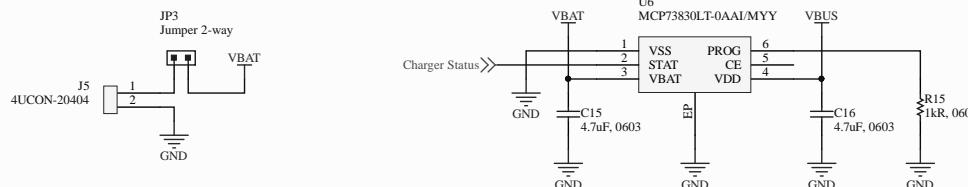


VSUPPLY is enabled when: VBUS is present, the switch is closed, Power Latch is high, or RTC IRQ (open-drain) is asserted
Button must be a 5V tolerant pin

EEPROM and RTC

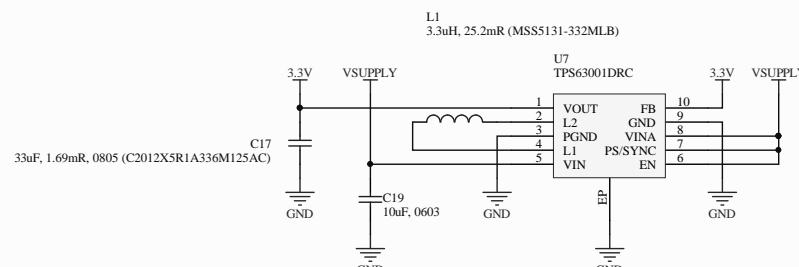


Battery Charger



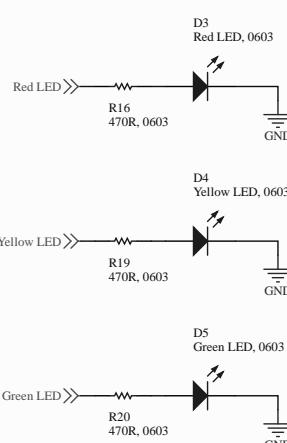
PROG = 1kR for 200mA fast charge current regulation

Voltage Regulator



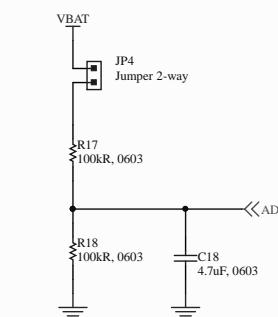
Design as per TI Webench Power Designer for: Vin = 3.0V to 4.2V, Iout = 800mA

LEDs



PIC32MZ maximum source current = 15mA

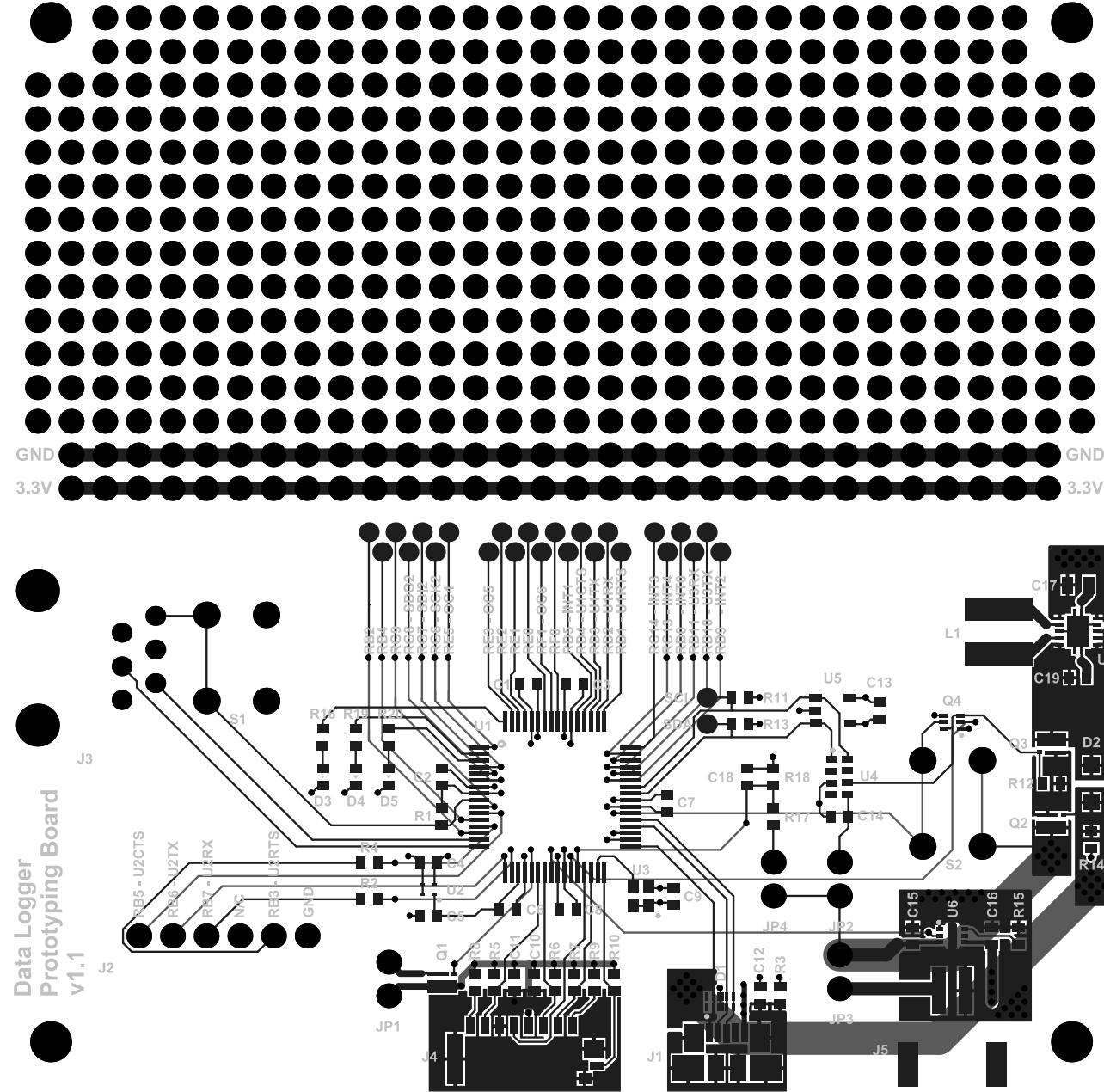
Battery Measurement



Low-pass filter corner frequency = 0.34 Hz

Title

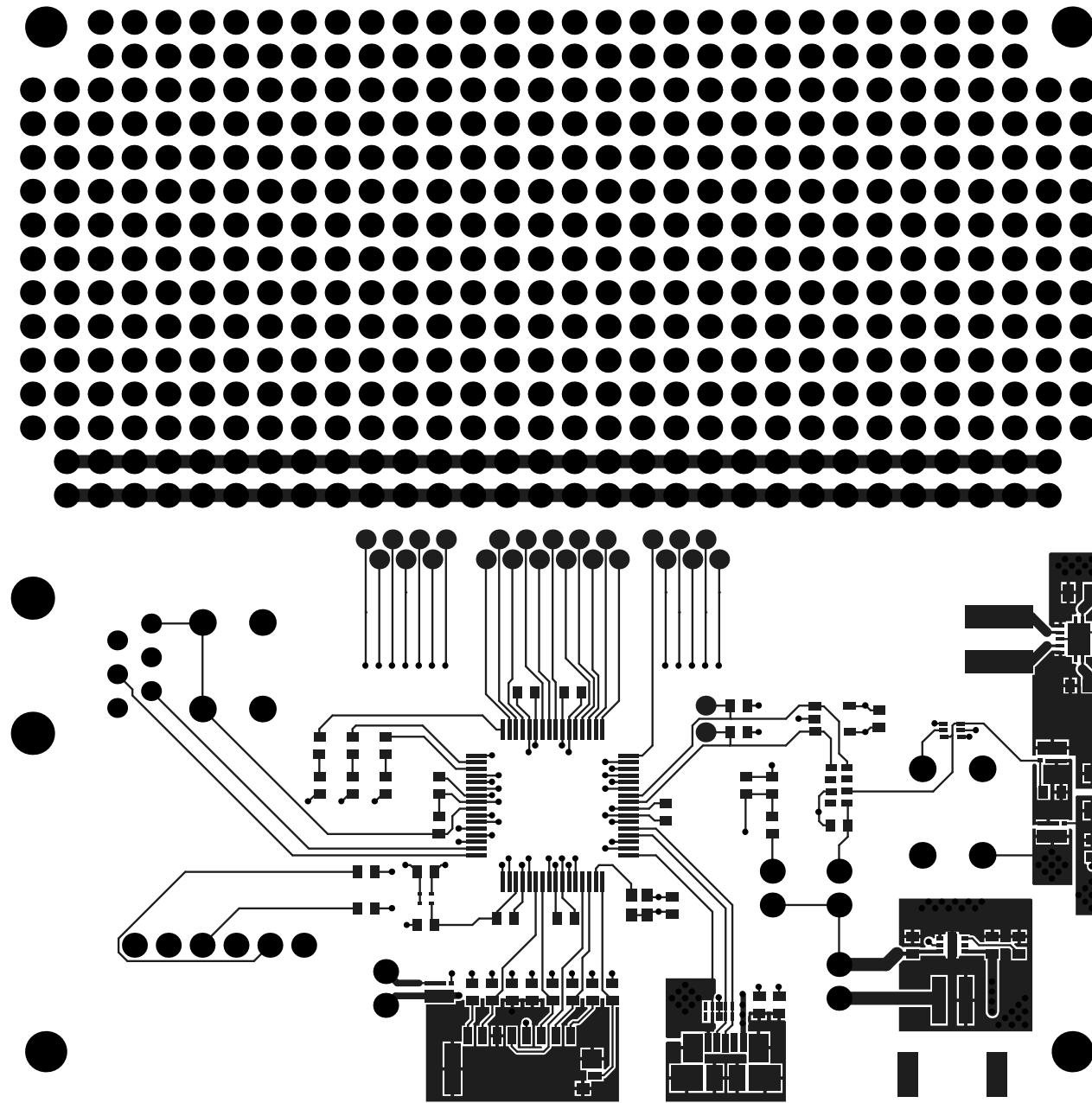
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A3		
Date: 30/09/2018	Sheet of	
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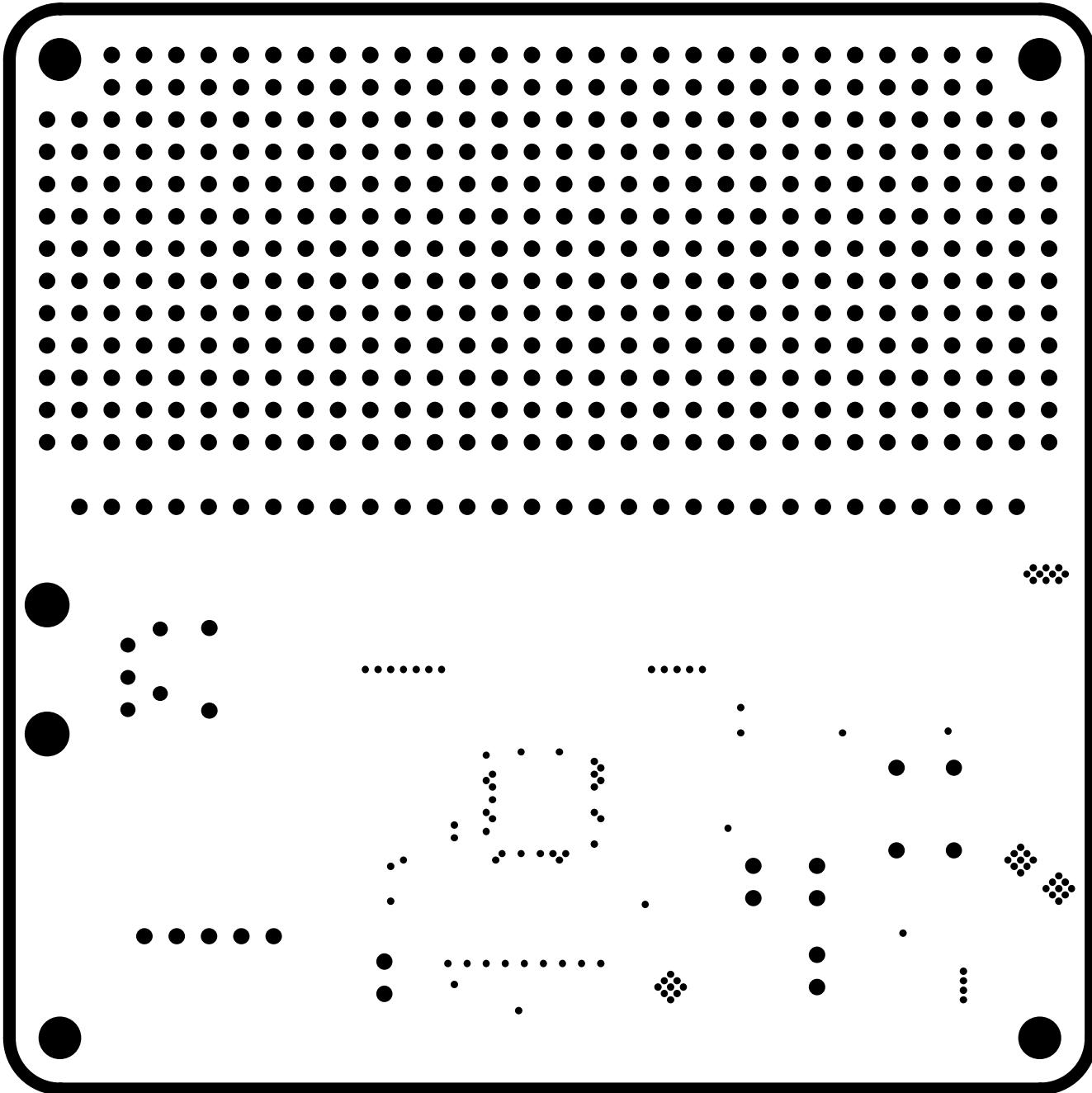


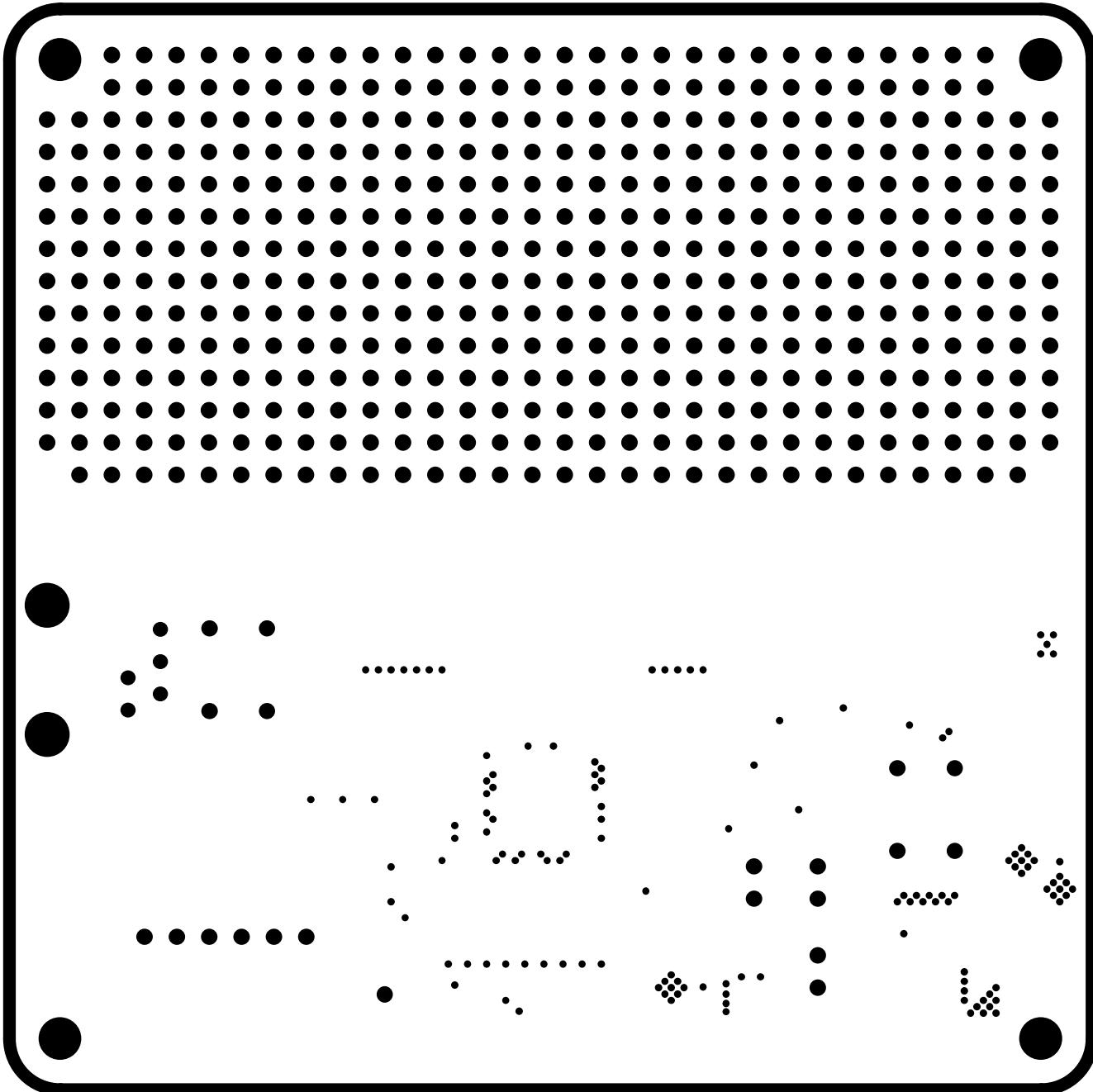
Data Logger
Prototyping Board
v1.1

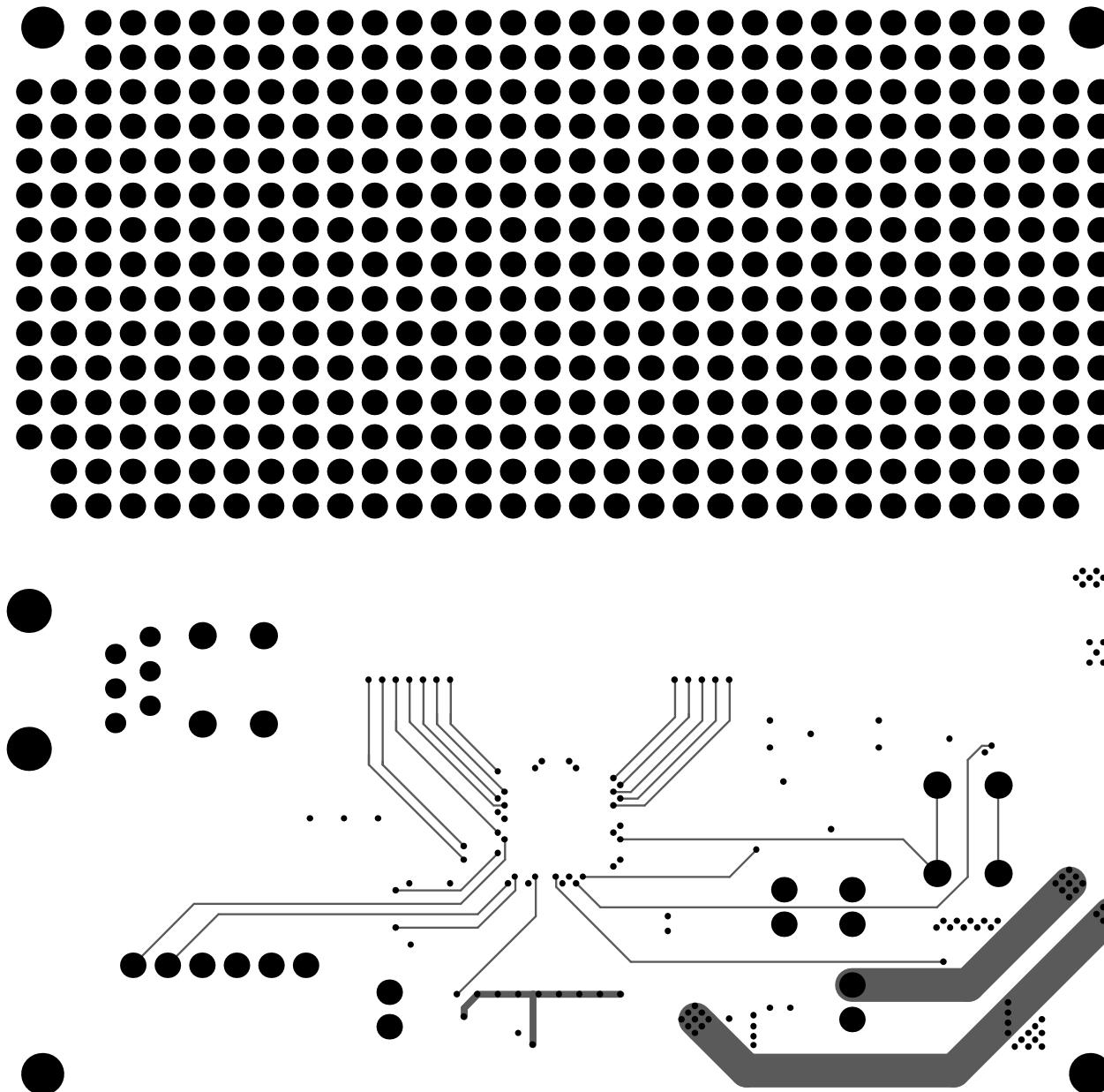
GND
3.3V

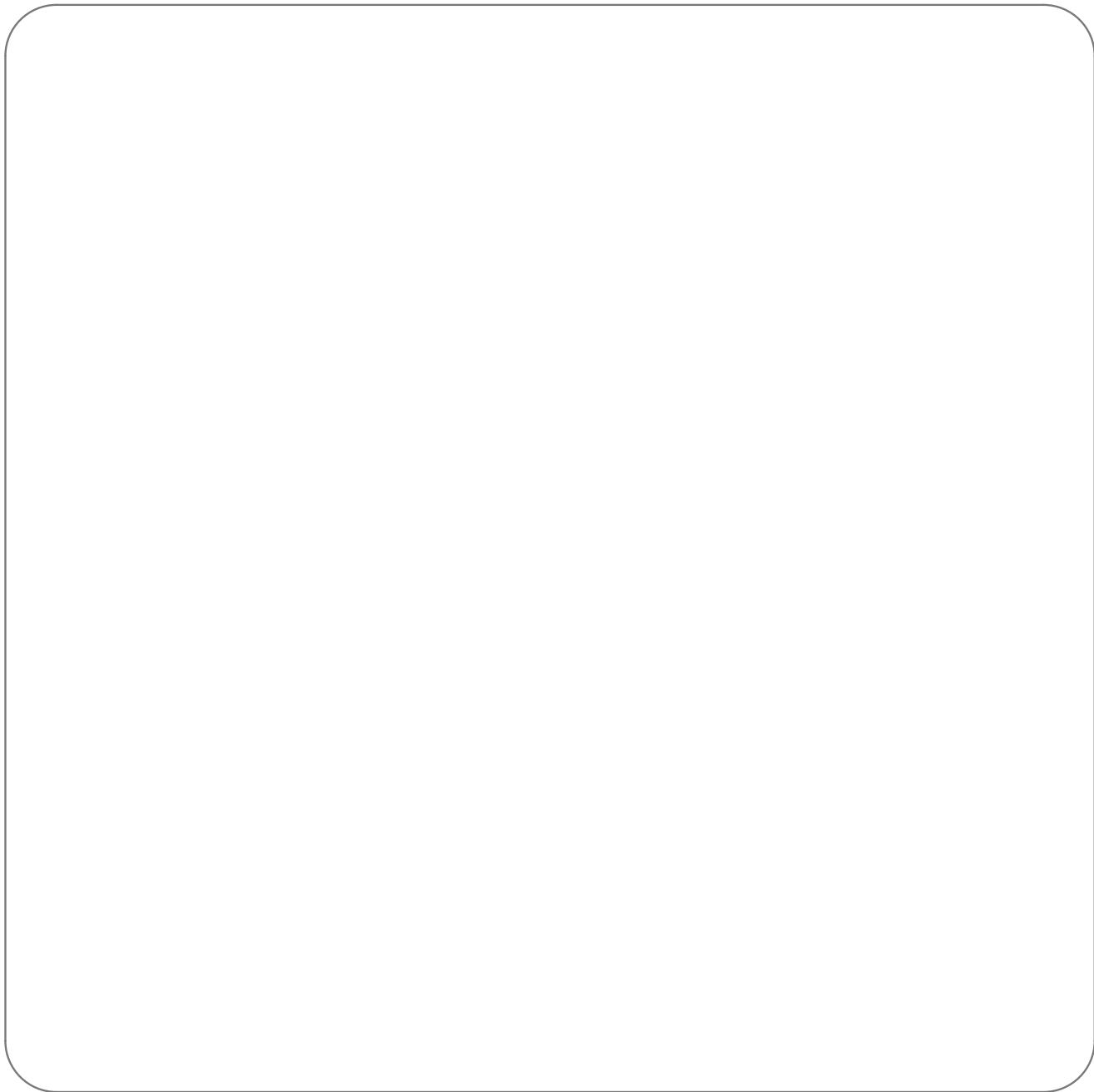




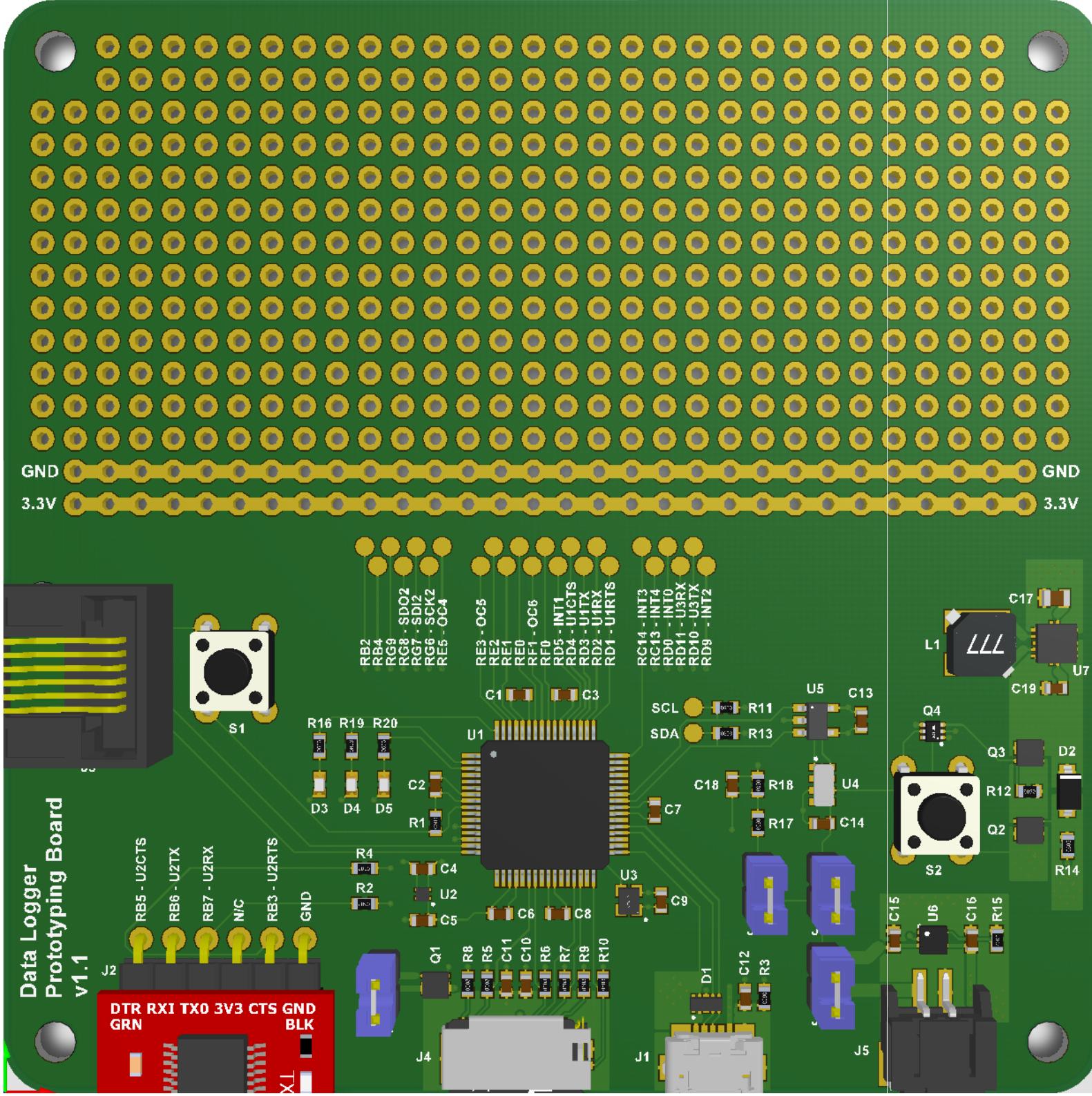








Data Logger
Prototyping Board
v1.1



Data Logger Prototyping Board v1.1

