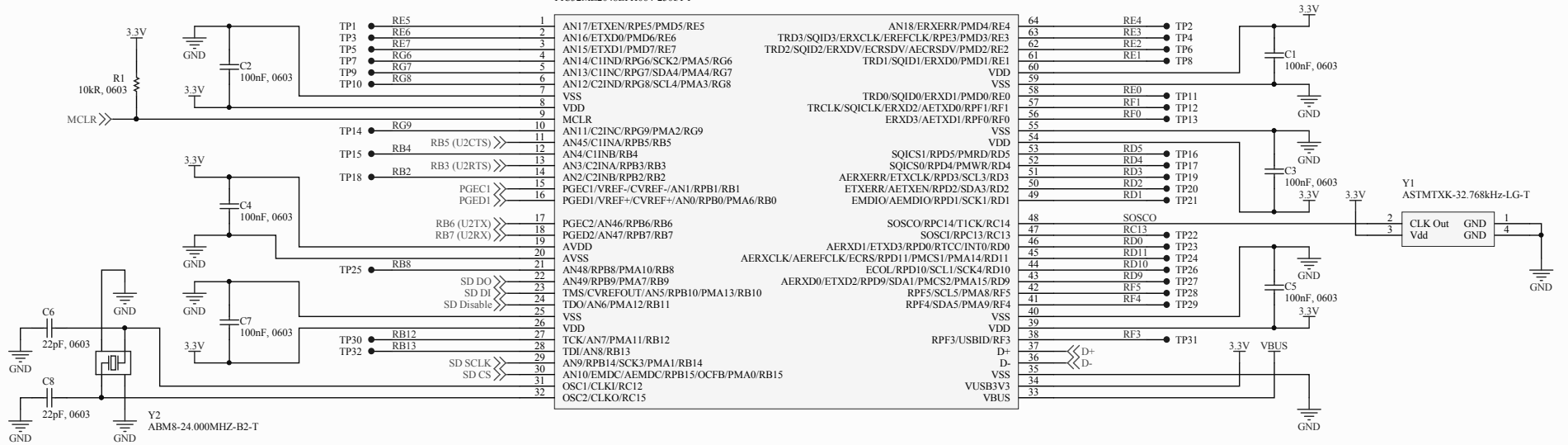


Designator	Quantity	Comment	Footprint
C1, C2, C3, C4, C5, C7, C10	7	100nF, 0603	Capacitor 0603
C6, C8	2	22pF, 0603	Capacitor 0603
C9	1	4.7nF, 0603	Capacitor 0603
C11, C12, C13	3	10uF, 0603	Capacitor 0603
D1	1	D3V3X4U10LP	D3V3X4U10LP
J1	1	MOLEX-47346-0001	MOLEX-47346-0001
J2	1	FTDI Breakout	FTDI Breakout
J3	1	MOLEX-95501-2661	MOLEX-95501-2661
J4	1	MOLEX-47309-2851	MOLEX-47309-2851
JP1	1	Jumper 2-way	Jumper 2-way
JP2	1	Jumper 3-way	Jumper 3-way
Q1	1	SIA437DJ-T1-GE3	SIA437DJ-T1-GE3
R1, R7, R8, R9, R10, R11, R12	7	10kR, 0603	Resistor 0603
R2, R4	2	22R, 0603	Resistor 0603
R3, R5	2	470R, 0603	Resistor 0603
R6	1	1MR, 0603	Resistor 0603
S1	1	FSM4JHX	FSM4JHX
TP1, TP2, TP3, TP4, TP5, TP6, TP7, TP8, TP9, TP10, TP11, TP12, TP13, TP14, TP15, TP16, TP17, TP18, TP19, TP20, TP21, TP22, TP23, TP24, TP25, TP26, TP27, TP28, TP29, TP30, TP31, TP32, TP33	33	Test Point	Test Point 1.5mm
U1	1	PIC32MZ2048EFH064-250I/PT	TQFP-64
U2	1	ADP3338AKCZ-3.3-RL	SOT-223
Y1	1	ASTMTXK-32.768kHz-LG-T	ASTMTXK
Y2	1	ABM8-24.000MHZ-B2-T	ABM8

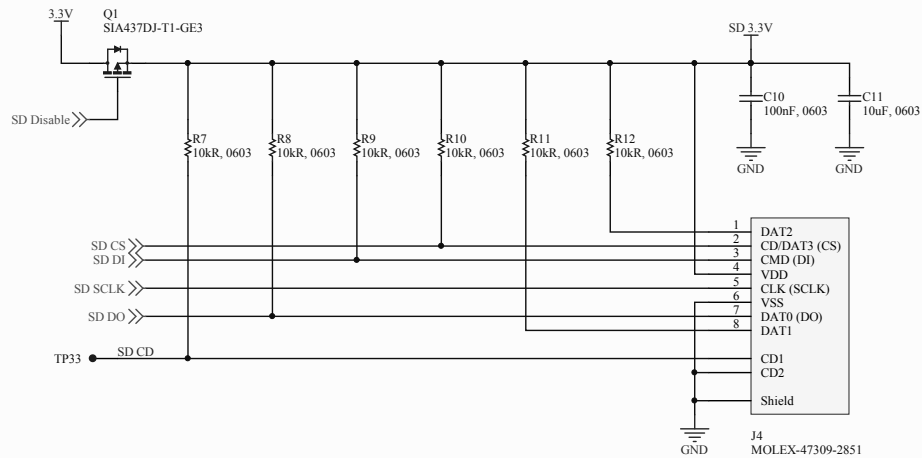
Microcontroller

U1
PIC32MZ2048EFH064-250I/PT

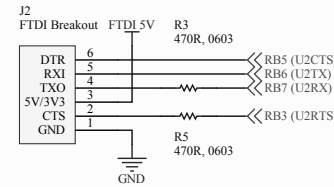


Assumes silicon revision A3 else silicon errata issues may prevent operation

SD card

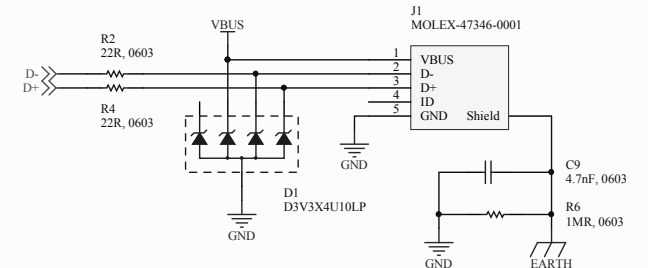


FTDI Breakout

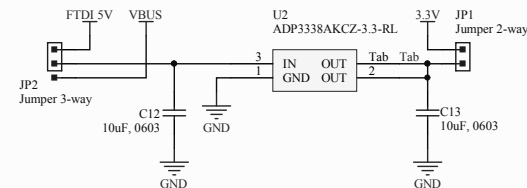


PIC32MZ max input clamp current = 5mA
5V clamped to 3.3V through 470R = 3.6mA

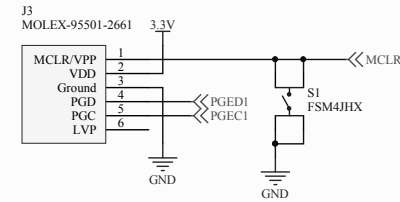
USB



Voltage regulator

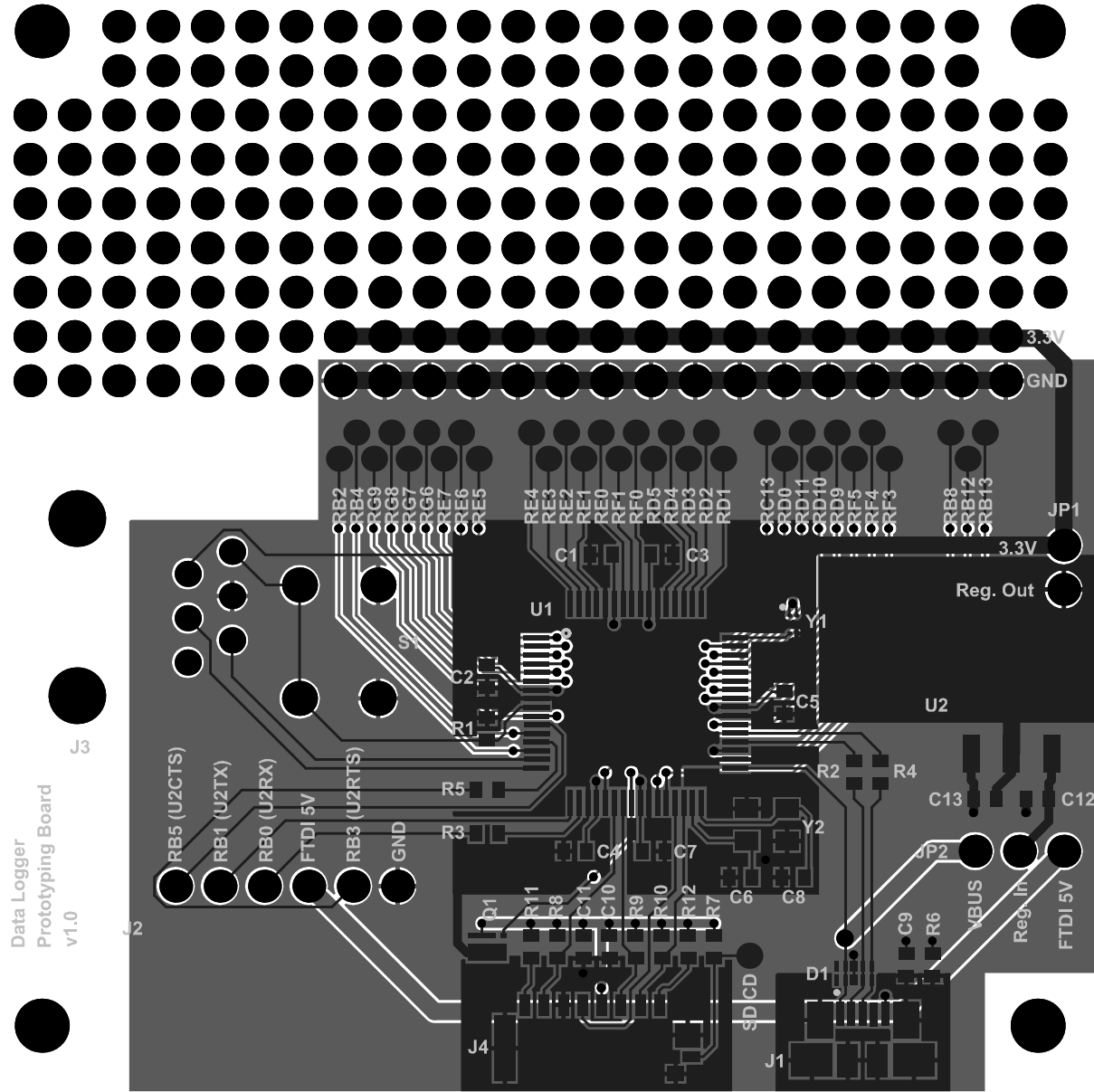


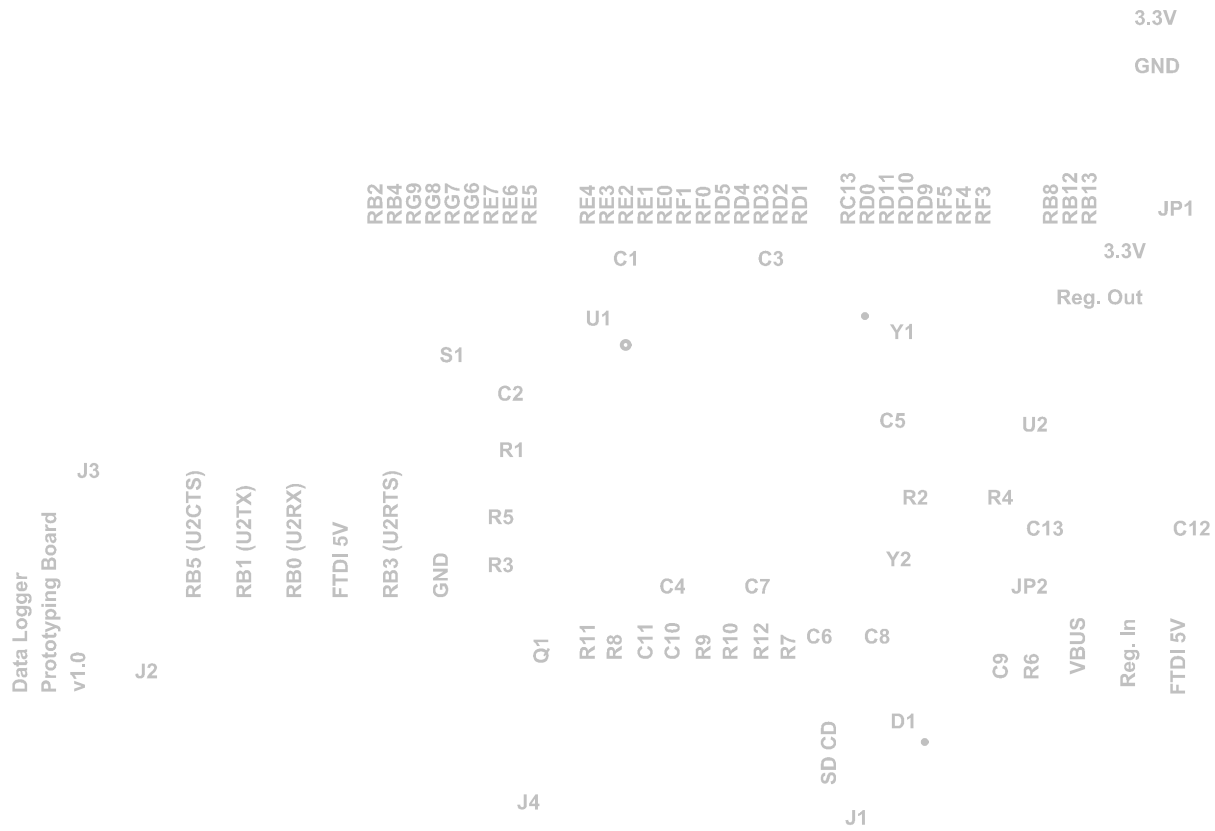
ISCP

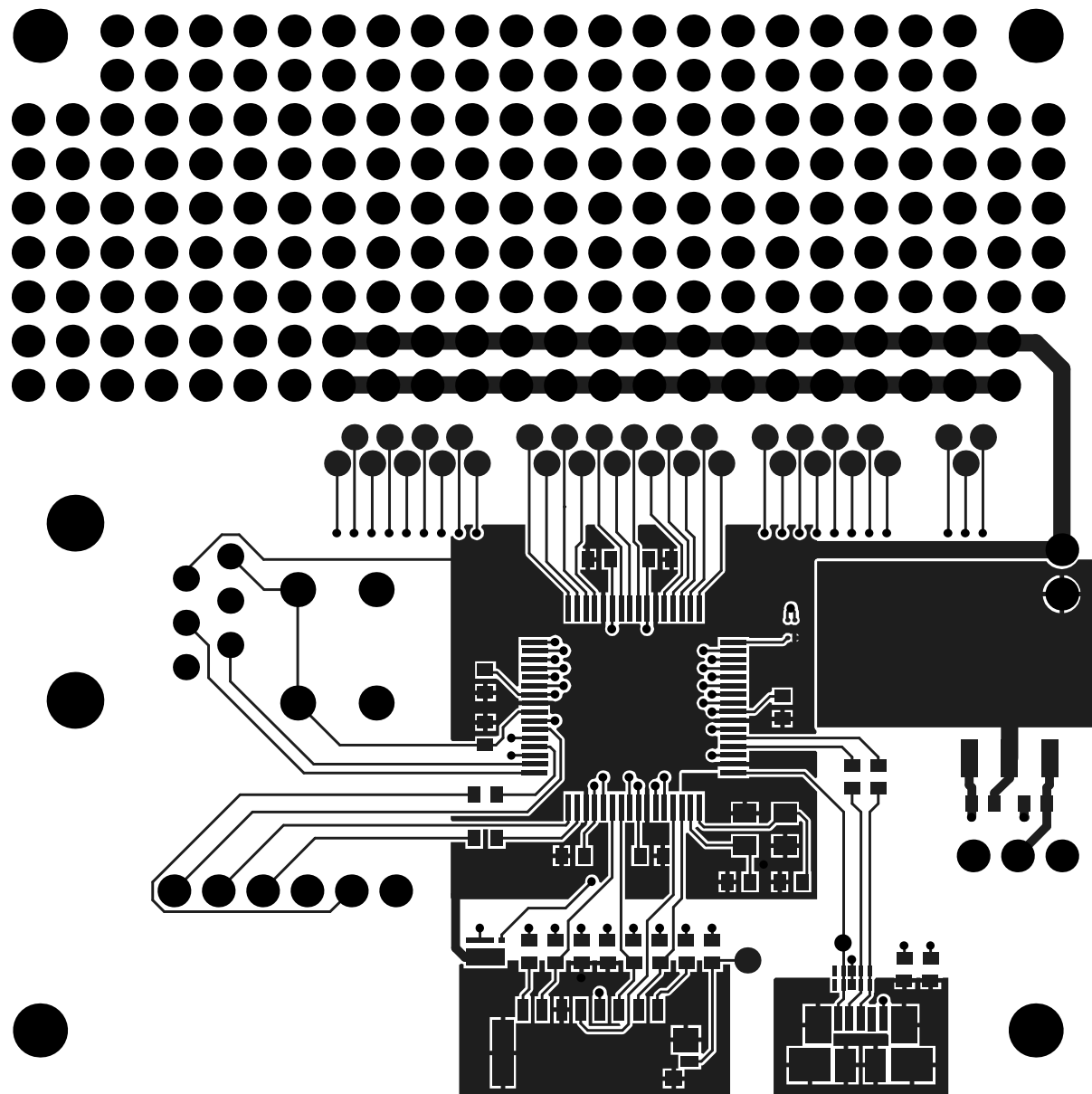


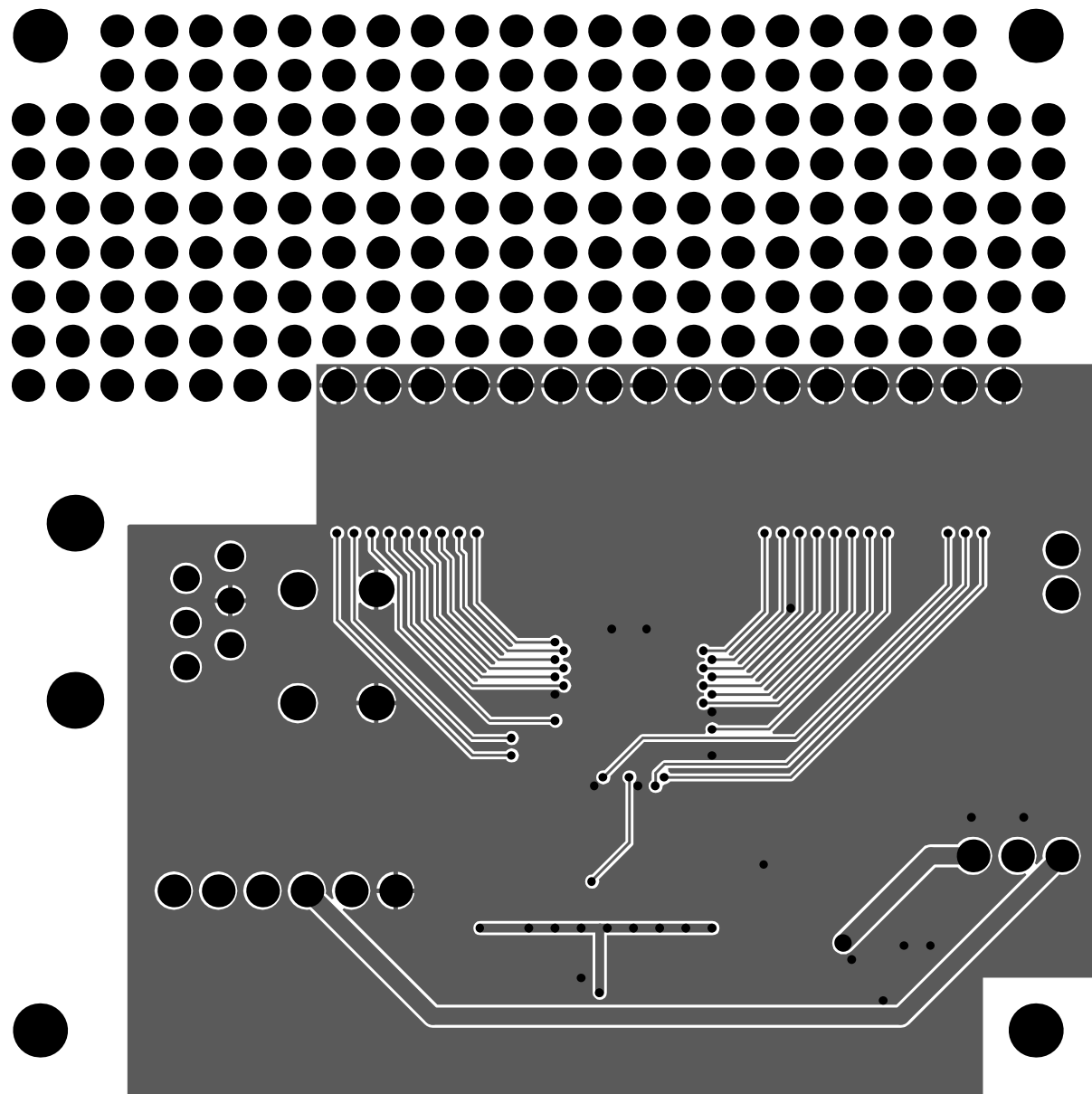
Title		
Size	Number	Revision
A3		
Date:	15/03/2018	Sheet of
File:	C:\MyFiles\Sheet1.SchDoc	Drawn By:

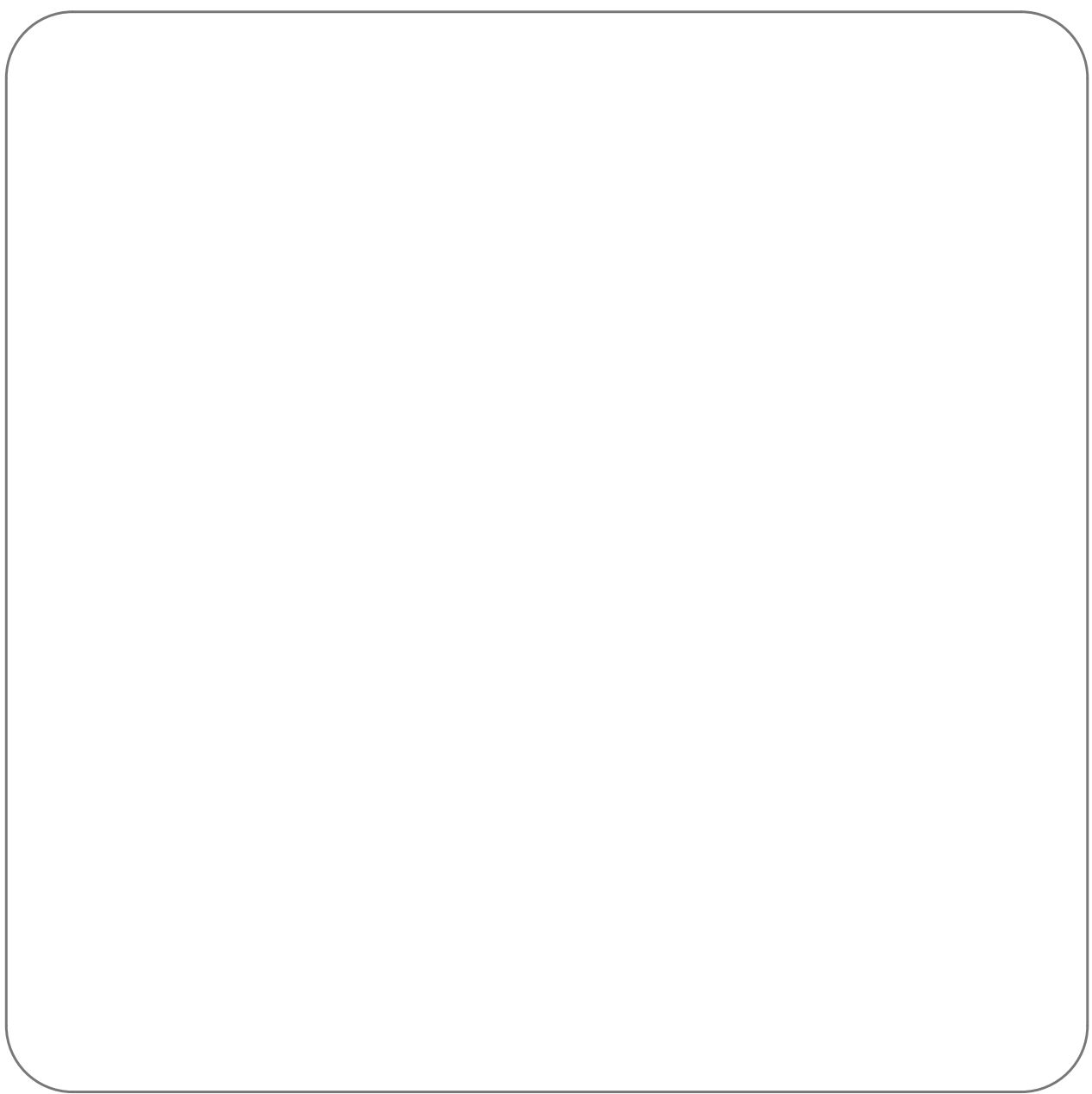
Data Logger
Prototyping Board
v1.0

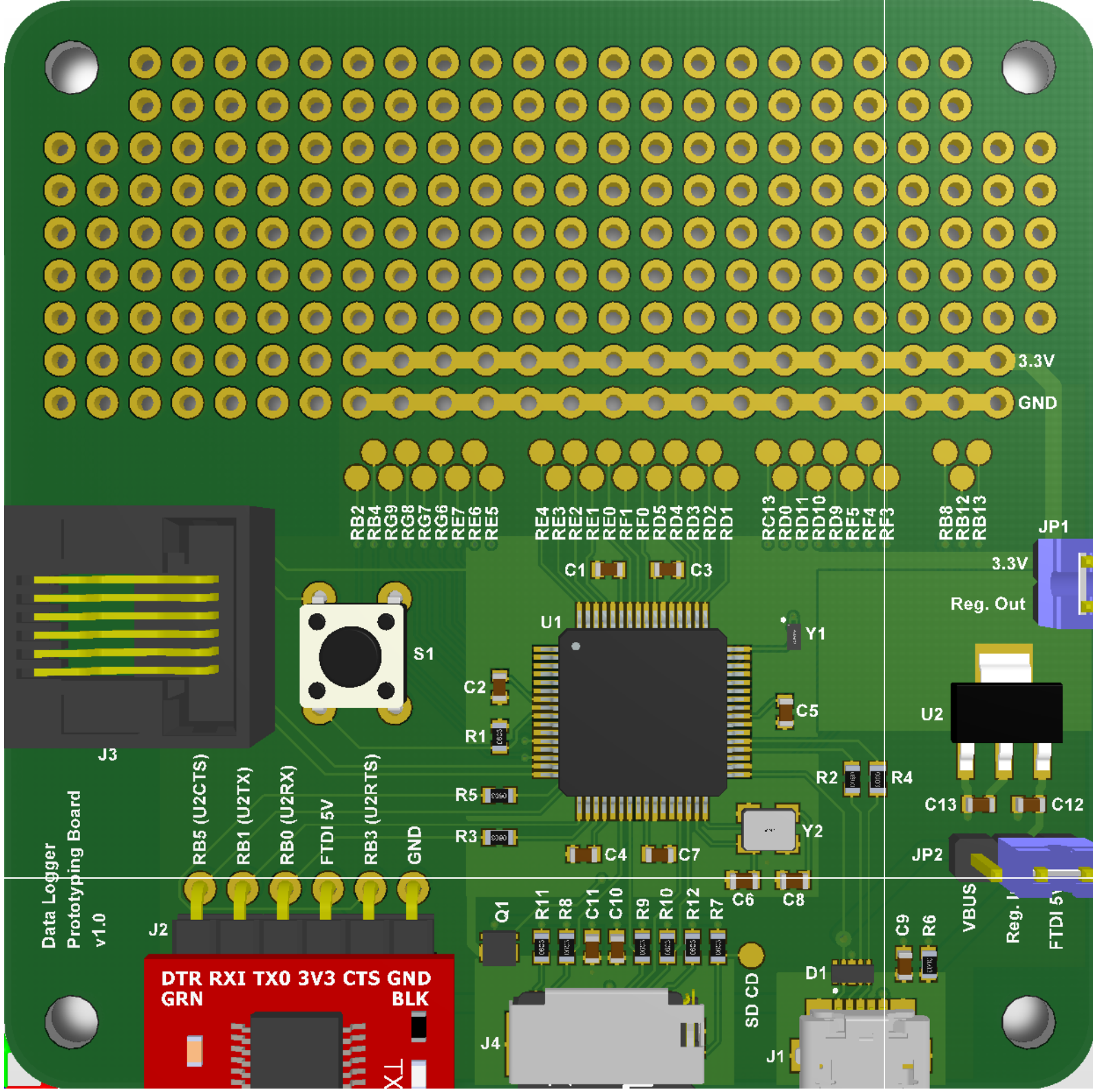












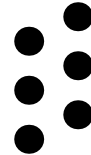
Data Logger
Prototyping Board
v1.0



J3

J2

RB5 (U2CTS)
RB1 (U2TX)
RB0 (U2RX)
FTDI 5V
RB3 (U2RTS)
GND



S1

RB2
RB4
RG9
RG8
RG7
RG6
RE7
RE6
RE5

RE4
RE3
RE2
RE1
RF1
RF0
RD5
RD4
RD3
RD2
RD1

RC13
RD0
RD11
RD10
RD9
RF5
RF4
RF3

RB8
RB12
RB13

3.3V
GND

3.3V

Reg. Out



U2

C13 C12

JP2

VBUS
Reg. In
FTDI 5V

C1 C3

U1

C2
R1
R5
R3

Q1

J4

R11
R8
C11
C10
R9
R10
R12
R7

C4 C7



Y1

C5

R2

R4

Y2
C8

C6

SD CD

J1

D1

C9
R6

