## ME 360 Lecture 3.

· Recap from last lecture.

	VI
(49.25)10 to	
4912=24R1	0.25x2=0.5t0
2412=12RO	0.5x2 =0.0+1
12/2 = 6RO	
612 = 3Ro	
3/2 = 181	
1/2 =081	
•	

(101.0101)2 to decimal  $(101)_2 = (5)_{10}$  $\chi = (0.0101)_2$ 24x = (101, 101)2 2x=(0.101)2  $(2^{4}-2)\chi = (101)_{2} = (5)_{10}$  $\chi = 5/(24-2) = 5/14$ 

## 0.3 Floating Point Representation of Real Numbers

10.3.1 Floating Point Format

We use the IEEE 754 standard to represent floating point number in computers ( veleased 1985),

Precision	sign	Paponent	Mantissa
single	1	8	23
double	1	11	52
long double		15	64

Example:  $(9)_{10} = (1001)_2 = +1.001 \times 2^3$ 

Let's look at double precision numbers:
* Exponent P
We have 11 bits. So the largest binary number we can
Store is: $(1111111111)_2 = 2^{11} - 1 = 2047$
so, the exponent range can be [0,2047]
But we also want negative exponents!
But we also want negative exponents! Using IEEE standard, we apply an "exponent bias"
[0,2041] = p+1023
We need "o" & "2047" for special cases, so
P = [-1022, 1023]
P = [m  OCC    OC)]
$2047$ : Inf $(\infty)$ or -Inf $(-\infty)$ if all mantissa bits are zero NaN (not a number) otherwise
0: Subnormal floating point number
<b>3</b> ·
10, b, b2 b62 × 2-1022
we extend the smallest precision from 2-1022 to 2-1074
* Mantissa
· Double Precision number 1 is.
+1.10000 x 2°
52 zeros. (mantissa).
Next floating point number greater than I is:
+1.0001) ×20, or 1+2-52
MANAGARA DIGUISIAN GARALA
machine precision Emach distance between I and the smallest floating point number
greater than 1.
For double precision: Emach = 2-52
CIVAIN

Let's look at an example:

What is  $(9.4)_{10}$  in double precision?

We have  $(9.4)_{10} = (1001.0110)_{2}$ Vepeating

How do we approximate this infinite number in 52 mantissa?

Method #1. Chopping

we just remove all the bits beyond 52nd.

Pro: simple. Con: bias results toward zero.

Method #12: Rounding.

In base 10, we round up if the next digit is 5 or higher and vice versa.

In binary, we round up if the bit is I, with one exception. if the bits following 52nd is 10000..., exactly half way between up a down, we round up or down according to which choice makes the final bit 52 equal to 0.

Example: [....]10000... round up

Why? to avoid bias in long computation.

Method 2 is the IEEE Rounding to Nearest Rule, for any double precision floating point number associated to x; we denote it as f(x).

Kecipe for floating point representations 1. Justify shift radix point to the right of the leftmost 1, and compensate with the exponent. 2. Round apply a rounding rule, such as the TEEE Rounding to Nearest Rule, to reduce mantissa to 52 bits. Let's go back to (9.4)10.  $(9.4)_{10} = (1001.0110)_{2} = 1.0010110...01100110 \times 2^{3}$ Justify  $f(9.4) = 1.0010110 \cdots 01101 \times 2^3$ Round What happened to this rounding?

• We removed the tail (.1100) 2 x 2<sup>-52</sup> x 2<sup>3</sup>  $= (,\overline{010})_{z} \times 2^{-51} \times 2^{3} = 0.4 \times 2^{48}$ • We added  $2^{-52} \times 2^{3} = 2^{-49}$ 80.  $f(9.4) = 9.4 + 2^{-49} = 0.4 \times 2^{-48} = 9.4 + 0.2 \times 2^{-49}$ rounding error Error Definition \* Let X be a computed version of the exact quantity X. Then: 1Xc-X1. Absolute Error is 1 x 1 - x 1 Relative Error is

Relative Rounding Error

In the IEEE machine arithmetic model, the relative vounding error of f(x) is no more than 1/2 machine precision  $\frac{|f(x)-\chi|}{|\chi|} \leq \frac{1}{2}$  Emach

For example, the rounding error of $X=9.4$ is $0.2\times2^{-49}$ , so the relative rounding error is: $ \frac{1 + (9.4) - 9.41}{9.4} = \frac{0.2\times2^{-49}}{9.4} = \frac{8}{47} \times 2^{-52} < \frac{1}{2} + \frac{1}{2$	
Exercise in class:	