Symbolic Verification via Program Transformation

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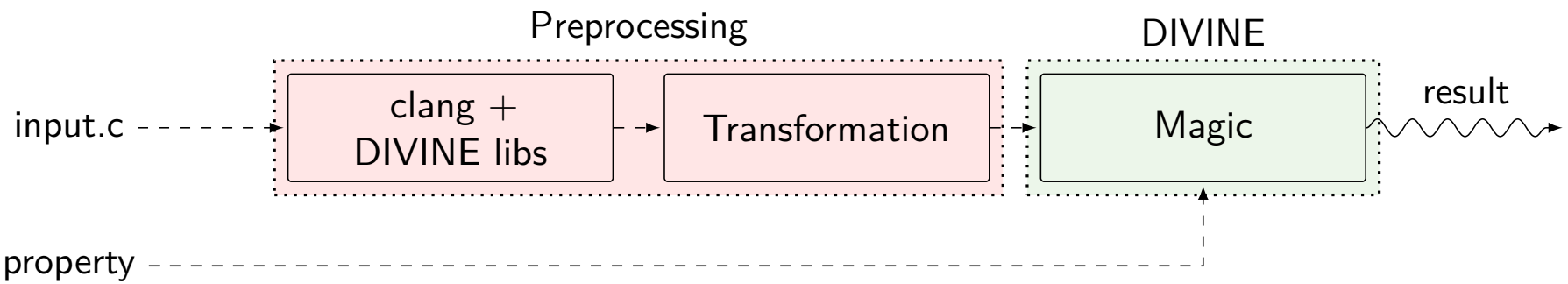

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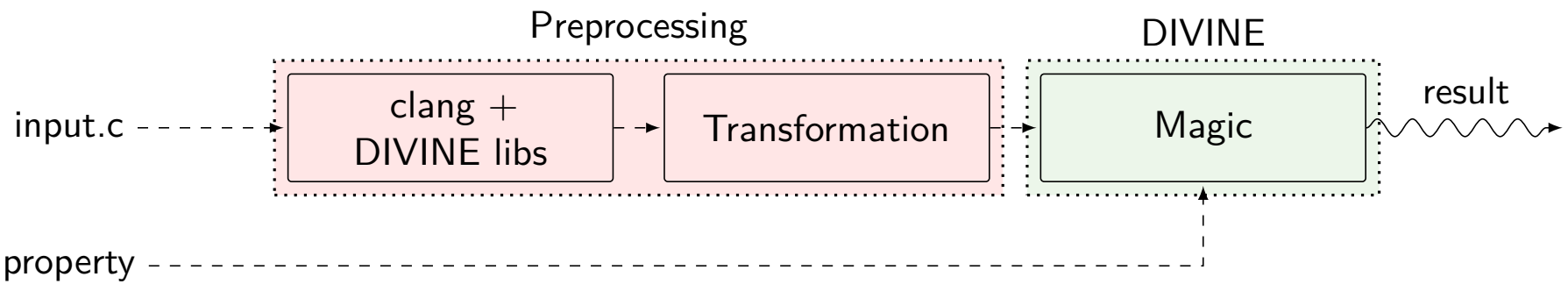

- **Diploma thesis:** prototype that can handle values on stack

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     - enables arbitrary structure with abstract data
2. **polishing of code:**
   - simplification of whole process (from 6000 loc. to 2500 loc.)
3. **finishing paper resubmition**
   - evaluation should now cover bigger portion of SV-COMP

Plans for the rest of 2018

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- evaluation + paper submission

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- work on utilization of SMT solving in verification