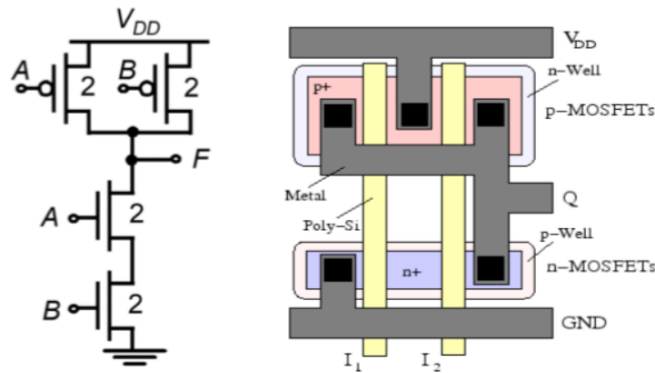


ENR 325 Assignment #9

Due: 11/14/25 10:00 pm

Today in class we mostly talked about the schematics of CMOS design, converting Boolean equations to CMOS connections right away. Once you designed all the PMOS and NMOS, it's time to actually design the layered IC:



Although we don't have real digital design license from Cadence or Synopsys, we do have the next thing: a teaching tutorial of the lay out basics:

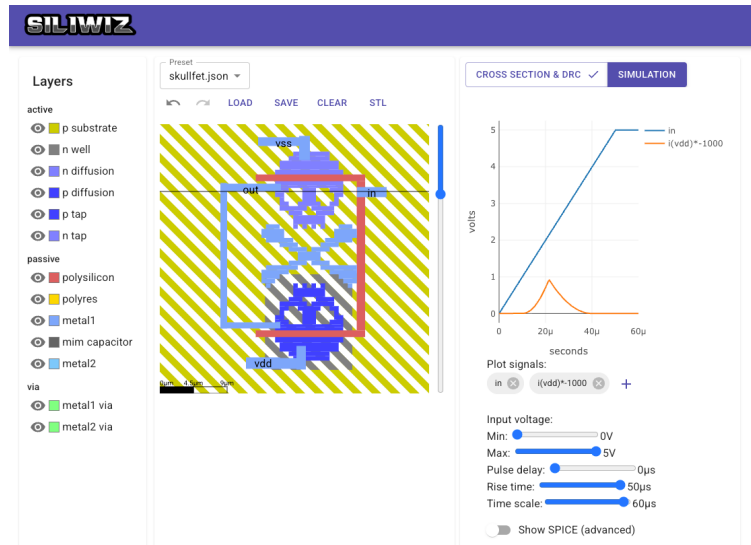
[Tiny Tapeout > How do semiconductors work?](#)

HOW DO SEMICONDUCTORS WORK?



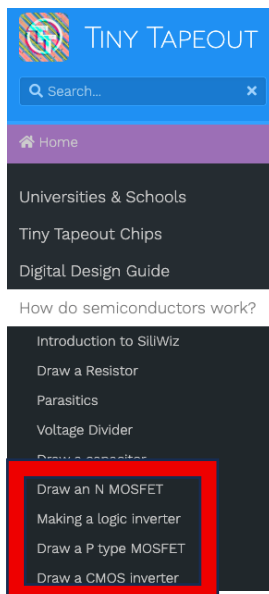
<https://tinytapeout.com/siliwiz/>

It has a web-based simulator:



<https://app.siliwiz.com/>

Your task is to use the tutorial and simulator and finish the following sections:



<https://tinytapeout.com/siliwiz/nmos/>

<https://tinytapeout.com/siliwiz/inverter/>

<https://tinytapeout.com/siliwiz/pmos/>

<https://tinytapeout.com/siliwiz/cmosinverter/>

All the builds have pre-set answers in the website.

Submit your own work with screen shots

And you know it might be part the oral exam...