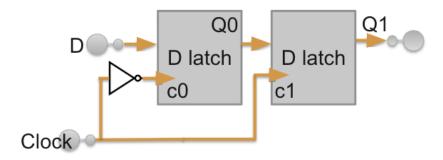
ENR 325 Assignment #7

Due: 11/6/25 10:00 pm

Task 1:

. 11/0/25 10.00 pr

This Monday we did the rising edge triggered, Master-slave D type flip flops:

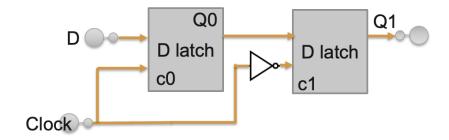


And here's the timing diagram we made in class (https://wavedrom.com/editor.html):

```
1 { "signal" : [
     { "name": "c0",
 { "name": "D", '
 { "name": "Q0",
 { "name": "c1",
 { "name": "Q1",
 3
 5
 6
                         "wave": "0..1.0.1..."},
 7
  ],
 8
 9
10
     }
11
Clock
   c0
    D
   Q0
   c1
   Q1
```

Now let's do a timing diagram for a falling edge triggered one:

You can use the same D input: 0..1.0.1..0:



Task 2: Work on and pass the Altium Education Course Quiz, there's a certificate available?



https://education.altium.com/

The criteria of completion are to pass the quiz. **Send me a screen capture of the finished course if you like.**