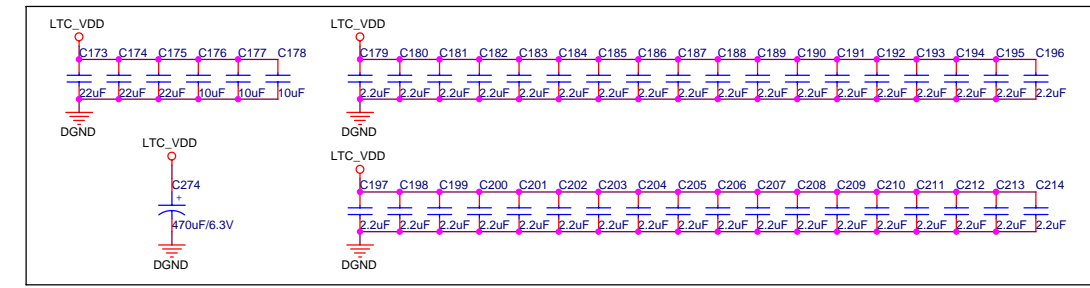
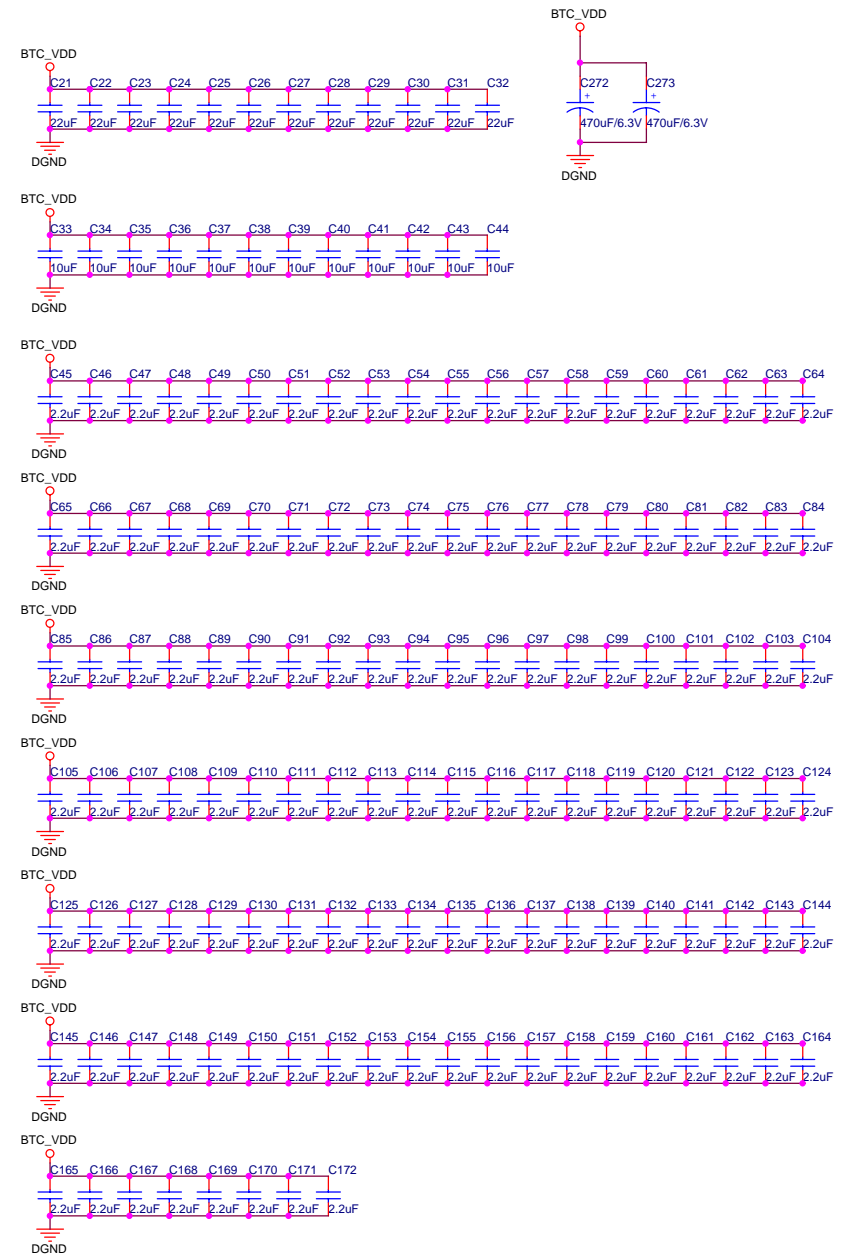
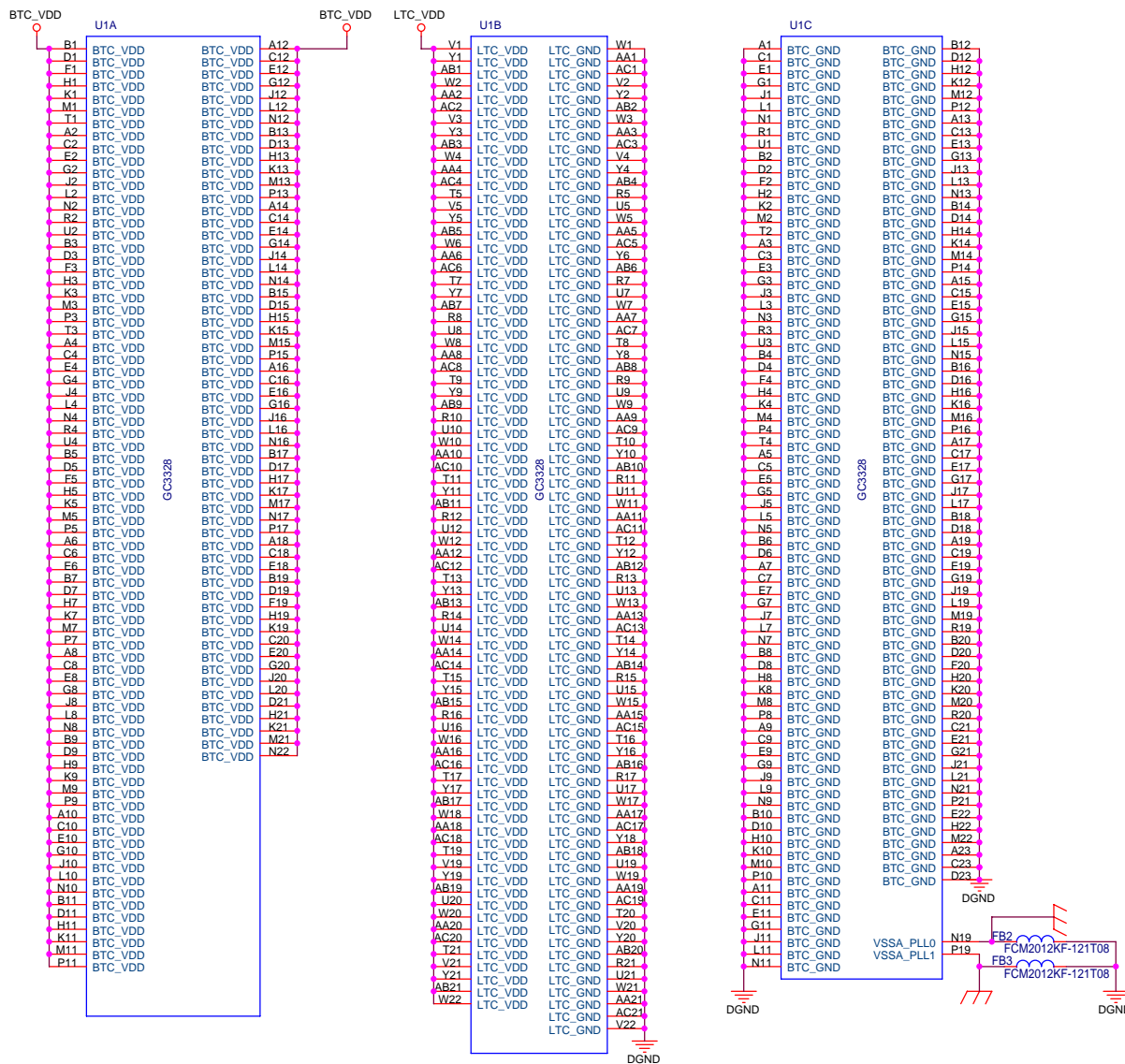
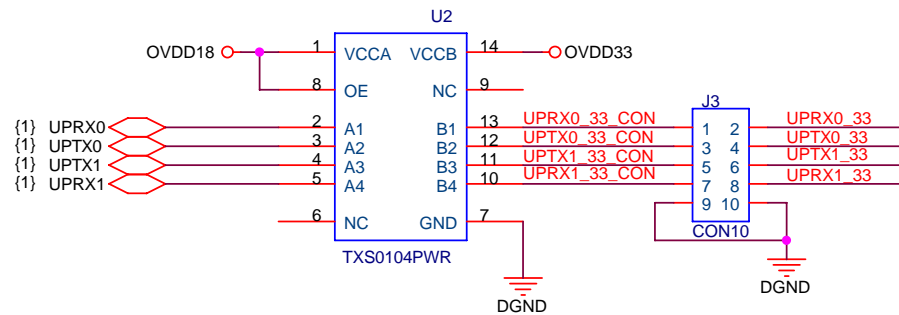


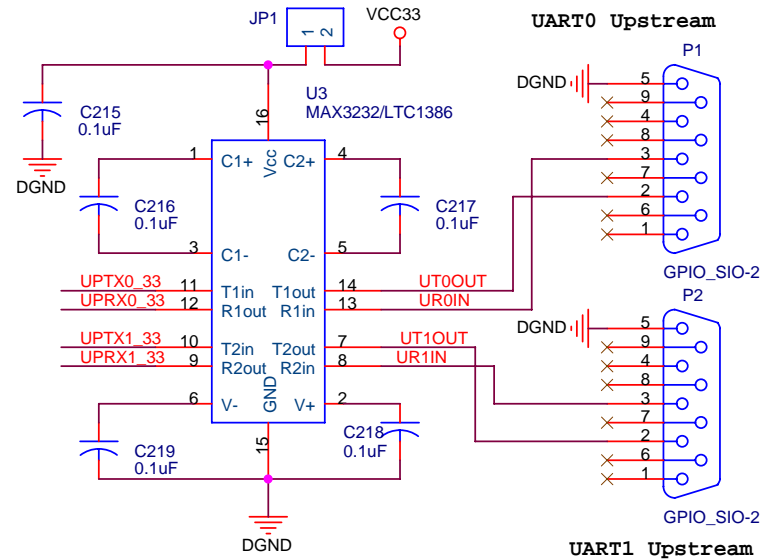
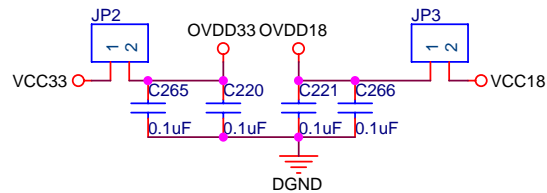
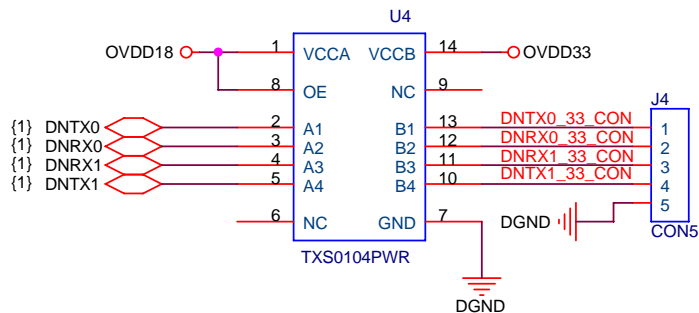
Title		
<Title>		
Size	Document Number	Rev
A4	<Doc>	0.4
Date:	Friday, February 13, 2015	Sheet 1 of 4



Title			
<Title>			
Size	Document Number		Rev
A3	<Doc>		0.4
Date:	Friday, February 13, 2015	Sheet	2 of 4

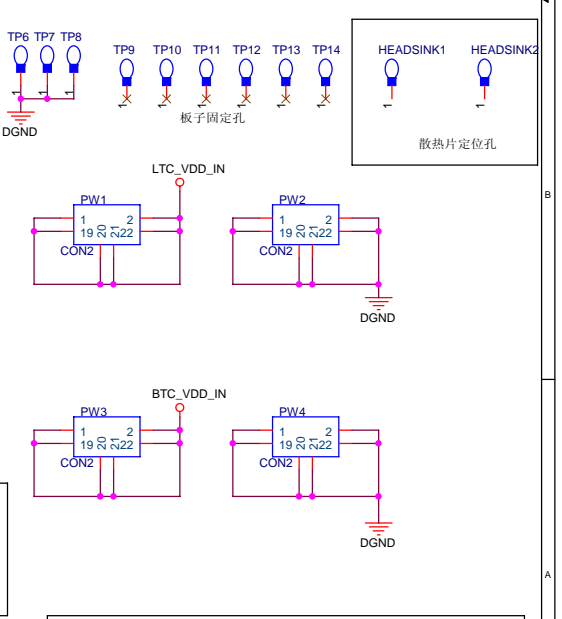
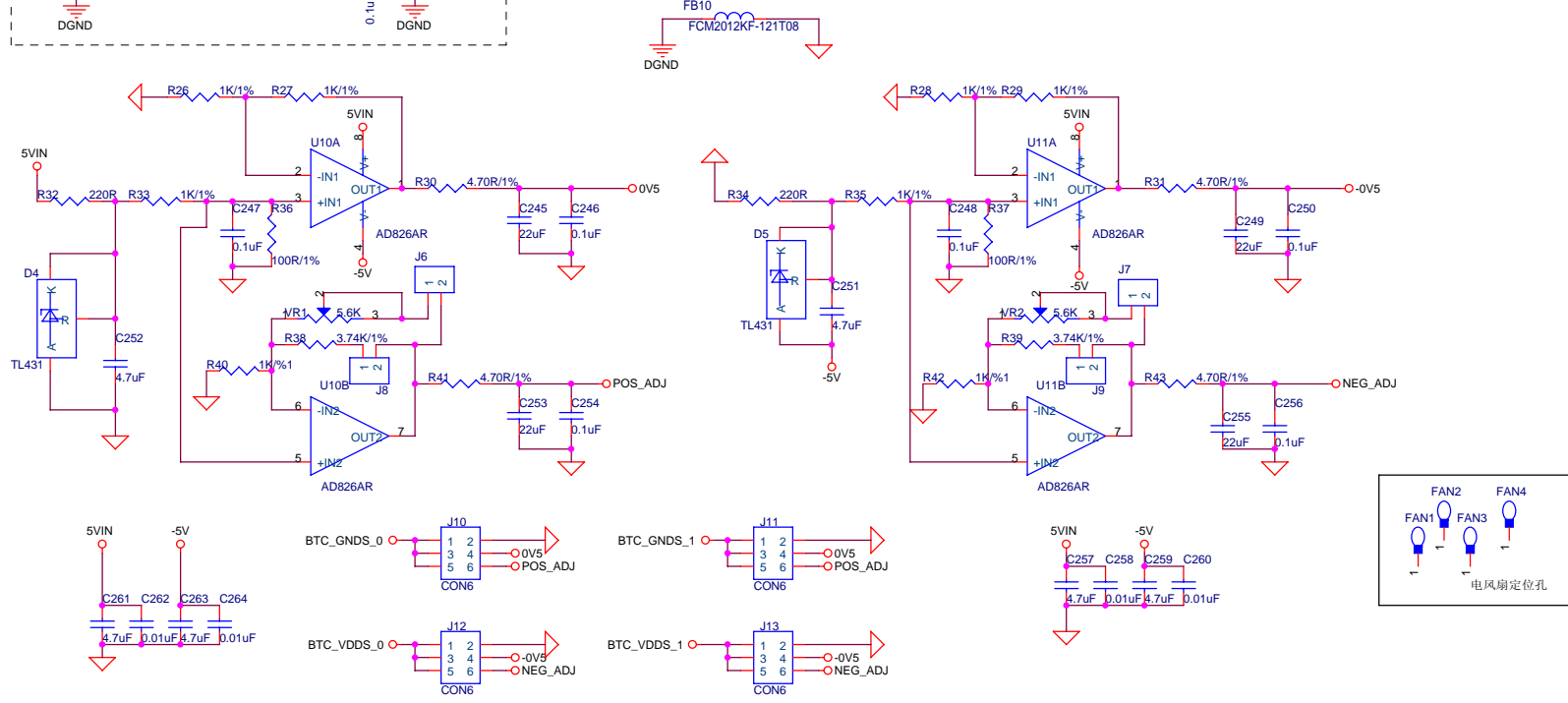
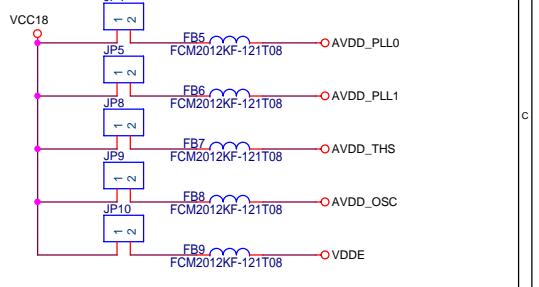
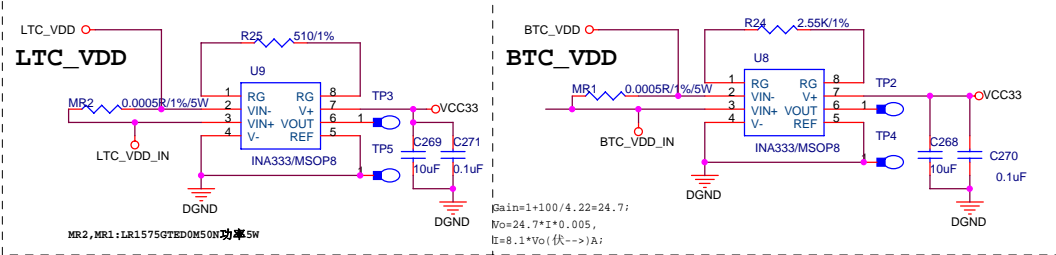
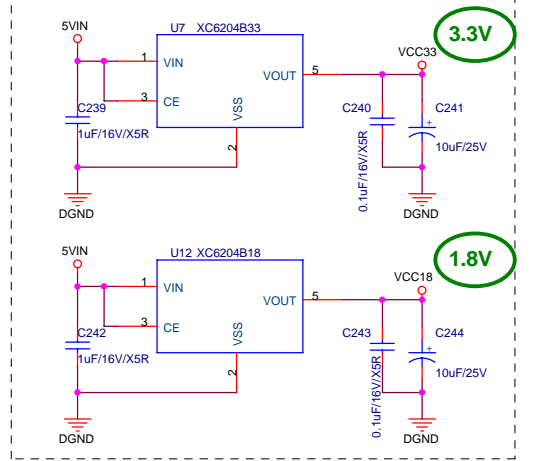
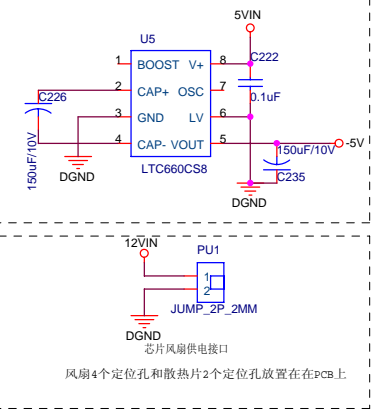
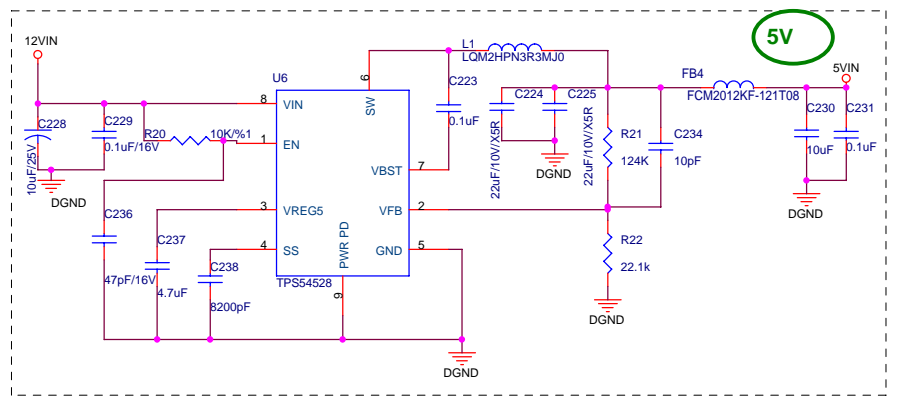
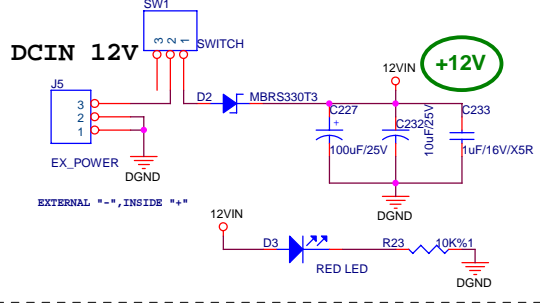


UP* need connect to DN*;
JP7PIN1,3,5,7 connect
to another chip's J5



串口线:直连

Title		
<Title>		
Size	Document Number	Rev
A4	<Doc>	0.4
Date:	Friday, February 13, 2015	Sheet 3 of 4



Title			
<Title>			
Size	Document Number		Rev
A3	<Doc>		0.4
Date:	Friday, February 13, 2015	Sheet	4 of 4