



Fig. 2-2 RK3399 boot procedure flow

2.3 System Interrupt Connection for Cortex-A72/Cortex-A53

RK3399 provides an general interrupt controller(GIC) for Cortex-A72/Cortex-A53, which has 148 SPI(shared peripheral interrupts) interrupt sources and 8 PPI(Private peripheral interrupt) interrupt sources. GIC communicate with CPU through two axi stream interrupt interfaces separately for each cluster. The triggered type for each SPI interrupt is high level sensitive, and for each PPI interrupt is low level sensitive, not programmable. The detailed interrupt sources connection is in the following table. For detailed GIC setting, please refer to Chapter