Embedded Linux Arch





SOC RK3399

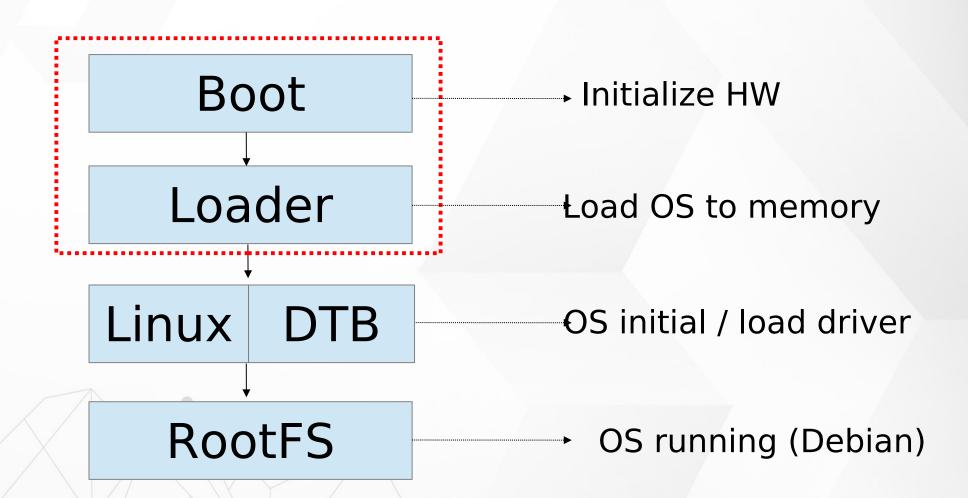
System Peripheral	RK3399		Connectivity
Clock & Reset			USB OTG0 3.0/2.0
PMU	Cortex-A72 Dual-Core	Cortex-A53 Quad-Core (32K/32K L1 I/D Cache)	USB OTG1 3.0/2.0
PLL x 8	(48 K/32K L1 I/D Cache)	(32K/32KL1 VD Cache)	Type-C x 2
System register	1MB L2 Cache	512KB L2 Cache	USB HOSTO 2.0
Timer x 26	CCI500		USB HOST1 2.0
PMW(4ch)	CoreSight		USIC
Watchdog x 3	L '		PCIe2.1
Crypto x 2	Cortex-M0 Dual-Core		12S/PCM x 3
SAR-ADC	Parist No. No. Parist		
TS-ADC	Multi-Media Processor		SPDIF(8ch)
Interrupt Controller	Mali-T860MP4 GPU	2D Graphics Engine	UART x 5
DMAC x 2	(256K L2 Cache)		SPI x 6
PVTM x 5	JPEG Encoder	JPEG Decoder	12C x 9
Mailbox x 2			Giga-Ethernet
Multi-Media Interface	Image Enhancement	Dual pipe ISP	SDIO 3.0
Dual MIPI-CSI 4 Lane	Processor	3.33,1,7,3.33	
	1080p Video Encoder	4K Video Decoder	GPIO x 122
eDP1.3 4 Lane	Today video Elicodei		Embedded Memory
Dual MIPI-DSI 4 Lane	External Memory Interface		SRAM
DP1.3 4 Lane with HDCP2.2	eMMC5.11/F	SD3.0/MMC4.5	ROM
HDMI2.0 3 Lane with HDCP2.2	DDR3/DDR3L/LPDDR3/LPDDR4		Secure eFuse
Dual Display Controller	Hardware-based DDR frequency scaling		Non secure eFuse

http://wiki.friendlyarm.com/wiki/index.php/NanoPi_M4#Diagram.2C_Layout_and_Dimension





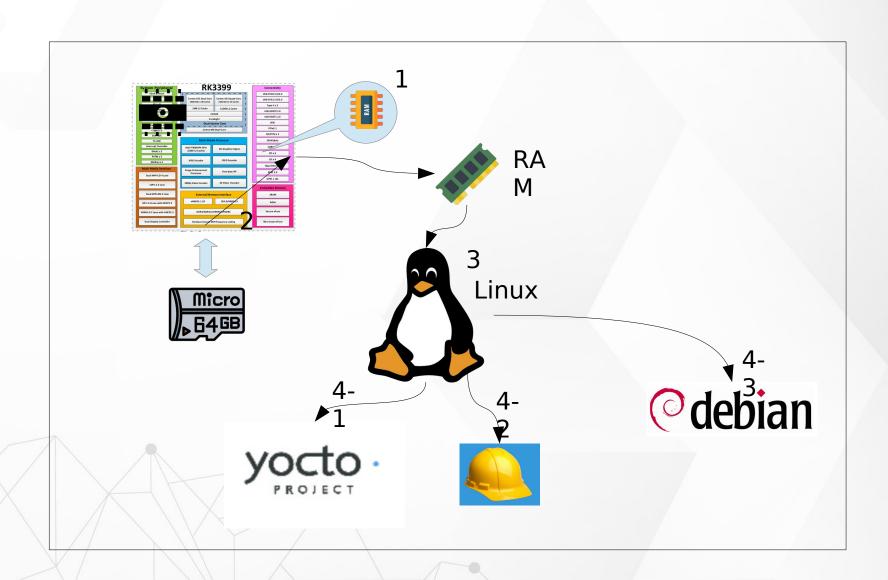
Embedded Linux System Booting





System Start Up









Boot

- Power On BootROM code (work in cache)
 - Load BL1
- BL1 (work in cache IDB_Loader)
 - Initial simple exception vectors, PLL (clock)
 - Initial Multi-CPU
 - Load BL2
- ▶ BL2 (work in cache)
 - Initial DDR memory
 - Initial C environment (stack, heap,)
 - Load BL31





Boot

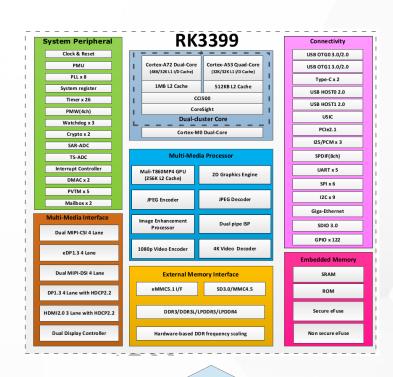
- > BL31 (work in DDR)
 - Initial exception vectors
 - Load BL32 (u-boot)
- BL32 U-boot
 - Initial storage device
 - Load Linux Kernel
- Xernel
 - kernel/Documentation/arm64/booting.txt
 - Load RootFS





Embedded Linux System

















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