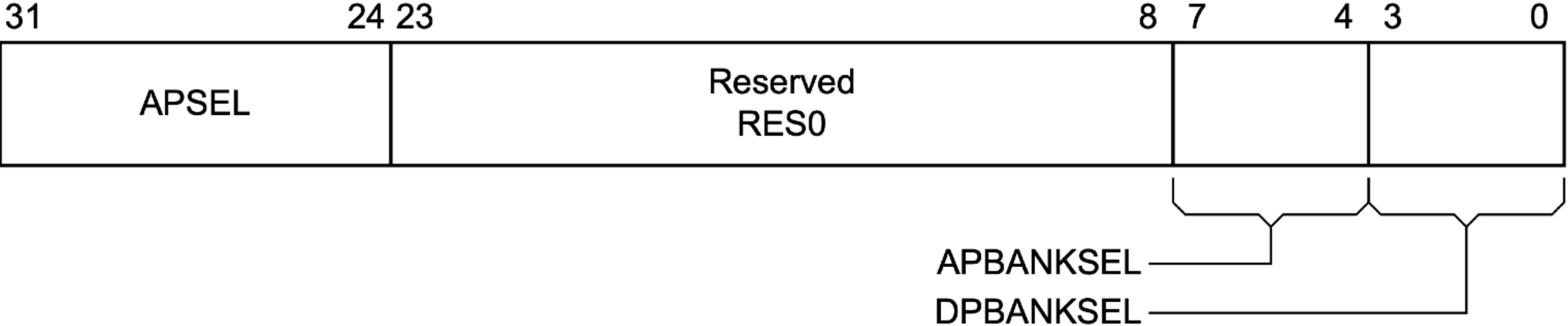
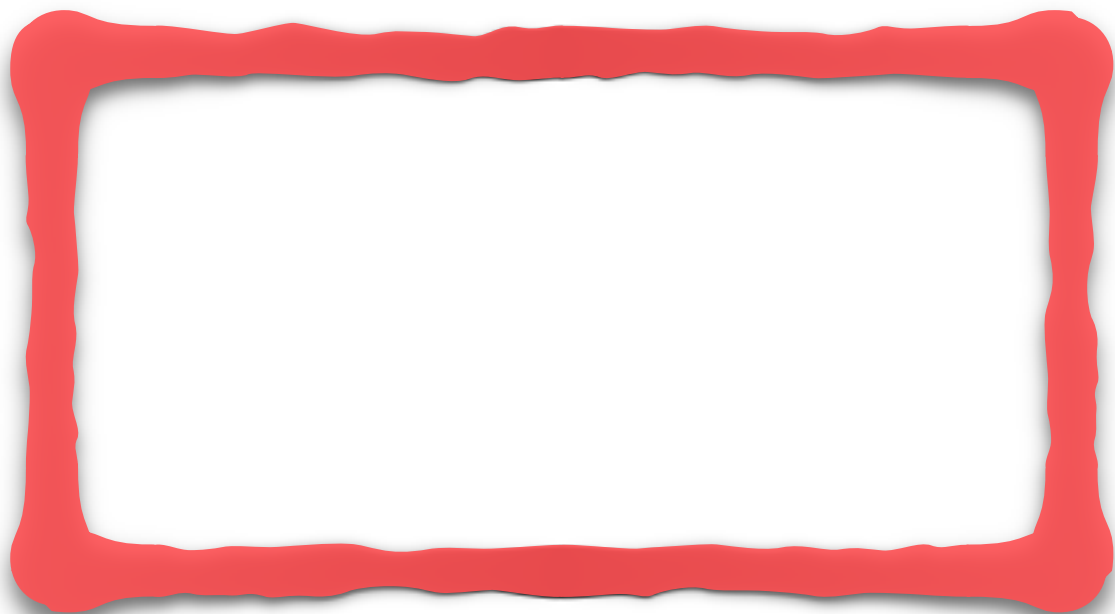
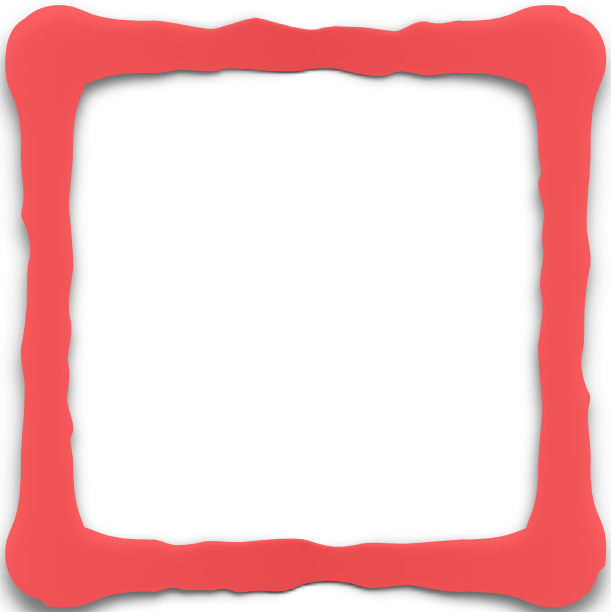
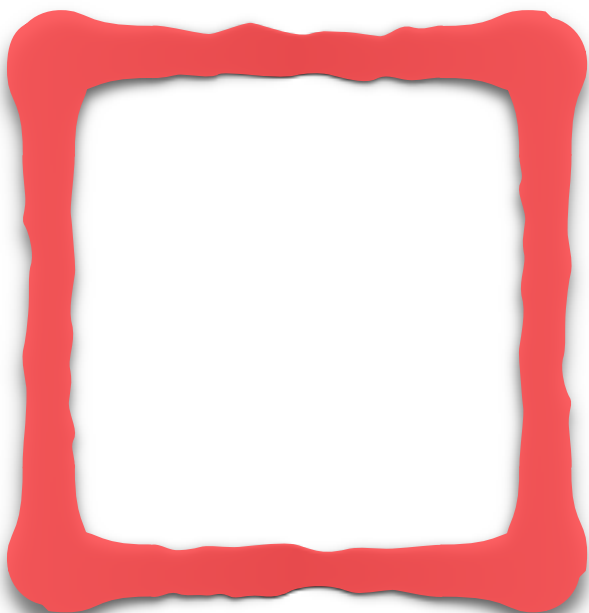


The **SELECT** bit assignments are:





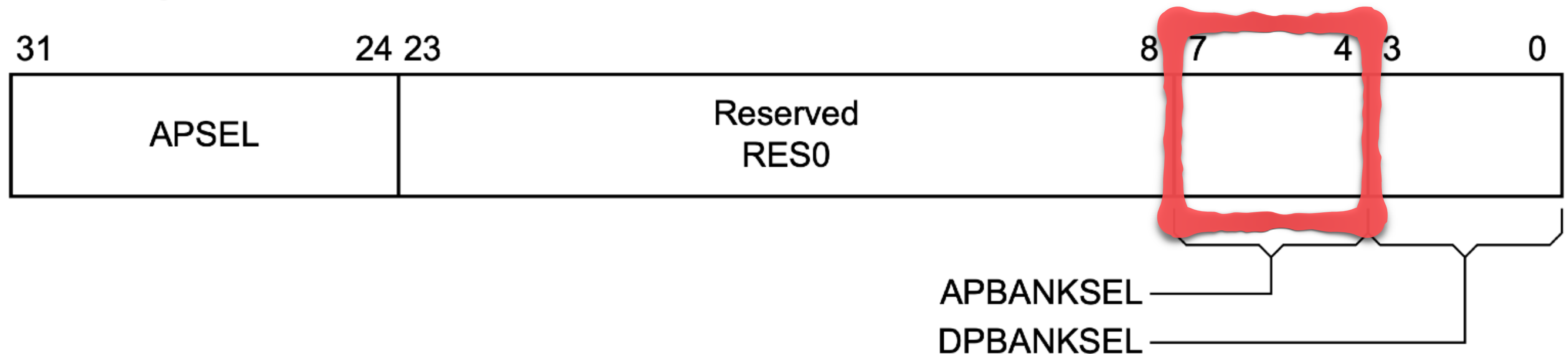




SELECT

Selecting banks in the Debug Port and Access Port

The **SELECT** bit assignments are:



ADIV5 MEM-AP

