VexRiscv Debug Interface

DebugPlugin.scala

0x04: Inject Instruction

31															0
Write						instruction[31:0]									

0x04: RISC-V Rd Value

31															0
Read						result[31:0]									

Fomu Reference Manual — https://rm.fomu.im/
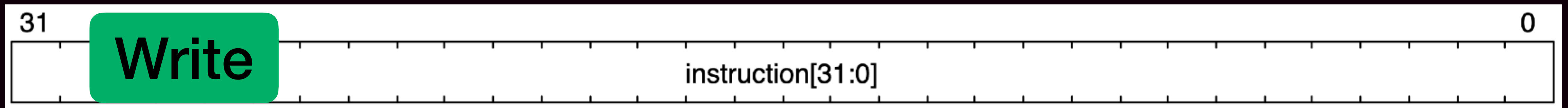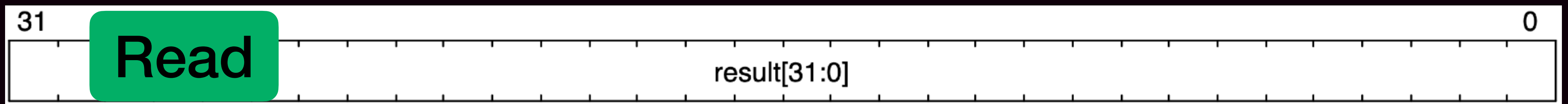
What Do We Need for a Debugger?
VexRiscv

Memory	Registers	Control
? Peek ? Poke	? Read ? Write	✓Halt ✓Step ✓Reset ✓Break