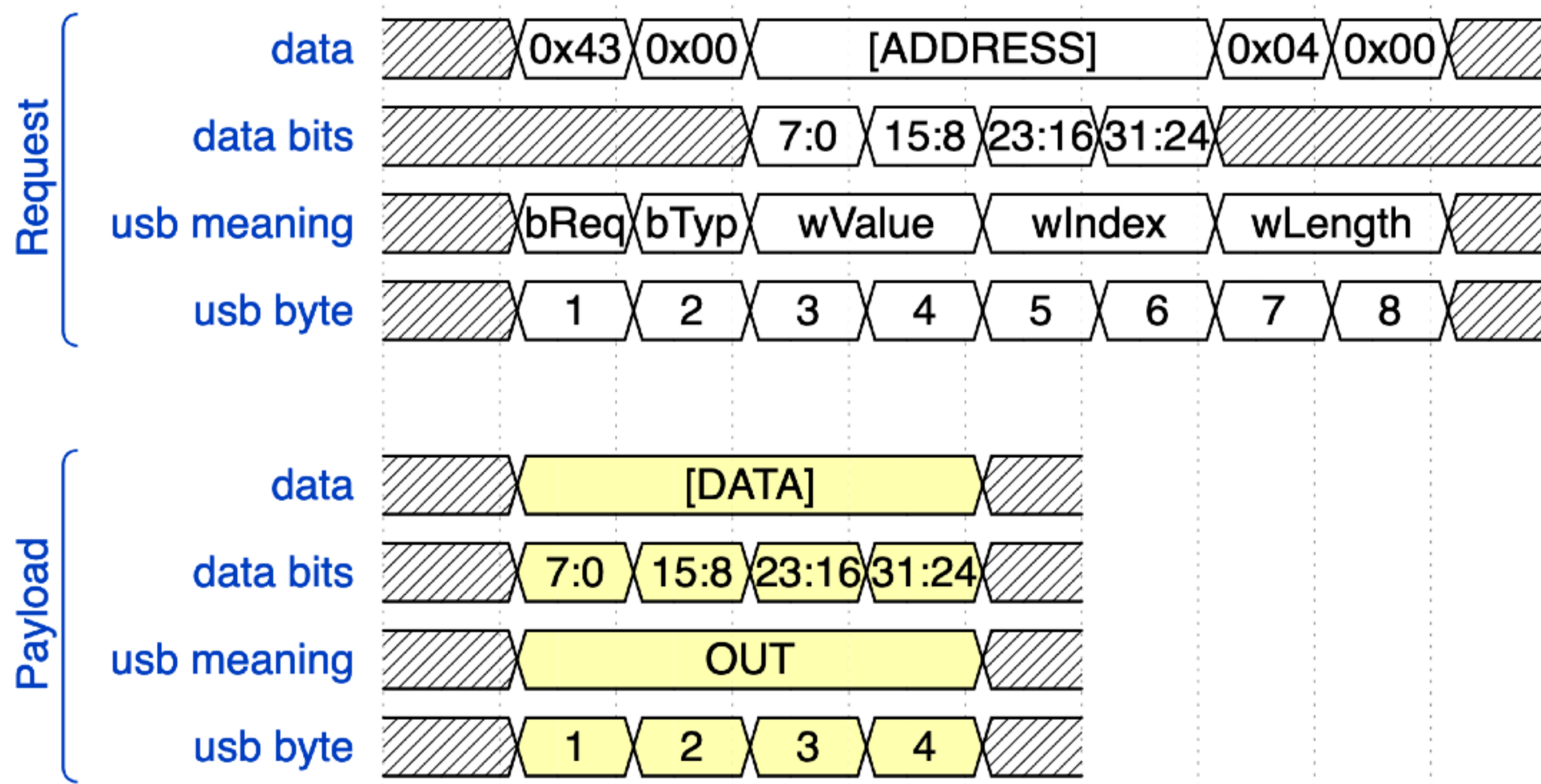
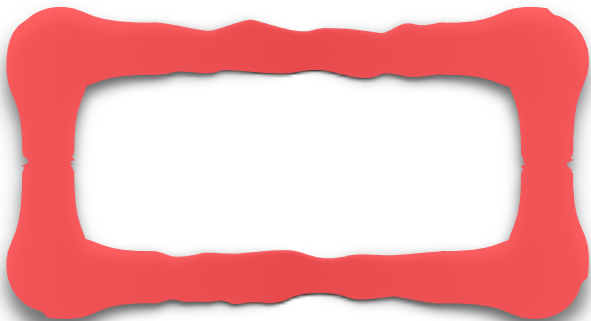
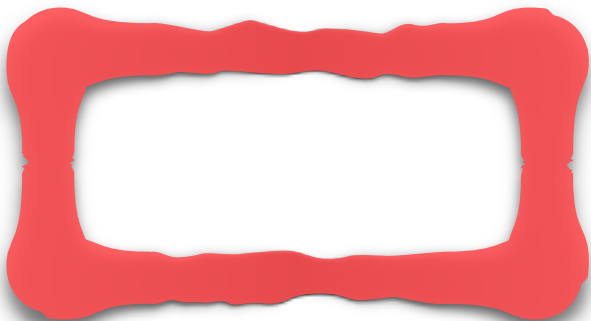


USB Wishbone Debug Protocol

The protocol transfers four bytes a time in big-endian (i.e. USB) order. It uses SETUP packets with the special type (0x43) as an *attention* word. This is then followed by an **OUT** packet.



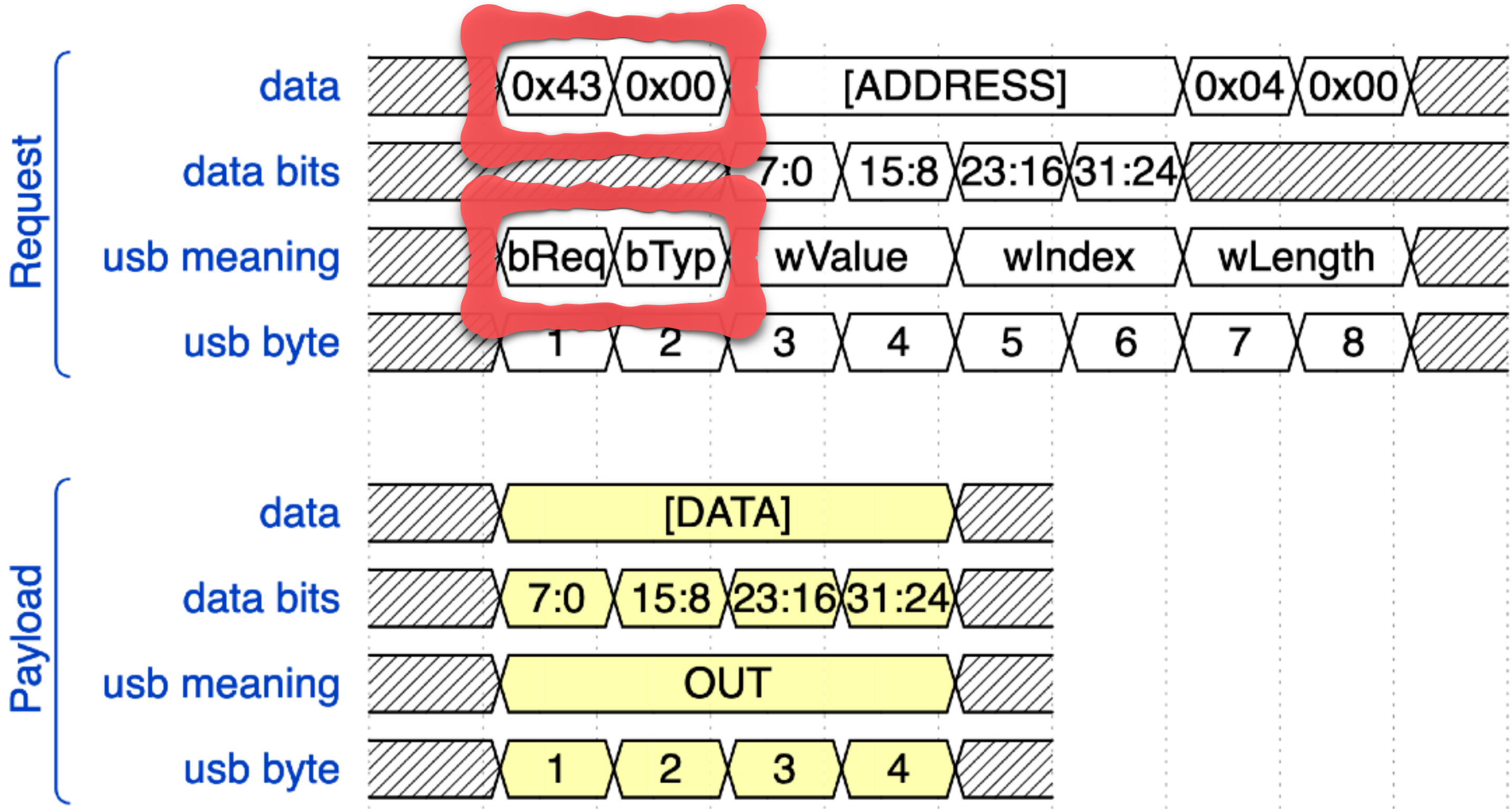




Connecting This to the World

USB Wishbone Debug Protocol

The protocol transfers four bytes a time in big-endian (i.e. USB) order. It uses SETUP packets with the special type (0x43) as an *attention* word. This is then followed by an OUT packet.



What Do We Need for a Debugger?

VexRiscv on LiteX

Memory

- ✓ Peek
- ✓ Poke

Registers

- ✓ Read
- ✓ Write

Control

- ✓ Halt
- ✓ Step
- ✓ Reset
- ✓ Break