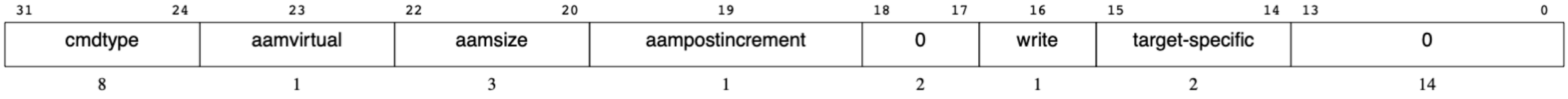
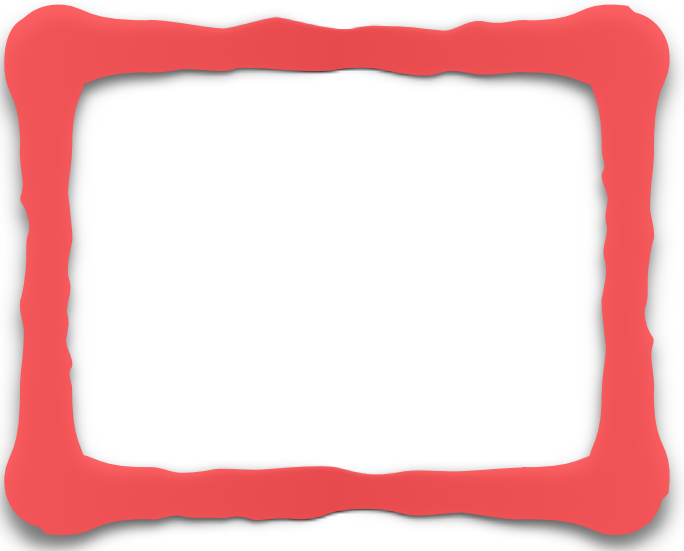
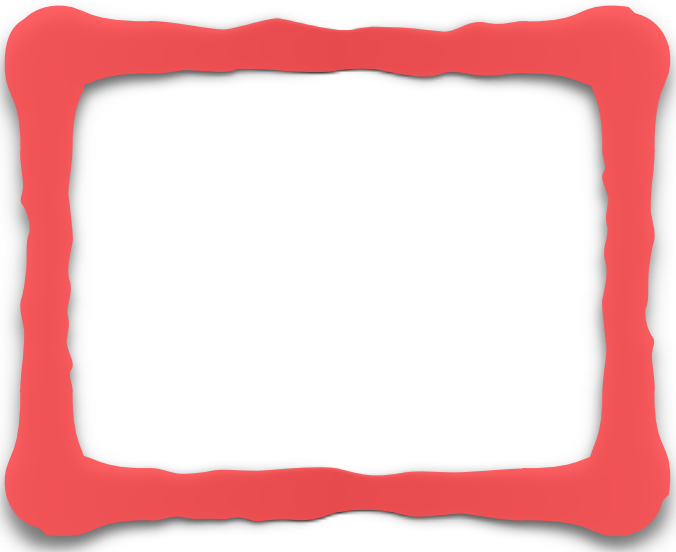
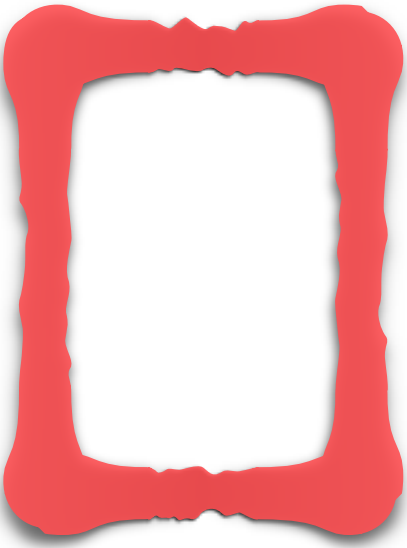
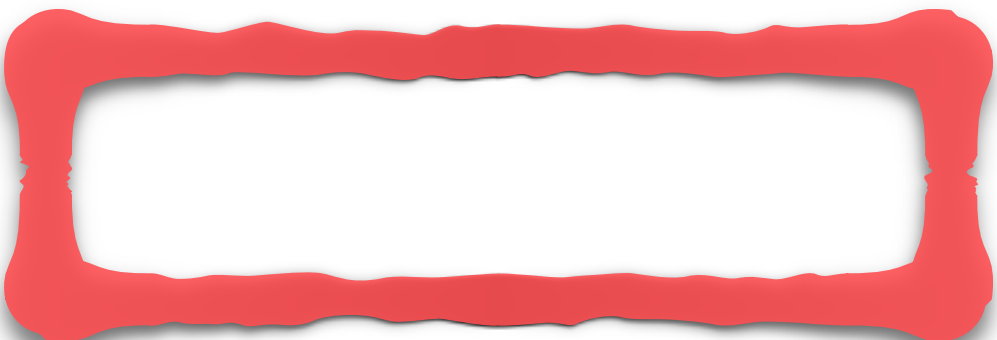
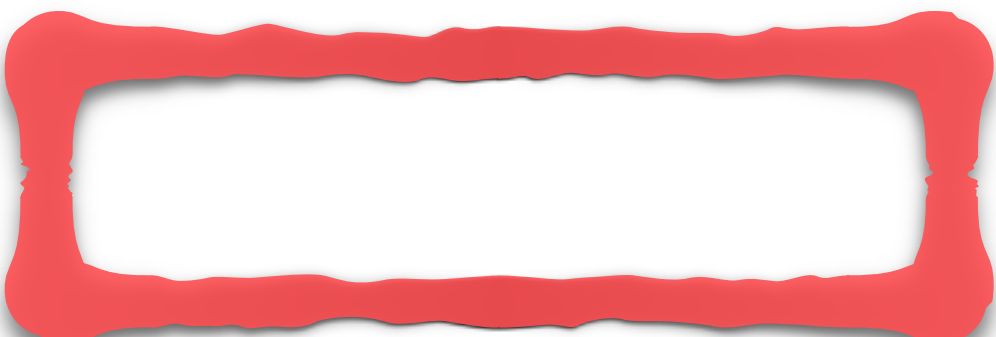

31
24
23
22
20
19
18
17
16
15
14
13
0
cmdtype
aamvirtual
aamsize
aampostincrement
0
write
target-specific
0
8
1
3
1
2
1
2
14







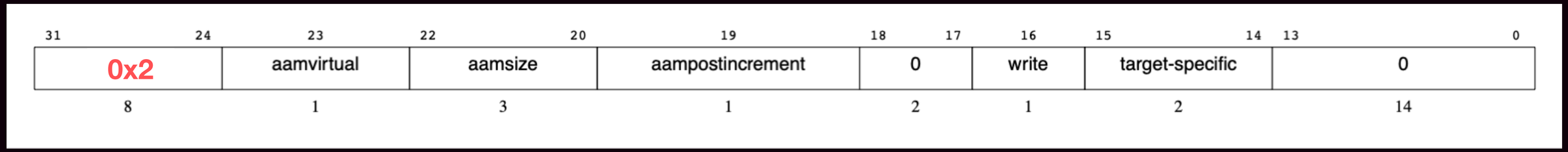




0x2

Abstract Access Memory

`command: 0x17 / data0: 0x04 / data1: 0x05`



- cmdtype: 2 (memory)
- aamsize: 2 (32-bit)
- write: 1 to write, 0 to read
- Value: **`data0`**
- Address: **`data1`**

Abstract Access Memory

RP2350 Hazard3 Core

```
wire [N_HARTS-1:0] hart_instr_data_vld_nxt = {{N_HARTS-1{1'b0}},
  acmd_state_nxt == S_ISSUE_REGREAD || acmd_state_nxt == S_ISSUE_REGWRITE || acmd_state_nxt == S_ISSUE_REGEBREAK ||
  acmd_state_nxt == S_ISSUE_PROGBUF0 || acmd_state_nxt == S_ISSUE_PROGBUF1 || acmd_state_nxt == S_ISSUE_IMPEBREAK
} << hartsel;

wire [31:0] hart_instr_data_nxt =
  acmd_state_nxt == S_ISSUE_REGWRITE  ? 32'hbff02073 | {20'd0, acmd_regno,  7'd0} : // csrr xx, dmdata0
  acmd_state_nxt == S_ISSUE_REGREAD   ? 32'hbff01073 | {12'd0, acmd_regno, 15'd0} : // csrw dmdata0, xx
  acmd_state_nxt == S_ISSUE_PROGBUF0  ? progbuf0                                  :
  acmd_state_nxt == S_ISSUE_PROGBUF1  ? progbuf1                                  :
                                        32'h00100073;                               // ebreak
```

https://github.com/Wren6991/Hazard3/blob/stable/hdl/debug/dm/hazard3_dm.v lines 745-755