

Control

? Halt

? Step

? Reset

? Break

Registers

✓ Read

✓ Write

Control

✓ Halt

✓ Step

? Reset

? Break

Memory



★ Peek

★ Poke

What Do We Need for a Debugger?

ARM Cortex-M via ADIv5 / MEM-AP



Memory

★ Peek

★ Poke

Registers

✓ Read

✓ Write

Control

✓ Halt

✓ Step

? Reset

? Break

CPU Control: Memory Mapping

AIRCR: Application Interrupt and Control Register

