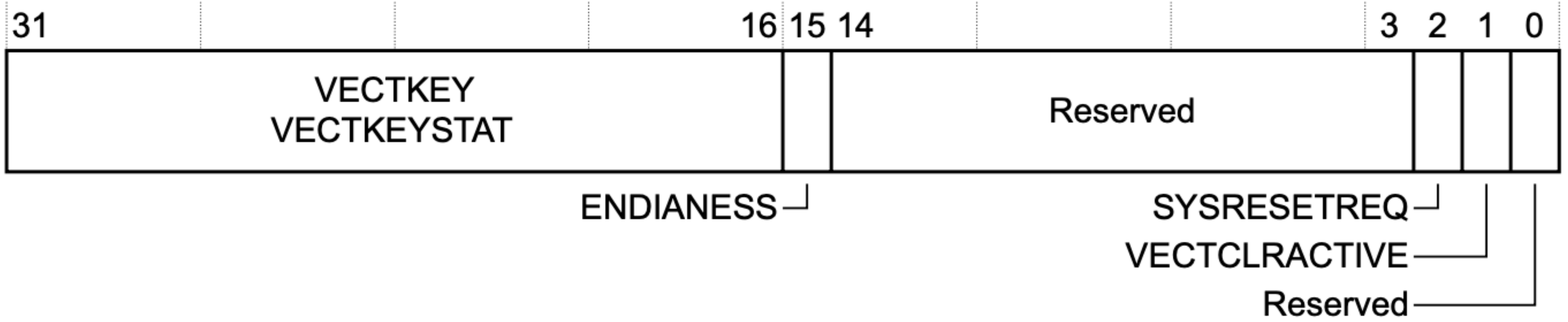
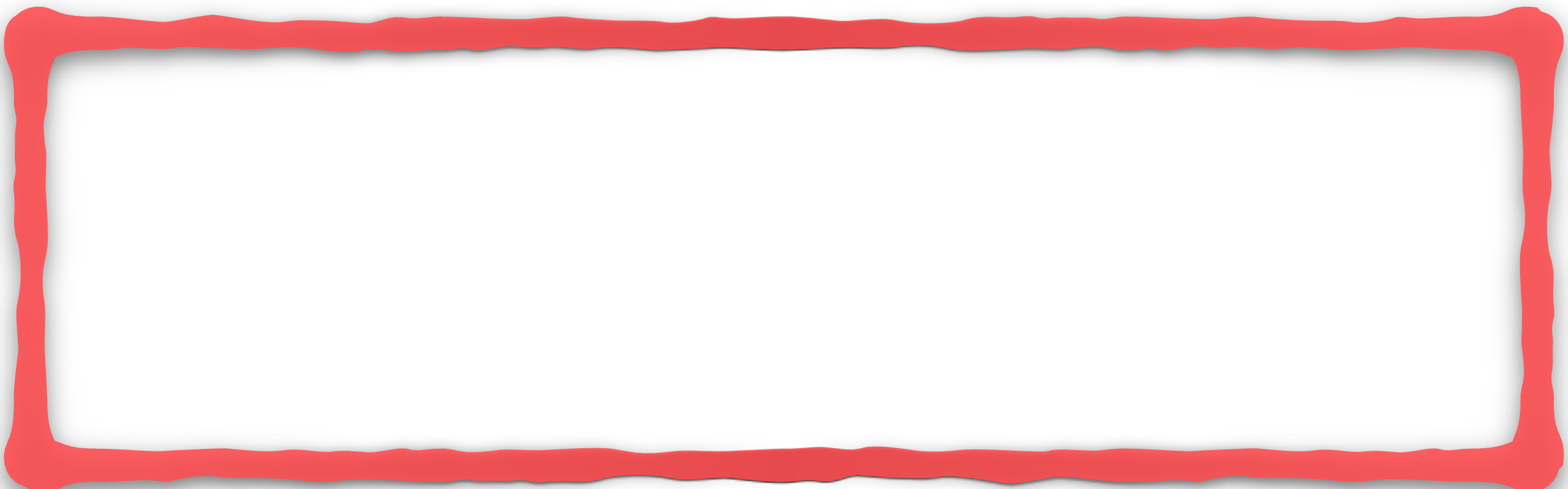


Figure B3-4 AIRCR bit assignments

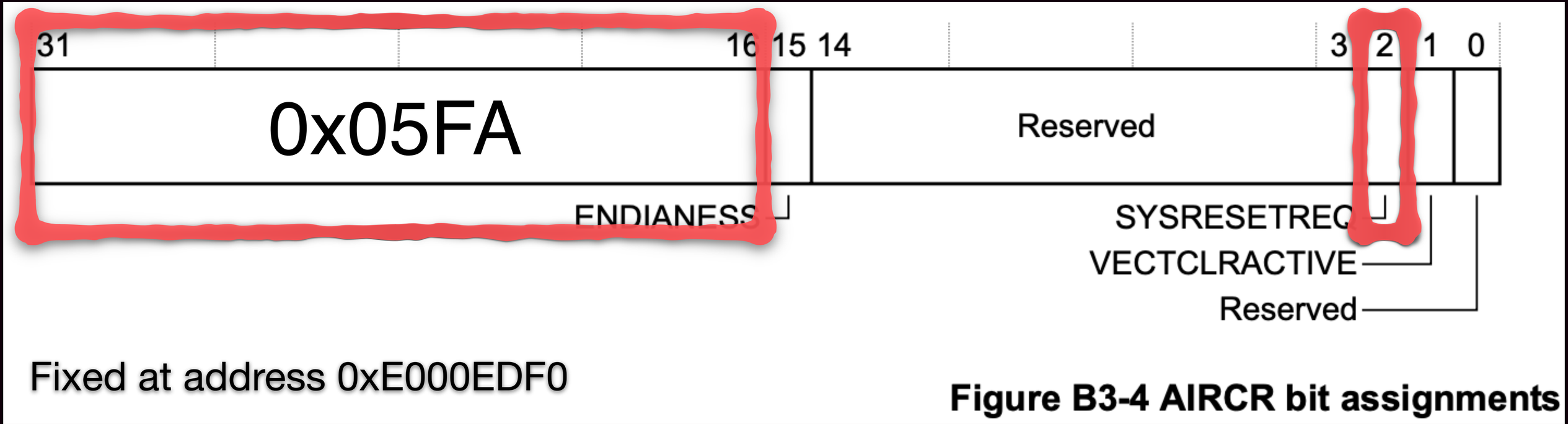


Fixed at address 0xE000EDF0

0x05FA

CPU Control: Memory Mapping

AIRCR: Application Interrupt and Control Register



Fixed at address 0xE000EDF0

Figure B3-4 AIRCR bit assignments

From: ARMv6-M Architecture Reference Manual

What Do We Need for a Debugger?

ARM Cortex-M via ADIv5 / MEM-AP