### Steps for Accessing RAM

- 1. Write target address to TAR
- 2. Read or write DRW

#### What Do We Need for a Debugger?

ARM Cortex-M via ADIv5 / MEM-AP

## Memory

- ? Peek
- ? Poke

# Registers

- ? Read
- ? Write

### Control

- ? Halt
- ? Step
- ? Reset
- ? Break