

# RISC-V Debug Summary

- Four different address spaces
  - JTAG, DMI, RAM, CSRs
- Three different ways to access RAM
  - Program Buffer, Abstract Access Memory, System Bus Access
- Two different ways to access CSRs
  - Program Buffer, Abstract Access Register
- One way to access GPRs
  - Abstract Access Register

**RP2350: RISC-V over SWD**