

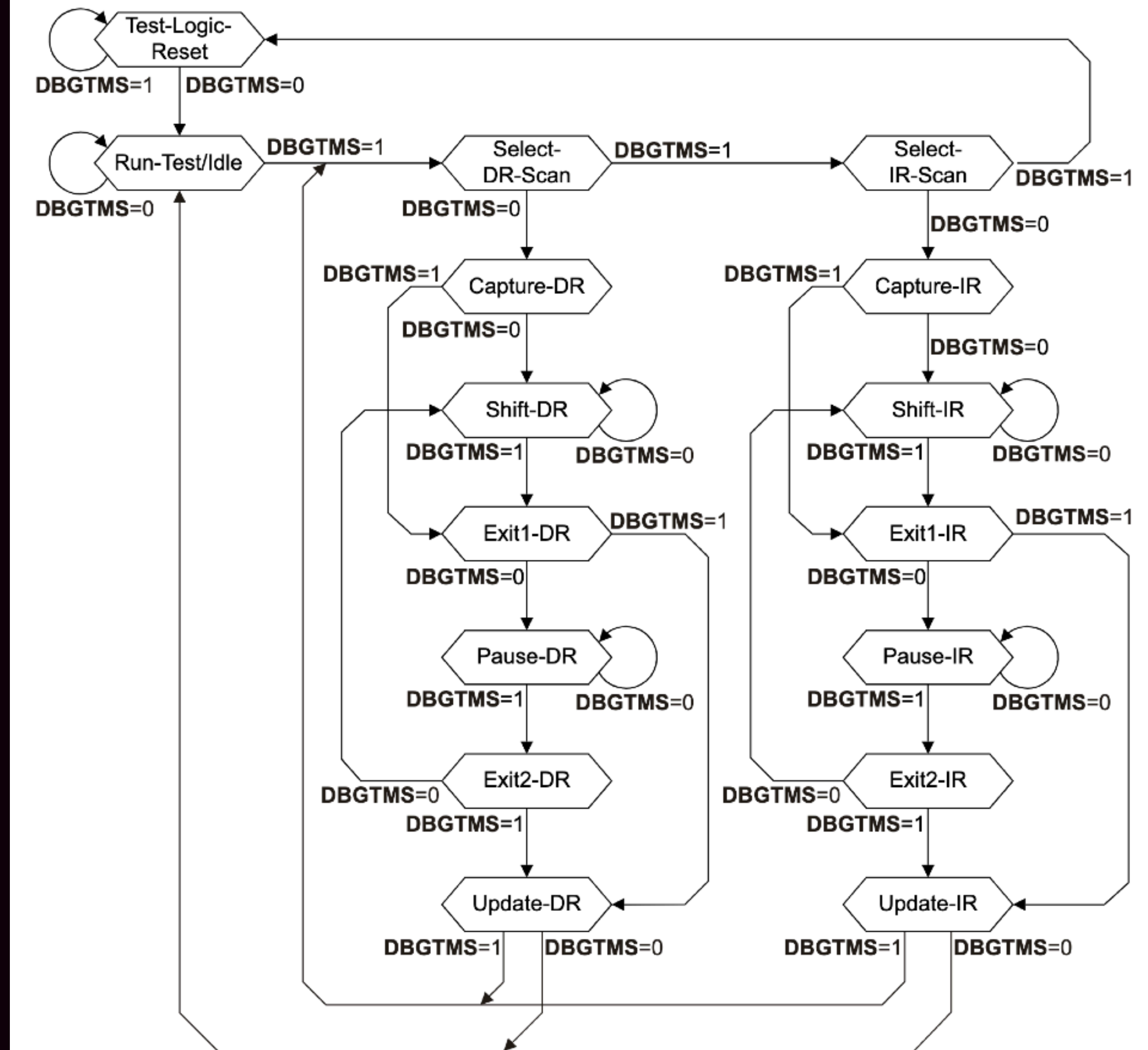
JTAG

How Do Other Chips Do Debug?

JTAG Overview

Whirlwind Tour

- 4 Wires: TCK, TDO, TDI, TMS
- Data captured on TCK rising edge



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Note that Arm signal names differ from those used in the IEEE diagram.

From: Arm Debug Interface Architecture Specification ADIv5.0 to ADIv5.2