System Bus Access

System Bus Access: Control and Status

sbcs: 0x38 / sbaddress0: 0x39 / sbdata0: 0x3c

31	29	28	23	22		21	20		19 17		16	
sbversion		0		sbbusyerror		sbbusy	sbreadonaddr		sbaccess		sbautoincrement	
3	3 6			1		1	1		3		1	
15 sbreadondata	14	sberror	11	sbasize 5	sbacc	ess128	sbaccess64	sb	2 access32	sbac	1 cess16	sbaccess8
1	3 7		7	1		1	1			1	1	

- sbaddress0: Address to use for memory operations
- sbreadonaddr: Perform a read when **sbaddress0** is written
- sbdata0: Write to this register to write data at sbaddress0
- Works when core is not halted!