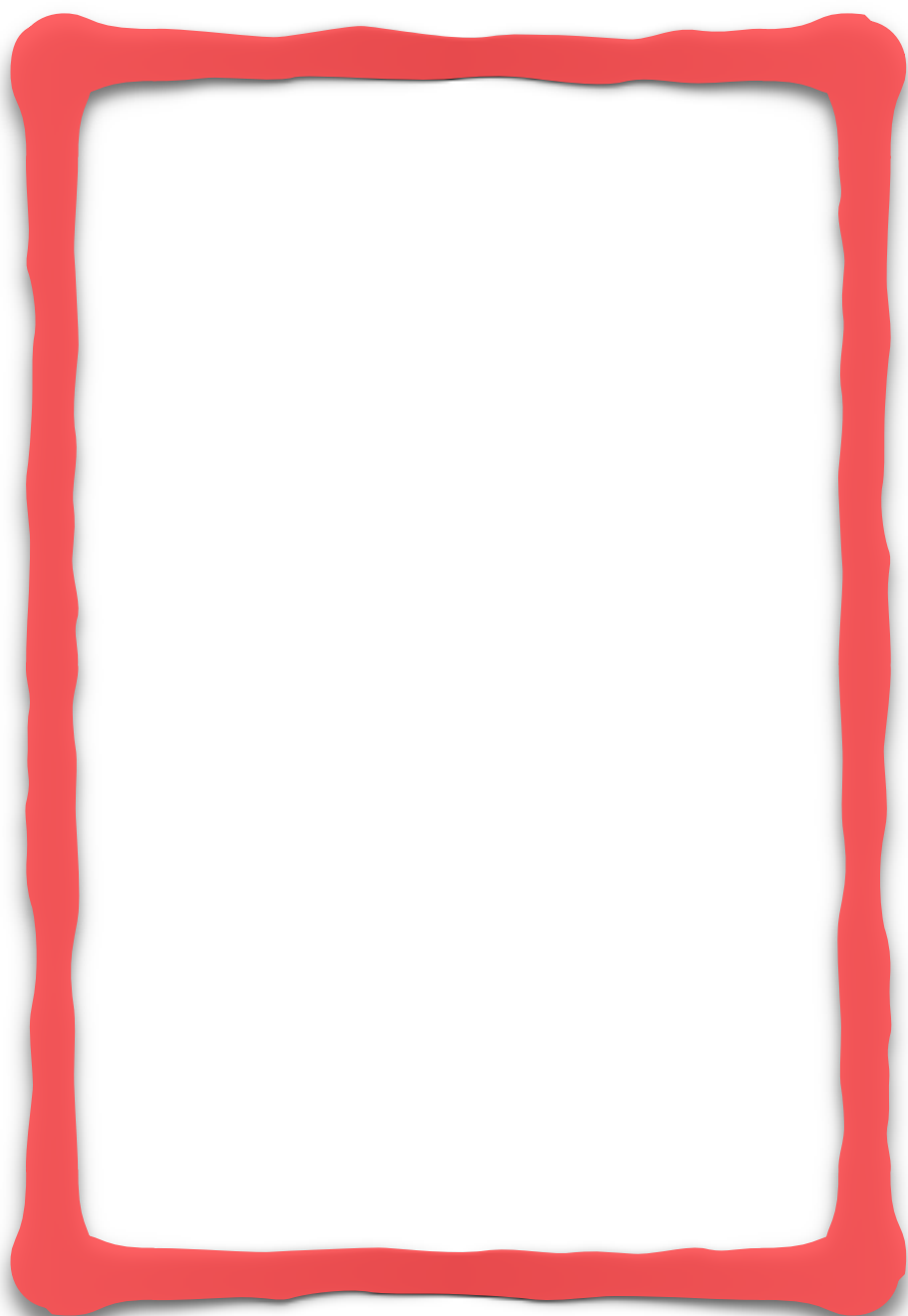


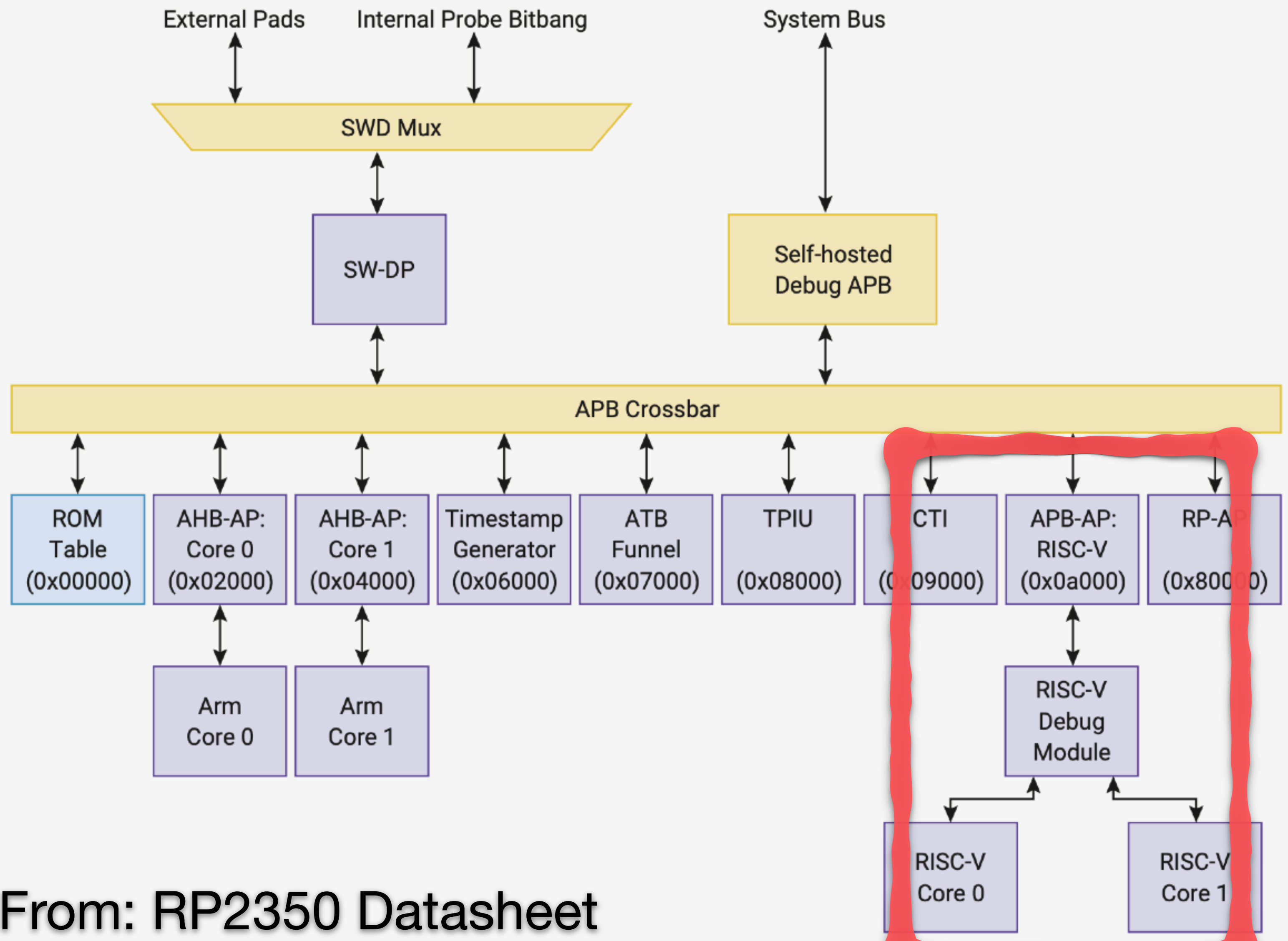
From: RRP2350 Dashboard



RP2350: ADbv6

Different addressing

- RISC-V Debug Module over ADbv6
- Connected to MEM-AP
- “Memory Space” is DMI
- TAR/DRW to access data



From: RP2350 Datasheet

Debugging Recap

What we did for the last 45 minutes

- Discussed JTAG basics
 - Including a peek at SWD
- Covered ADRv5 + MEM-AP
- RISC-V JTAG
 - Including RP2350

