

Control

? Halt

? Step

? Reset

? Break

Registers

? Read

? Write

Memory

? Peek

? Poke

Control

✓ Halt

? Step

✓ Reset

? Break

What Do We Need for a Debugger?

RISC-V Debug Specification

Memory

? Peek
? Poke

Registers

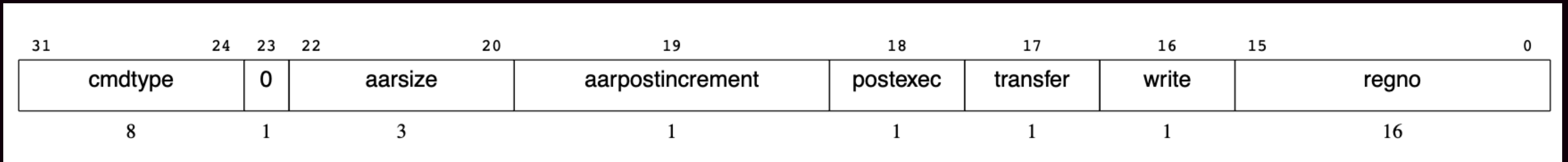
? Read
? Write

Control

✓ Halt
? Step
✓ Reset
? Break

Abstract Access Register

command: 0x17 / data0: 0x04



cmdtype: 0 (register)

aarsize: 2 (32-bit)

transfer: must be 1

write: 1 to write, 0 to read

regno: register index | 0x1000