Programming Flash on a Target A very manual process

4.9.38	FLASH register map and reset values																																		
Table 28. Register map and reset value table																																			
Offset	Register name reset	31	30	59	28	27	56	25	24	23	22	21	20	19	18	17	16	15	14	13	12	1	10	6	00	7	9	5	4	m	2	-	0		
0x000	FLASH_ACR	Res.	Res.	Res.	Res.	Res.	Res.	Res.	Res.	Res.	Res.	Res.	Res.	Res.	Res.	Res.	Res.	Res.	Res.	Des.	Res.	Res	Ses	Res	S C C	Res	C C		WRHIGHFREG		LATENCY				
	0x00000037																											1	1	0	1	1	1		
0x004	FLASH_KEYR1	KEY1R																																	
	0x00000000	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
0x008	FLASH_ OPTKEYR															0	PT	KEY	′R																
	0x00000000	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
0x00C	FLASH_CR1	Res.	Res.	Res.	CRCRDERRIE1	CRCENDIE1	DBECCERRIE1	SNECCERRIE1	RDSERRIE1	RDPERRIE1	OPERRIE1	INCERRIE1	Res.	STRBERRIE1	PGSERRIE1	WRPERRIE1	EOPIE1	CRC EN	25 (600)	Res.	Res	Res		SNE	31	START1	FW1		PSIZE1	RFR1	SER.	PG1	LOCK1		
	0x00000031				0	0	0	0	0	0	0	0		0	0	0	0	0					0	0	0	0	0	1	1	0	0	0	1		
	ELAQUI QD1	50	62	97	65	END1	CERR1	CERR1	ERR1	ERR1	RIE1	RR1	53	ERR1	ERR1	ERR1	P1		50	67	50	U	0	07	00	60		0	0 0	NI ISY1		E I	. E		

Programming Flash on a Target

"Flash algorithms" provide canned alternatives

```
struct FlashDevice const FlashDevice = {
  FLASH_DRV_VERS, // Driver Version, do not modify!
  "New Device 256kB Flash", // Device Name
          // Device Type
 ONCHIP,
 0x00040000, // Device Size in Bytes (256kB)
                    // Programming Page Size
  1024,
                     // Reserved, must be 0
 0,
                     // Initial Content of Erased Memory
 0xFF,
 100,
                     // Program Page Timeout 100 mSec
                     // Erase Sector Timeout 3000 mSec
 3000,
// Specify Size and Address of Sectors
 0x010000, 0x010000, // Sector Size 64kB (2 Sectors)
 SECTOR_END
```