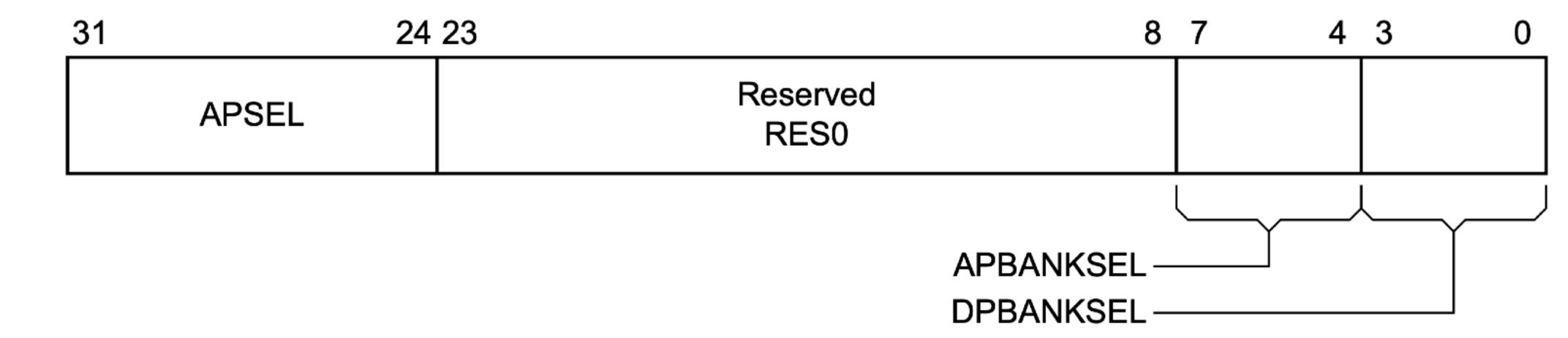
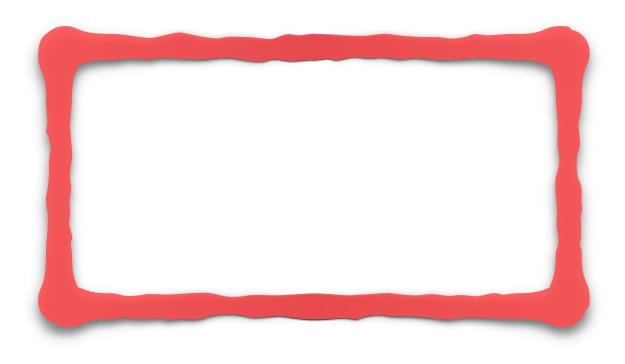
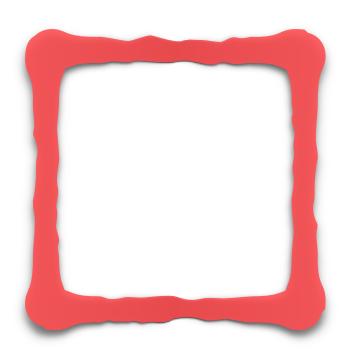


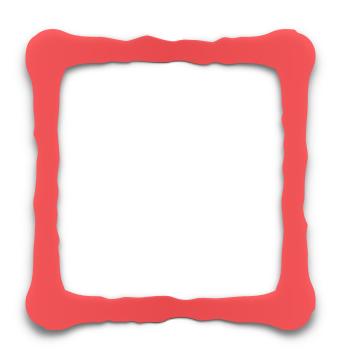
The **SELECT** bit assignments are:





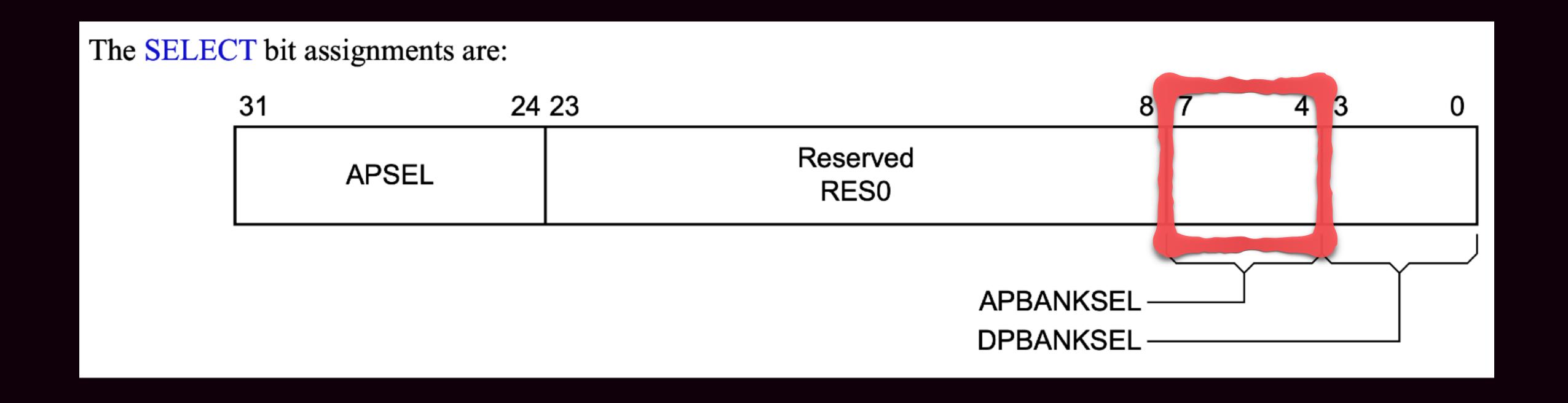






## SELECT

## Selecting banks in the Debug Port and Access Port



From: Arm Debug Interface Architecture Specification ADIv5.0 to ADIv5.2

## ADIV5 MEM-AP

