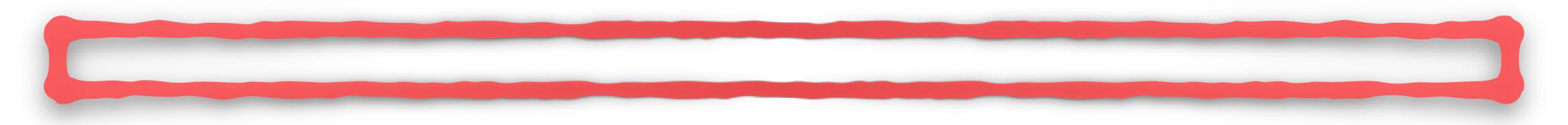


A b	Sticky?c	AP stated	Read result	ACK		
X	X	Busy	UNKNOWN	WAIT	If the Overrun Detect flag is 0b1, this access and response sequence causes the Sticky Overrun flag to be set to 0b1. See <i>CTRL/STAT</i> , <i>Control/Status register</i> on page B2-55.	
0b00	X	Not Busy	UNKNOWN	OK/ FAULT	The return value depends on the DP version: DPv1, DPv2 Returns the value of DPIDR. DPv0 Returns UNKNOWN value.	
0b01			CTRL/STAT		Returns CTRL/STAT value.	
0b10			SELECT		Returns SELECT value.	
0b11			0x0000000		Returns RDBUFF value, always zero.	





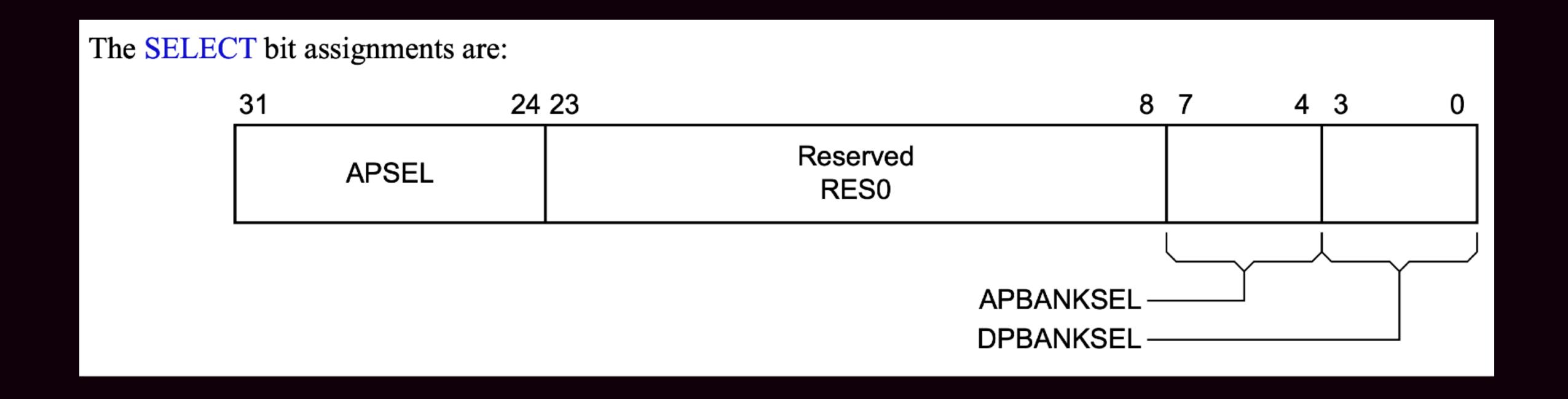
The DPACC Register

A b	Sticky?c	AP stated	Read result	ACK	
X	X	Busy	UNKNOWN	WAIT	If the Overrun Detect flag is 0b1, this access and response sequence causes the Sticky Overrun flag to be set to 0b1. See <i>CTRL/STAT</i> , <i>Control/Status register</i> on page B2-55.
0b00	X	Not Busy	UNKNOWN	OK/ FAULT	The return value depends on the DP version: DPv1, DPv2 Returns the value of DPIDR. DPv0 Returns UNKNOWN value.
0b01			CTRL/STAT		Returns CTRL/STAT value.
0b10			SELECT		Returns SELECT value.
0b11			0x00000000		Returns RDBUFF value, always zero.

From: Arm Debug Interface Architecture Specification ADIv5.0 to ADIv5.2

SELECT

Selecting banks in the Debug Port and Access Port



From: Arm Debug Interface Architecture Specification ADIv5.0 to ADIv5.2