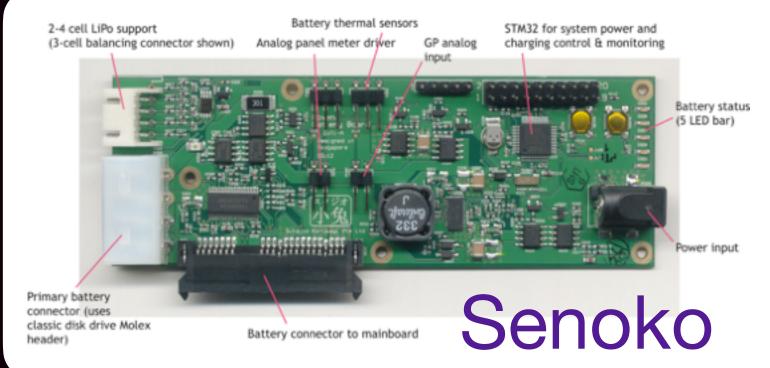
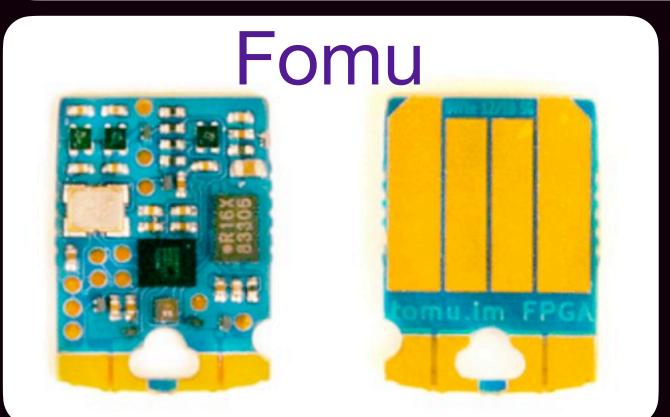
About Me Open Hardware & Software

- Novena Open Laptop
 - Senoko Power Supply
- Fomu FPGA Dev Board
- Betrusted Communicator









Talk Outline

Common ways of talking to chips

- 1. Minimum Viable Debug
- 2. JTAG
- 3. ARM ADIV5 + MEM-AP
- 4. RISC-V

