

Registers

✓ Read

✓ Write

Control

✓ Halt

✓ Step

✓ Reset

? Break

Control

✓ Halt

✓ Step

✓ Reset

✓ Break



Memory

★ Peek

★ Poke

What Do We Need for a Debugger?

ARM Cortex-M via ADIv5 / MEM-AP



Memory

★ Peek

★ Poke

Registers

✓ Read

✓ Write

Control

✓ Halt

✓ Step

✓ Reset

✓ Break

Putting It All Together

Accessing RAM from JTAG



DATA



ADDRESS

