
Registers
? Read
? Write

Control

? Halt
? Step
? Reset
? Break

Control

- ✓ Halt
- ✓ Step
- ✓ Reset
- ✓ Break


Memory
? Peek
? Poke

What Do We Need for a Debugger?

VexRiscv

Memory	Registers	Control
? Peek	? Read	✓ Halt
? Poke	? Write	✓ Step
		✓ Reset
		✓ Break

RISC-V Encoding