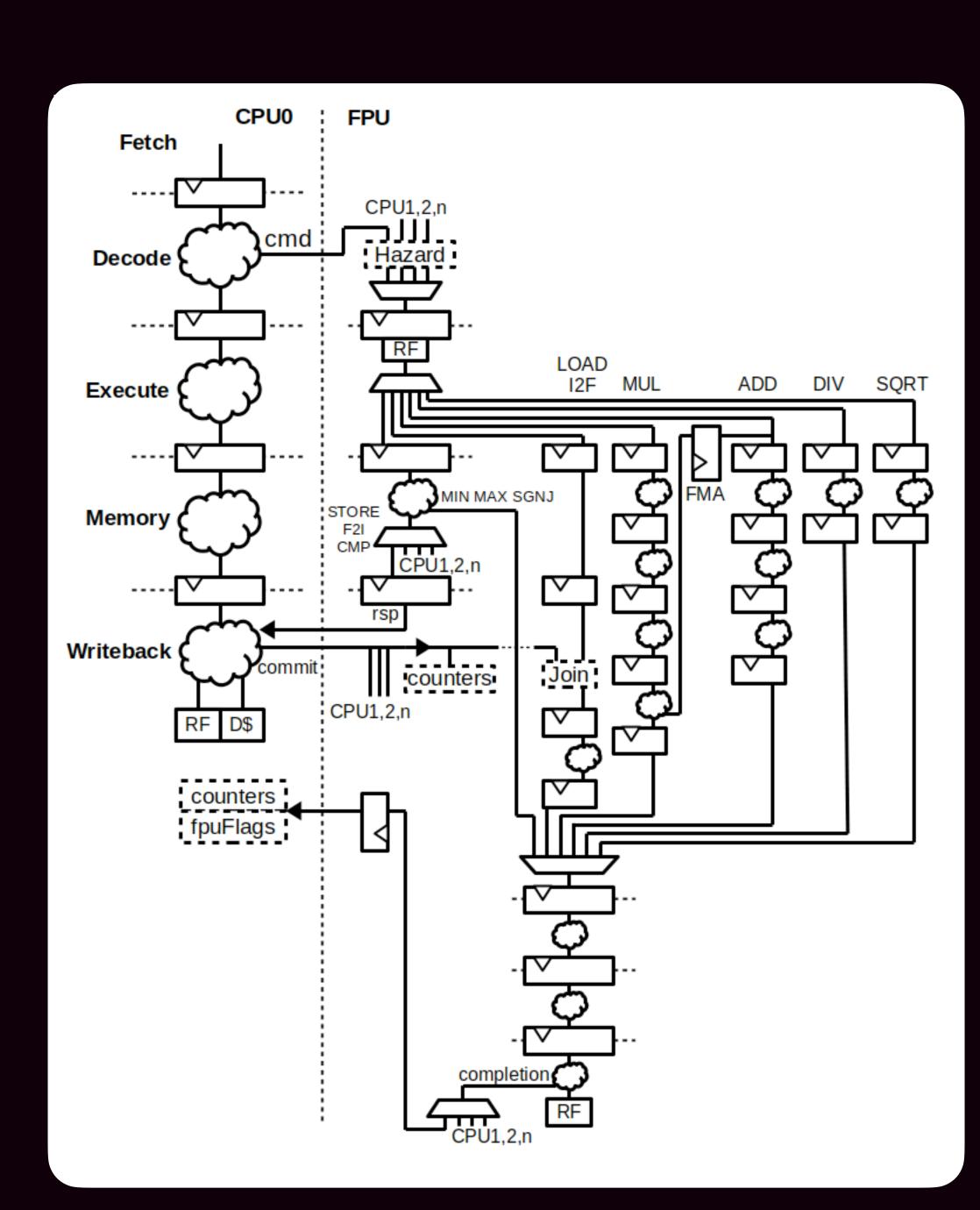
Example: VexRiscv Soft CPU

VexRiscv: A CPU in Scala FPGA-targeted soft core

- 32-bit RISC-V core
- Plugin support
- FPUPlugin.scala
- DebugPlugin.scala
 - Only two registers!



From: VexRiscv README.md