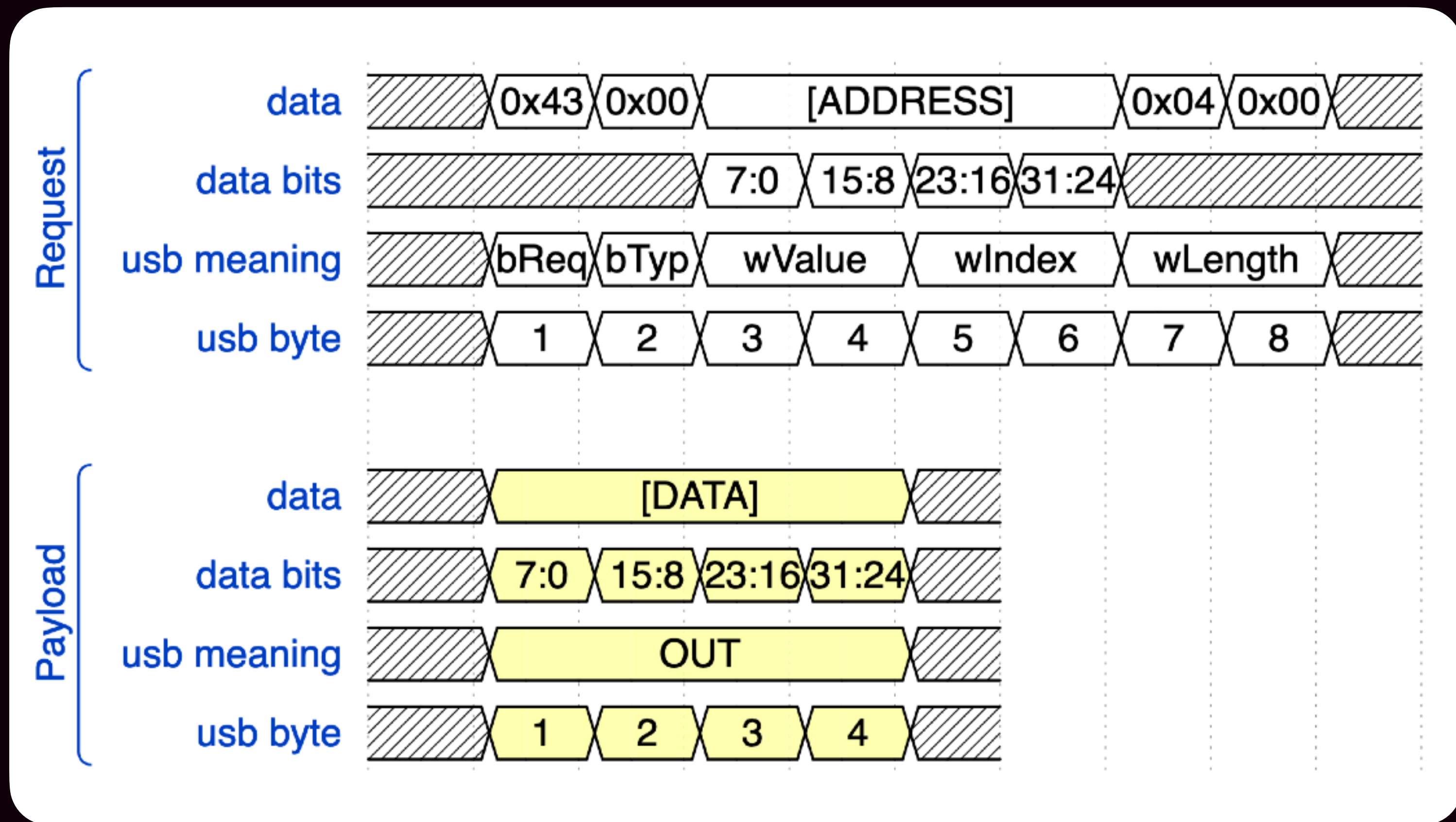


Talk Outline

Common ways of talking to chips

1. Minimum Viable Debug
2. JTAG
3. ARM ADIv5 + MEM-AP
4. RISC-V



Not Covering Debug Hardware

Lots of [in]expensive options

- Lauterbach
- Segger J-Link
- Black Magic Probe
- ST-Link
- ESP32 USJ
- **CMSIS-DAP**