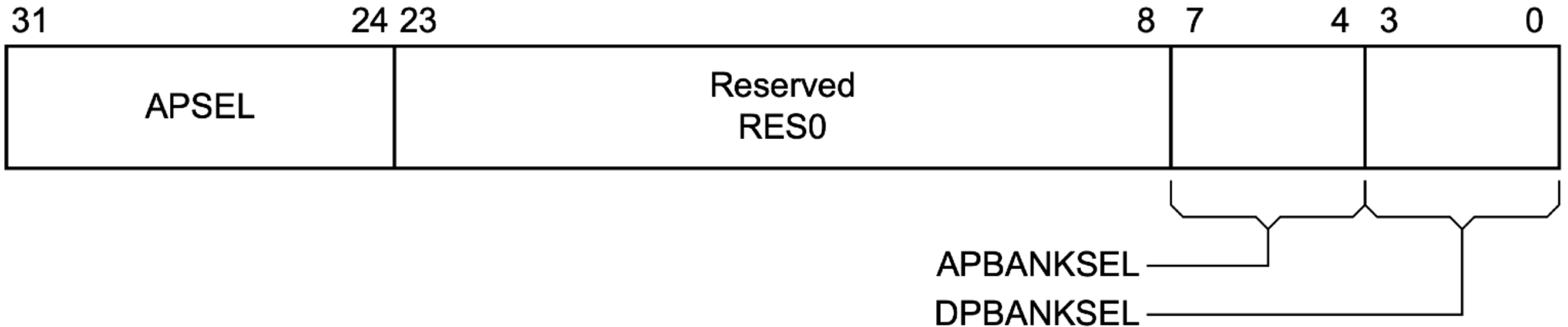
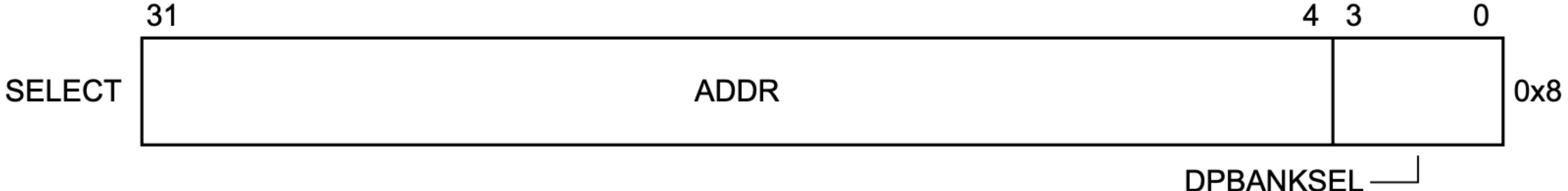
The SELECT bit assignments are:




31
4
3
0
SELECT
ADDR
0x8
DPBANKSEL

From: ADIv6 Reference Manual

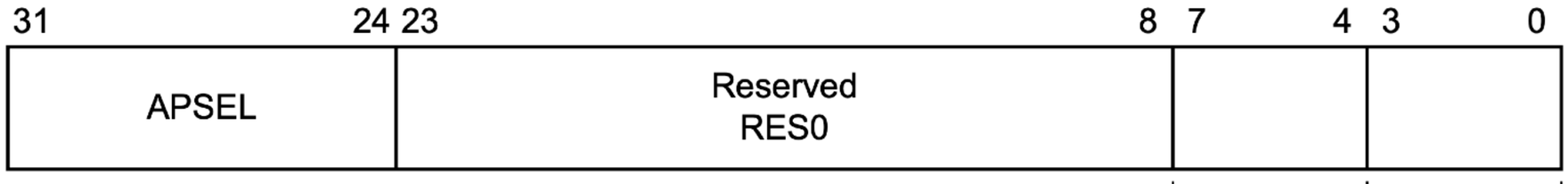
APSEL + APBANKSEL now just “ADDR”

From: ADIv5 Reference Manual

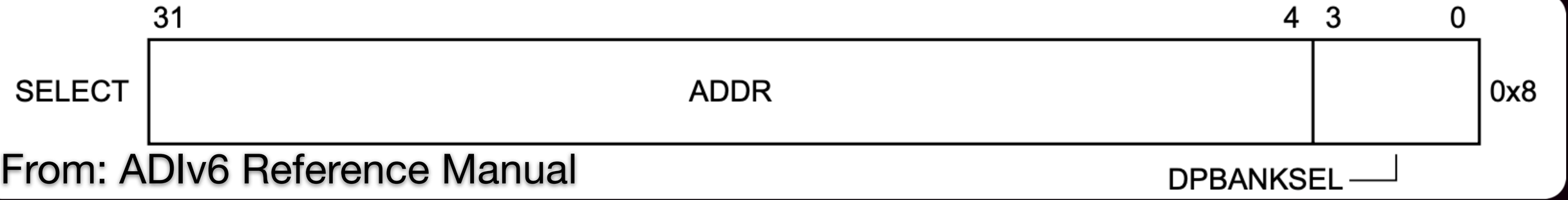
RP2350: ADIv6

Different addressing

The SELECT bit assignments are:



From: ADIv5 Reference Manual



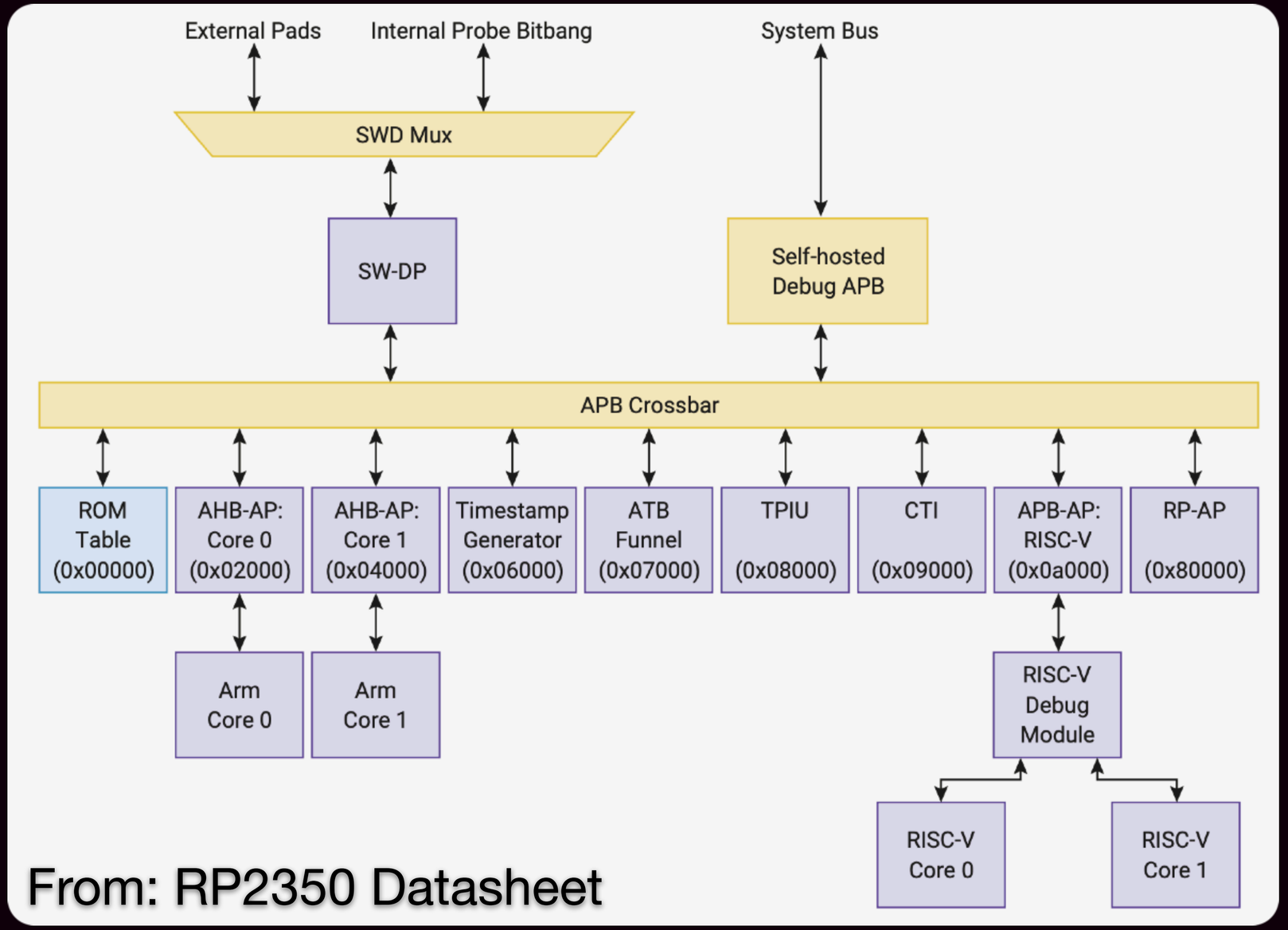
From: ADIv6 Reference Manual

APSEL + APBANKSEL now just “ADDR”

RP2350: ADIv6

Different addressing

- RISC-V Debug Module over ADIv6
- Connected to MEM-AP
- “Memory Space” is DMI
- TAR/DRW to access data



From: RP2350 Datasheet