Control

? Halt ? Step ? Reset ? Break



Registers



Control

✓ Halt √ Step ? Reset ? Break

Memory



What Do We Need for a Debugger?

ARM Cortex-M via ADIv5 / MEM-AP

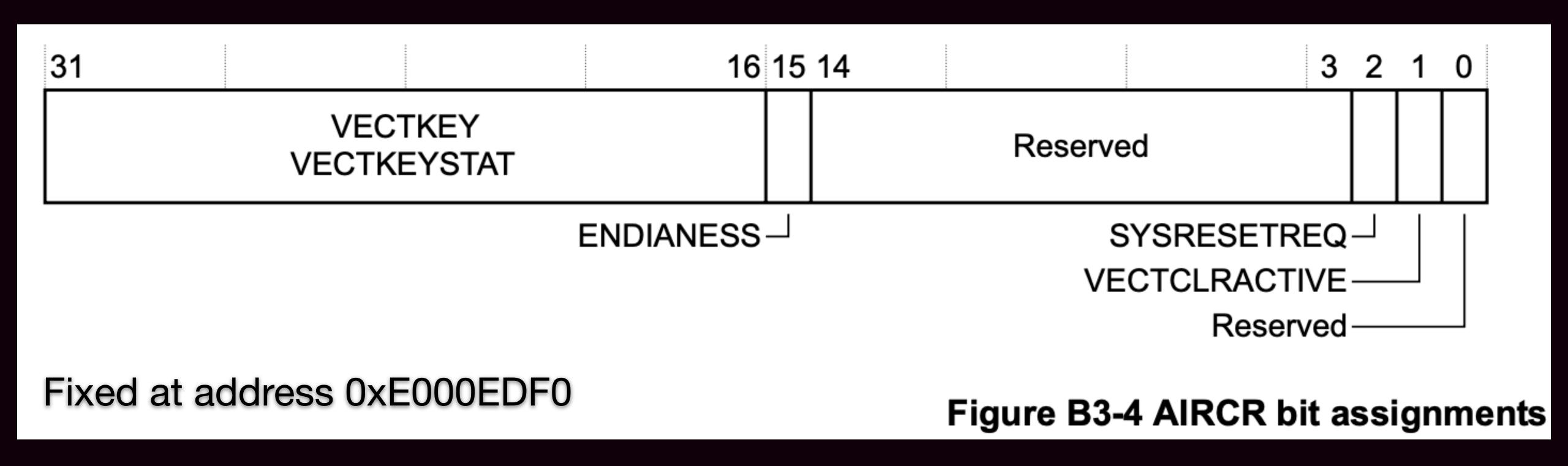




```
Control
✓ Halt
√ Step
? Reset
  Break
```

CPU Control: Memory Mapping

AIRCR: Application Interrupt and Control Register



From: ARM.v6-M Architecture Reference Manual