Registers

? Read? Write

Memory

? Peek? Poke



Registers



Memory



Control



What Do We Need for a Debugger? VexRiscv

Memory

- Peek
- Poke

Registers

- Read
- ✓ Write

Control

- ✓ Halt
- √ Step
- Reset
- ✓ Break

Connecting This to the World

Fomu: USB or nothin'

