

0

0

0

1

1

0

1

1

Instruction Register

31

30

...

1

0

0

19

18

...

1

0

41

40

...

1

0

Data Register

INDOCODE

INFO

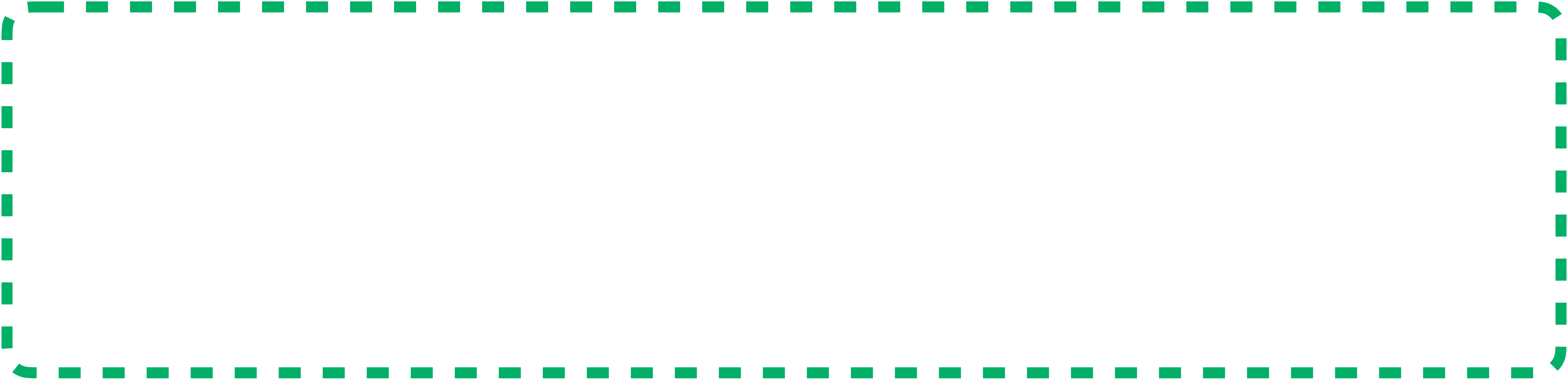
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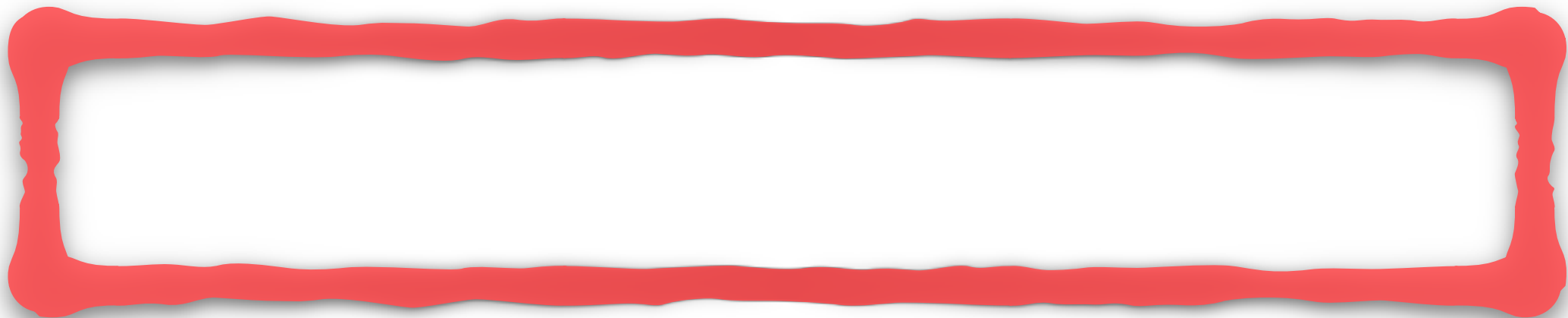
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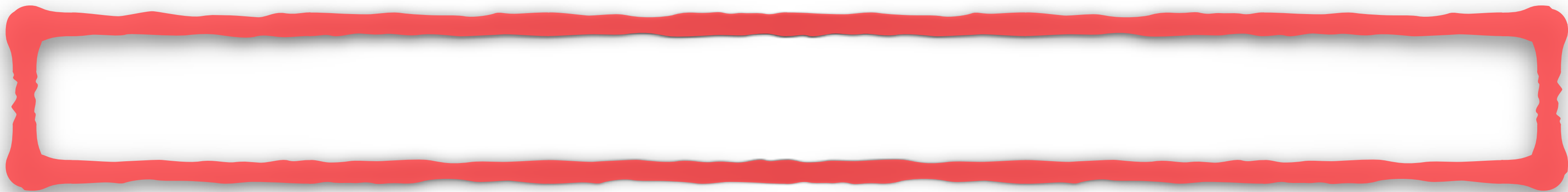
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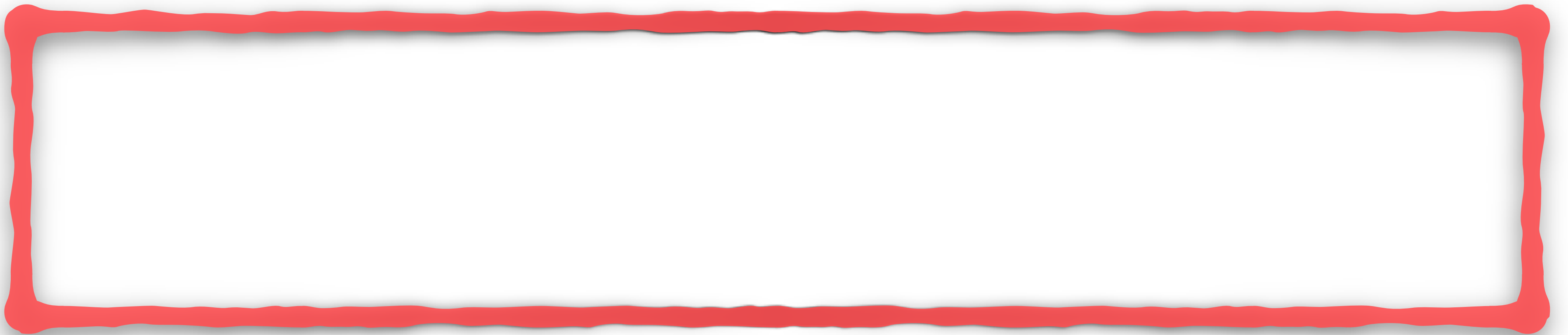
Hypothetical











Hypothetical JTAG Device

Two bits ought to be enough for anyone



**And that's
JTAG!**