

NC STATE

DrCCTProf: Supporting Fine-Grained Call Path Profiling on ARM and X86

Xu Liu
NC State University

ARM is Popular

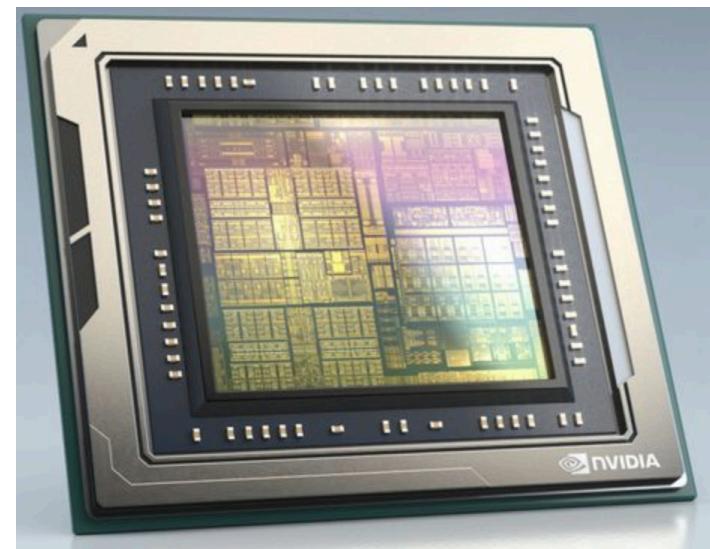
ARM for Smartphones



ARM for SoC



ARM for PC (laptop, tablet)



ARM for High Performance Computing

ARM Cloud Platform



Graviton
processors

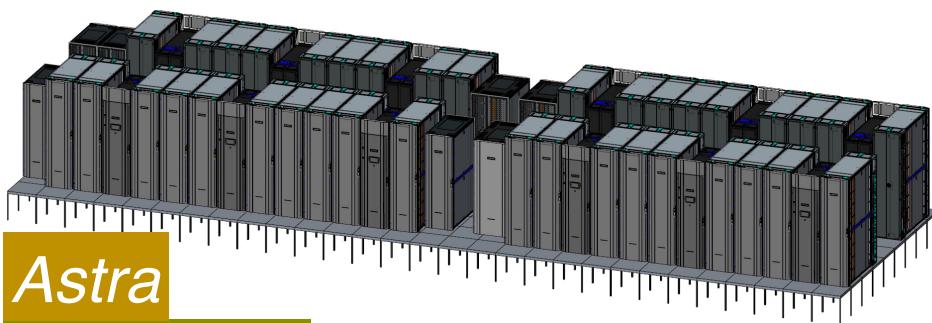


Kunpeng
processors



Marvell ThunderX2
processors

ARM Supercomputer



Astra
Aug 2018

Consists of 145152 ARM processors



Fugaku
June 2020

No.1 Supercomputer



Tianhe-3E
Before 2021

The exascale supercomputer

ARM Ecosystem

Example End Users	 Sandia National Laboratories	 THE UNIVERSITY OF UTAH	 Hartree Centre Science & Technology Facilities Council	 			
Key Applications Middleware 	 MySQL	 APACHE HTTP SERVER	 hadoop	 MariaDB	 Couchbase	 ceph	
	 JAVA	 openstack	 memcached	 NGINX	 Redis	 mongoDB	 GlusterFS
Operating System, Virtualization & Firmware	 efi	ACPI	 openSUSE	 KVM	 Xen Project	 Ubuntu Supported by Canonical	 debian
OEMs and ODMs	 CRAY THE SUPERCOMPUTER COMPANY  MITAC INTERNATIONAL CORP.	 FOXCONN  Inventec	 GIGABYTE™  STACK VELOCITY  wiwynn	 lenovo	 hyve solutions	 PEGATRON	
ARM SoC	 AMD	 applied micro	 BROADCOM	 CAVIUM	 MARVELL	 Qualcomm	 Texas INSTRUMENTS

ARM Ecosystem

Example
End Users



Sandia
National
Laboratories



Hartree Centre
Science & Technology Facilities Council



PayPal

ARM®

Key Applications

MISI

MySQL®



Demand performance tools on various systems

fedora®



redhat.

UBUNTU®

Supported by Canonical

debian

OEMs and ODMs



MITAC
MITAC INTERNATIONAL CORP.



FOXCONN®
Inventec

GIGABYTE™
STACK™
wiwynn



lenovo

PEGATRON
ASUS®

ARM SoC



apm applied
micro

BROADCOM.

CAVIUM

MARVELL™

QUALCOMM®

Texas
INSTRUMENTS

Mainstream Profilers

Popular profilers

MAP / Perf / TAU / Scalasca / HPCToolkit / VTune / ...

- Hotspot analysis: high resource utilization
- Various hardware metrics: cache misses, instructions per cycle, ...

However,

They cannot tell if resources were “**well spent**”

Hotspots may be symptoms rather than root causes

A Motivating Example

```
1 for ( i = T; i < N - T; i++) {  
2   for ( j = T; j < M - T; j++) {  
3     temp = 0;  
4     for ( k = 1; k < T; k++)  
5       temp += matrix[i-k][j] + matrix[i][j-k] +  
6                     matrix[i+k][j] + matrix[i][j+k];  
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Many redundant computations

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metrics	original	optimized	&reduction
#instructions	$73*10^9$	$40*10^9$	0.46
#cycles	$33*10^9$	$21*10^9$	0.36
IPC	2.2	1.9	

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Need techniques to shift from hotspot analysis to
wastage analysis

Wastage Analysis

Wasted memory accesses

Redundant memory accesses

- Redundant memory accesses: the same values involved

Useless memory accesses

- Dead stores: stored value got overwritten without use

Wasted arithmetic computation

Symbolic equivalent computation

Result equivalent computation

HMMER: An Example for Resource Wastage

Unoptimized

```
for (i = 1; i <= L; i++) {  
    for (k = 1; k <= M; k++) {  
        mc[k] = mpp[k-1] + tpmm[k-1];  
        if ((sc = ip[k-1] + tpim[k-1]) > mc[k])  
            mc[k] = sc;
```

-O3 optimized

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for (i = 1; i <= L; i++) {  
    for (k = 1; k <= M; k++) {  
        R1= mpp[k-1] + tpmm[k-1];  
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Declare as “restrict” pointers.
Can vectorize.

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> 16% running time improvement
> 40% with vectorization

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2 add 0x0(%r13,%rax,4),%ecx #mpp[k-1]+tpmm[k-1]  
3 mov %ecx, 0x4(%rdx)      #assign mc[k]  
4 mov 0x18(%rsp),%rbx  
5 mov (%r9,%rax,4),%r15d  
6 add (%rbx,%rax,4),%r15d #dpp[k-1]+tpdm[k-1]  
7 mov 0x20(%rsp),%rbx  
8 cmp %ecx,%r15d          #%ecx is mc[k]  
9 cmovge %r15d, %ecx  
10 mov %ecx, 0x4(%rdx)     #assign mc[k]
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Dynamic fine-grained binary analysis
is necessary

Dynamic Fine-Grained Binary Tools

Microscopic insights

Track each instruction

- Operators and operands

Track each register

- General and SIMD registers

Track each memory location

- Effective addresses

Track values in storage locations

- Values in registers and memory

Existing Dynamic Fine-Grained Binary Tools

Tools

Valgrind / **DynamoRIO** / Dyninst / Pin

- High overhead but **microscopic insights**
- Suitable for analyzing software performance/correctness bugs

However,

Difficult to master

- Many complex APIs

Significant engineering efforts

- Obtaining deep insights
- Reducing measurement overhead

Existing Dynamic Fine-Grained Binary Tools

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DrCCTProf facilitates the dynamic fine-grained binary analysis
with easy interfaces

DrCCTProf Highlights

Rich insights

Fine-grained **call path** profiling

Overhead optimization

Handling efficient instrumentation

Handling parallelism

Easy interfaces

Easy instrumentation

Easy analysis

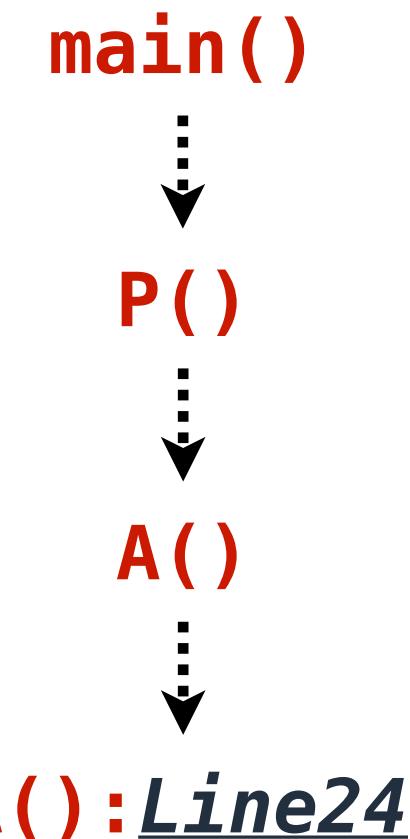
Easy visualization

What is a Calling Context ?

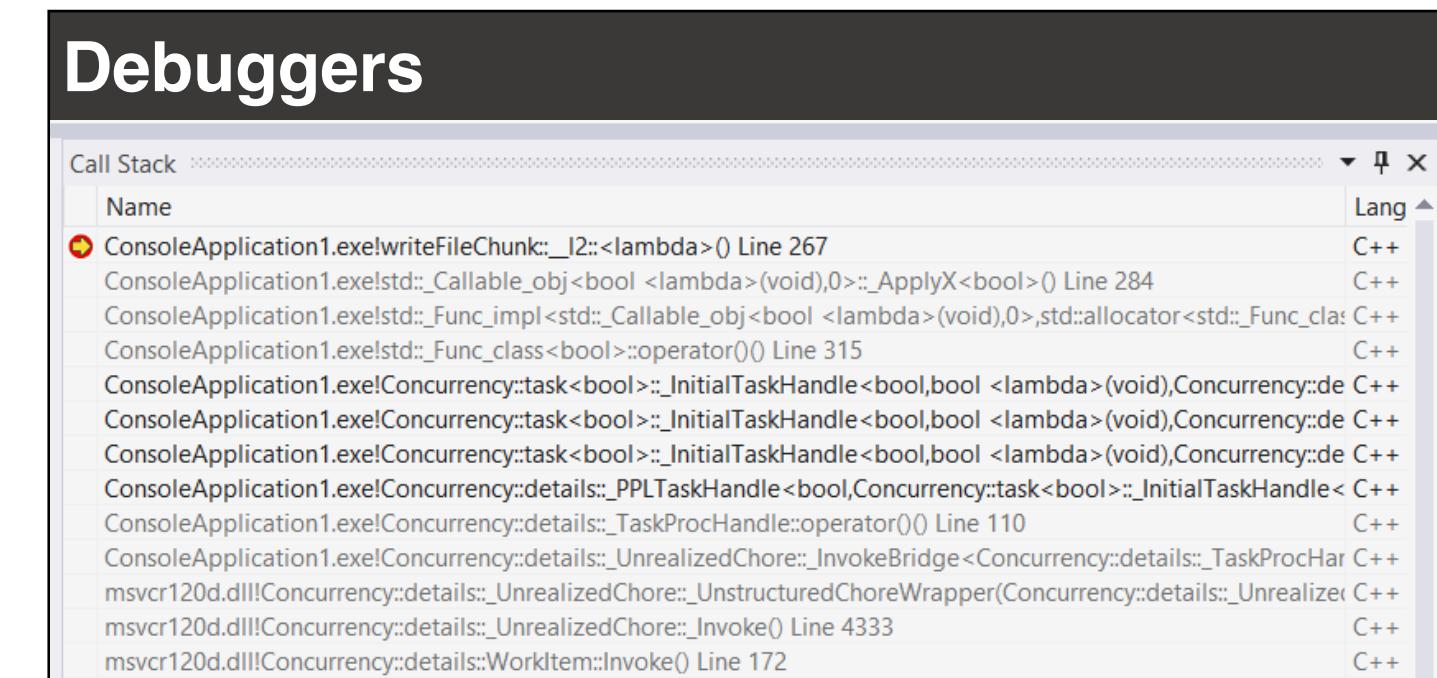
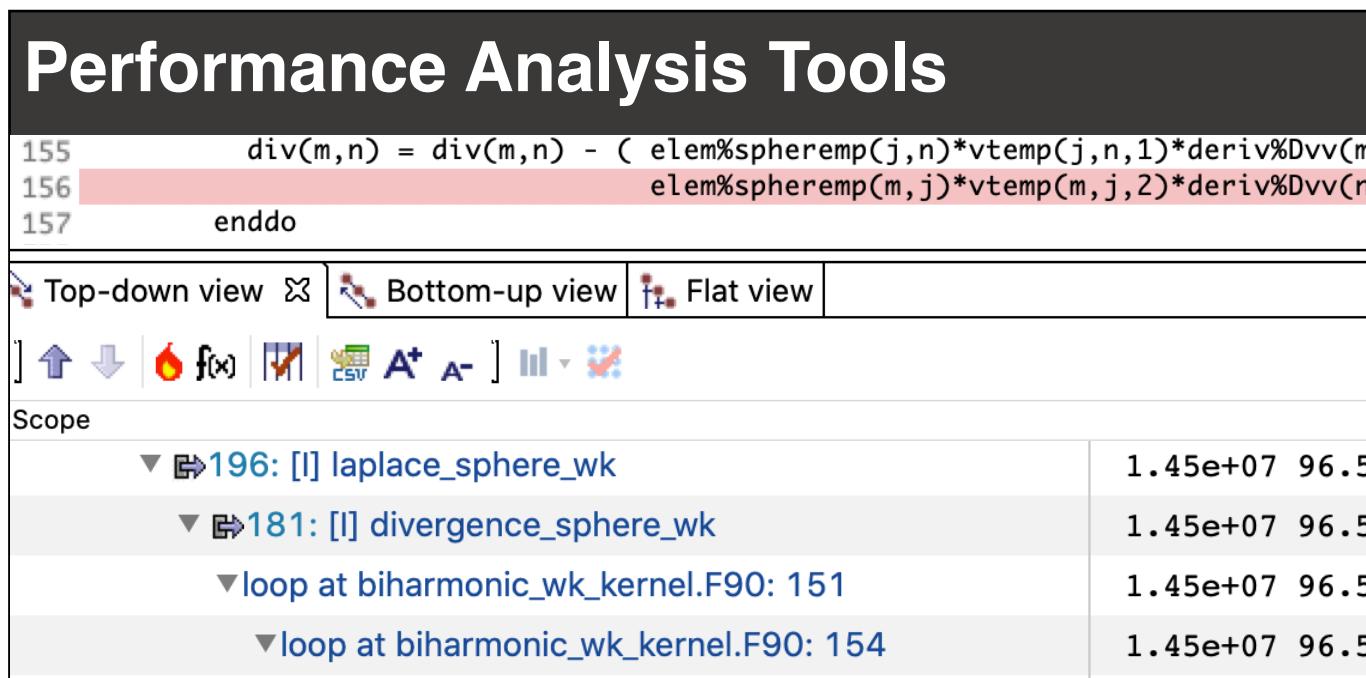
```
1: void main(){
2:     P();
3: }
4: void P(){
5:     A();
6:     B();
7:     C();
8:     D();
...
21:     A();
22: }
23: void A(){
24:     *ptr = 100;
25:     x++;
26:     return;
27: }
```

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What is a Calling Context?



Chain of function calls that led to the current point in the program.
(a.k.a **Call Path / Call Stack / Backtrace / Activation Record**)

Why Calling Contexts are Necessary?

```
movsdq 0x8(%rdi,%r10,8), %xmm0:_mul:<no src>
```

SPEC 2006: bwaves

A pair of redundant computation

*****REDUNDANT WITH*****

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SPEC 2006: bwaves

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...
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((gm-1.0d0)*(q(5,ip1,jp1,kp1)/ros-
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No insights without call path profiling

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DrCCTPProf Overview

Monitor unmodified, fully-optimized binary executables on ARM.

Ubiquitous call path collection **[code-centric]**

- Associate **calling context** with their metrics potentially on **every executed machine instruction**

Ubiquitous object attribution **[data-centric]**

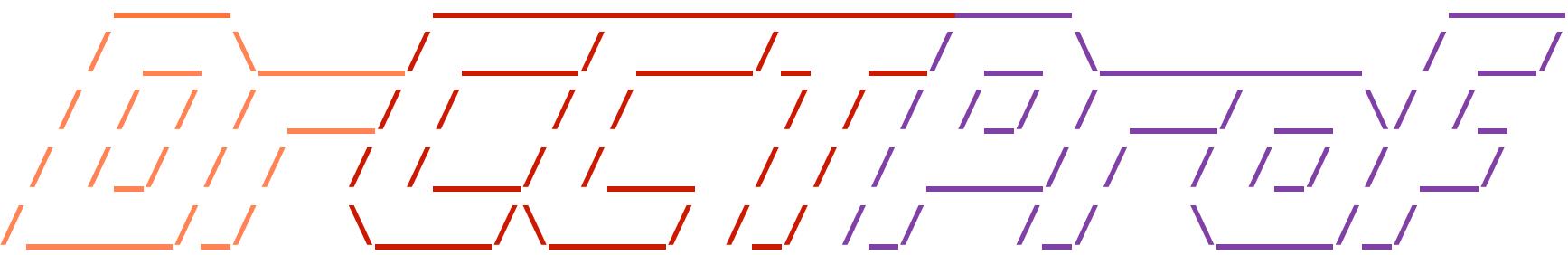
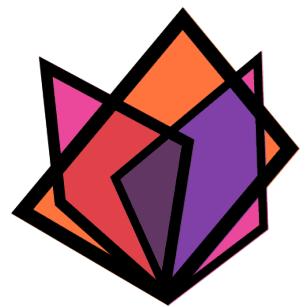
- Associate **every memory address** with the **corresponding data object**.

Programmability

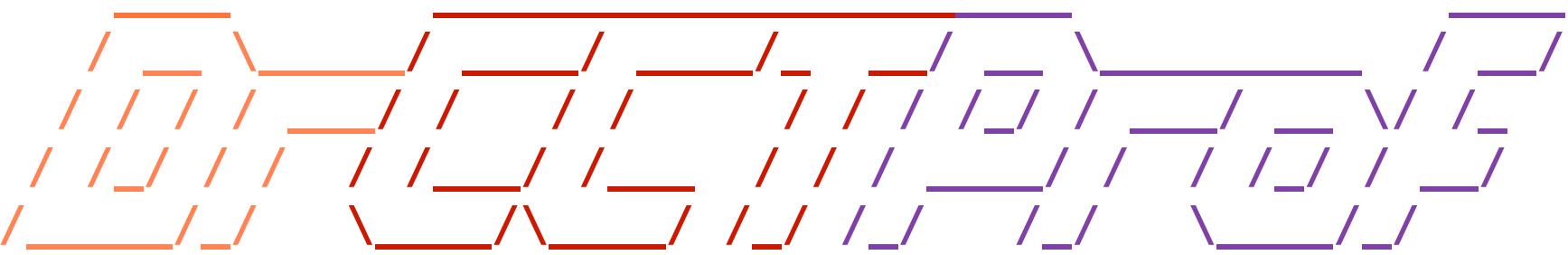
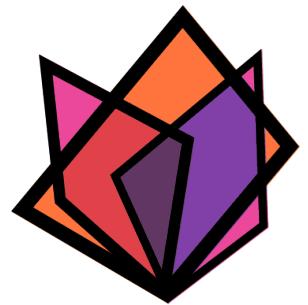
Framework to support various tools

Affordable overhead

Suitable for large scale execution



- Ubiquitous call path collection
- Attributing costs to data objects
- Handling parallelism
- Evaluation
- Case study
- Conclusions

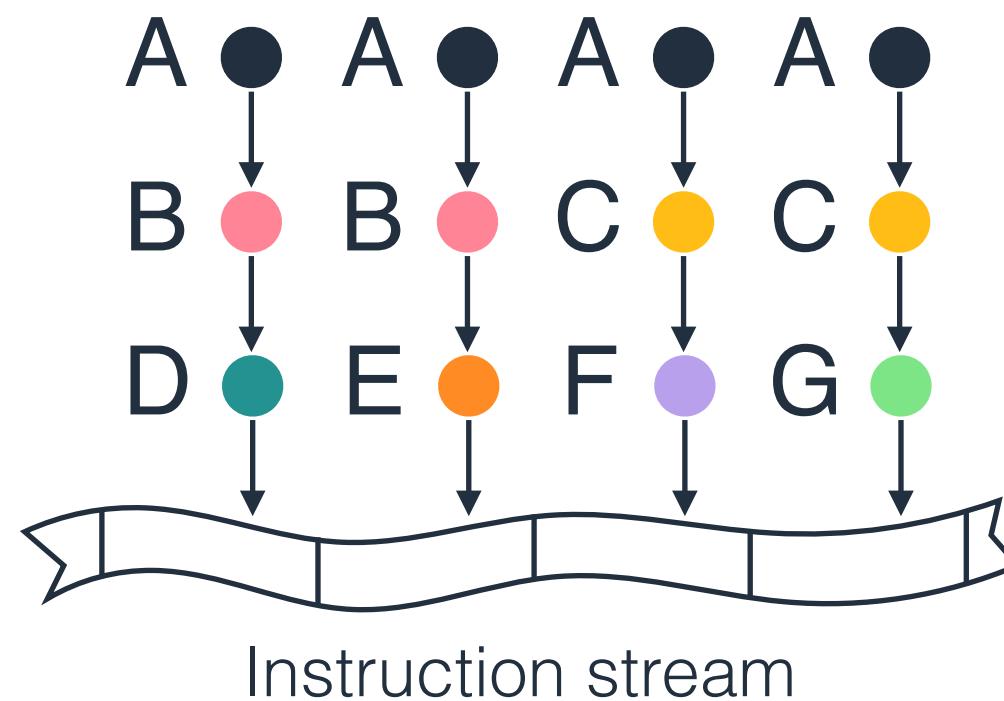


- **Ubiquitous call path collection**
- Attributing costs to data objects
- Handling parallelism
- Evaluation
- Case study
- Conclusions

Store History of Contexts Compactly

Problem

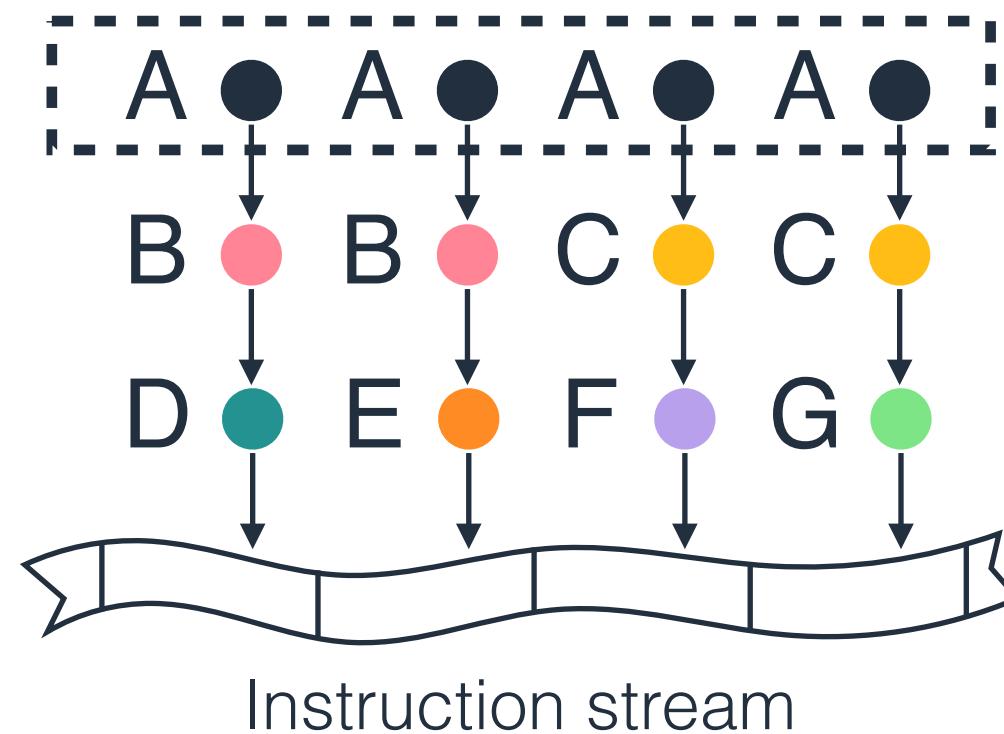
Deluge of call paths



Store History of Contexts Compactly

Problem

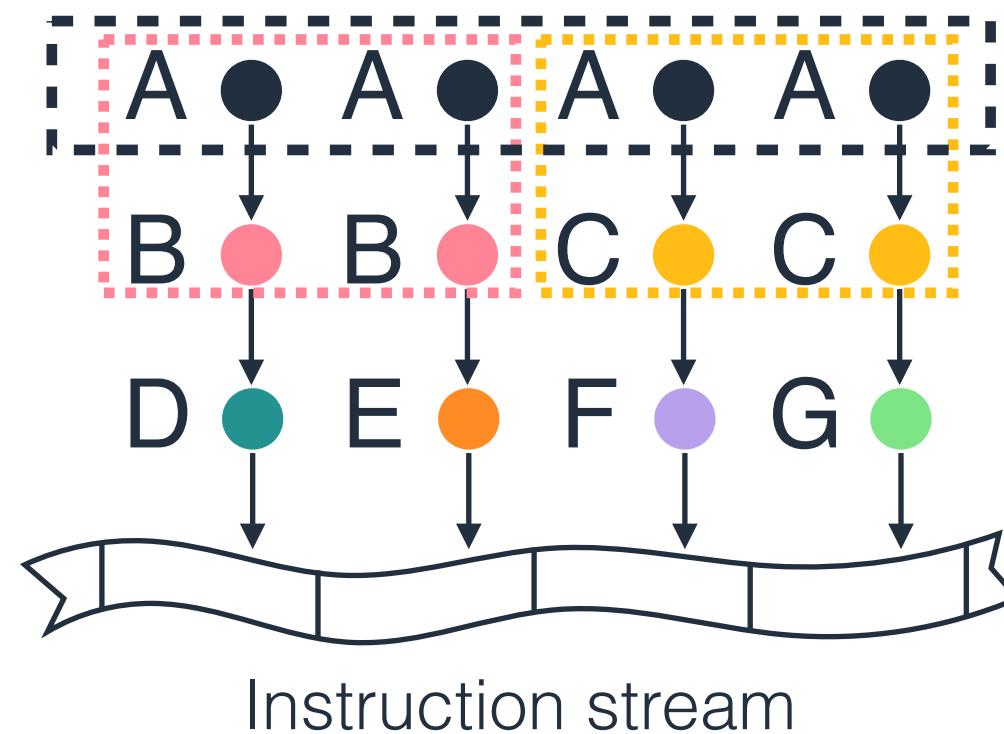
Deluge of call paths



Store History of Contexts Compactly

Problem

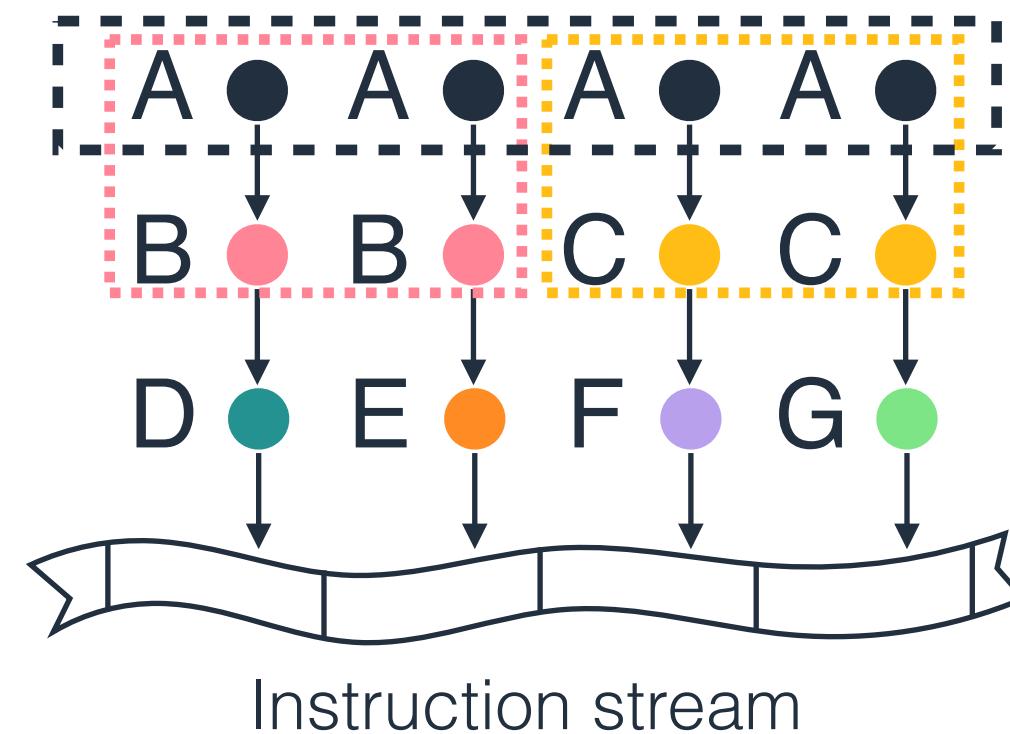
Deluge of call paths



Store History of Contexts Compactly

Solution

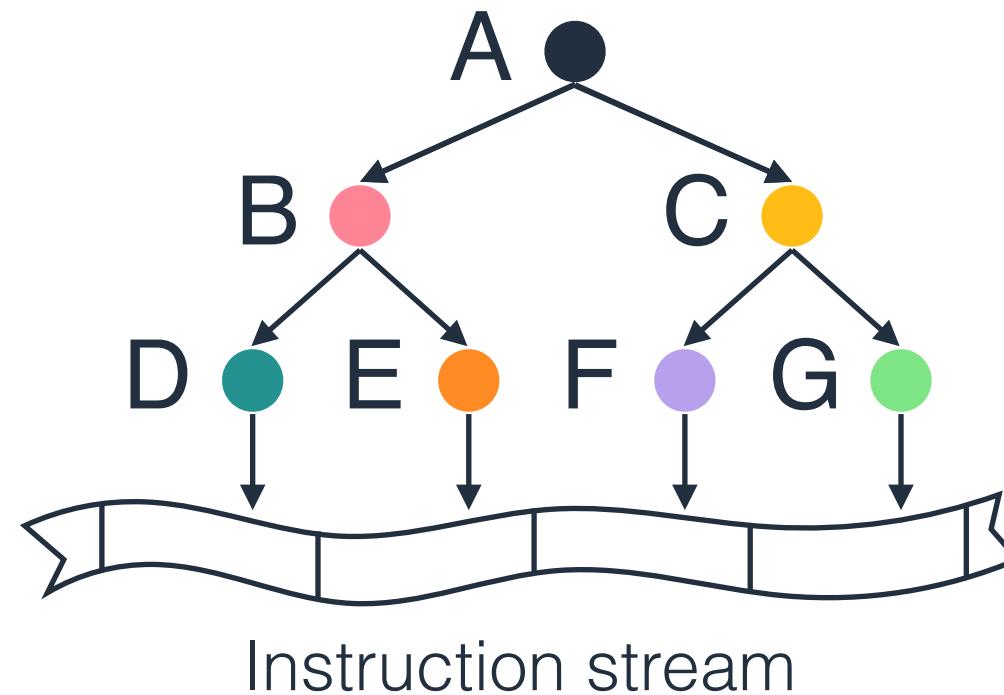
- ✓ Call paths share common prefix
- ✓ Store call paths as a calling context tree (CCT)
- ✓ One CCT per thread



Store History of Contexts Compactly

Solution

- ✓ Call paths share common prefix
- ✓ Store call paths as a calling context tree (CCT)
- ✓ One CCT per thread



Shadowing Call Stack

Solution Reverse the process. Eagerly build a replica/shadow stack on-the-fly.

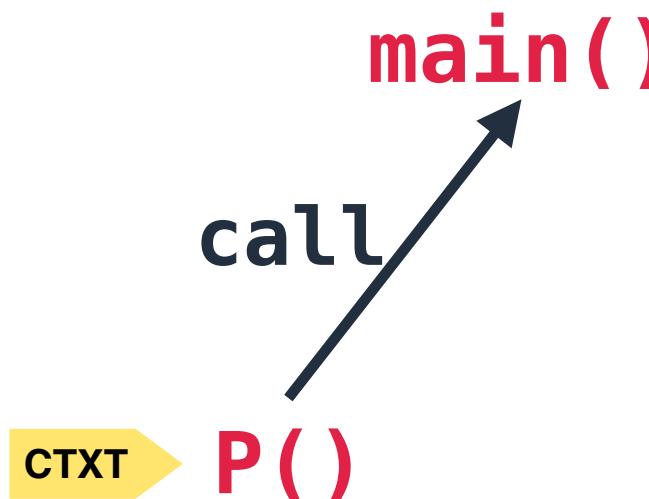
```
1: void main(){
2:     P();
3: }
4: void P(){
5:     A();
6:     B();
7:     C();
8:     D();
...
21:     A();
22: }
23: void A(){
24:     *ptr = 100;
25:     x++;
26:     return;
27: }
```

main()

Shadowing Call Stack

Solution Reverse the process. Eagerly build a replica/shadow stack on-the-fly.

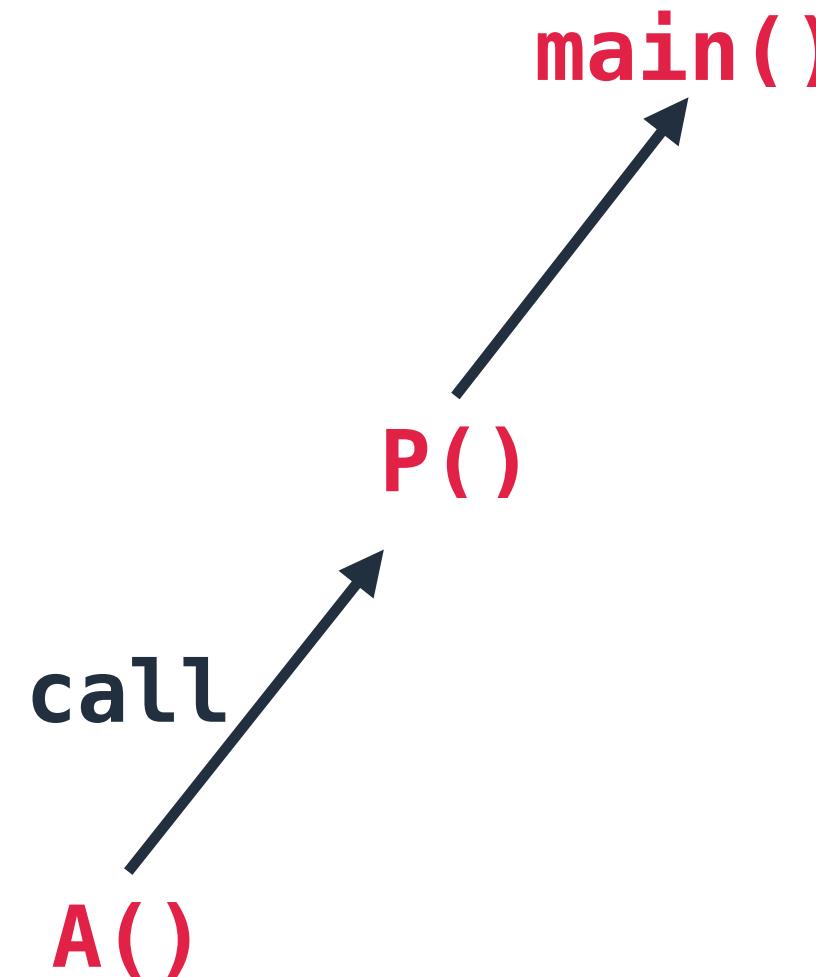
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Shadowing Call Stack

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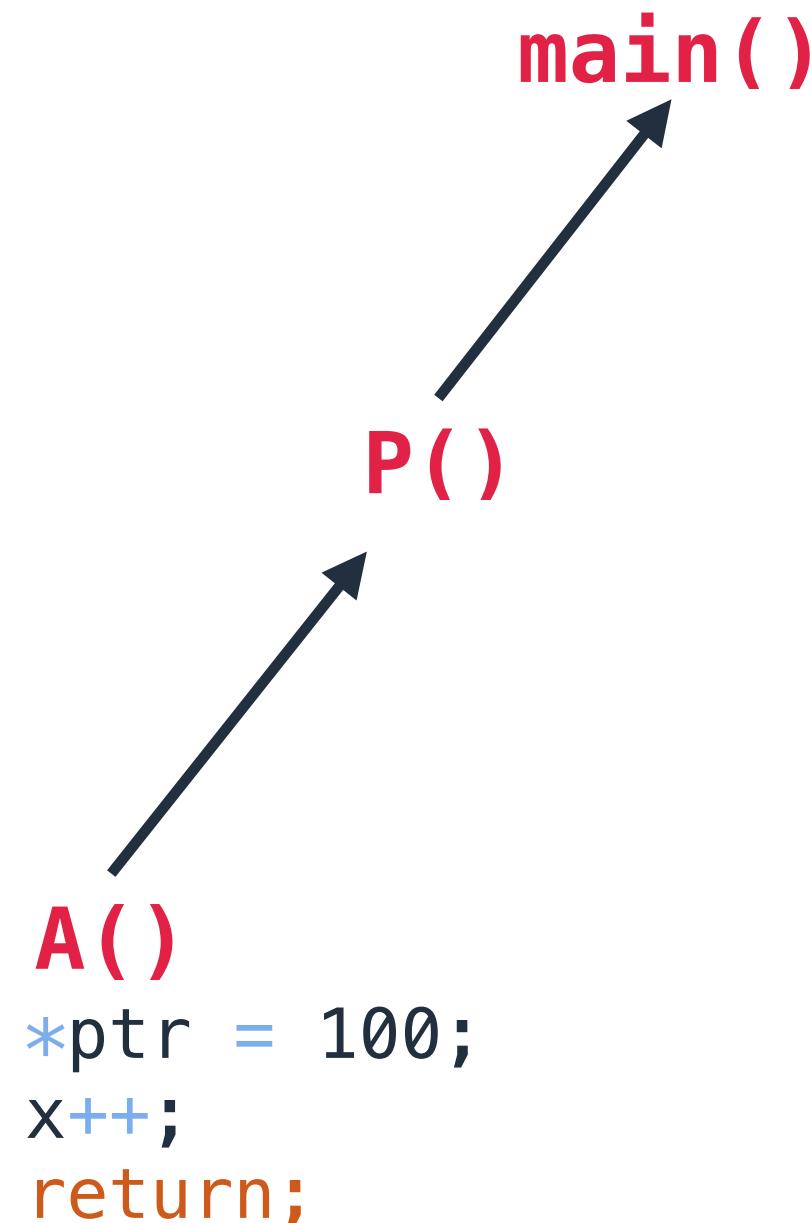
```
1: void main(){
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5:     A();           ←
6:     B();
7:     C();
8:     D();
...
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22: }
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24:     *ptr = 100;
25:     x++;
26:     return;
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```



Shadowing Call Stack

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4: void P(){
5:     A();
6:     B();
7:     C();
8:     D();
...
21:     A();
22: }
23: void A(){
24:     *ptr = 100; <-- CTXT
25:     x++;
26:     return;
27: }
```

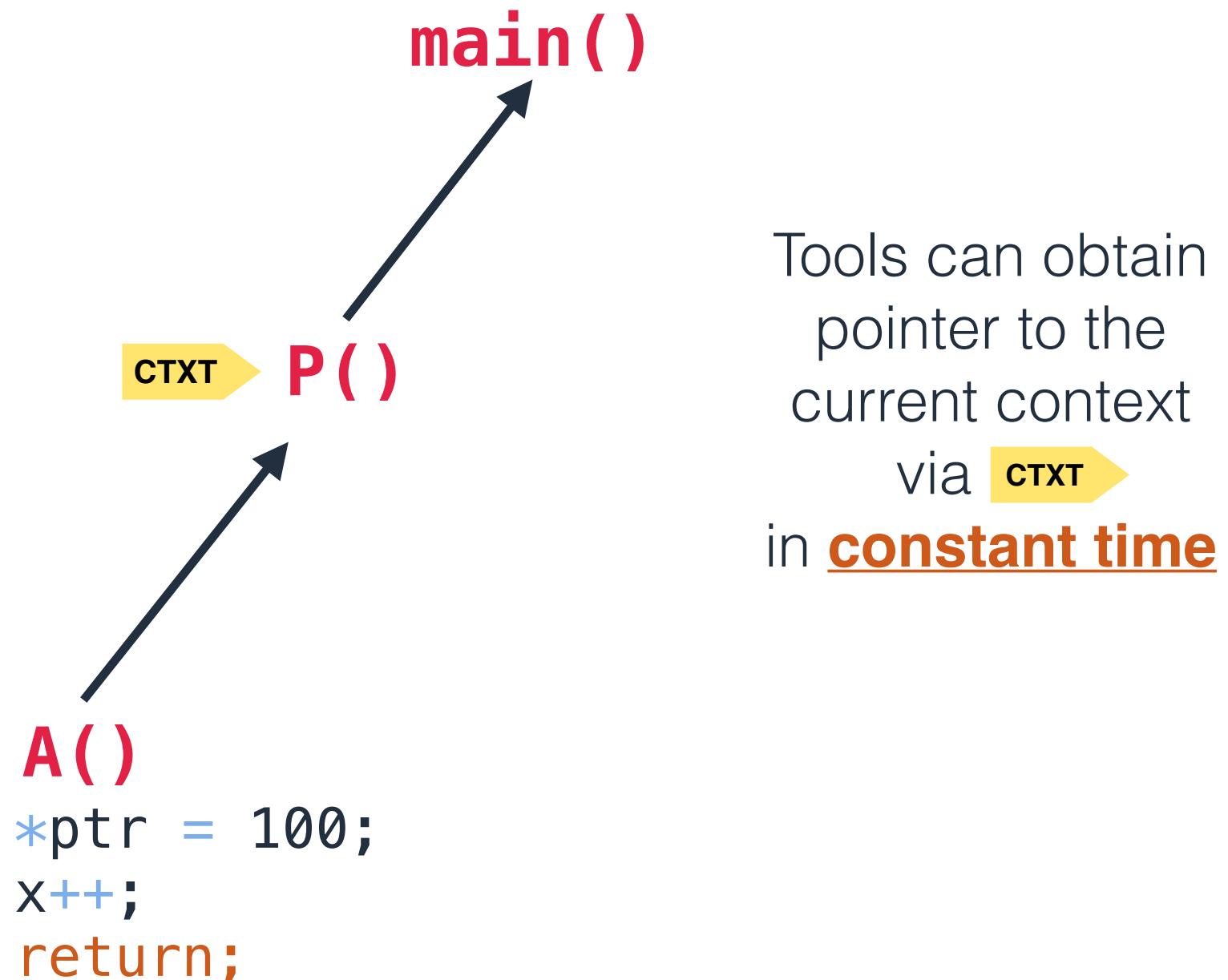


Tools can obtain
pointer to the
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via **CTX**
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Shadowing Call Stack

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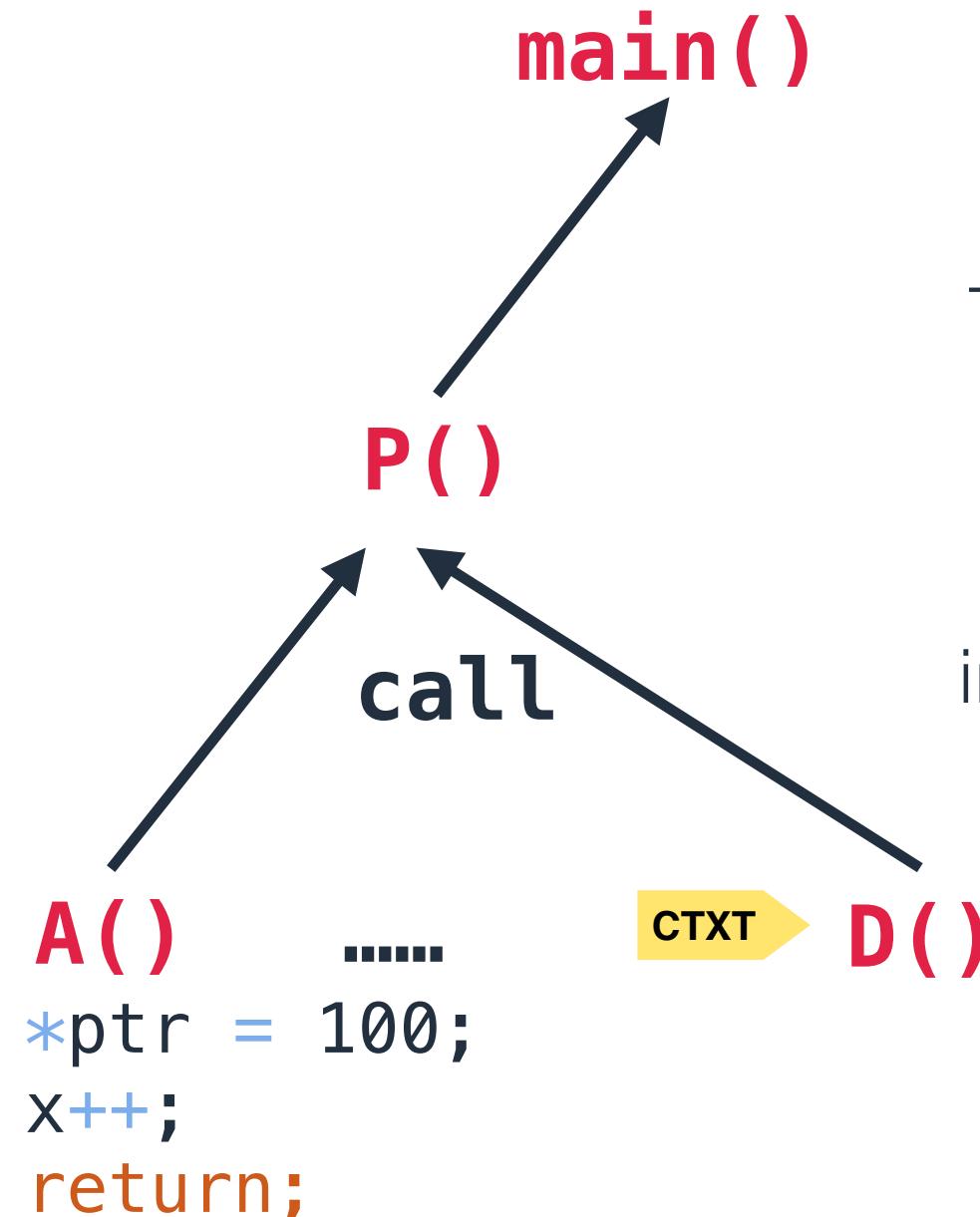


Shadowing Call Stack

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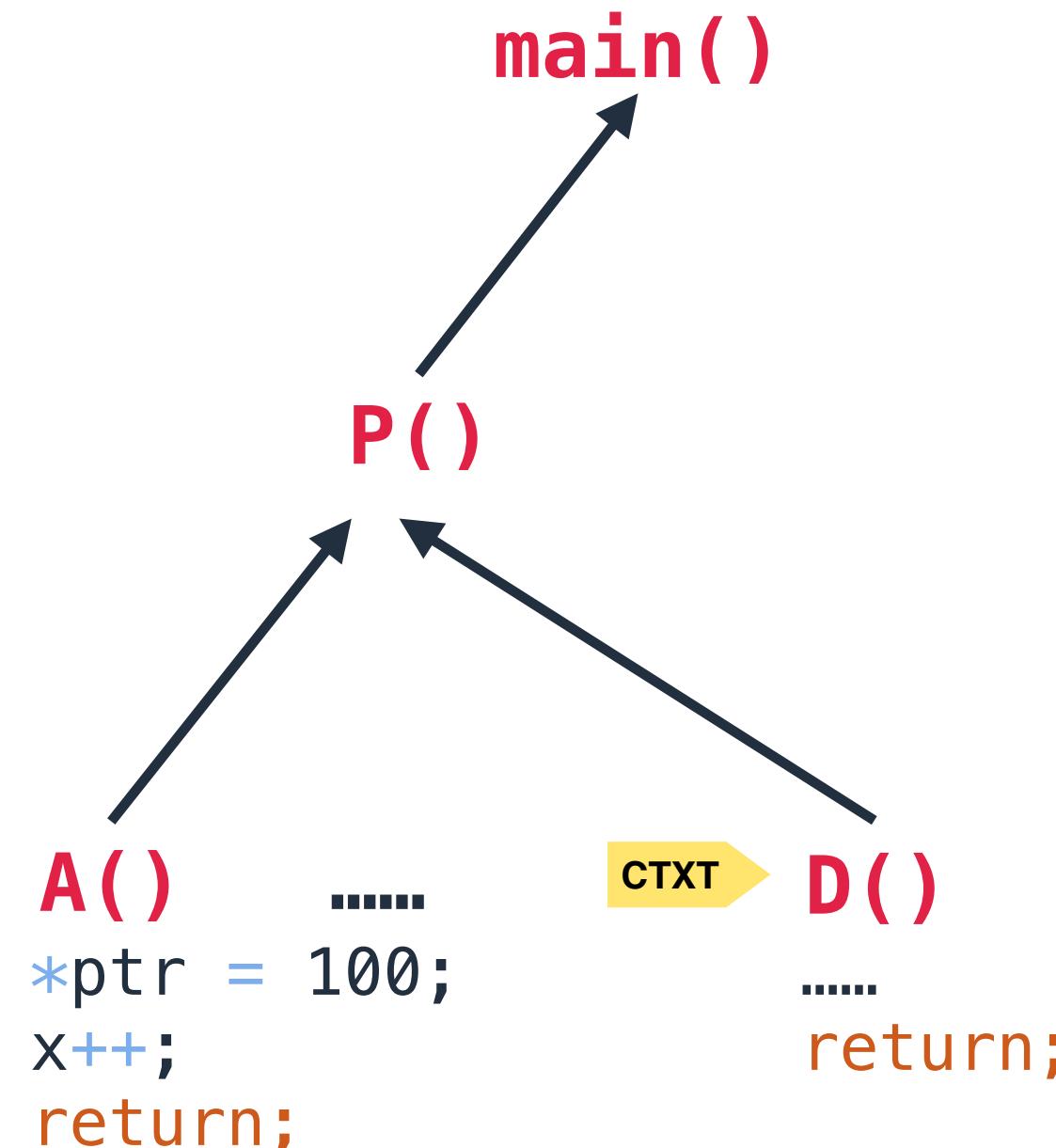


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Shadowing Call Stack

Maintaining CTXT

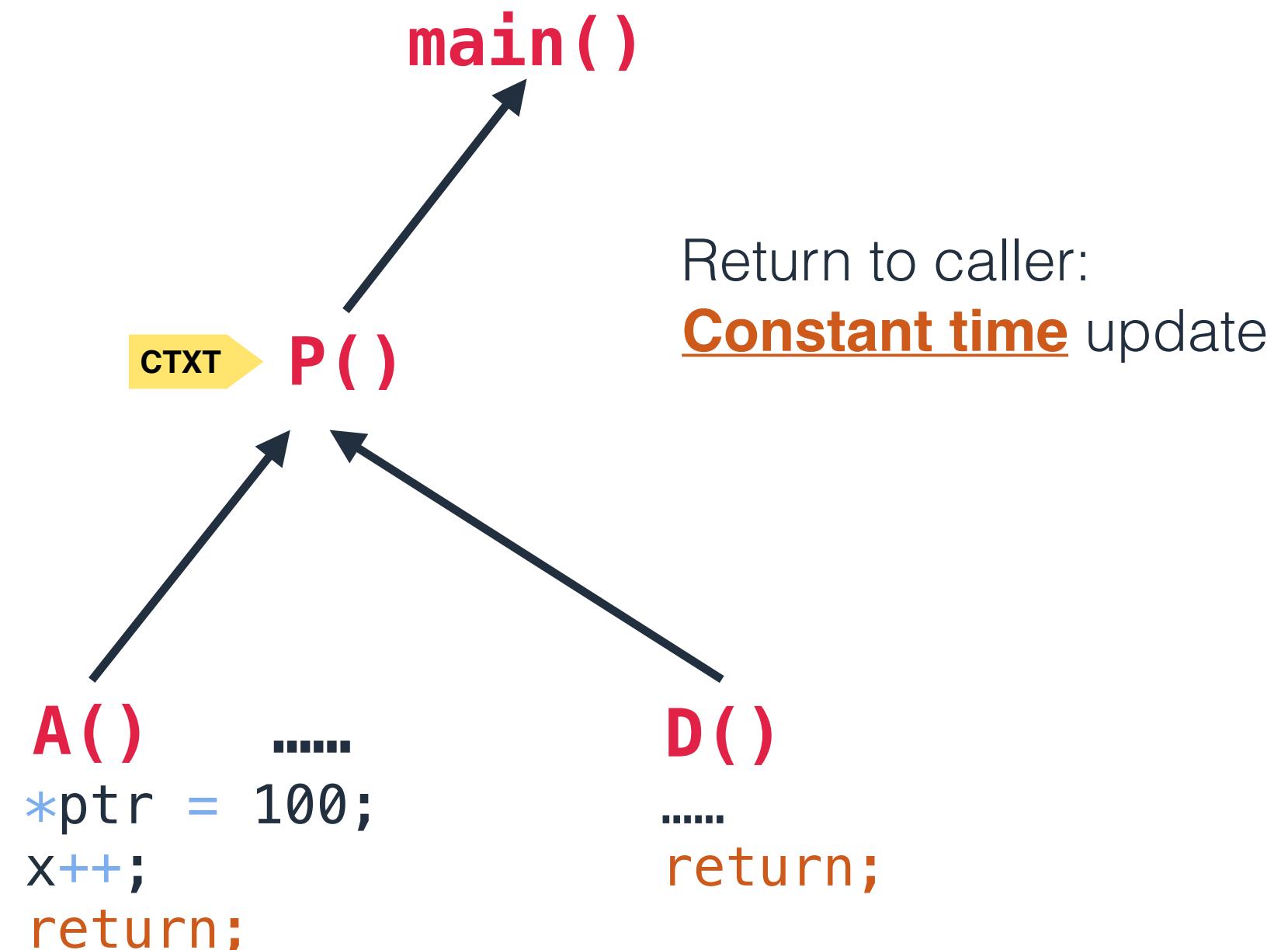
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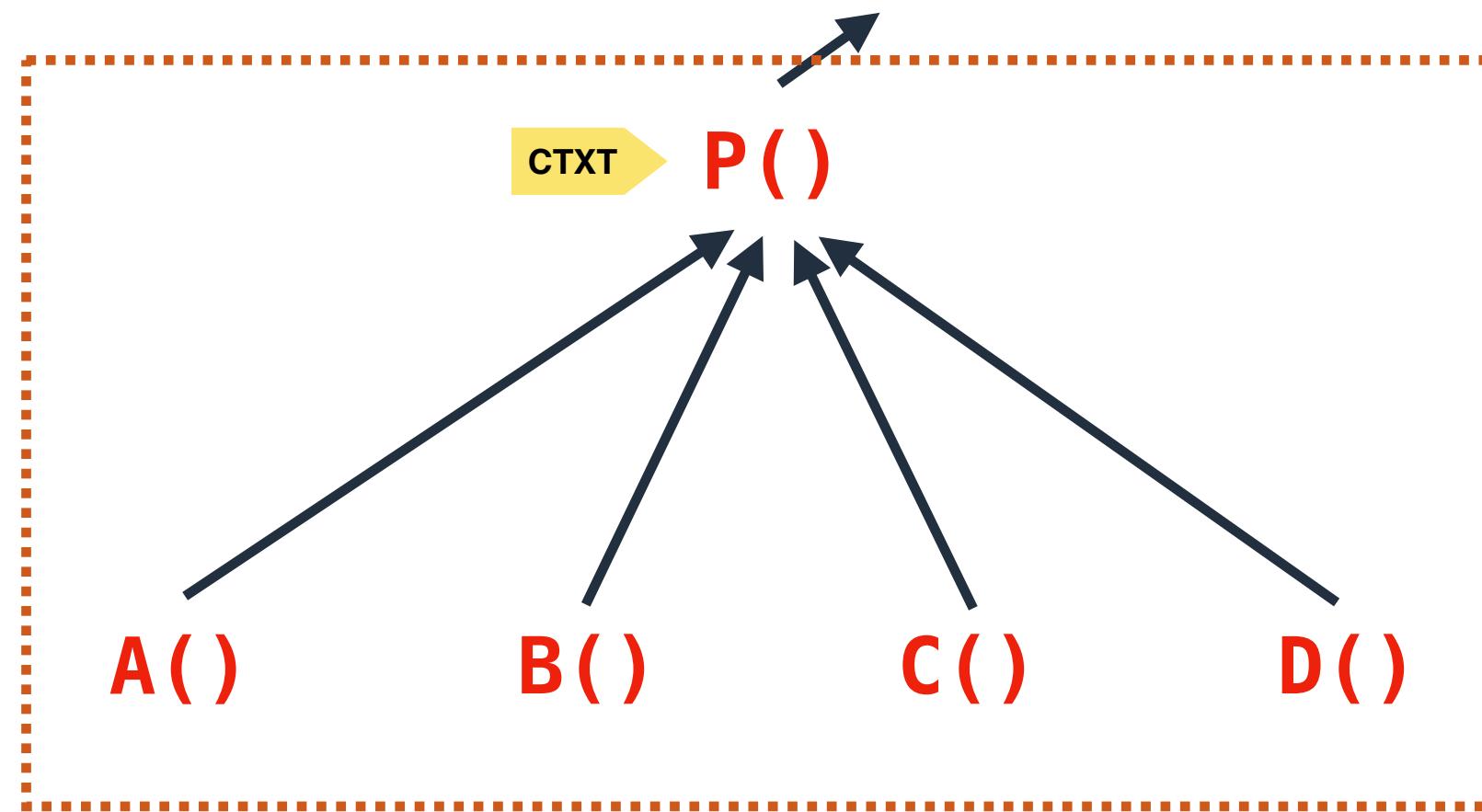
Shadowing Call Stack

Maintaining CTXT

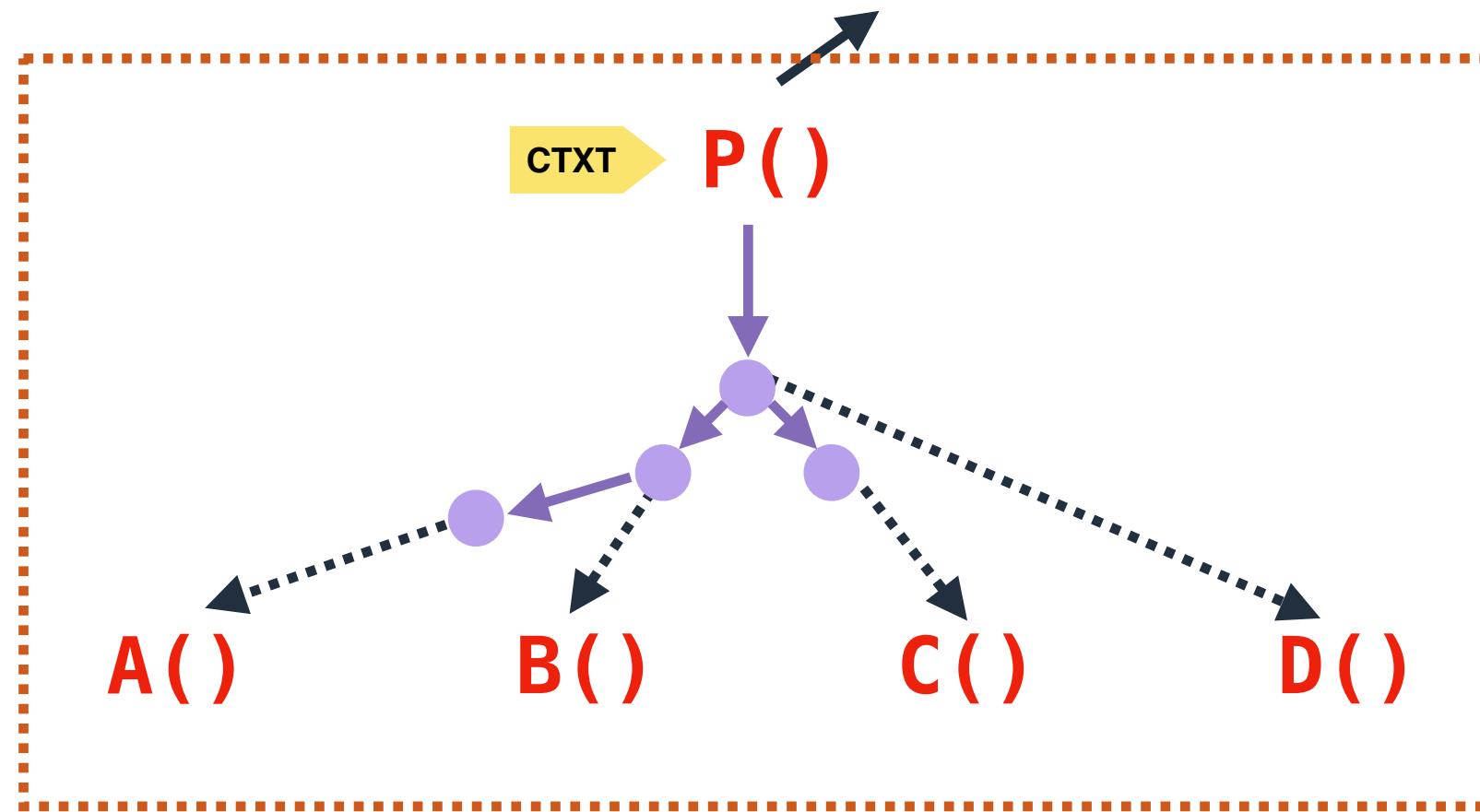
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Accelerate Lookup with Splay Trees

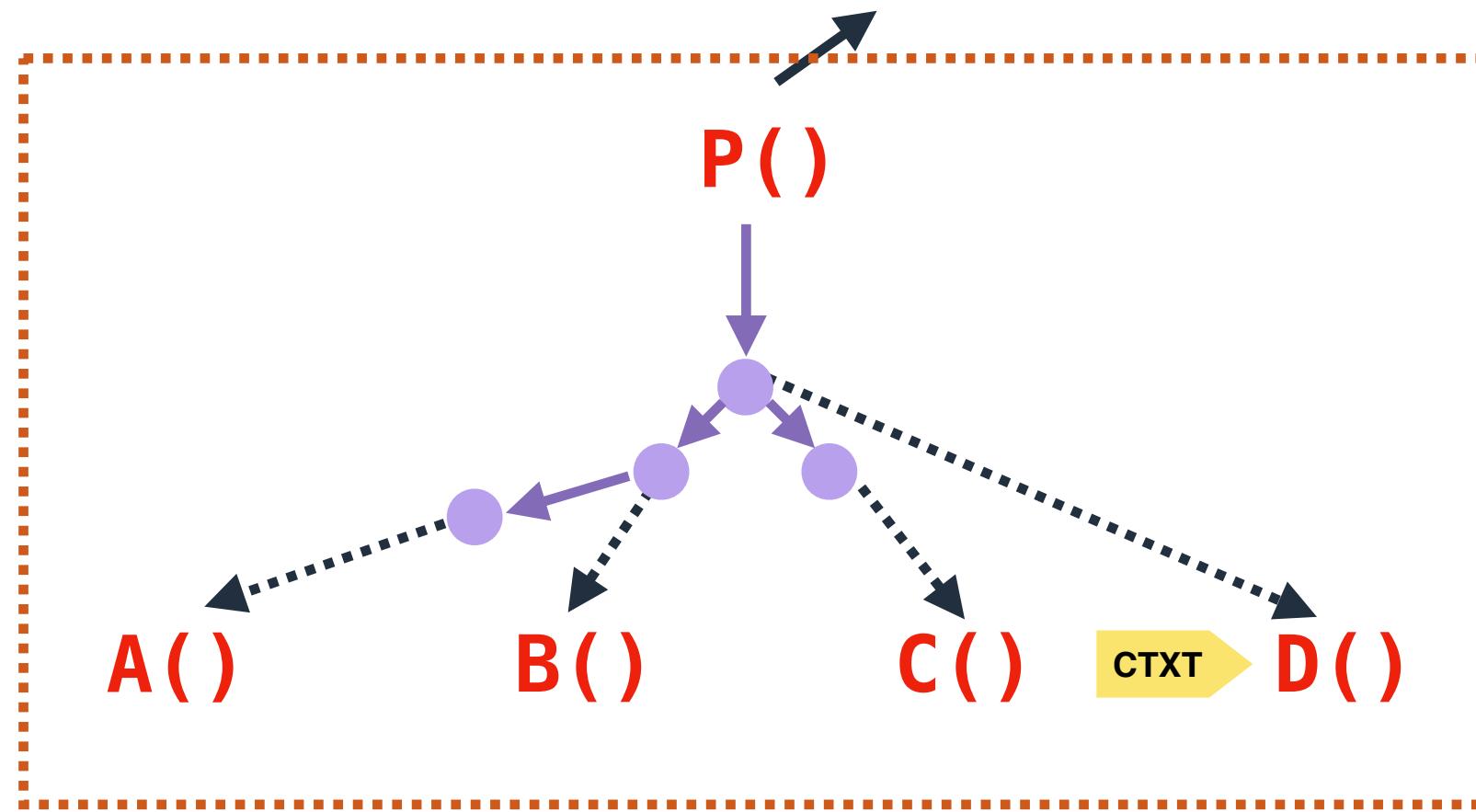


Accelerate Lookup with Splay Trees



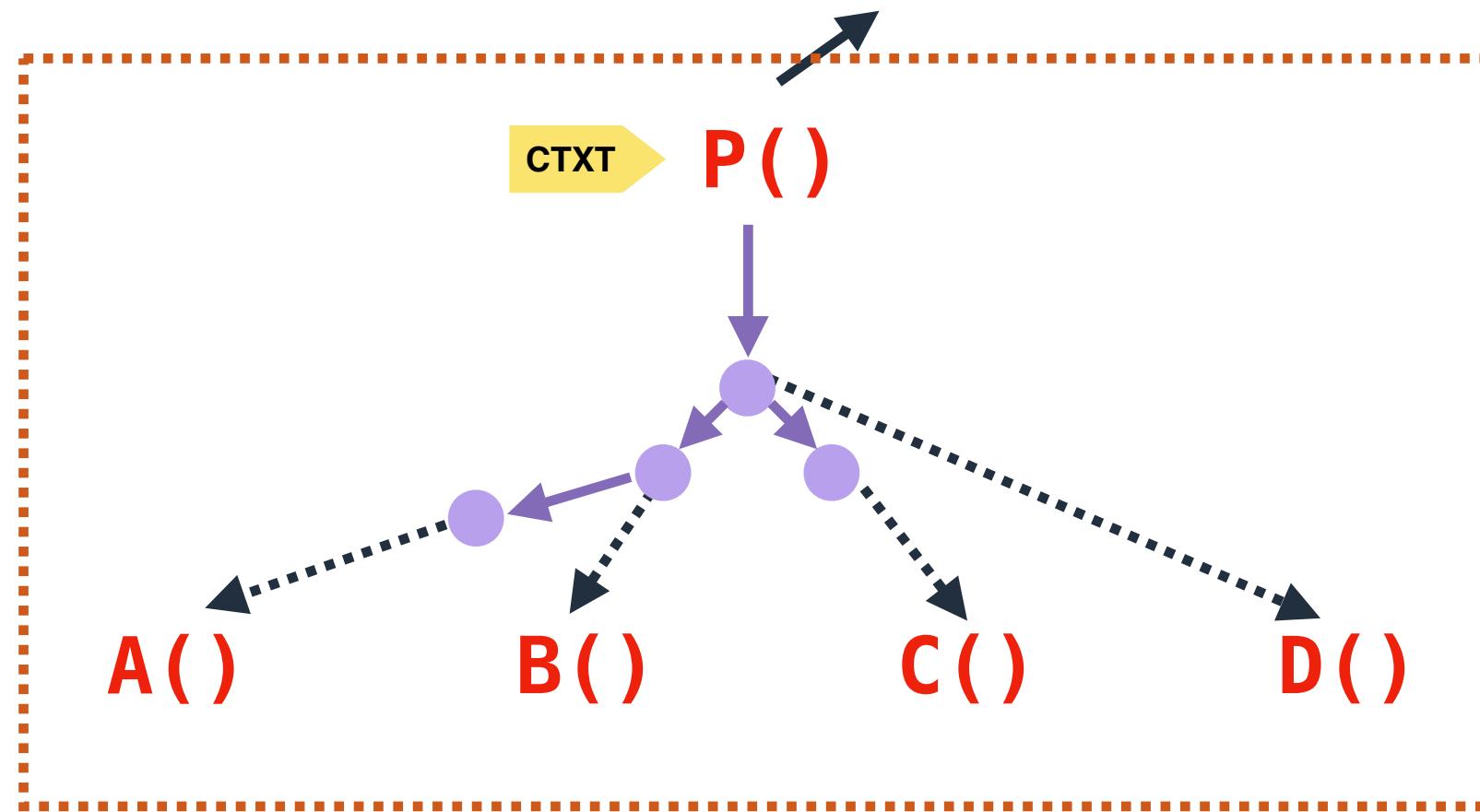
Splay tree [“Self-adjusting binary search trees” by Sleator et al. 1985]
ensures frequently called functions are near the root of the tree

Accelerate Lookup with Splay Trees



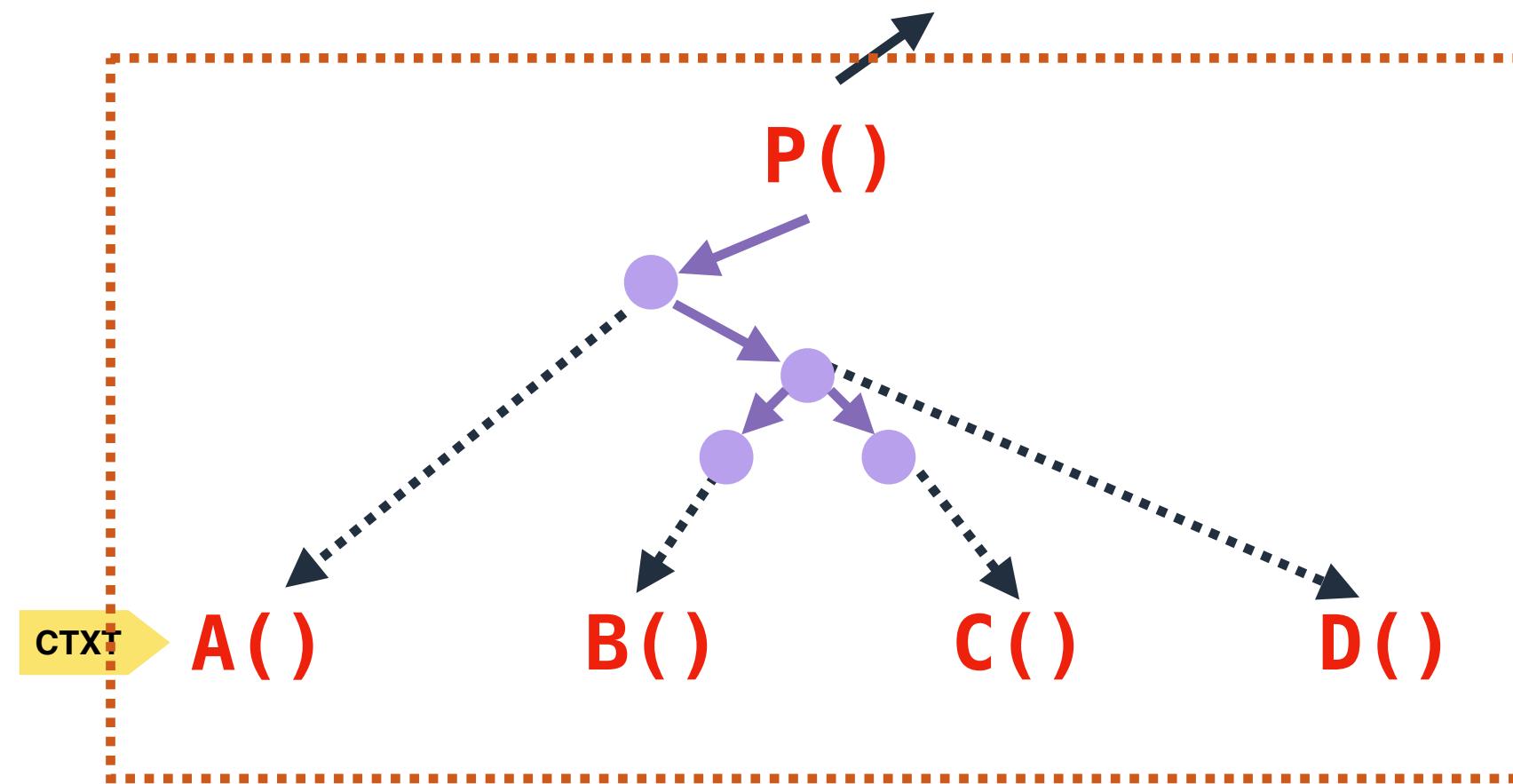
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Accelerate Lookup with Splay Trees



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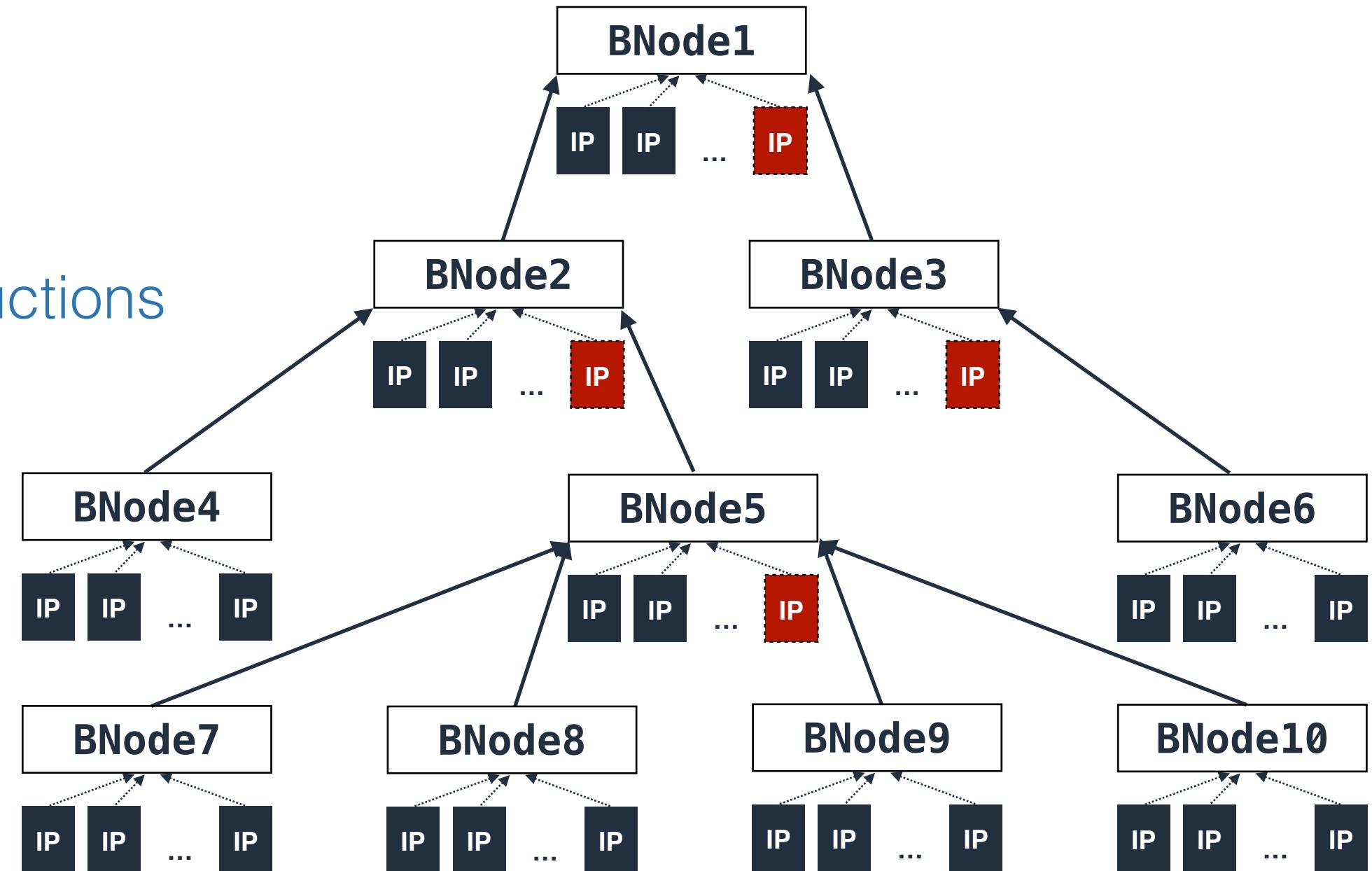
Overview of Calling Context Tree

BNode

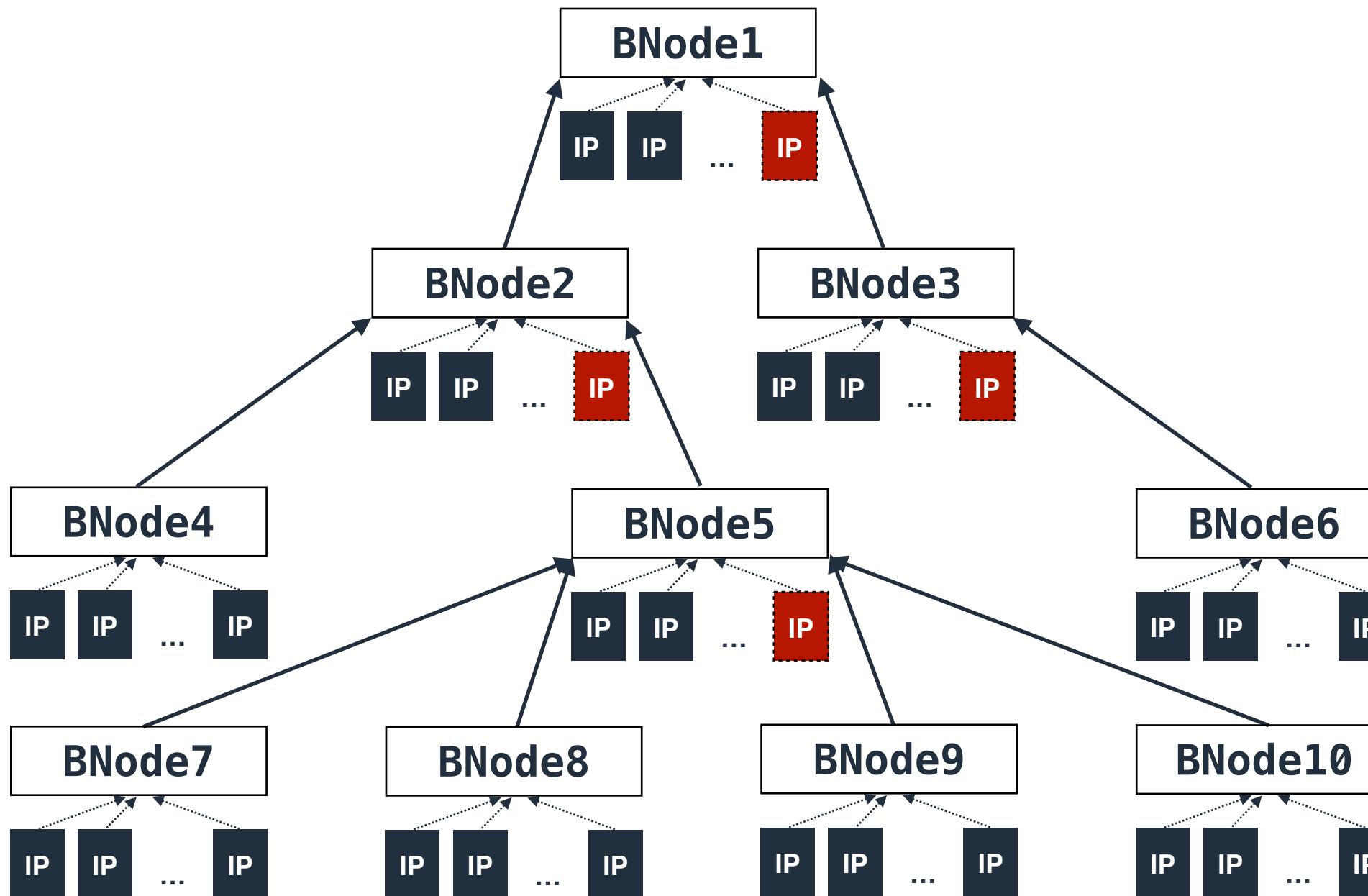
represents a basic block

IPNode

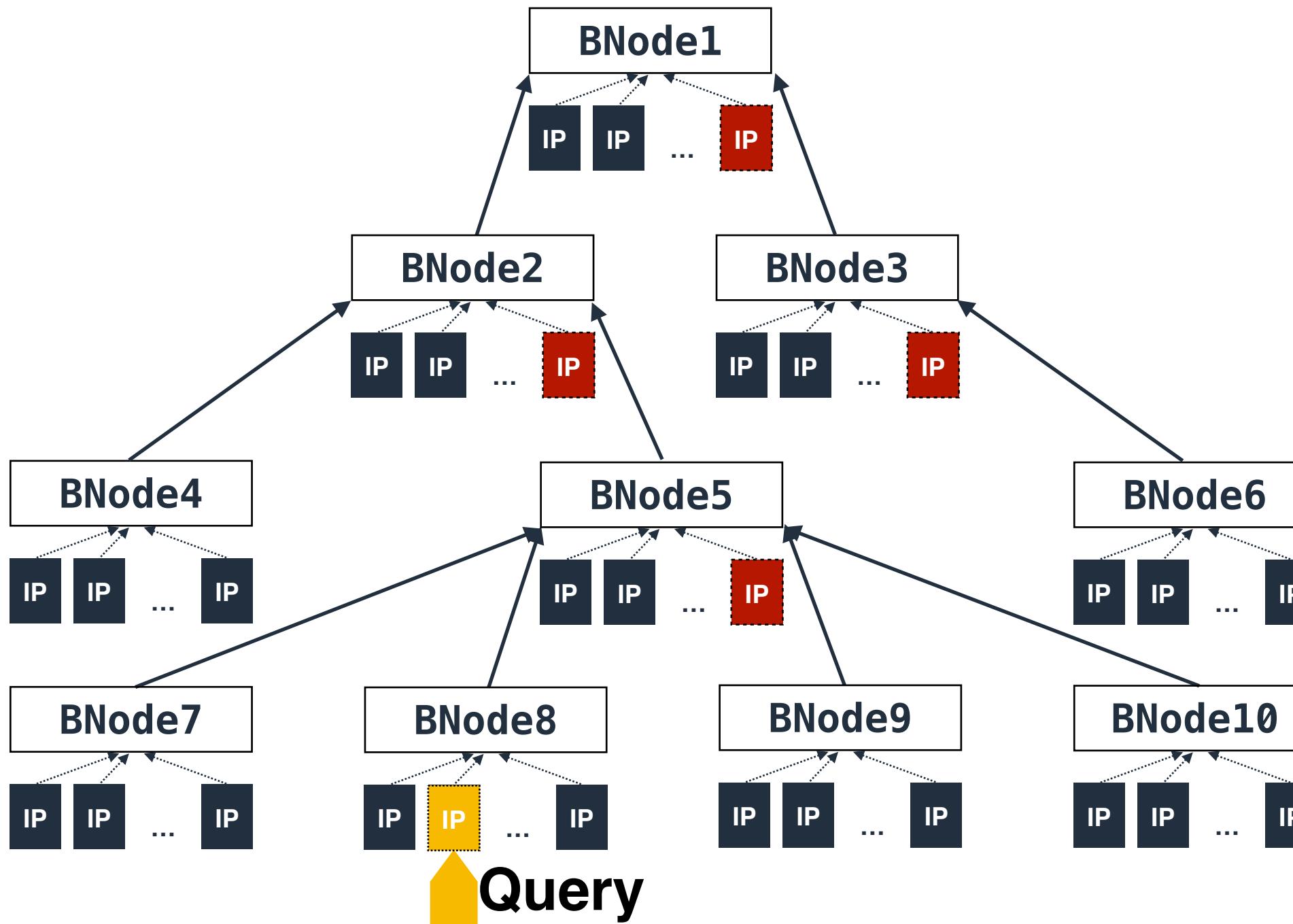
represents individual instructions



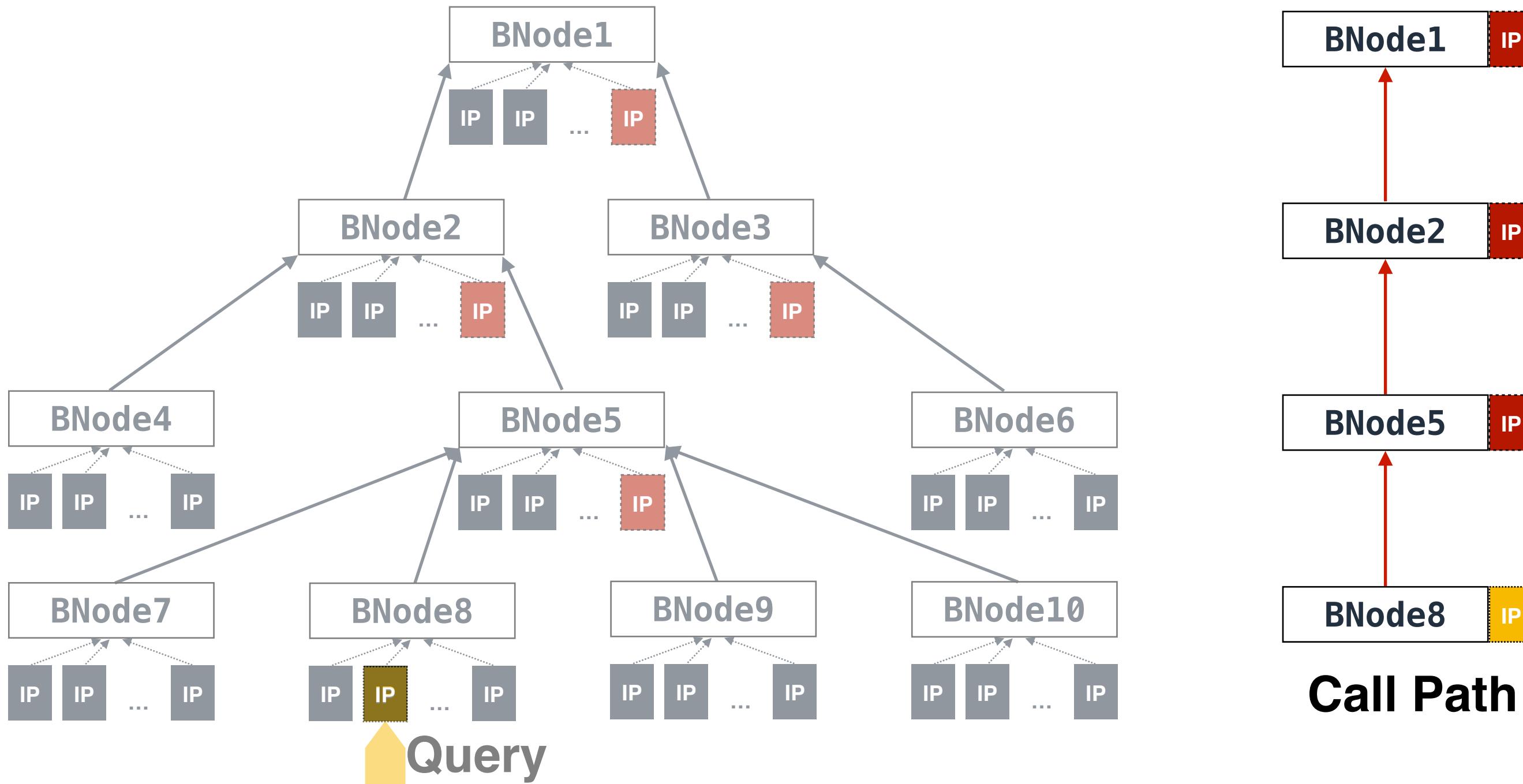
Query Call Path for Any Instruction

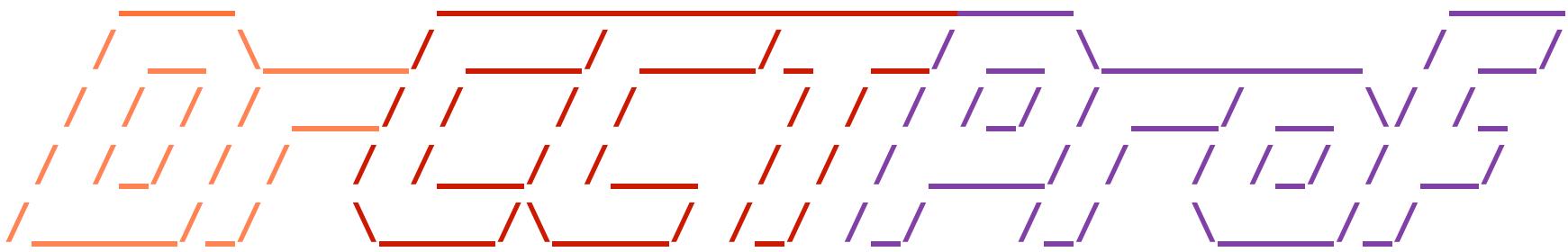
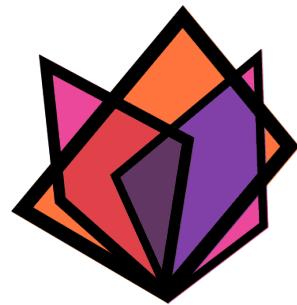


Query Call Path for Any Instruction



Query Call Path for Any Instruction





- Ubiquitous call path collection
- **Attributing costs to data objects**
- Handling parallelism
- Evaluation
- Case study
- Conclusions

Data-Centric Attribution

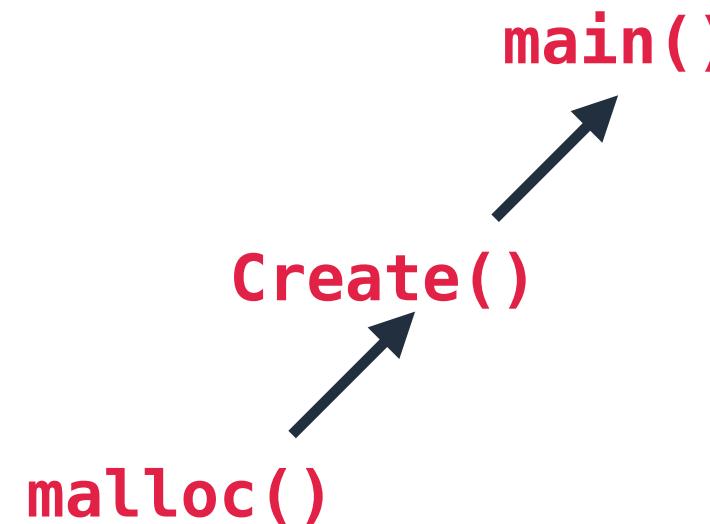
```
1: int MyArray[s];
2:
3: void Create(){
4:     return malloc();
5: }
6:
7: void Update(int * ptr){
8:     for(...)
9:         ptr[i]++;
10: }
11:
12: void main(){
13:     int *m;
14:     p1 = Create();
15:     Update(p1);
16:     p2 = MyArray;
17:     Update(p);
18: }
```

main()

Associate each data access with its **data object**

Data-Centric Attribution

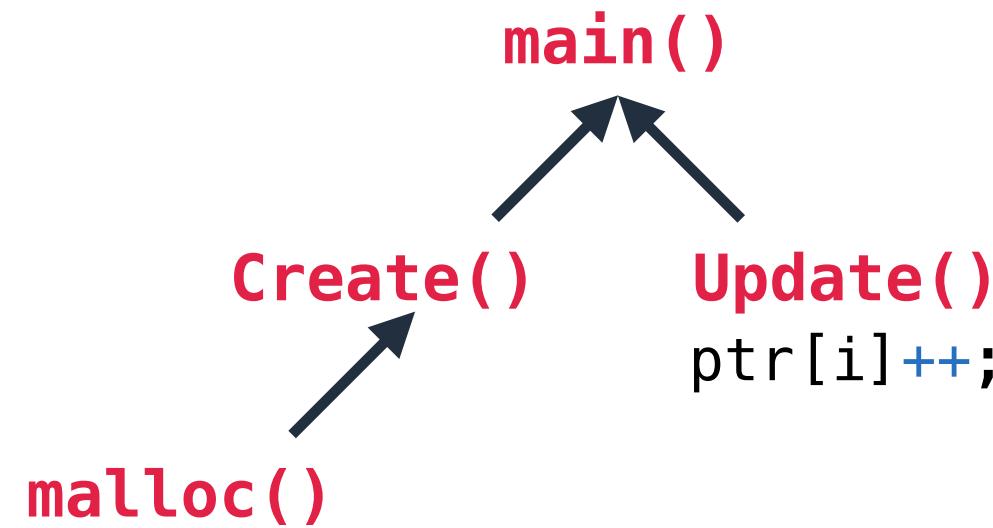
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Data-Centric Attribution

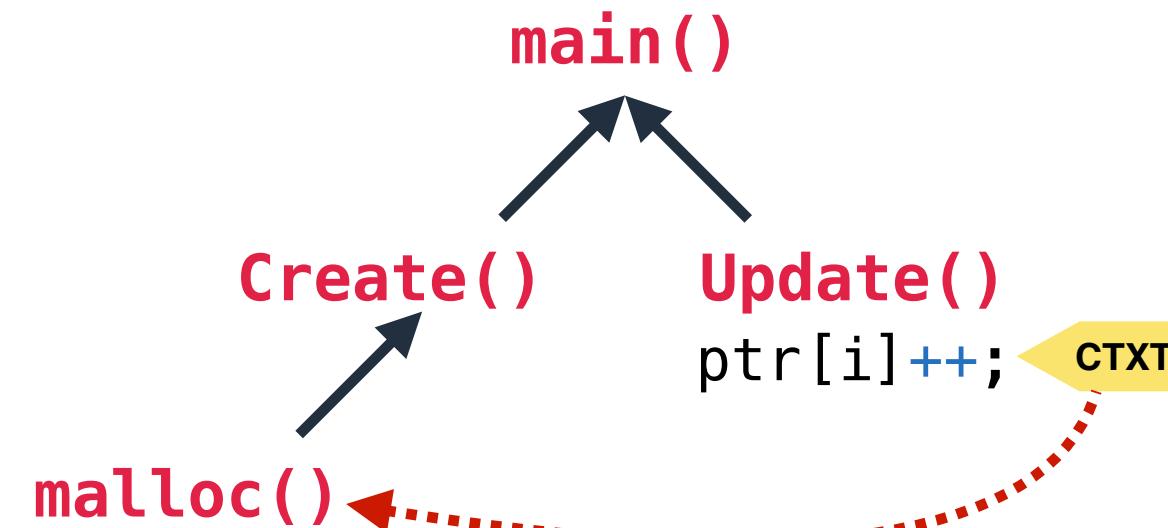
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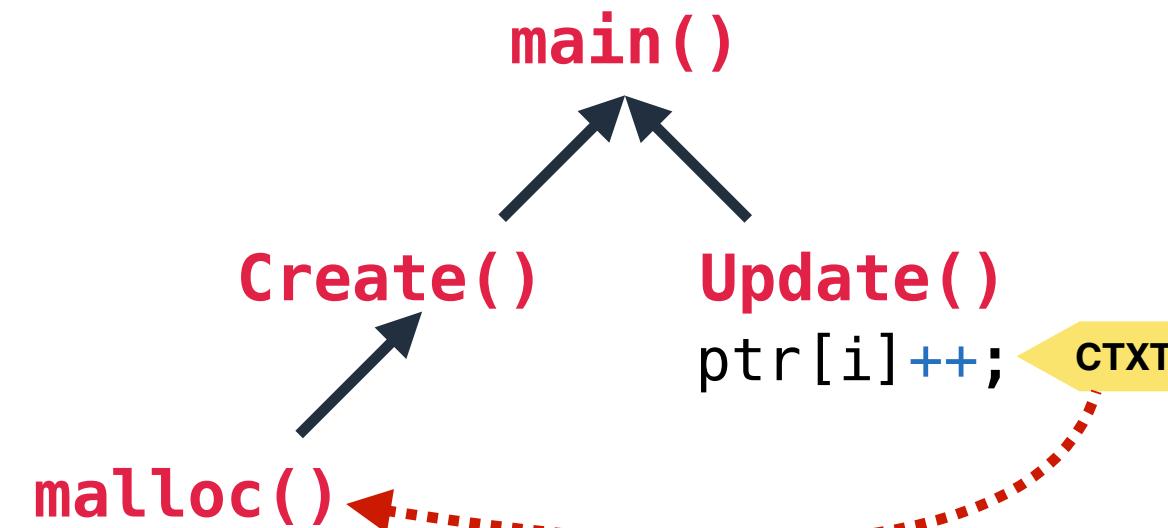
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Associate each data access with its data object
- Dynamic allocation: **Call path of allocation site**

Data-Centric Attribution

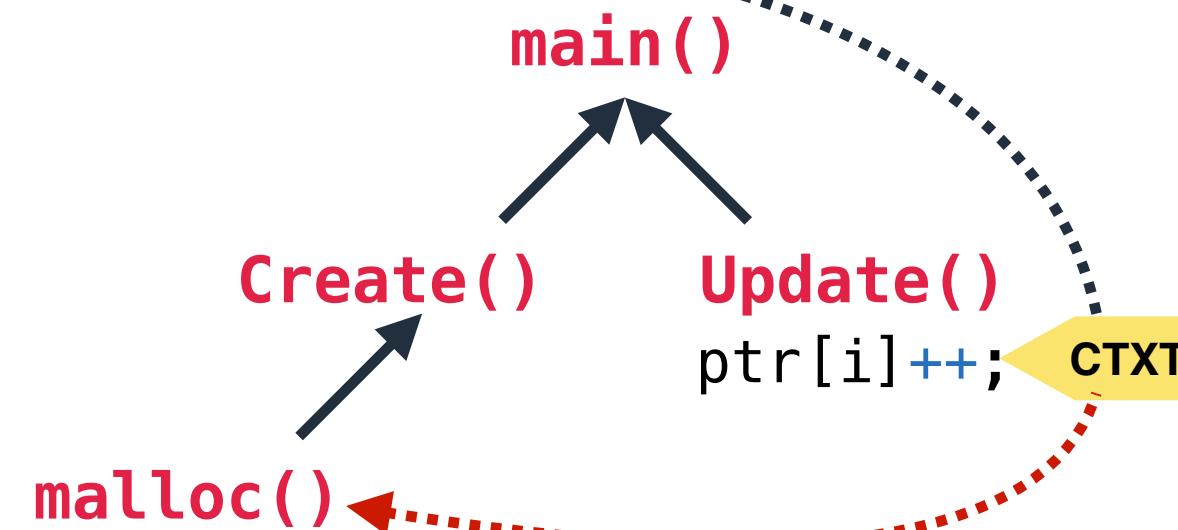
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Associate each data access with its **data object**
- Dynamic allocation: **Call path of allocation site**

Data-Centric Attribution

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```



Associate each data access with its **data object**

- Dynamic allocation: **Call path of allocation site**
- Static objects: **Variable name**

Data-Centric Attribution

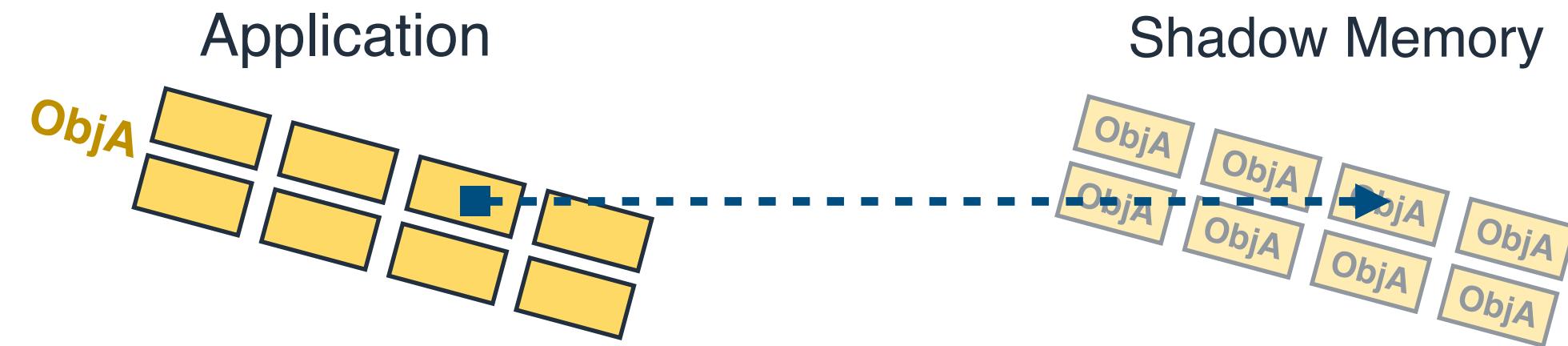
Implementation

Shadow memory

Data-Centric Attribution

Implementation

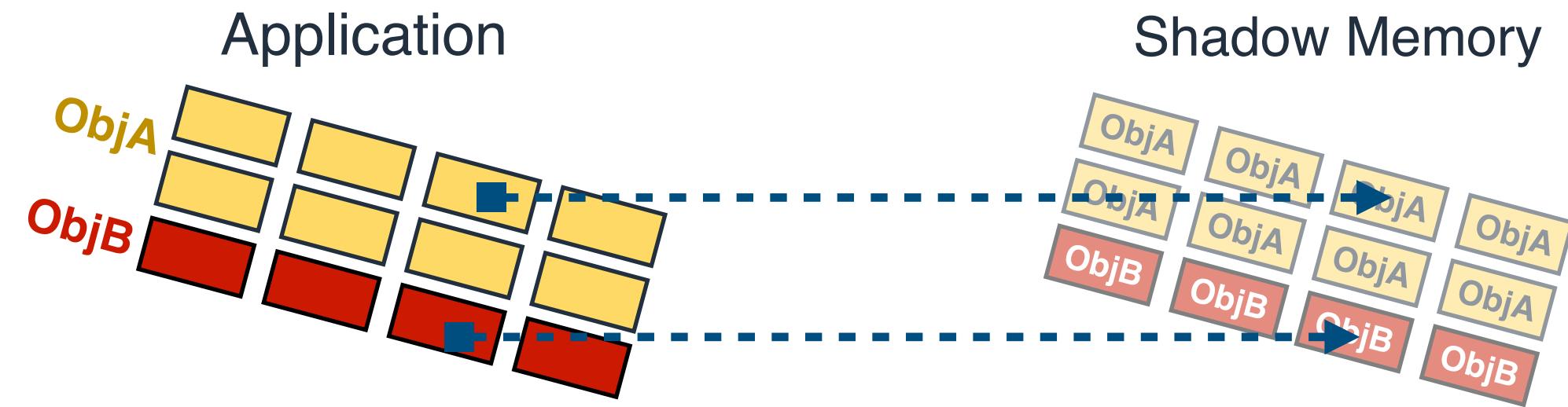
Shadow memory



Data-Centric Attribution

Implementation

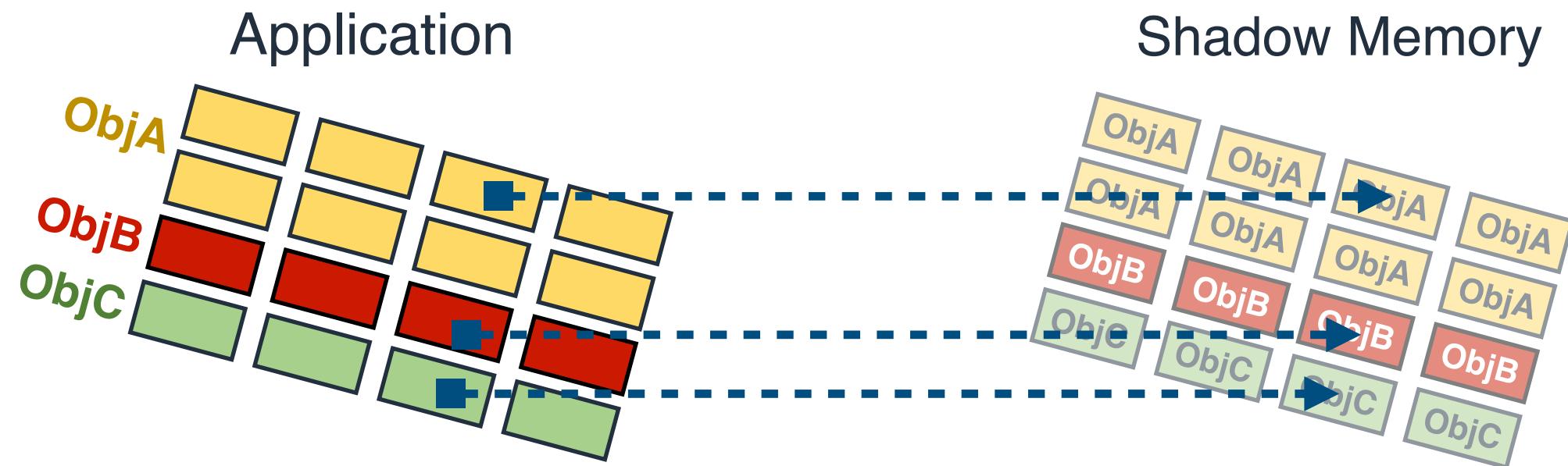
Shadow memory



Data-Centric Attribution

Implementation

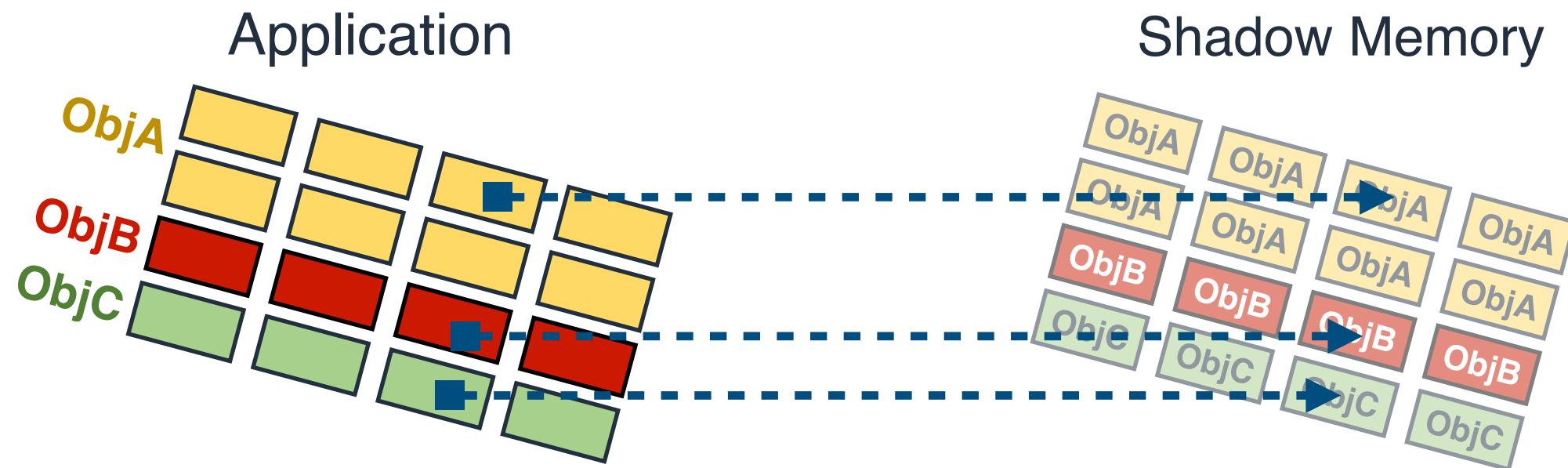
Shadow memory



Data-Centric Attribution

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Shadow memory



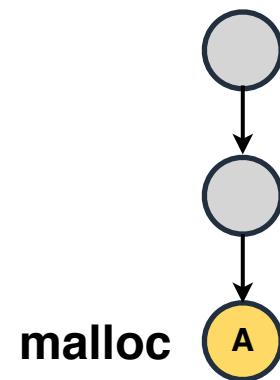
For each memory cell, a shadow cell holds a handle for the memory cell's data object

- Low lookup cost—**O(1)**
- Support **concurrent** access

Correlate Data-Attribution with Code-Attribution

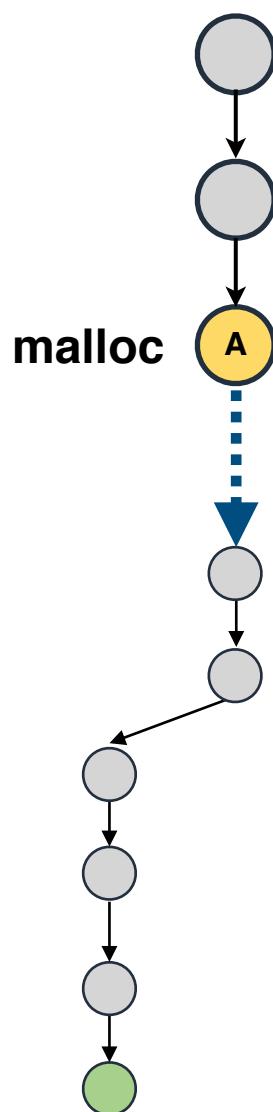
Correlate Data-Attribution with Code-Attribution

CCT for
heap allocated variables



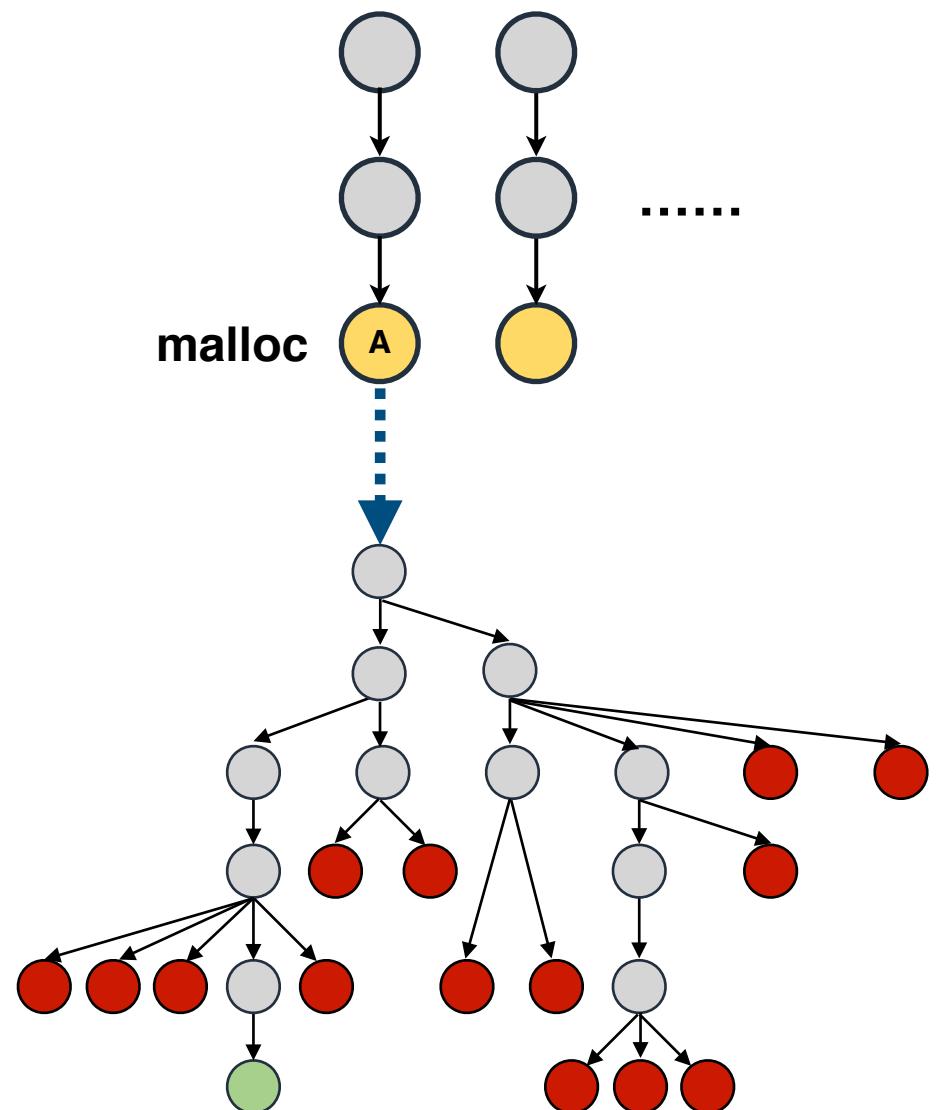
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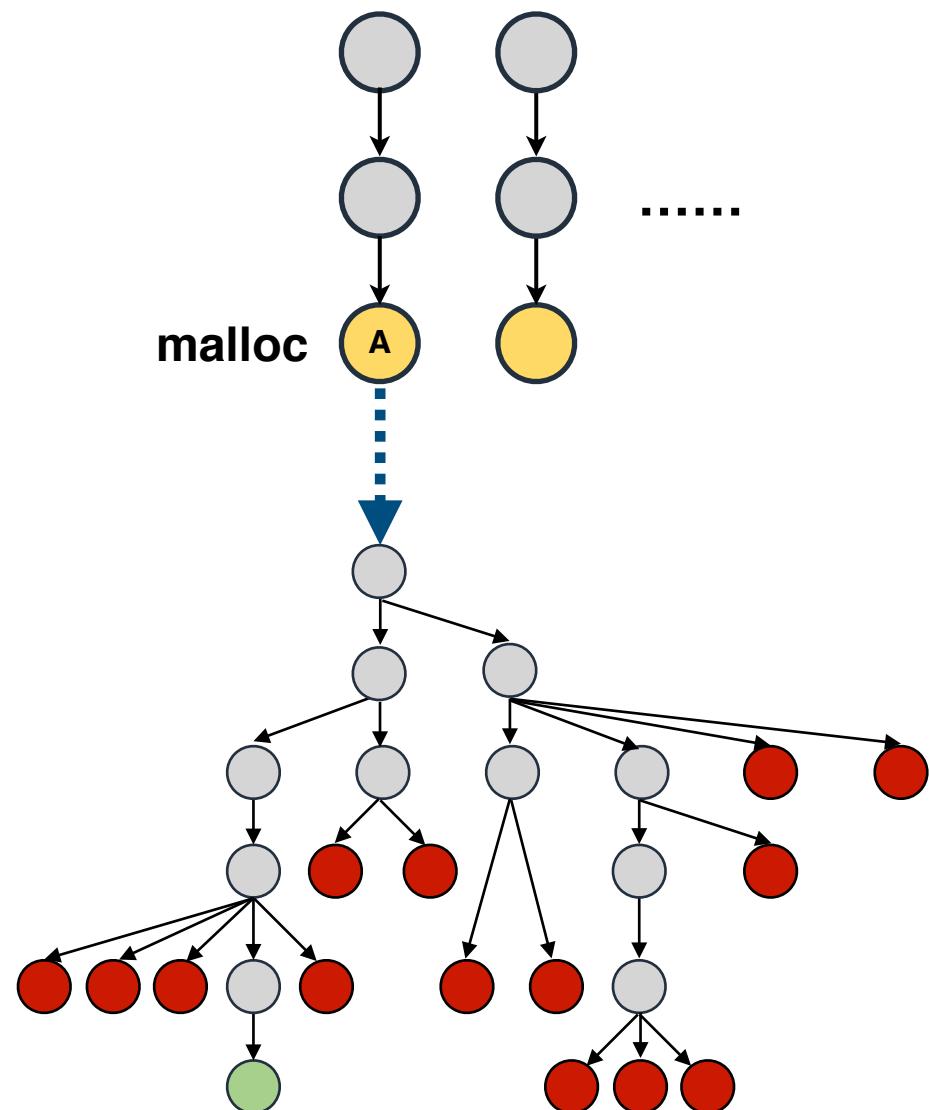
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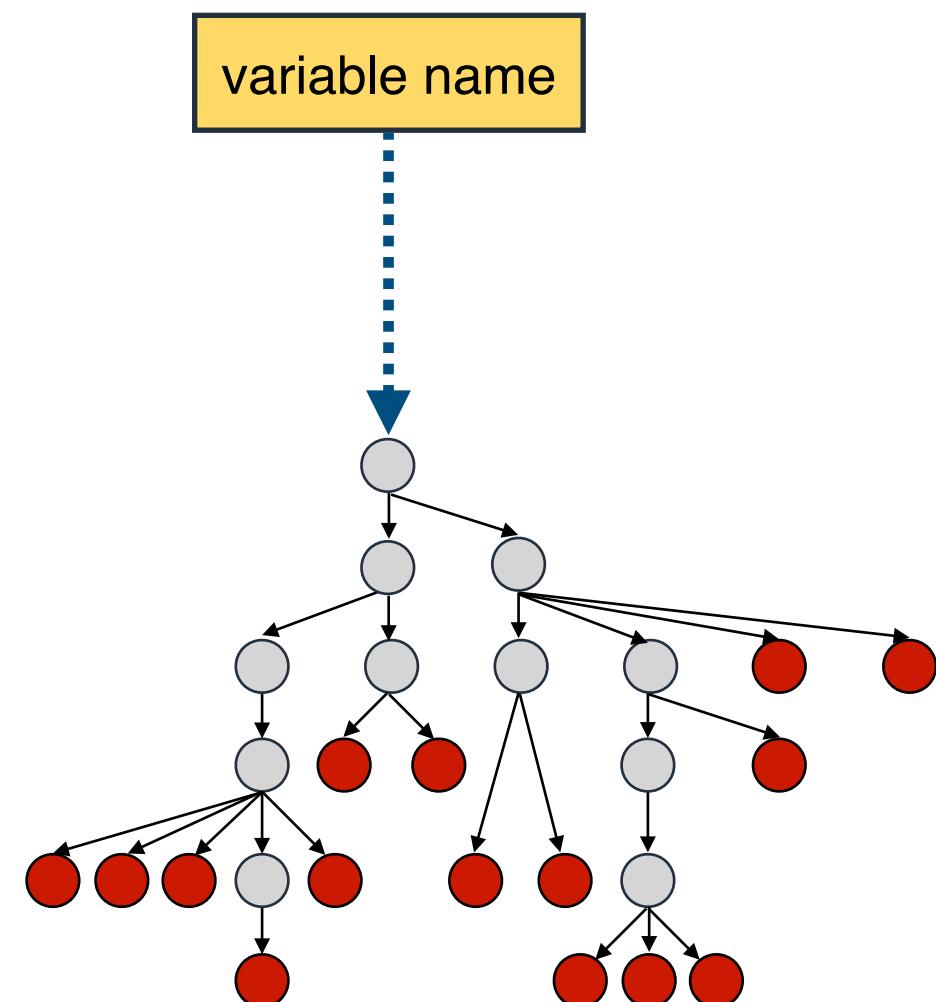


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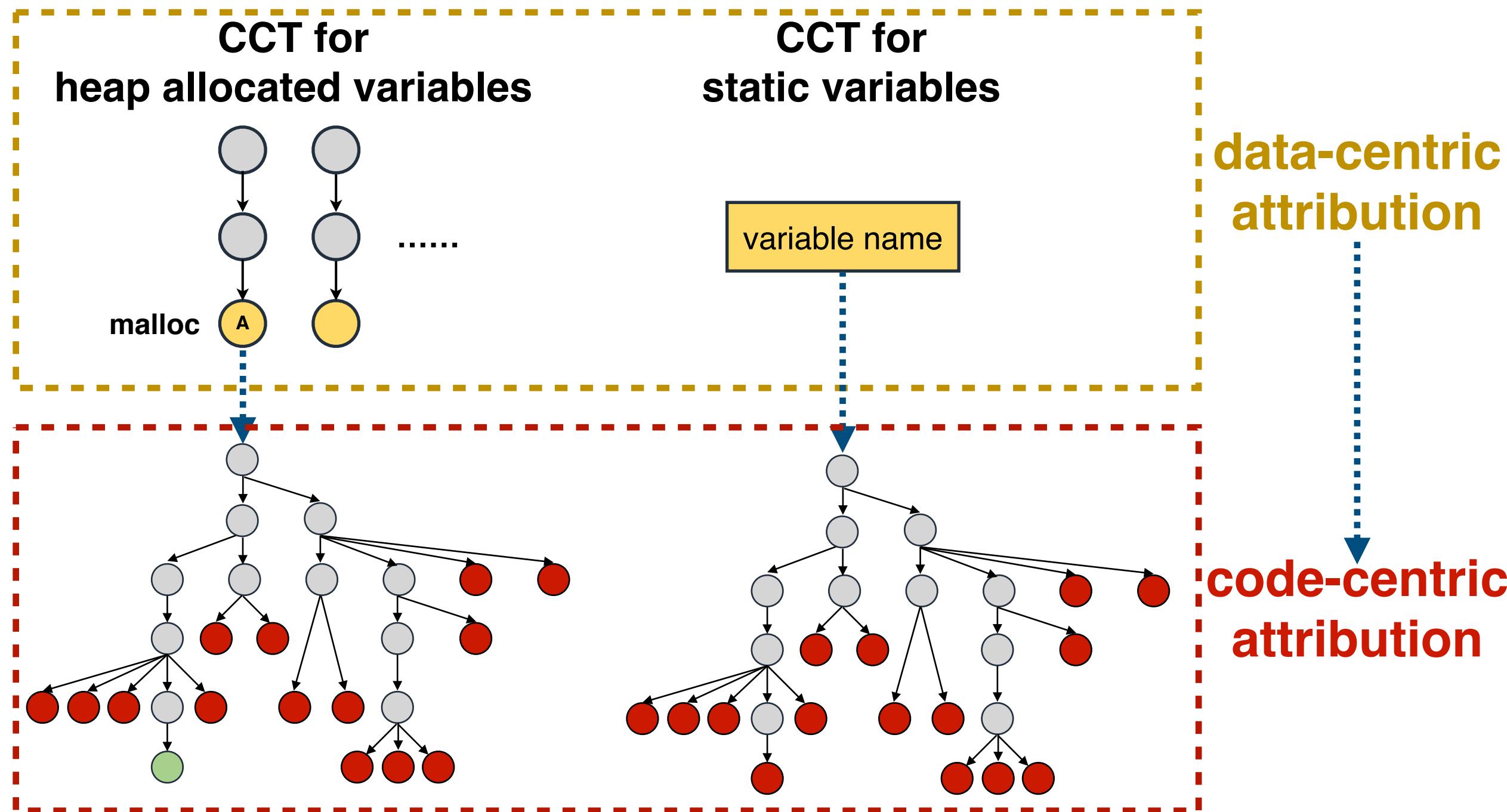
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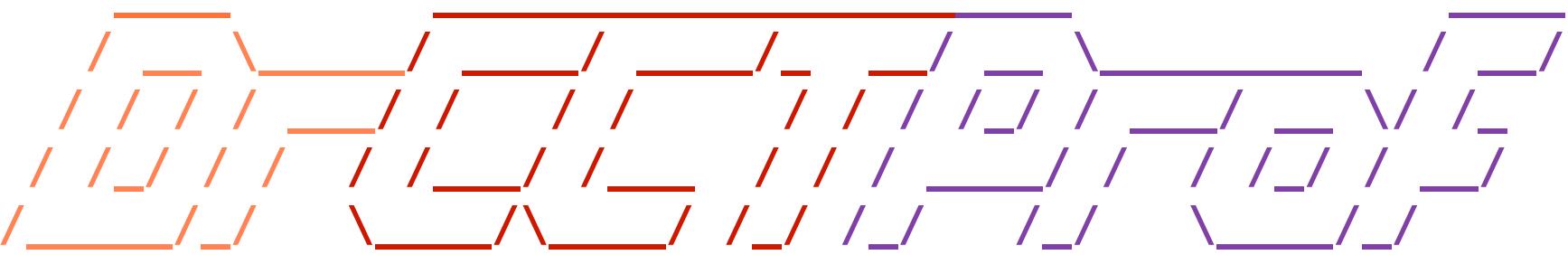
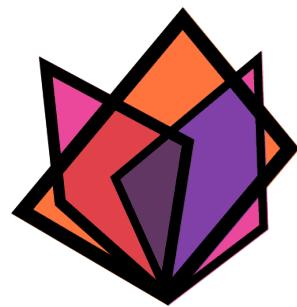


CCT for
static variables



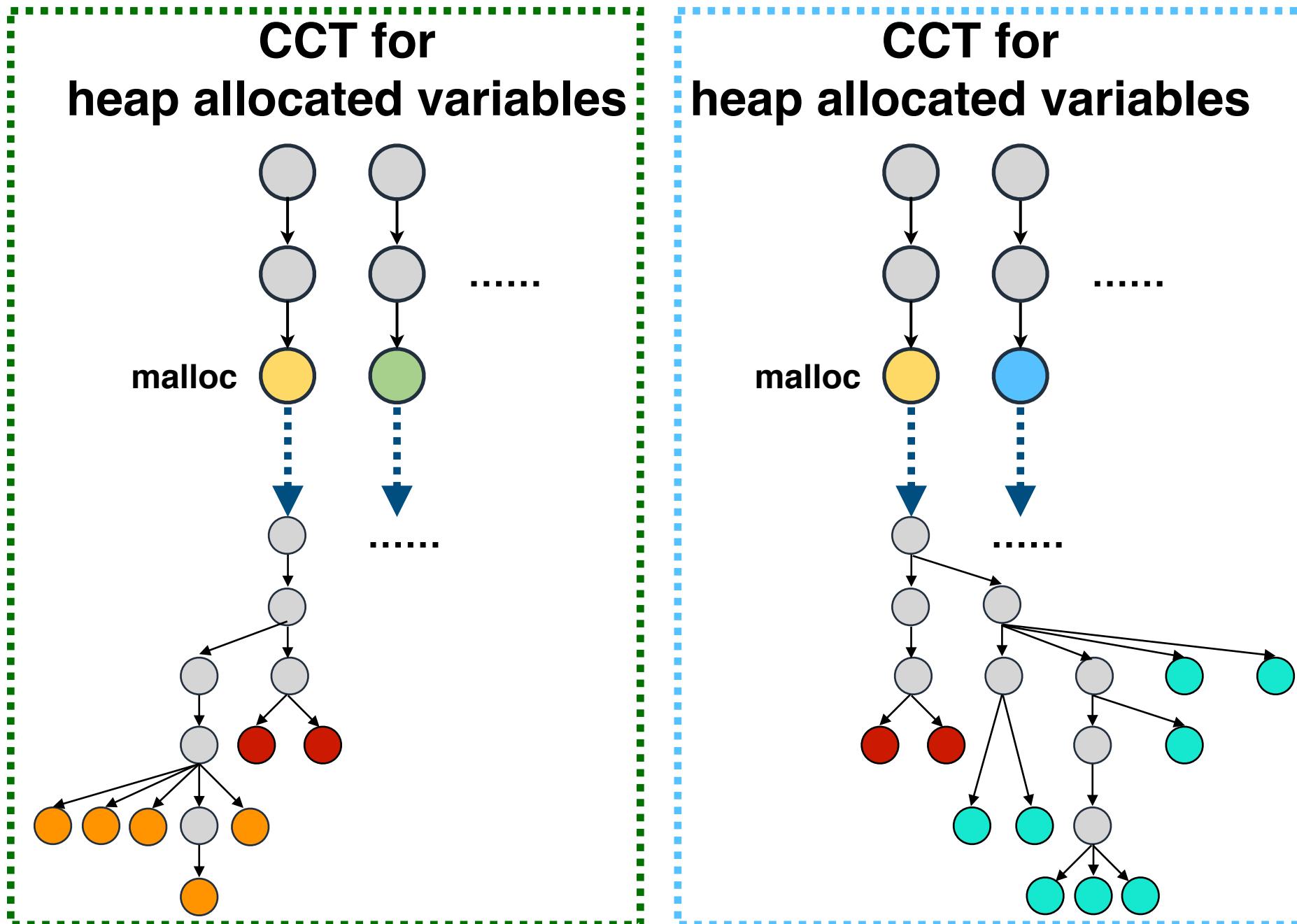
Correlate Data-Attribution with Code-Attribution



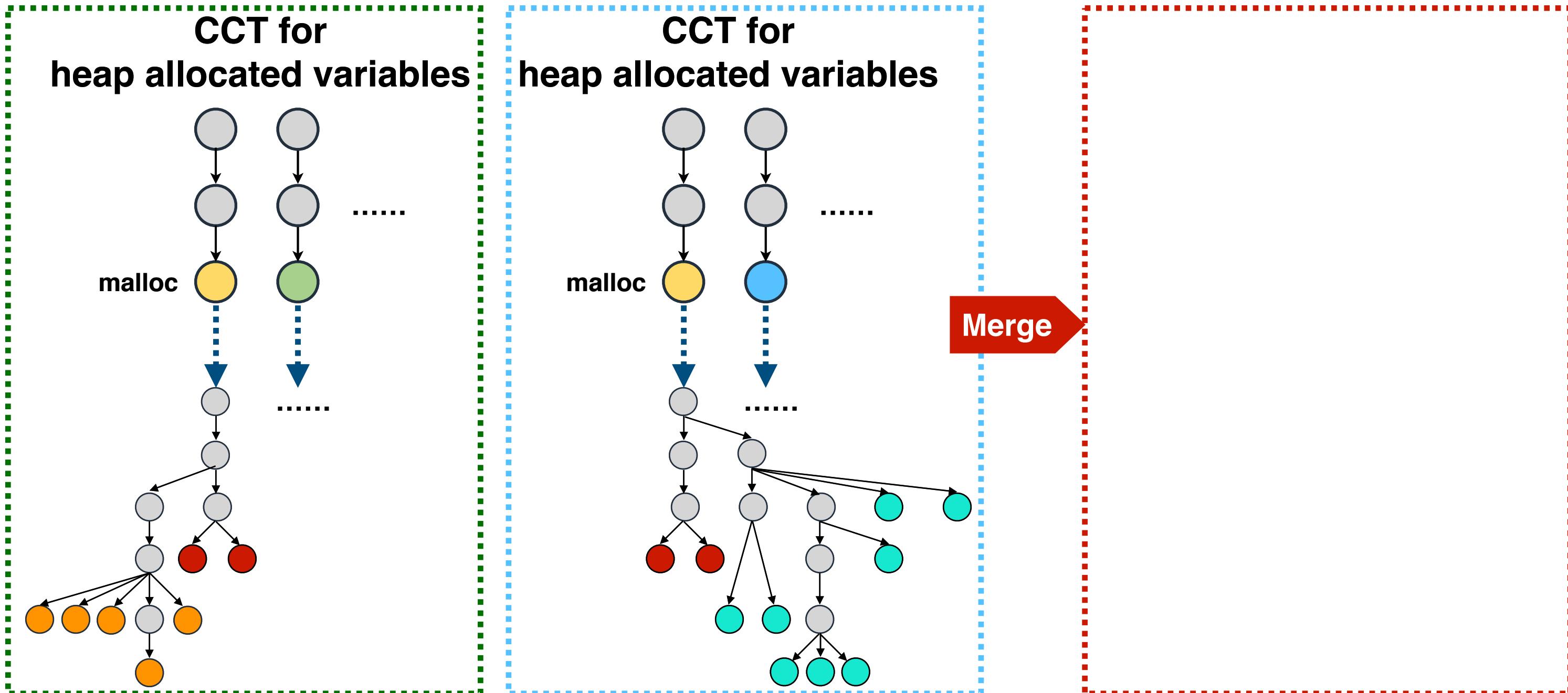


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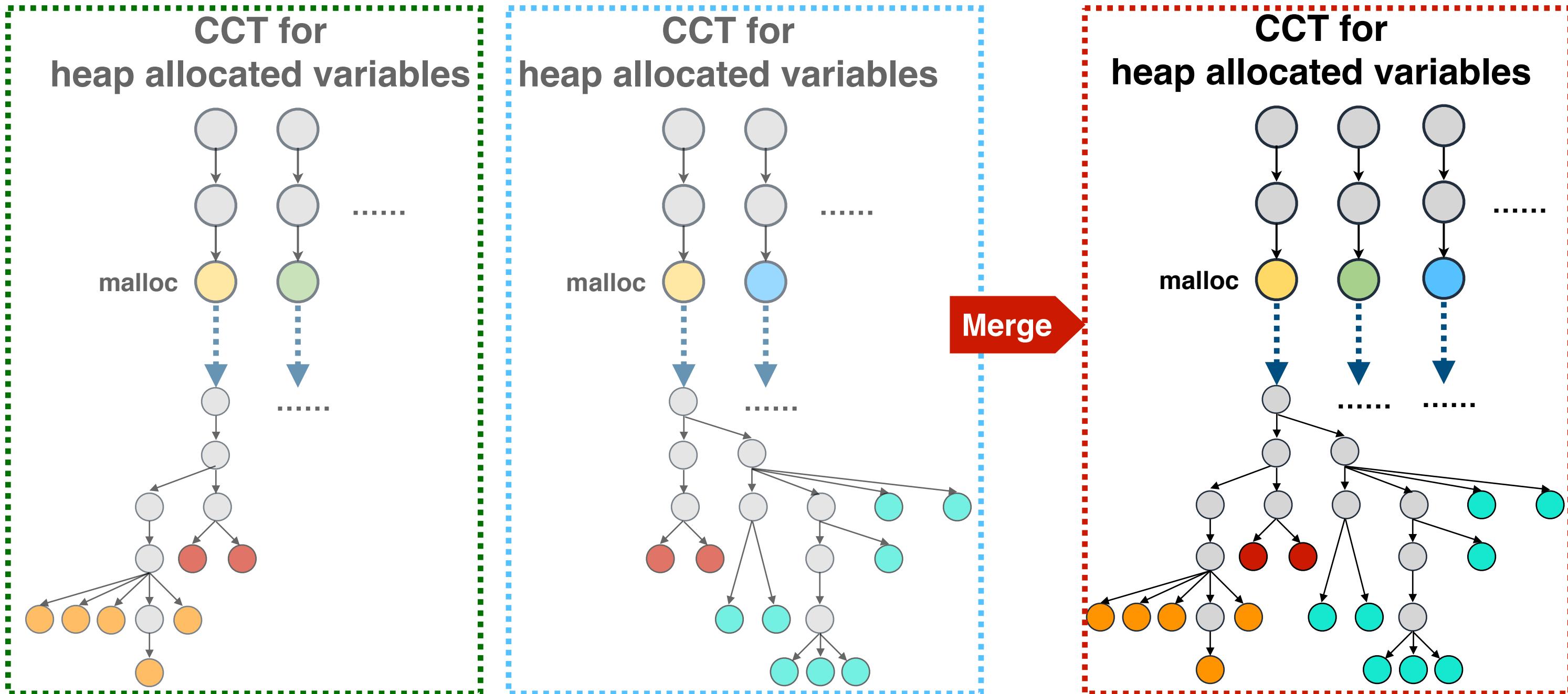
Merge CCTs across Threads/Processes

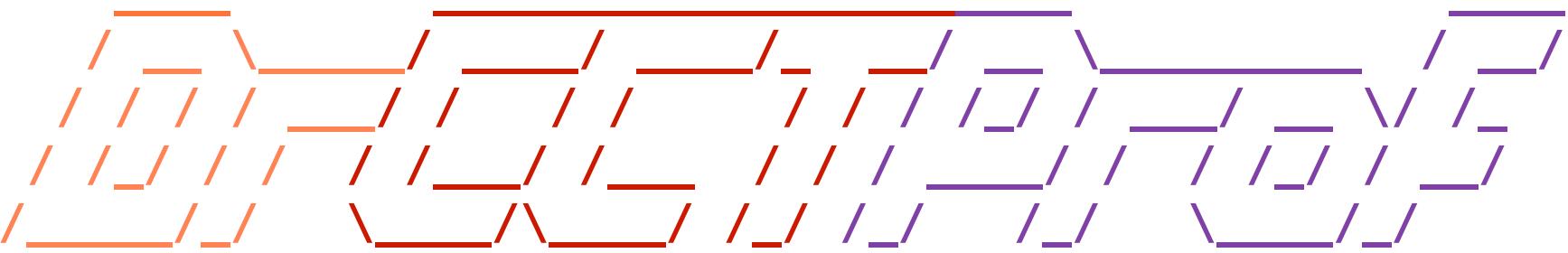
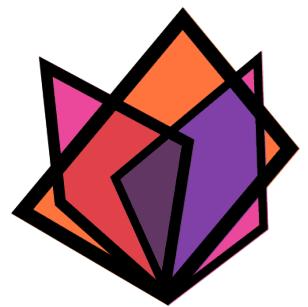


Merge CCTs across Threads/Processes



Merge CCTs across Threads/Processes





- Ubiquitous call path collection
- Attributing costs to data objects
- Merge attributions
- **Evaluation**
- Case study
- Conclusions

DrCCTProf Overhead

Machine configuration

Cluster

- Amazon Web Services (AWS)

Node

- Graviton2 CPU **32 ARMv8 cores** clocked at 2.3 GHz

Workloads

NPB-MPI

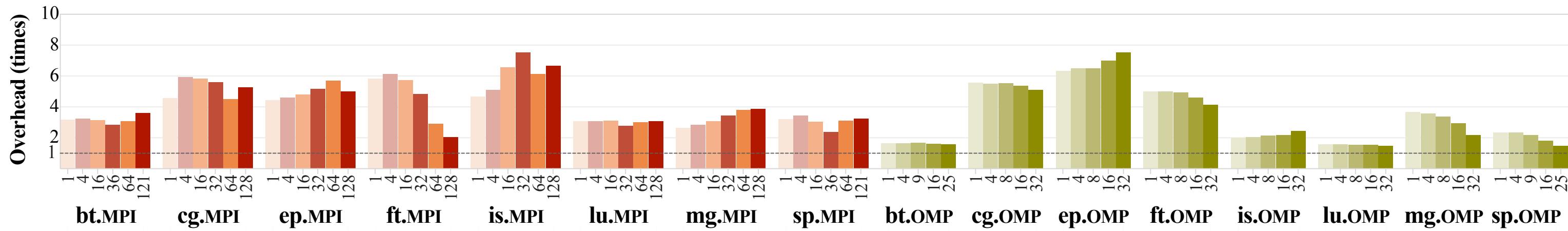
- **4 Nodes 128 processes in total**

NPB-OMP

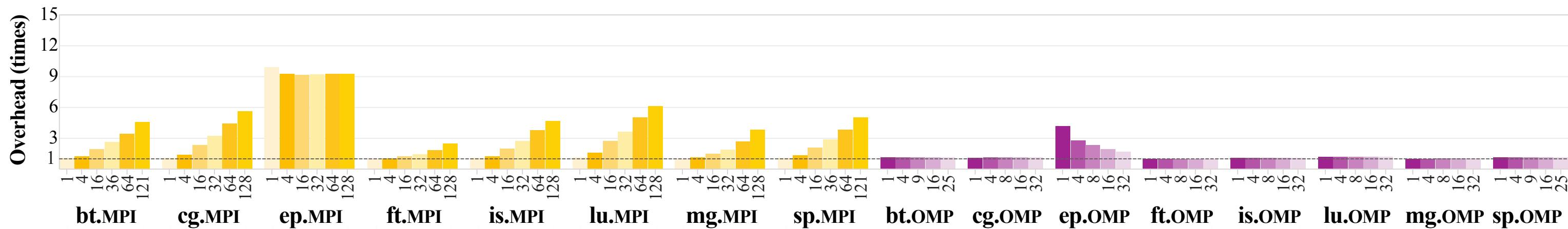
- **1 Node 32 threads in total**

NPB Code-Centric Overhead

Runtime Overhead [MPI 3.8x] [OMP 2.3x]

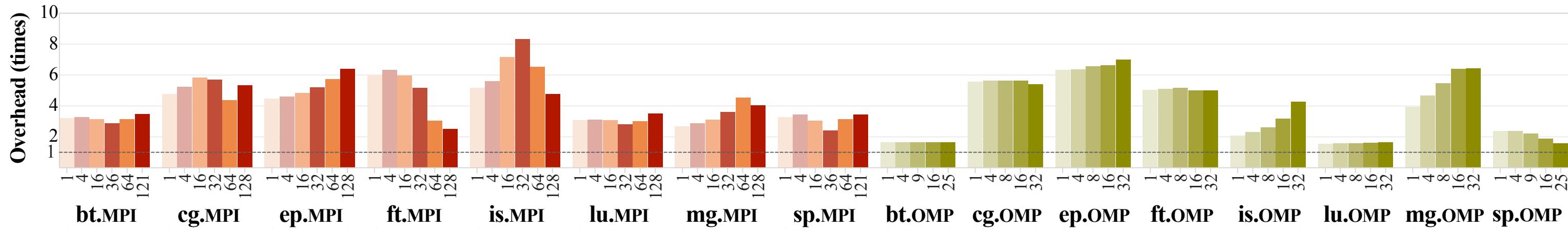


Space Overhead [MPI 4.9x] [OMP 1.2x]

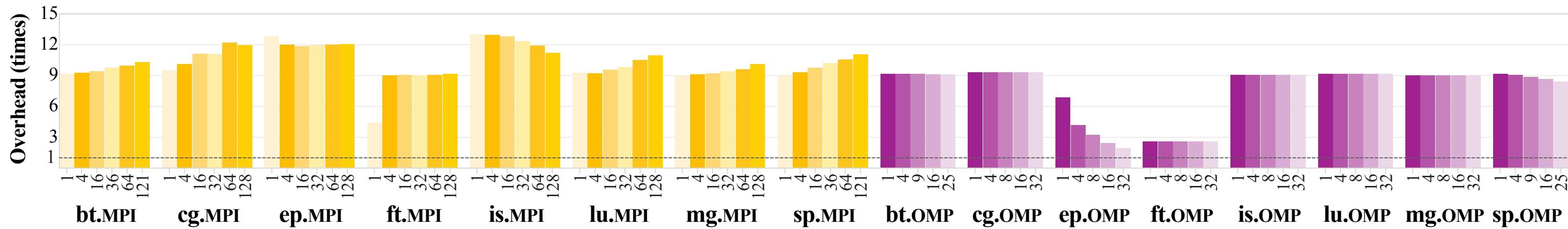


NPB Data-Centric Overhead

Runtime Overhead [MPI 3.8x] [OMP 4.6x]



Space Overhead [MPI 11x] [OMP 9x]



Real Application Overhead

Real application configuration

LAMMPS, Sweep3D

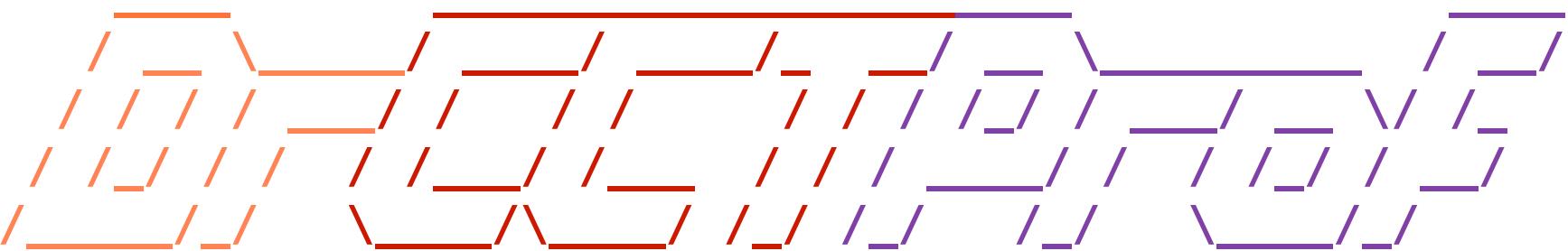
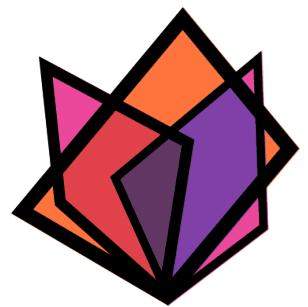
- **16 Nodes** with **512 MPI processes**

GROMACS

- **16 Nodes** with **128 MPI processes** and **4 threads** in each process

Real Applications	Original program		Code centric		Data centric	
	Runtime in sec	Memory in KB	Time Overhead	Memory Overhead	Time Overhead	Memory Overhead
LAMMPS	27.05	50968576	4.98×	2.44×	5.22×	11.06×
Sweep3D	34.80	99567728	7.56×	1.73×	7.63×	11.28×
GROMACS	35.71	4686336	5.95×	4.56×	6.17×	11.91×

The runtime and memory overheads of real applications.



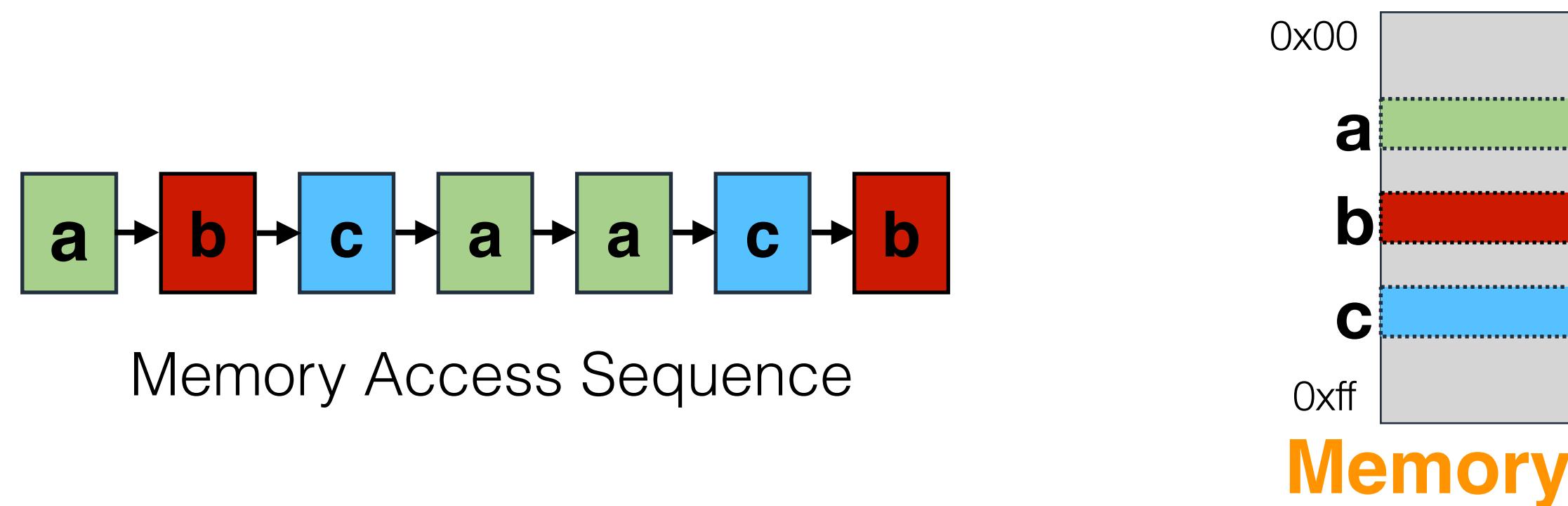
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A DrCCTProf Client Tool: ARMREUSE

ARMREUSE

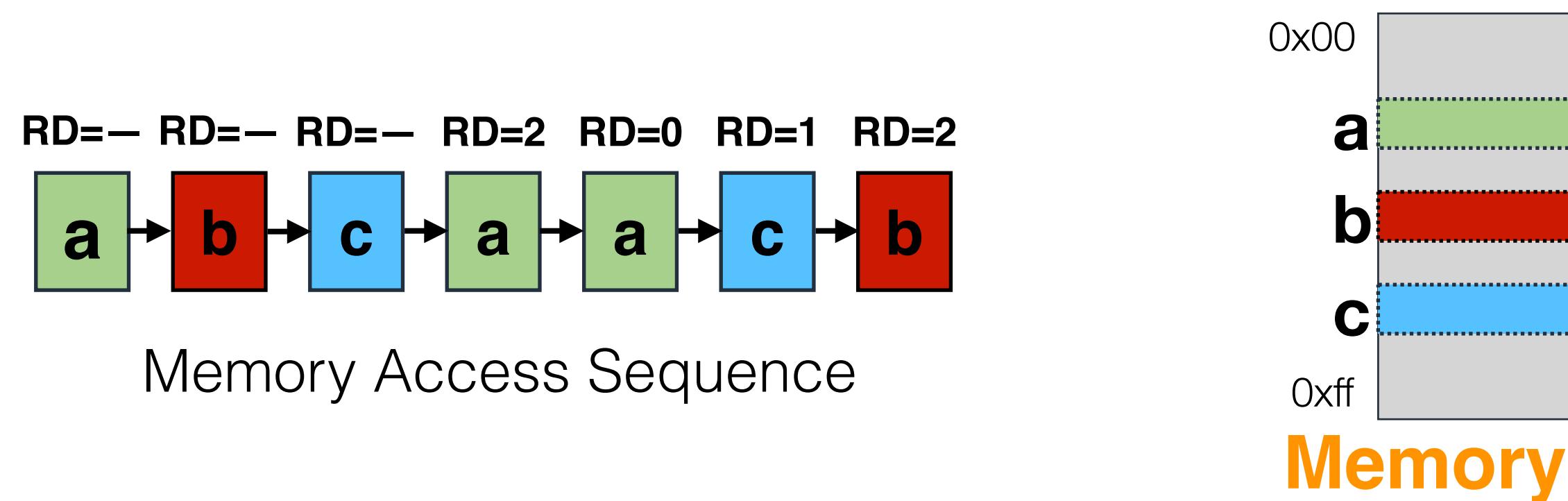
A DrCCTProf Client Tool: ARMREUSE

ARMREUSE



A DrCCTProf Client Tool: ARMREUSE

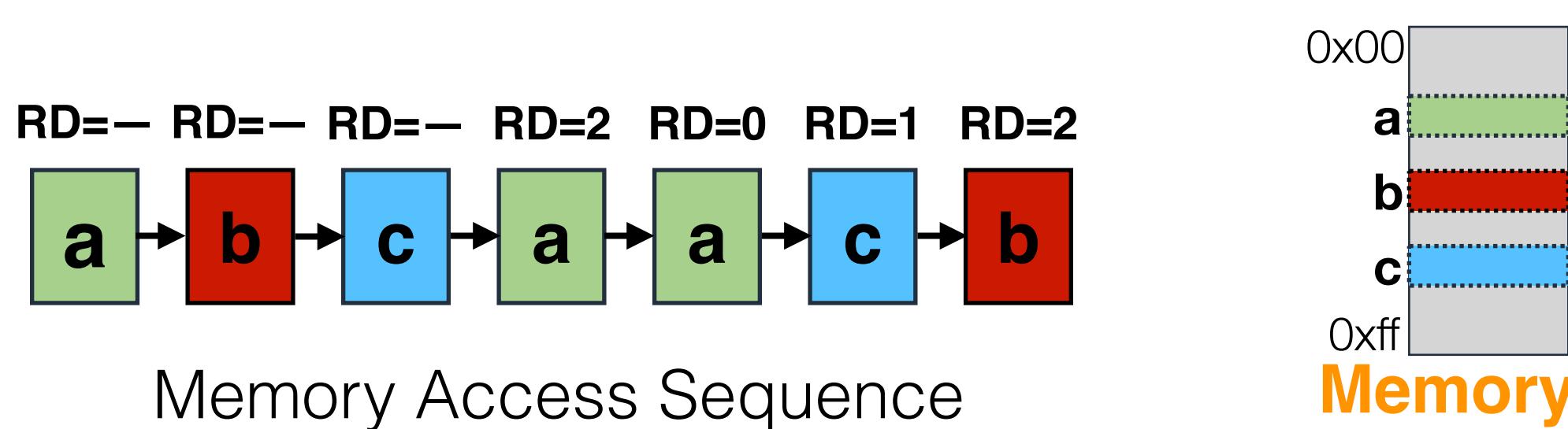
ARMREUSE



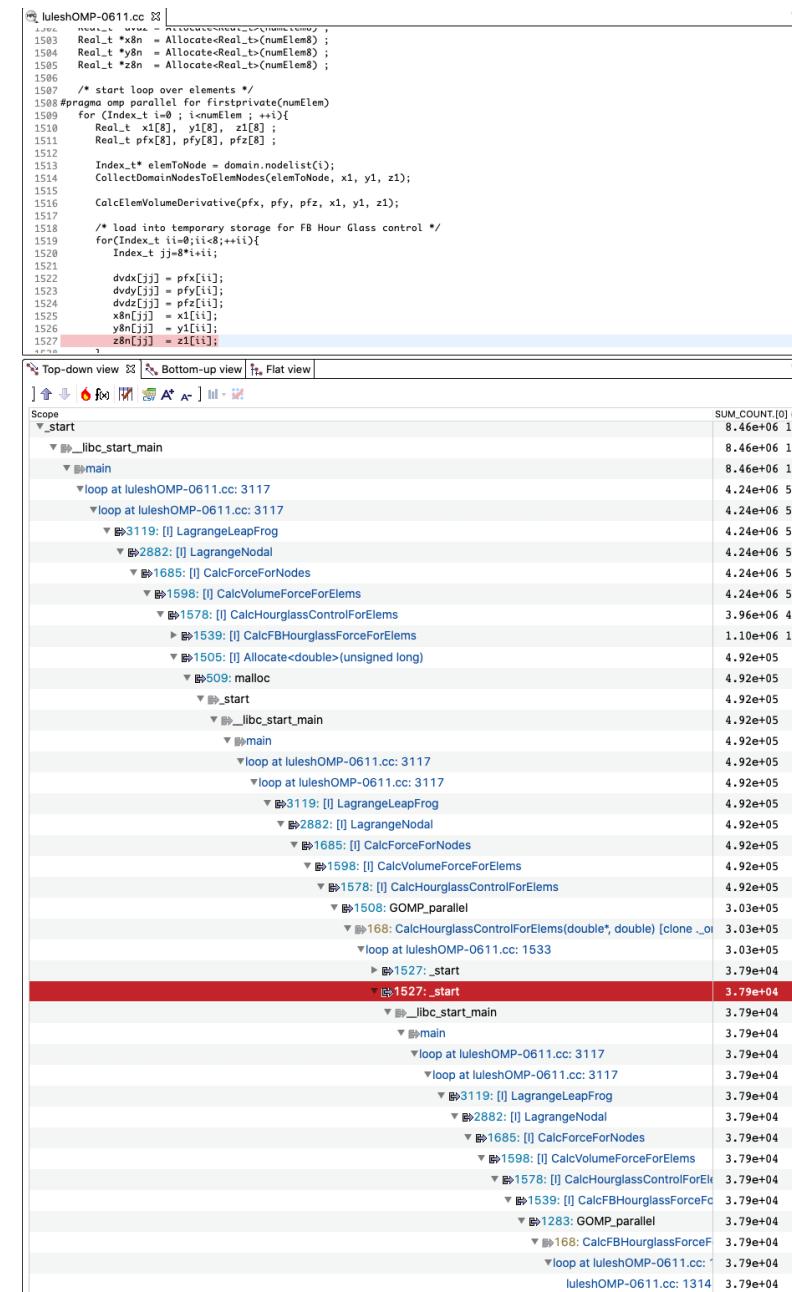
A DrCCTProf Client Tool: ARMREUSE

ARMREUSE

- Identifying temporal/spatial memory reuse pairs and computing reuse distances
- Obtaining rich insights: full call paths for use and reuse, data-centric attribution
- Providing intuitive analysis: GUI
- Implementing in an easy way: ~**500 lines** of code
- Guiding code optimization: LULESH, Sweep3D, E3SM ASME



LULESH: An LLNL Proxy Application



LULESH: An LLNL Proxy Application

1527 z8n[jj] = z1[ii];

Top-down view Bottom-up view Flat view

Scope

_start

↳ _libc_start_main

↳ main

↳ loop at luleshOMP-0611.cc: 3117

↳ loop at luleshOMP-0611.cc: 3117

↳ 3119: [I] LagrangeLeapFrog

↳ 2882: [I] LagrangeNodal

↳ 1685: [I] CalcForceForNodes

↳ 1598: [I] CalcVolumeForceForElems

↳ 1578: [I] CalcHourglassControlForElems

↳ 1539: [I] CalcFBHourglassForceForElems

↳ 1505: [I] Allocate<double>(unsigned long)

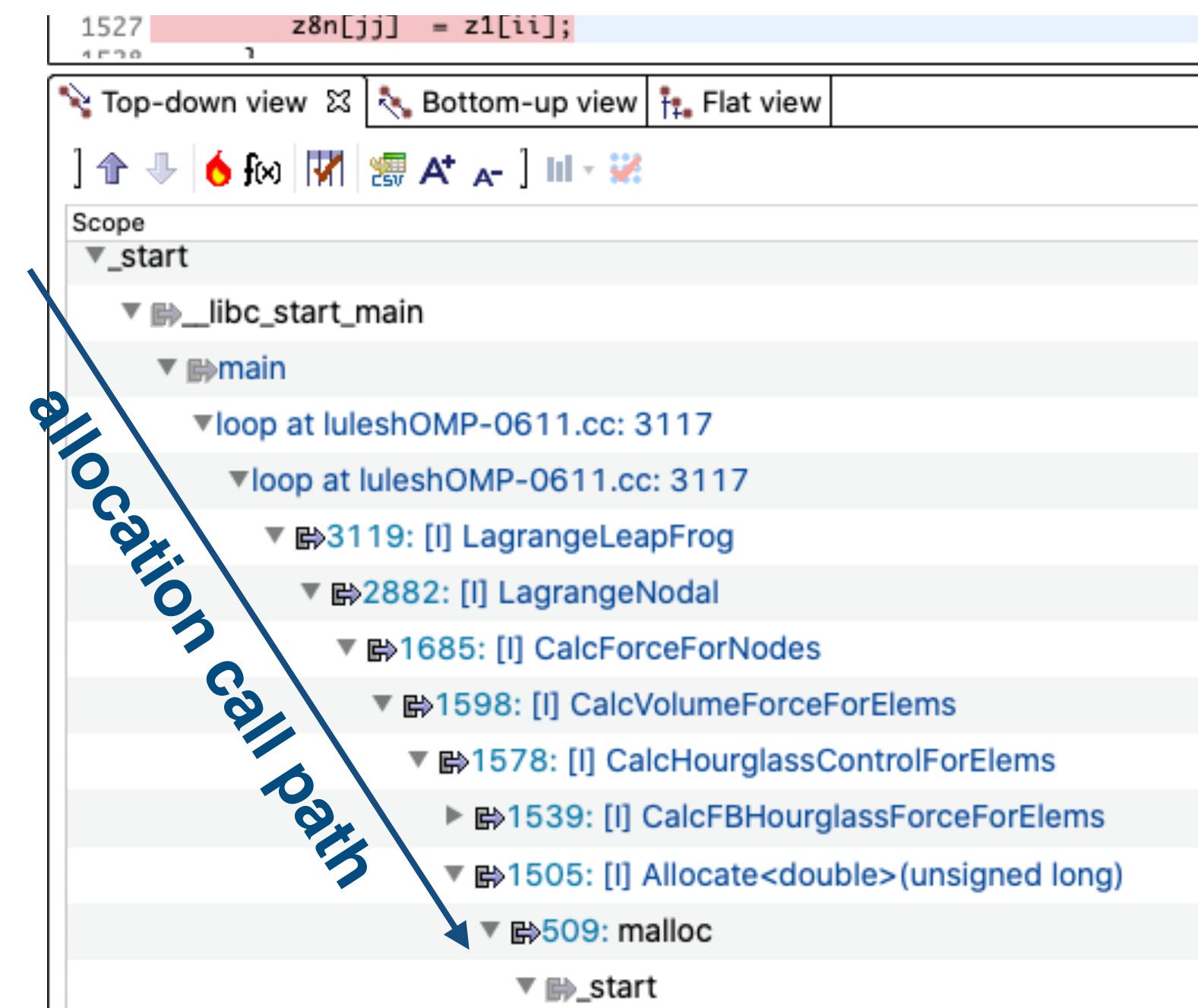
↳ 509: malloc

↳ _start

LULESH: An LLNL Proxy Application

Allocation Call Path

The top reuse pairs on ***z8n*** accounts for 3.6% of total temporal reuse.



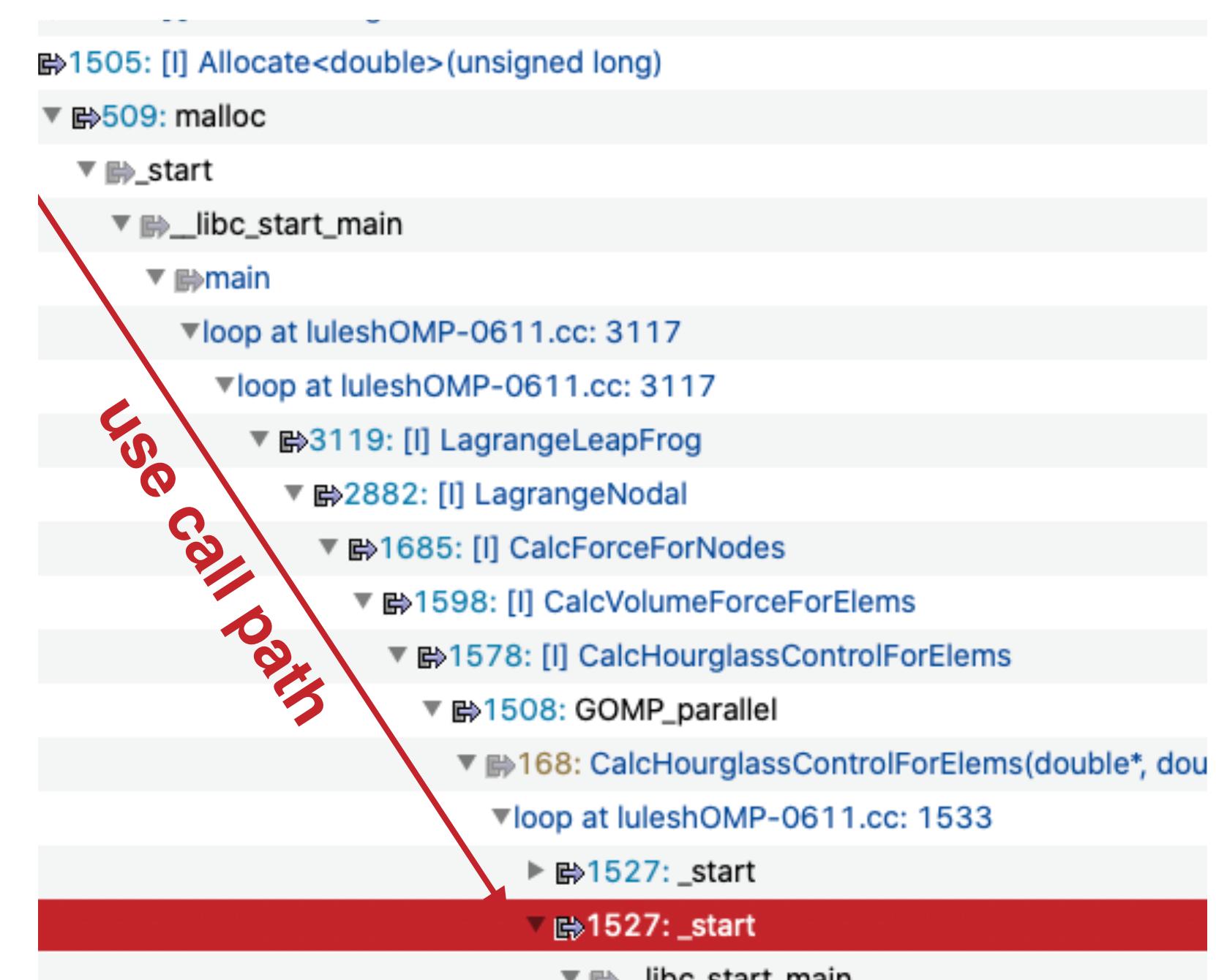
LULESH: An LLNL Proxy Application

Allocation Call Path

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The use is the memory access at line 1527 (in the loop at line 1509)



LULESH: An LLNL Proxy Application

Allocation Call Path

The top reuse pairs on **z8n** accounts for 3.6% of total temporal reuse.

Use Call Path

The use is the memory access at line 1527 (in the loop at line 1509)

Reuse Call Path

The reuse is the memory access at line 1314 (in the loop at line 1284)

loop at luleshOMP-0611.cc: 1533	3.03e+05	3.03e+05
▶ 1527: _start	3.79e+04	0.000000
▶ 1527: _start	3.79e+04	0.000000
▶ _libc_start_main	3.79e+04	0.000000
▶ main	3.79e+04	0.000000
▶ loop at luleshOMP-0611.cc: 3117	3.79e+04	0.000000
▶ loop at luleshOMP-0611.cc: 3117	3.79e+04	0.000000
▶ 3119: [I] LagrangeLeapFrog	3.79e+04	0.000000
▶ 2882: [I] LagrangeNodal	3.79e+04	0.000000
▶ 1685: [I] CalcForceForNodes	3.79e+04	0.000000
▶ 1598: [I] CalcVolumeForceForElems	3.79e+04	0.000000
▶ 1578: [I] CalcHourglassControlForEle	3.79e+04	0.000000
▶ 1539: [I] CalcFBHourglassForceFo	3.79e+04	0.000000
▶ 1283: GOMP_parallel	3.79e+04	0.000000
▶ 168: CalcFBHourglassForceF	3.79e+04	0.000000
▶ loop at luleshOMP-0611.cc: 1314	3.79e+04	0.000000
luleshOMP-0611.cc: 1314	3.79e+04	0.000000

reuse call path

LULESH: An LLNL Proxy Application

Allocation Call Path

The top reuse pairs on **z8n** accounts for 3.6% of total temporal reuse.

Use Call Path

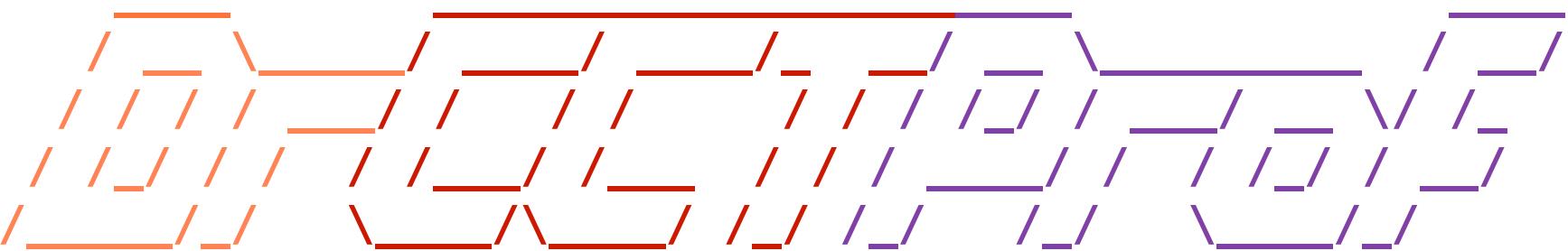
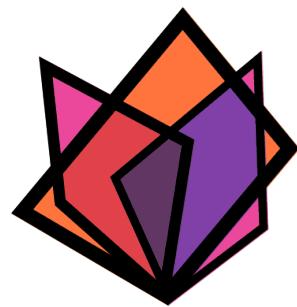
The use is the memory access at line 1527 (in the loop at line 1509)

Reuse Call Path

Hoist the two loops (line 1508 and 1283) into their least common ancestor in the call paths and fuse them

1.28x speedup





- Ubiquitous call path collection
- Attributing costs to data objects
- Merge attributions
- Evaluation
- Case study
- **Conclusions**

Conclusions

DrCCTProf

- A practical fine-grained call path profiler framework for ARM/x86 binaries
- Strong support for various analysis tools
- Moderate time and memory overheads
- Applicable to large-scale executions

Open source with MIT license

<https://github.com/Xuhpclab/DrCCTProf>

On-Going Work (Will Release Soon)

Supporting various binaries

Python

Go

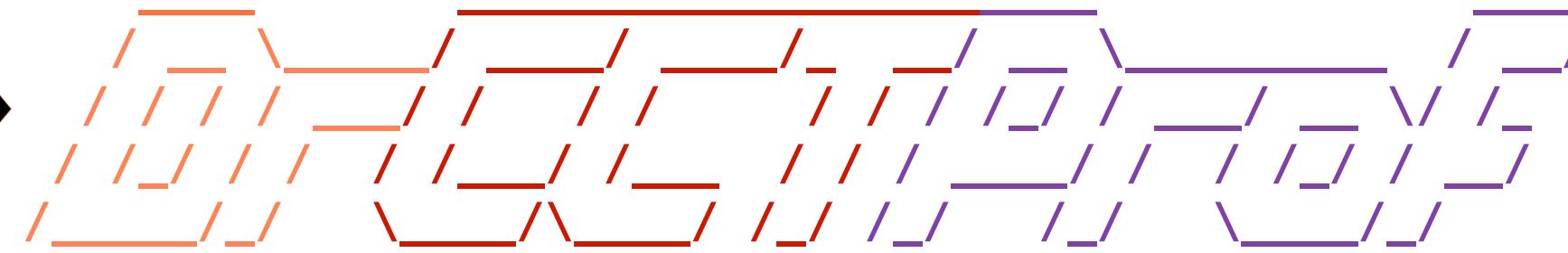
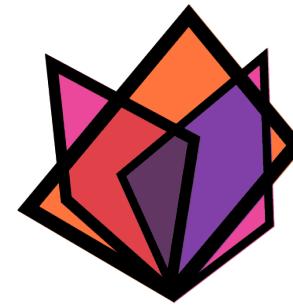
Rust

...

Supporting accelerators

X86+GPU, ARM+GPU

Q&A



<https://github.com/Xuhpclab/DrCCTProf>

Hands-on Lab

Machine access

```
ssh drcctprof1@rocco.cs.wm.edu -p 1111
```

Password: *drcctprofest1#

Create your own directory under this account

```
mkdir XX
```

```
cd XX
```

Download and build DrCCTProf

```
git clone --recurse https://github.com/Xuhpclab/drcctprof_tutorial.git
```

```
cd drcctprof_tutorial
```

```
./build.sh
```

Develop the client

[src/client.cpp](#): 42 - 55