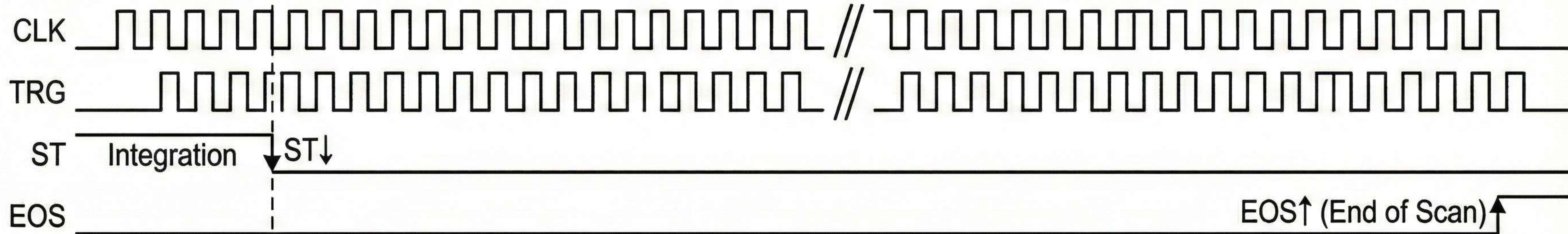
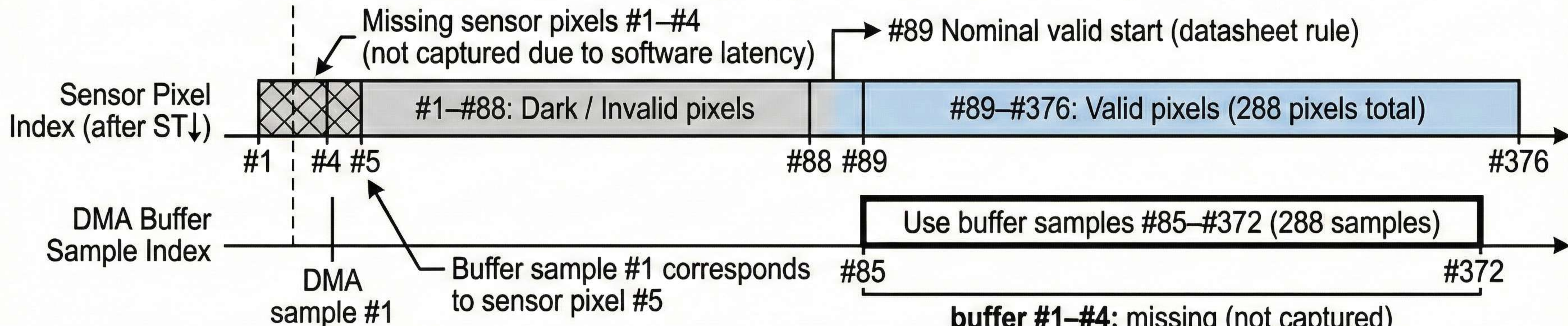


# Simplified Timing Diagram



## Pixel Index Mapping (Sensor vs. DMA Buffer)



DMA starts after ST↓ due to software latency

CLK = 5 MHz

DMA length N = 387 samples/frame (fixed-length acquisition)

EOS is used for timing verification; DMA is terminated by fixed length in current implementation.