

XR/17032 Processor Specification

Draft 6

March 25, 2023

SPECIFICATION COPYRIGHT

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REGISTERS

MEMORY

The memory layout of the address space of the XR/17032 architecture are discussed here.

TODO:

- finish writing this section

OPCODES

Example - EX imm16

nnnn nnnn nnnn nnnn ssss sddd dd11 1100

This is where the full English explanation of an opcode goes.

Implementation

// put the exact code of how its implemented in xremu here. md.py adds backticks.

Notes

Any auxiliary notes (such as referring to other opcodes or document sections) goes here.

This is the only optional opcode toml key.

Example 2 - EX2 imm16

nnnn nnnn nnnn nnnn ssss sddd dd11 1111

This is where the full English explanation of an opcode goes.

Implementation

// put the exact code of how its implemented in xremu here. md.py adds backticks.

Notes

Any auxiliary notes (such as referring to other opcodes or document sections) goes here.

This is the only optional opcode toml key.

