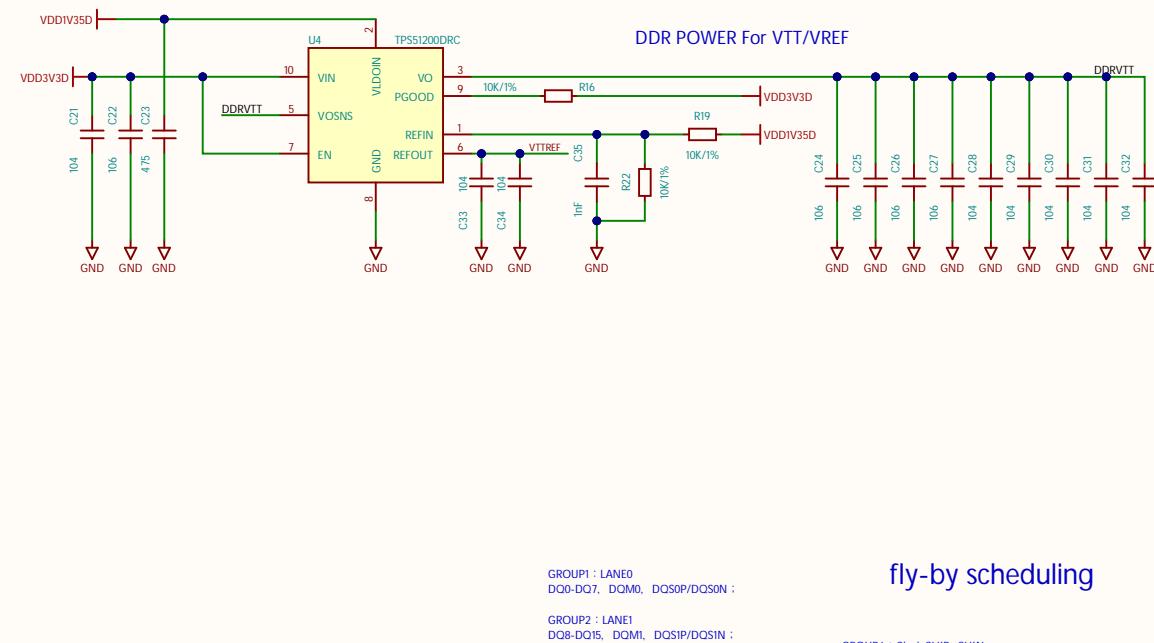
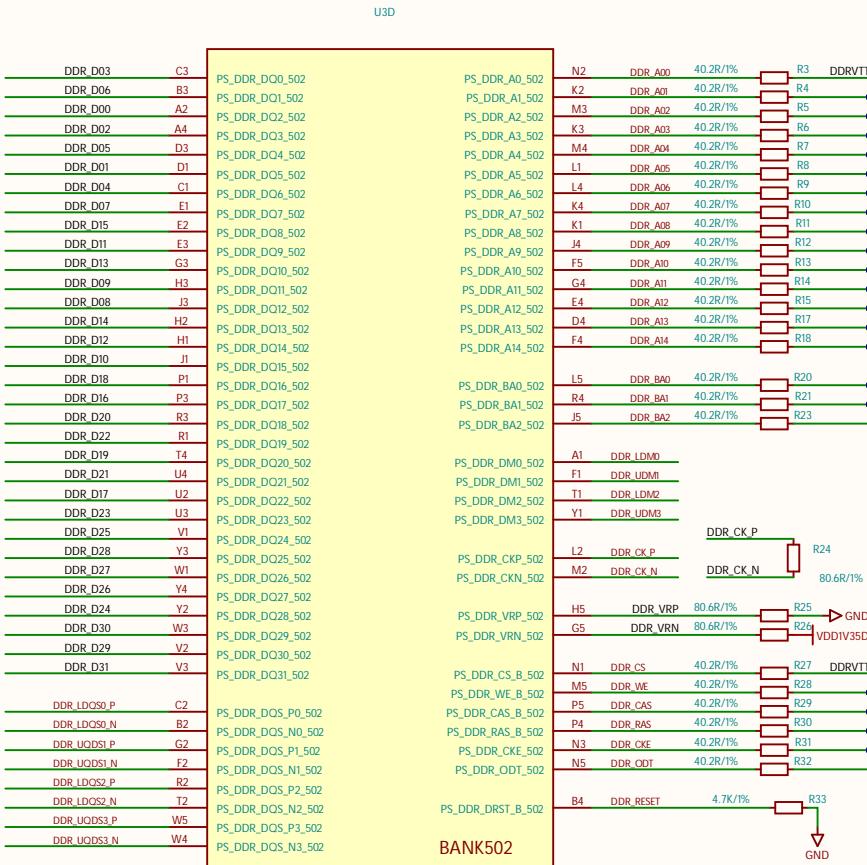


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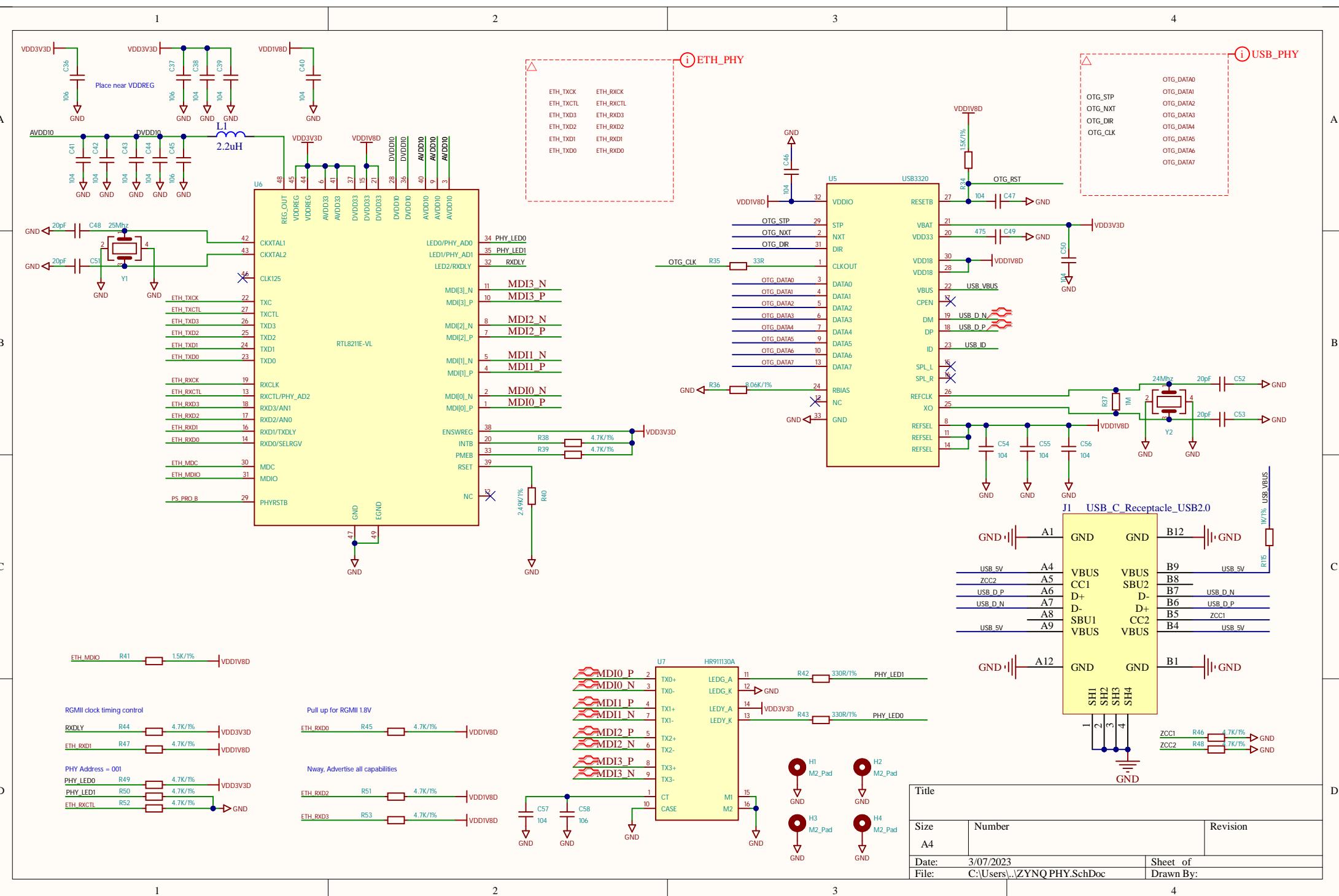
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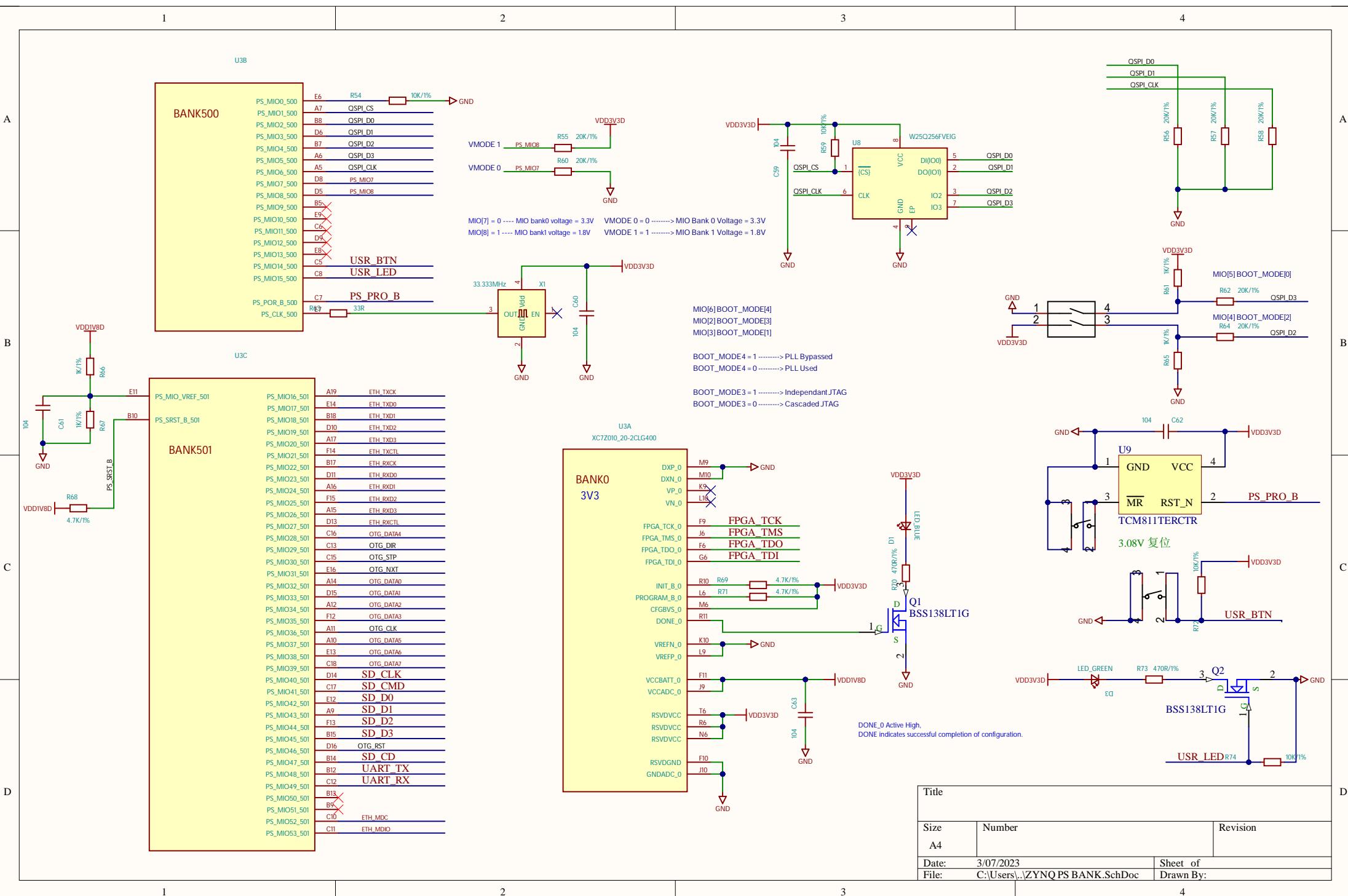
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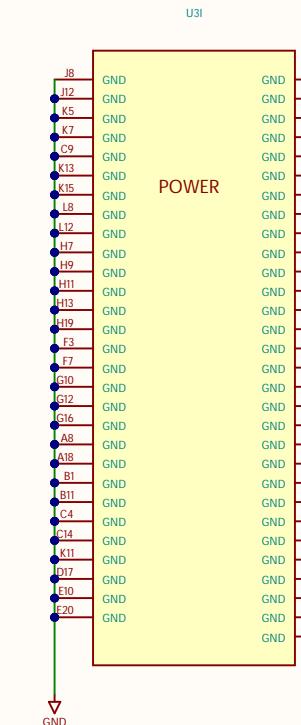
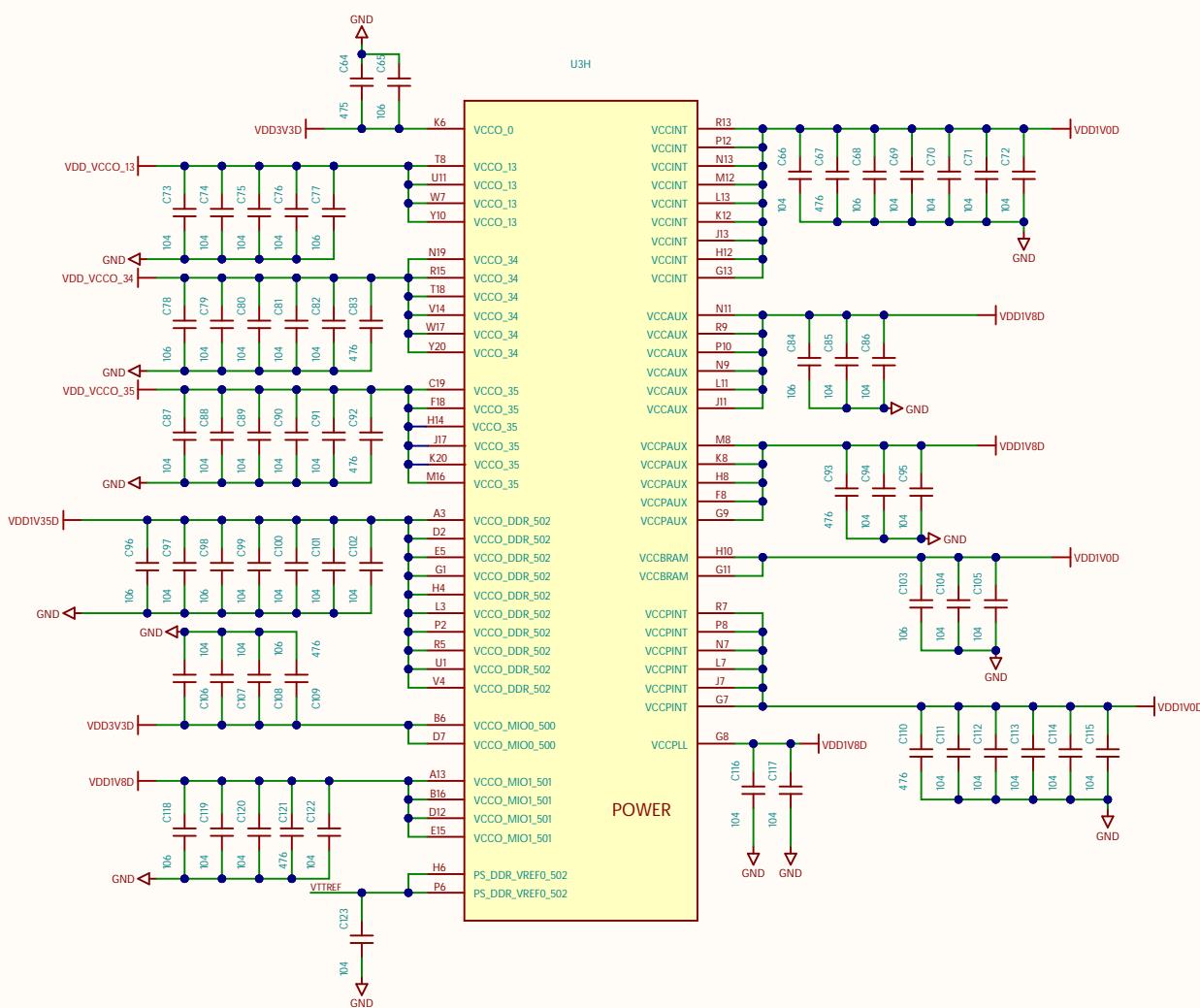


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A

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C

D

A

B

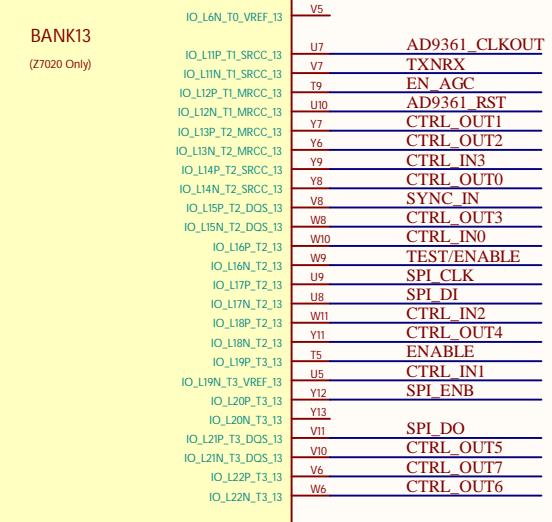
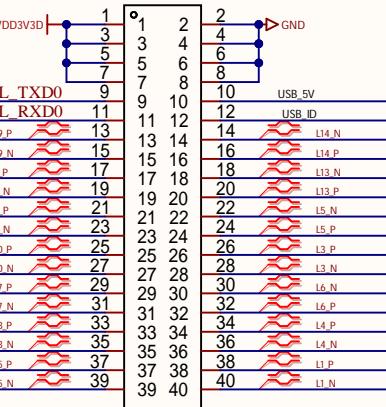
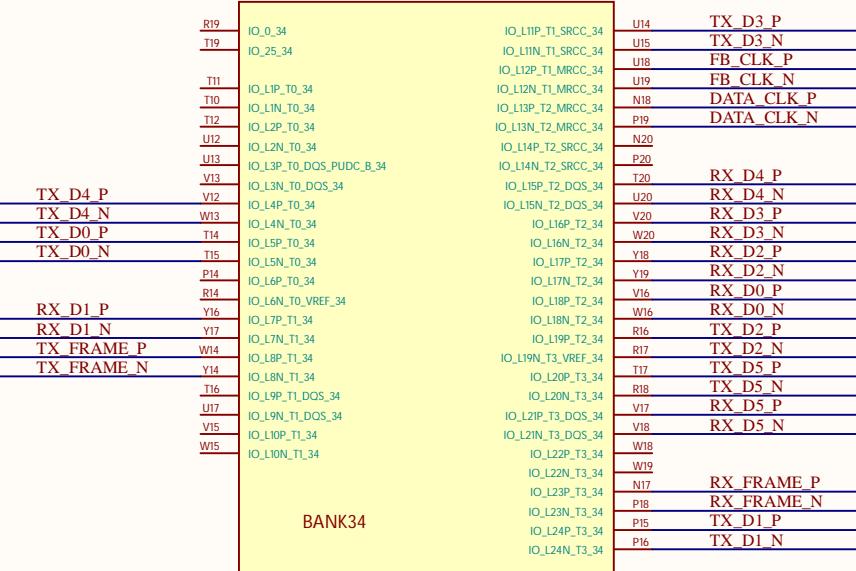
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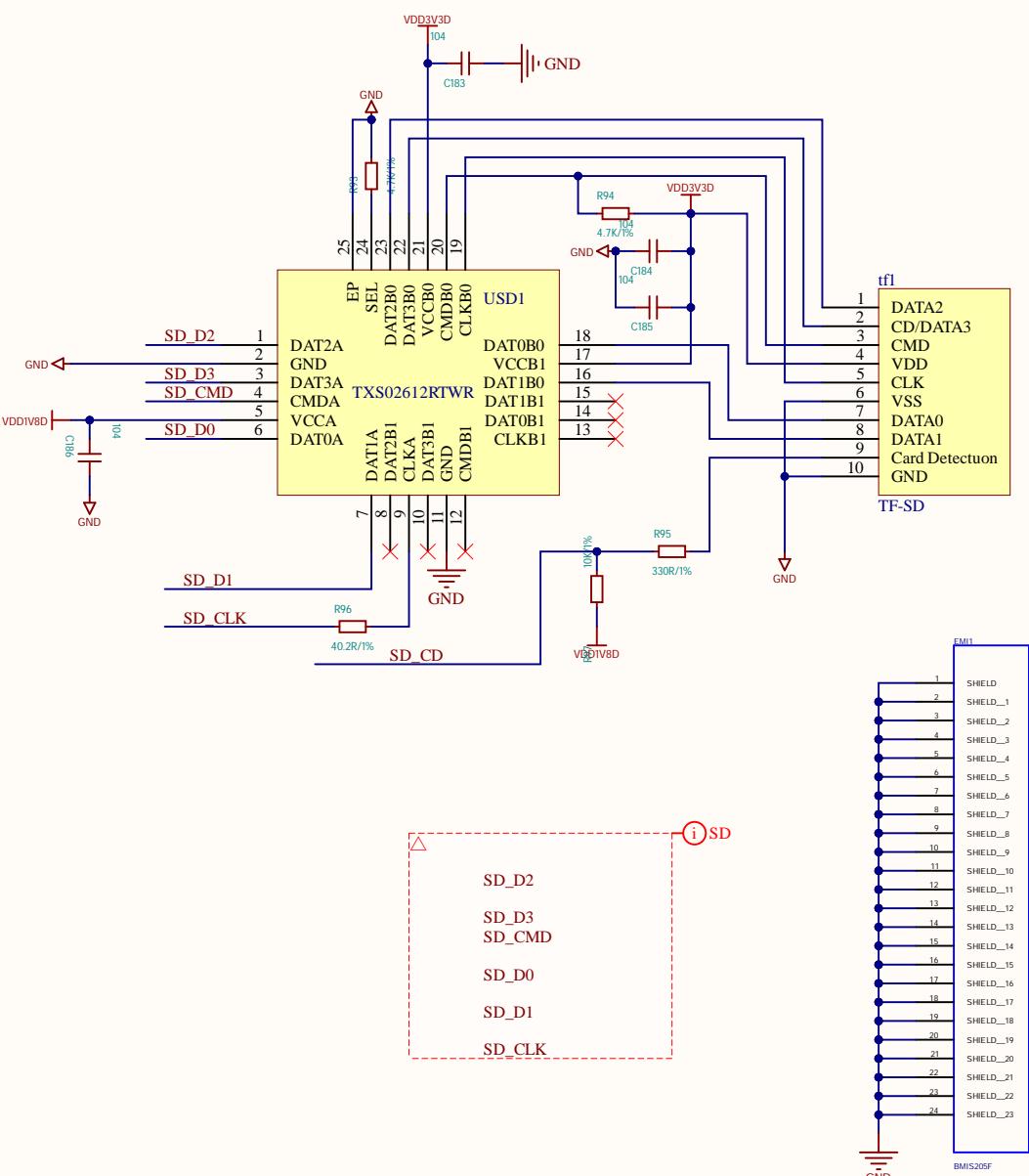
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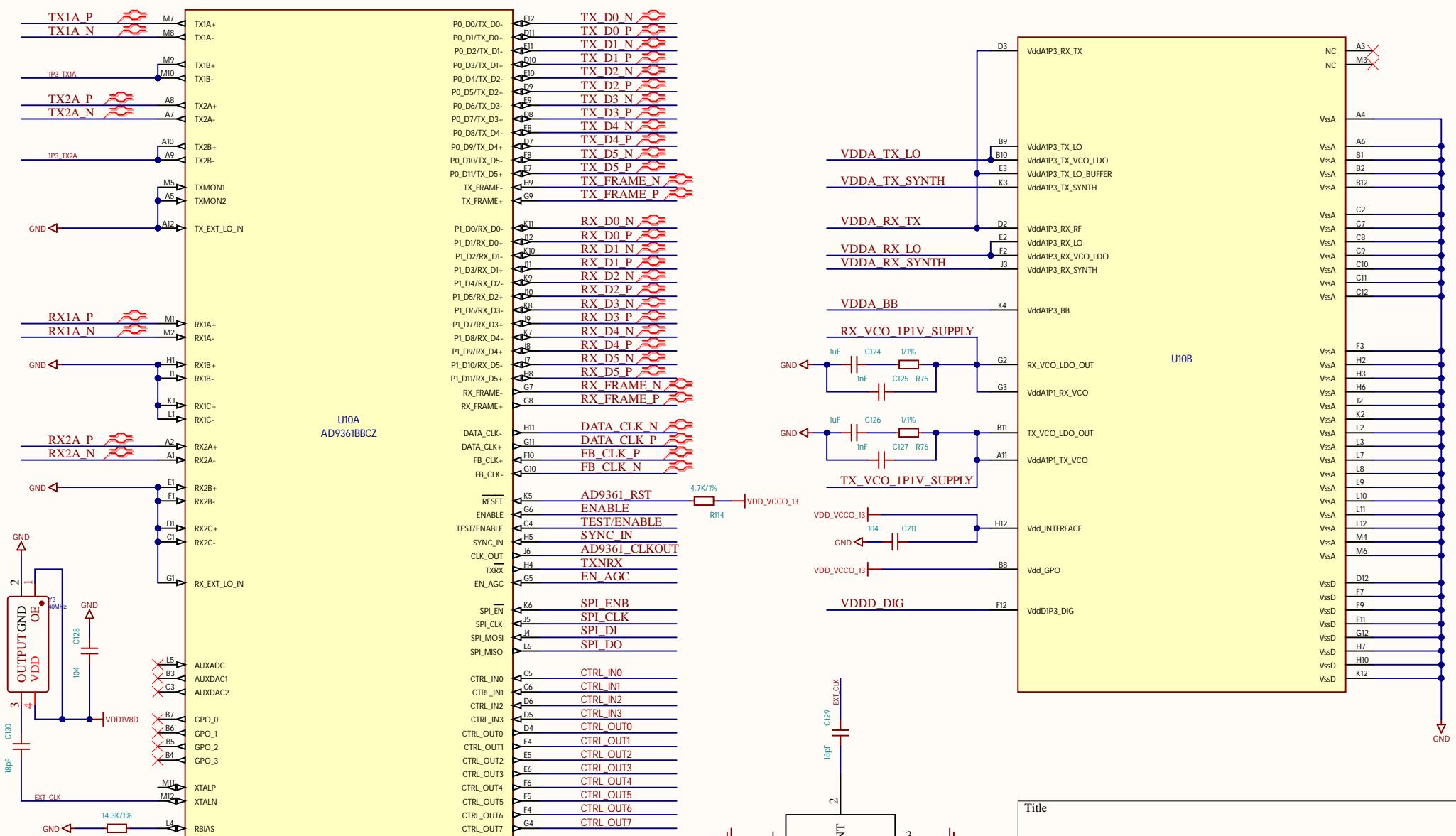
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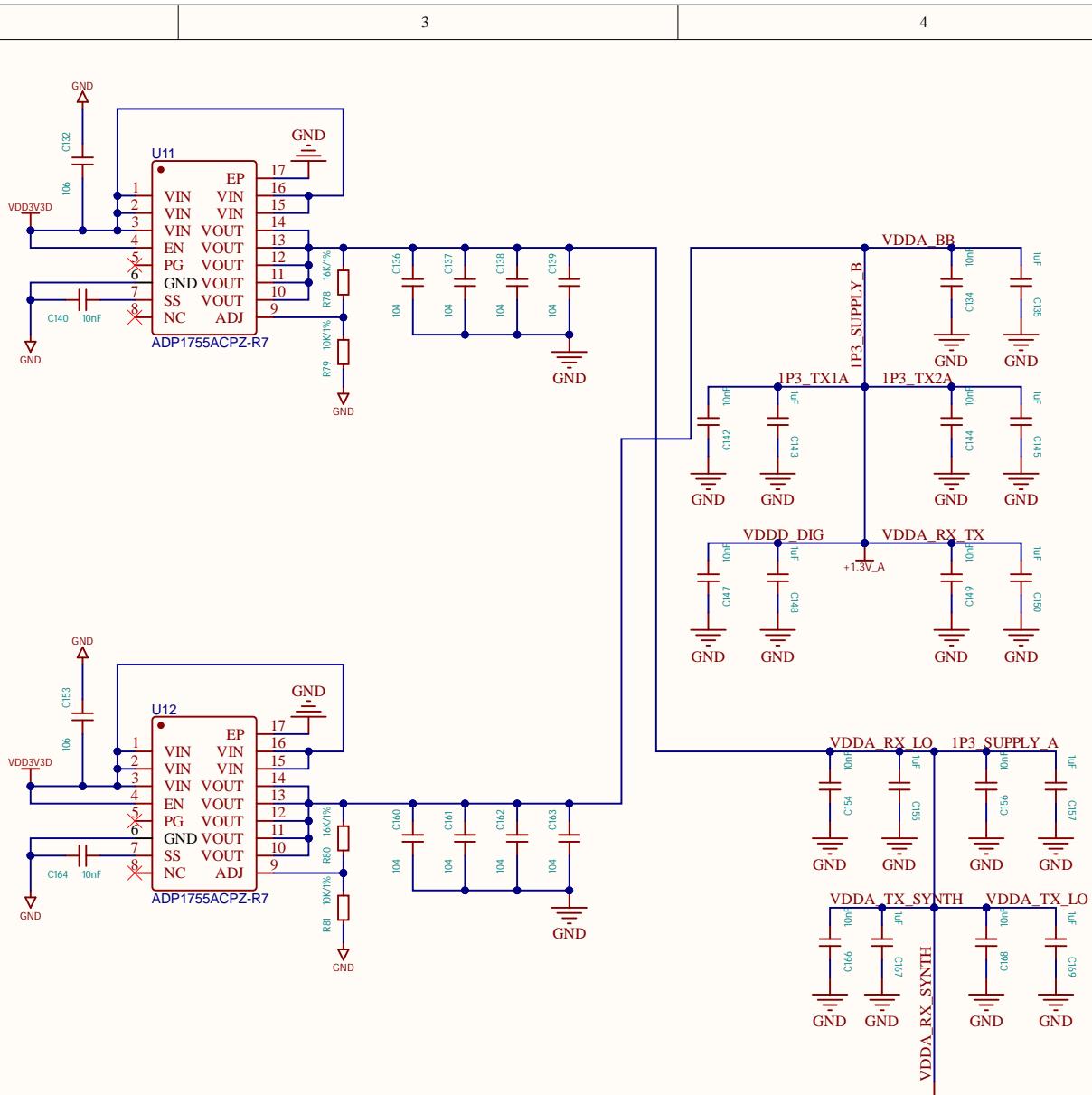
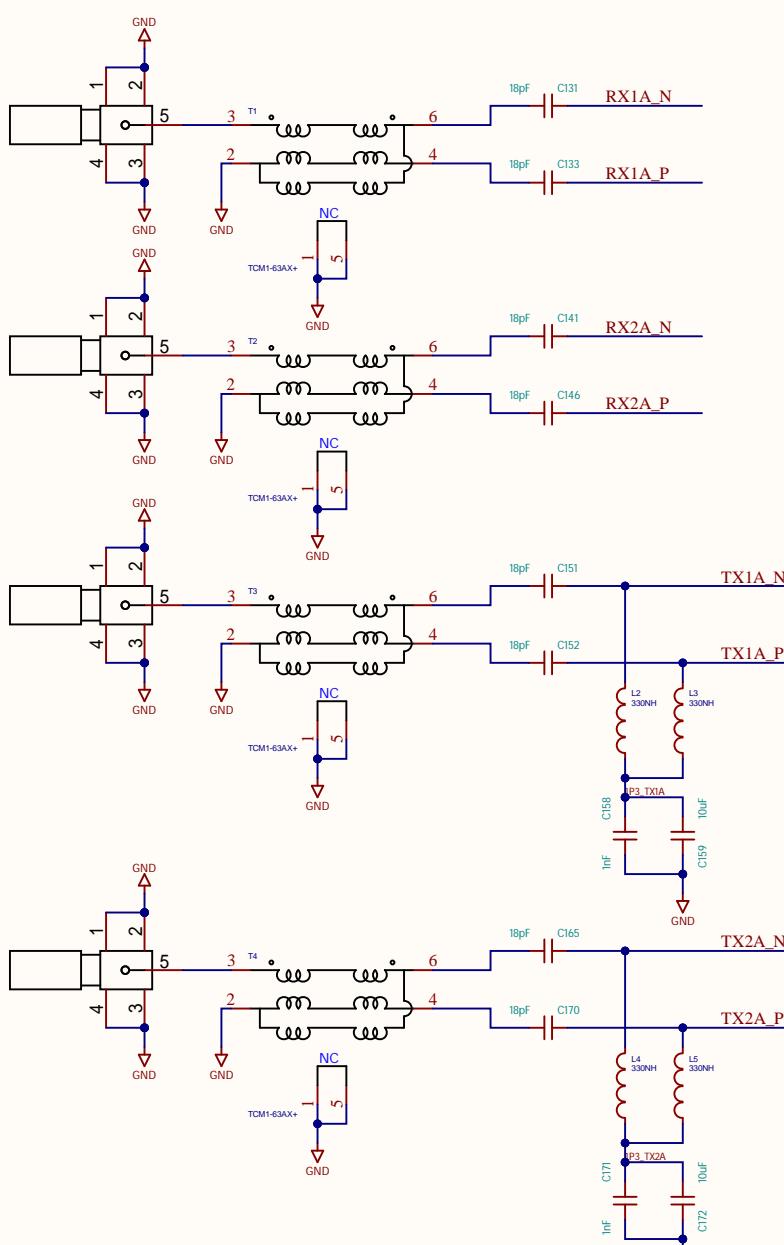


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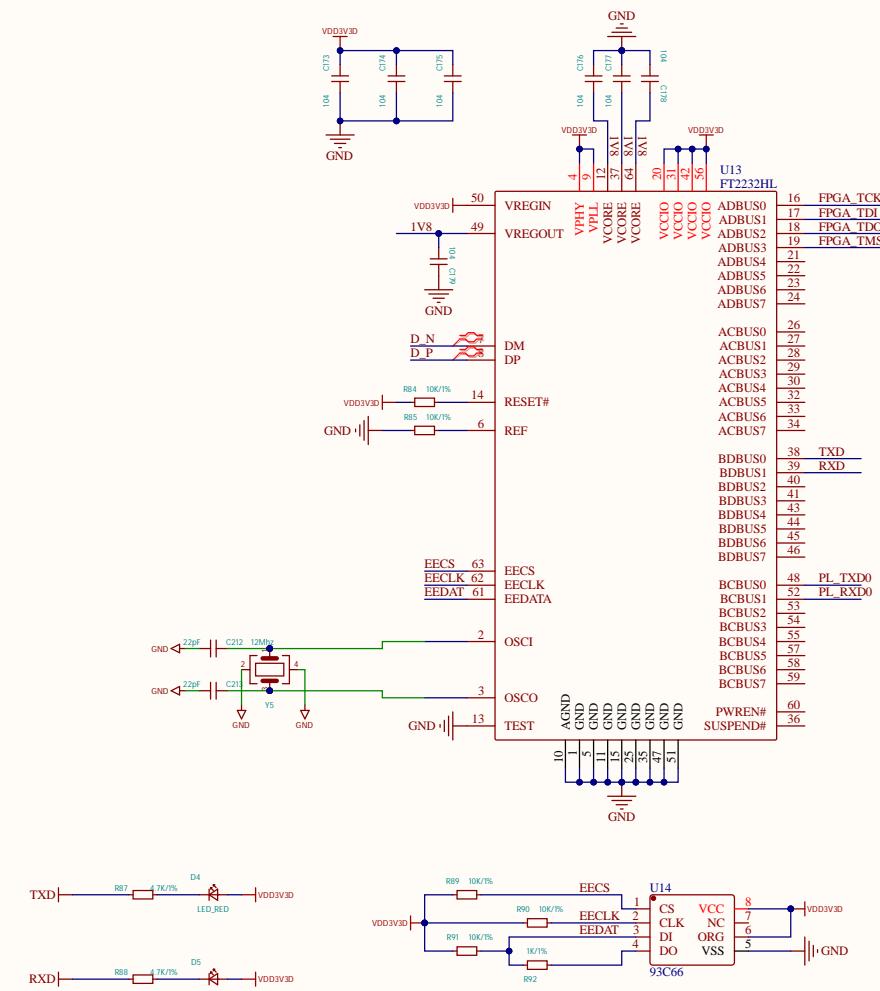
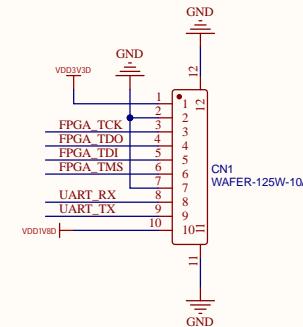
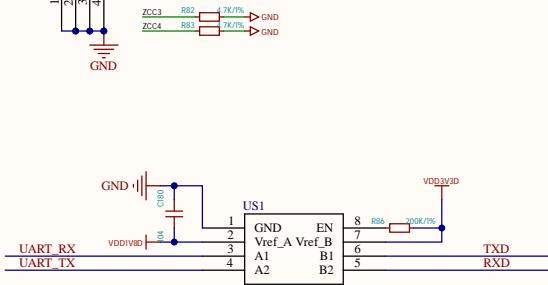
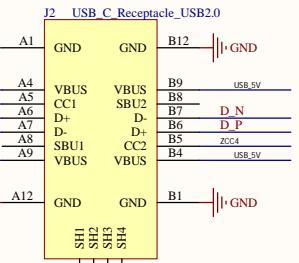


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Title		
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DIR:
低为in,B->A
高为out,A->B

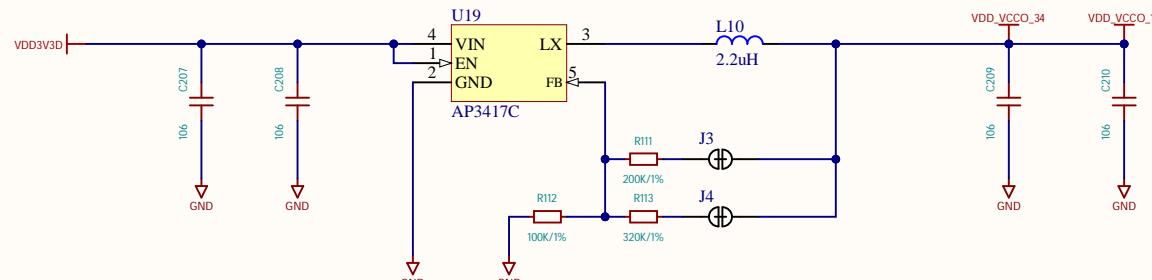
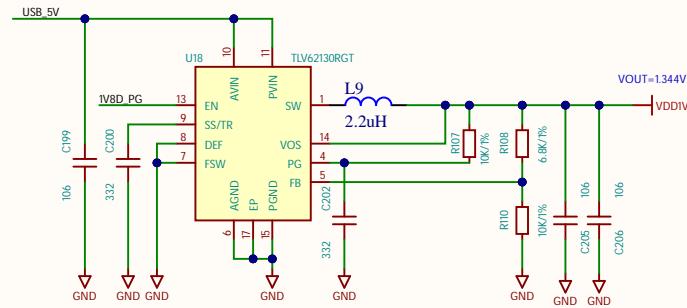
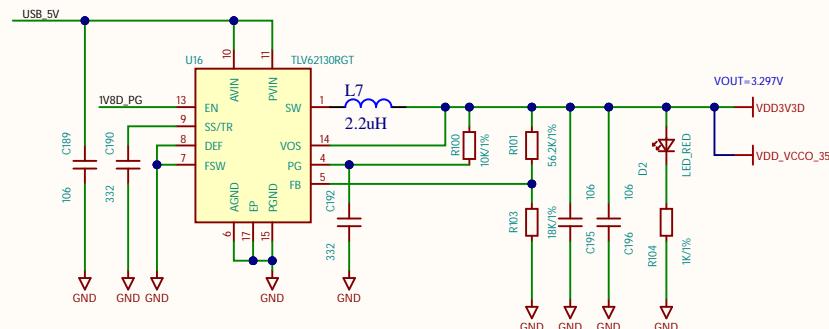
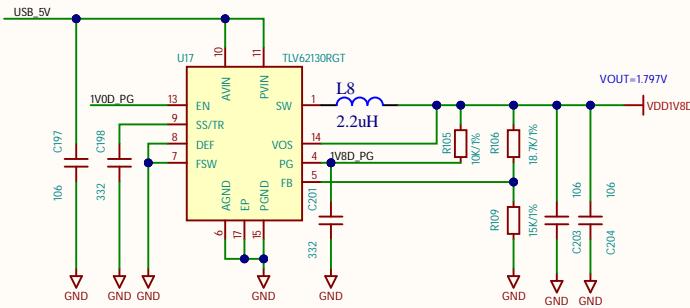
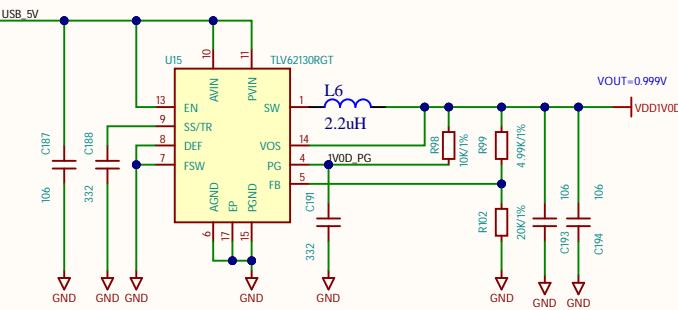
1 TLV62130RGT EN High Level Input Threshold Voltage=0.9V

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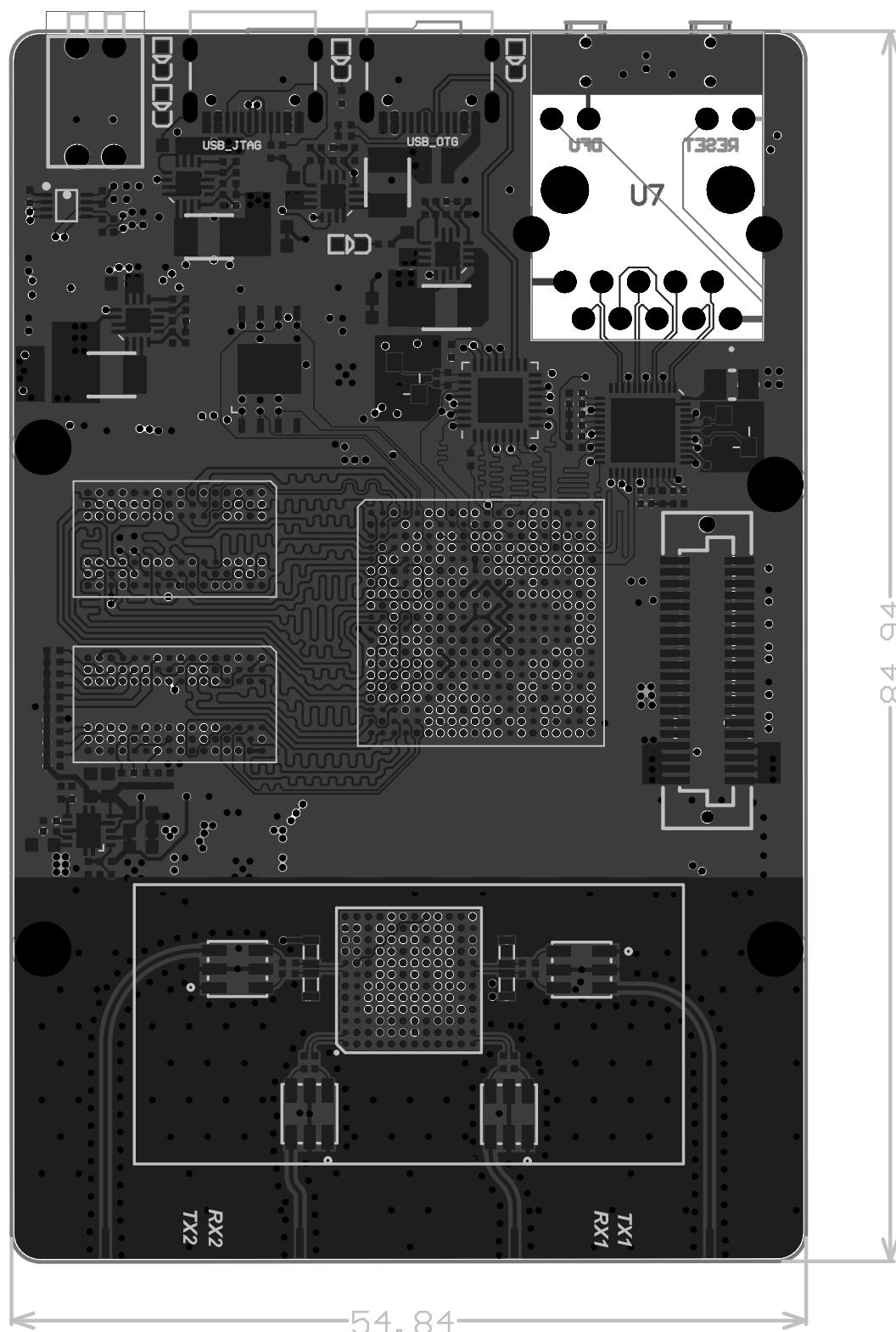
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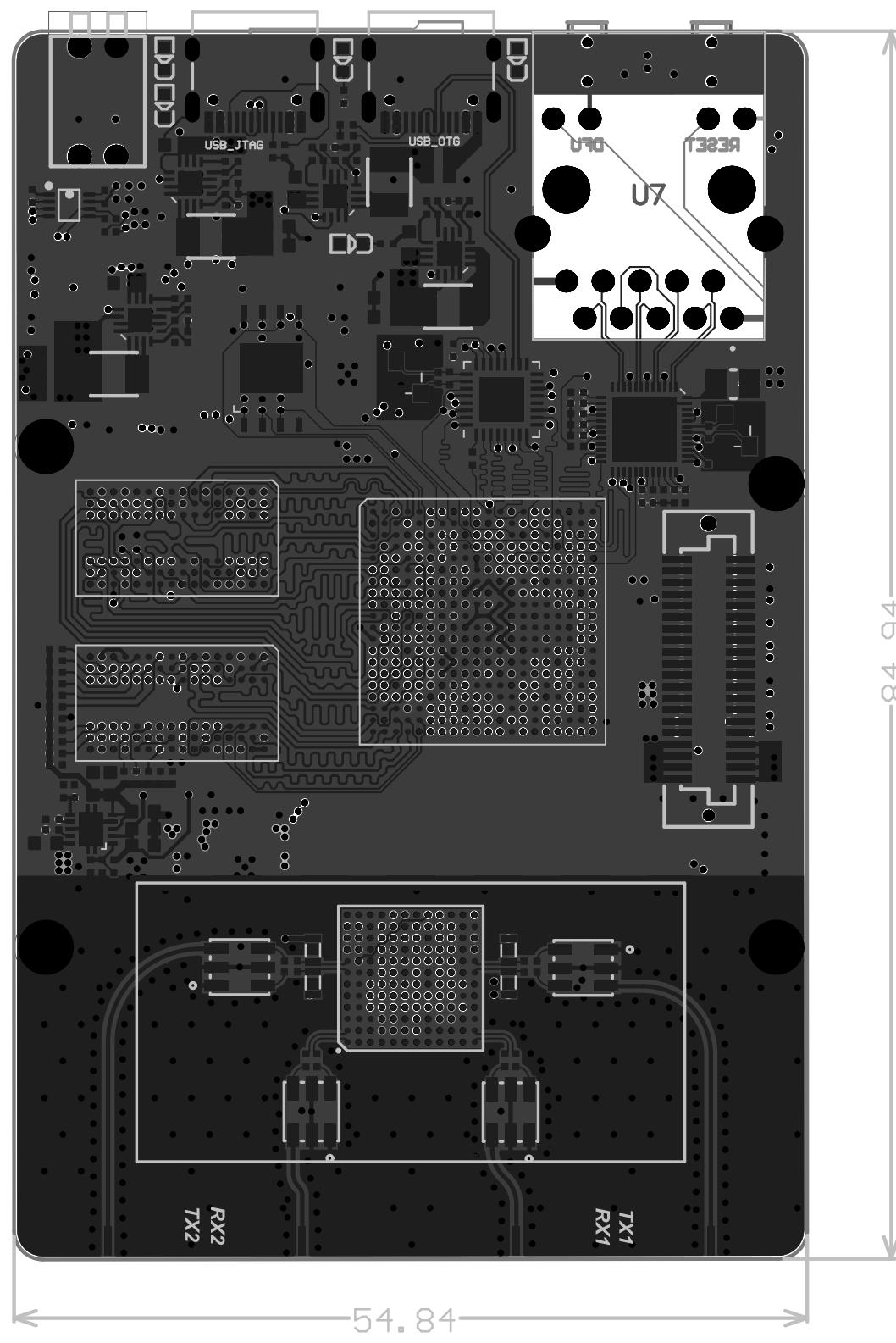
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1.0V>1.8V>1.35V&3V3V



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C67

C77

C83

C84

C92

C93

C109

C110

C121

