1—29---18

X=y+z

Storage of y+z to put in the storage of x

Data

Memory  
I/O register associated with address value when the address is access by the cup it is refer to as io device

Processor register

Contains a collections of cells (memory) that made up processor register

Some of it are addressable

Building a processor

1 Specification

(what it does, when is an interrupter happen?

What is the specification

2 And or not program (write your program

Ex: 0,1, and the three operators and or not

3 An interconnection of transistors using processor registers and memory

Variables will denote processor register and memory (array of bytes, word, etc)