International

Rectifier

### **SMPS MOSFET**

PD-94266

## IRLR3714 IRLU3714

HEXFET® Power MOSFET

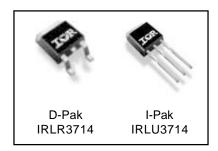
#### **Applications**

- High Frequency Isolated DC-DC Converters with Synchronous Rectification for Telecom and Industrial Use
- High Frequency Buck Converters for Computer Processor Power

#### **Benefits**

- Ultra-Low Gate Impedance
- $\bullet~$  Very Low  $R_{DS(on)}$  at 4.5V  $V_{GS}$
- Fully Characterized Avalanche Voltage and Current

V <sub>DSS</sub>	R <sub>DS(on)</sub> max	I <sub>D</sub>
20V	<b>20m</b> $Ω$	36A



#### **Absolute Maximum Ratings**

Symbol	Parameter	Max.	Units
$V_{DS}$	Drain-Source Voltage	20	V
$V_{GS}$	Gate-to-Source Voltage	± 20	V
I <sub>D</sub> @ T <sub>C</sub> = 25°C	Continuous Drain Current, V <sub>GS</sub> @ 10V	36 ⑤	
I <sub>D</sub> @ T <sub>C</sub> = 70°C	Continuous Drain Current, V <sub>GS</sub> @ 10V	31	Α
I <sub>DM</sub>	Pulsed Drain Current①	140	
P <sub>D</sub> @T <sub>C</sub> = 25°C	Maximum Power Dissipation 3	47	W
P <sub>D</sub> @T <sub>C</sub> = 70°C	Maximum Power Dissipation 3	33	W
	Linear Derating Factor	0.31	W/°C
$T_J$ , $T_{STG}$	Junction and Storage Temperature Range	-55 to + 175	°C

#### **Thermal Resistance**

	Parameter	Тур.	Max.	Units
$R_{\theta JC}$	Junction-to-Case		3.2	
$R_{\theta JA}$	Junction-to-Ambient		50	°C/W
$R_{\theta JA}$	Junction-to-Ambient (PCB mount) 4		110	

Notes ① through ⑤ are on page 10 www.irf.com

#### Static @ T<sub>J</sub> = 25°C (unless otherwise specified)

	Parameter	Min.	Тур.	Max.	Units	Conditions
V <sub>(BR)DSS</sub>	Drain-to-Source Breakdown Voltage	20			V	$V_{GS} = 0V, I_D = 250\mu A$
$\Delta V_{(BR)DSS}/\Delta T_{J}$	Breakdown Voltage Temp. Coefficient		0.022		V/°C	Reference to 25°C, I <sub>D</sub> = 1mA
R <sub>DS(on)</sub>	Static Drain-to-Source On-Resistance		15	20	mΩ	$V_{GS} = 10V, I_D = 18A$ ③
1103(01)	State Brain to Course On Resistance		21	28	11122	$V_{GS} = 4.5V, I_{D} = 14A$ ③
V <sub>GS(th)</sub>	Gate Threshold Voltage	1.0		3.0	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$
I <sub>DSS</sub>	Drain-to-Source Leakage Current			20	uА	$V_{DS} = 16V, V_{GS} = 0V$
200				100	μΛ	$V_{DS} = 16V, V_{GS} = 0V, T_{J} = 125$ °C
I <sub>GSS</sub>	Gate-to-Source Forward Leakage			200	n 1	V <sub>GS</sub> = 16V
.000	Gate-to-Source Reverse Leakage			-200	nA	$V_{GS} = -16V$

#### Dynamic @ T<sub>J</sub> = 25°C (unless otherwise specified)

Symbol	Parameter	Min.	Тур.	Max.	Units	Conditions
9 <sub>fs</sub>	Forward Transconductance	17			S	V <sub>DS</sub> = 10V, I <sub>D</sub> = 14A
Qg	Total Gate Charge		6.5	9.7		I <sub>D</sub> = 14A
Q <sub>gs</sub>	Gate-to-Source Charge		1.8		nC	V <sub>DS</sub> = 10V
Q <sub>gd</sub>	Gate-to-Drain ("Miller") Charge		2.9			$V_{GS} = 4.5V$
Q <sub>oss</sub>	Output Gate Charge		7.1			$V_{GS} = 0V, V_{DS} = 10V$
t <sub>d(on)</sub>	Turn-On Delay Time		8.7			$V_{DD} = 10V$
t <sub>r</sub>	Rise Time		78		ns	I <sub>D</sub> = 14A
t <sub>d(off)</sub>	Turn-Off Delay Time		10		] ""	$R_G = 1.8\Omega$
t <sub>f</sub>	Fall Time		4.5			V <sub>GS</sub> = 4.5V ③
C <sub>iss</sub>	Input Capacitance		670			V <sub>GS</sub> = 0V
C <sub>oss</sub>	Output Capacitance		470			V <sub>DS</sub> = 10V
C <sub>rss</sub>	Reverse Transfer Capacitance		68		pF	f = 1.0 MHz

#### **Avalanche Characteristics**

Symbol	Parameter	Тур.	Max.	Units
E <sub>AS</sub>	Single Pulse Avalanche Energy@		72	mJ
I <sub>AR</sub>	Avalanche Current①		14	Α

#### **Diode Characteristics**

Symbol	Parameter	Min.	Тур.	Max.	Units	Conditions	
I <sub>S</sub>	Continuous Source Current		36 <sup>⑤</sup>			MOSFET symbol	
	(Body Diode)		300		A	showing the	
I <sub>SM</sub>	Pulsed Source Current		140		^	integral reverse	
	(Body Diode) ①					p-n junction diode.	
$V_{SD}$	Diode Forward Voltage			1.3	V	$T_J = 25$ °C, $I_S = 18A$ , $V_{GS} = 0V$ ③	
<b>√</b> 3D	Diode i diward voltage		0.88			$T_J = 125$ °C, $I_S = 18A$ , $V_{GS} = 0V$ ③	
t <sub>rr</sub>	Reverse Recovery Time		35	53	ns	$T_J = 25$ °C, $I_F = 18A$ , $V_R = 10V$	
Q <sub>rr</sub>	Reverse Recovery Charge		34	51	nC	di/dt = 100A/µs ③	
t <sub>rr</sub>	Reverse Recovery Time		35	53	ns	$T_J = 125$ °C, $I_F = 18A$ , $V_R = 10V$	
Q <sub>rr</sub>	Reverse Recovery Charge		35	53	nC	di/dt = 100A/µs ③	

# International Rectifier

### IRLR3714/IRLU3714

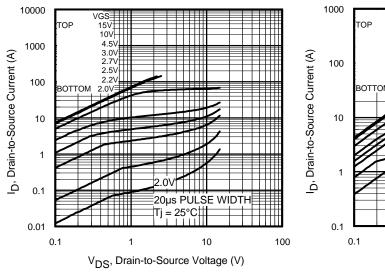


Fig 1. Typical Output Characteristics

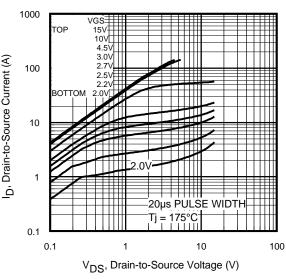


Fig 2. Typical Output Characteristics

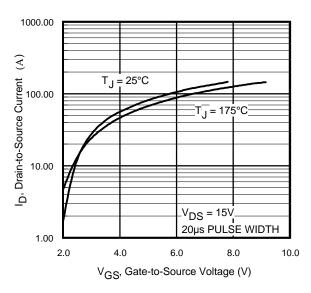
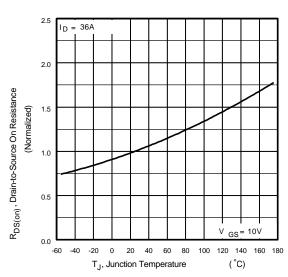
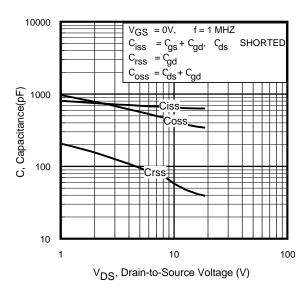


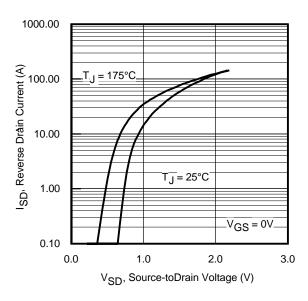
Fig 3. Typical Transfer Characteristics



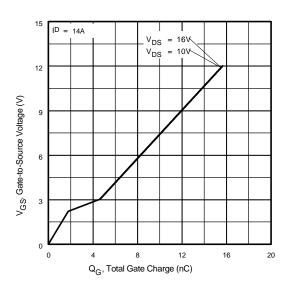
**Fig 4.** Normalized On-Resistance Vs. Temperature



**Fig 5.** Typical Capacitance Vs. Drain-to-Source Voltage



**Fig 7.** Typical Source-Drain Diode Forward Voltage



**Fig 6.** Typical Gate Charge Vs. Gate-to-Source Voltage

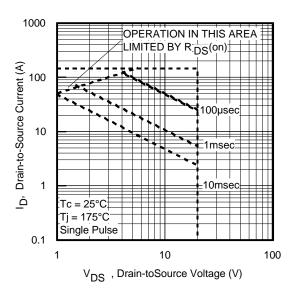
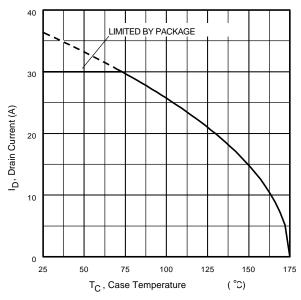


Fig 8. Maximum Safe Operating Area

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### IRLR3714/IRLU3714



**Fig 9.** Maximum Drain Current Vs. Case Temperature

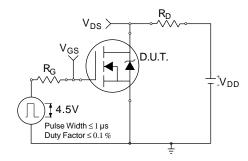


Fig 10a. Switching Time Test Circuit

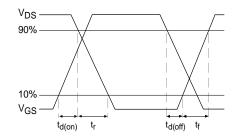


Fig 10b. Switching Time Waveforms

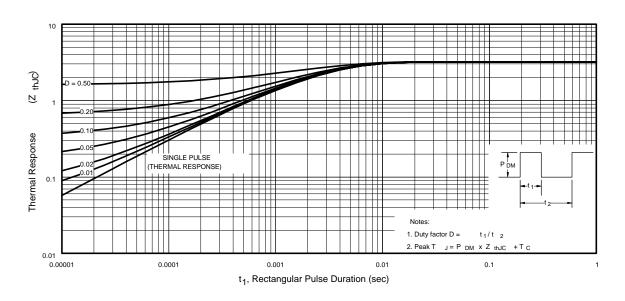


Fig 11. Maximum Effective Transient Thermal Impedance, Junction-to-Case

## International TOR Rectifier

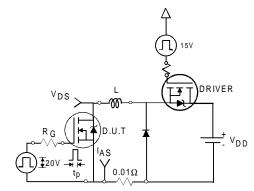


Fig 12a. Unclamped Inductive Test Circuit

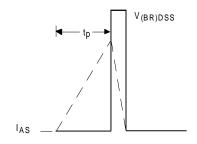
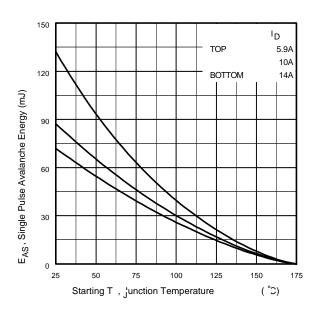


Fig 12b. Unclamped Inductive Waveforms



**Fig 12c.** Maximum Avalanche Energy Vs. Drain Current

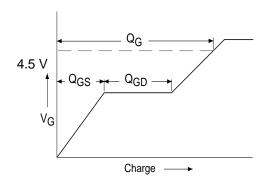


Fig 13a. Basic Gate Charge Waveform

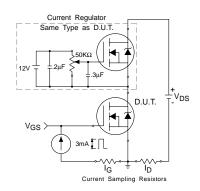
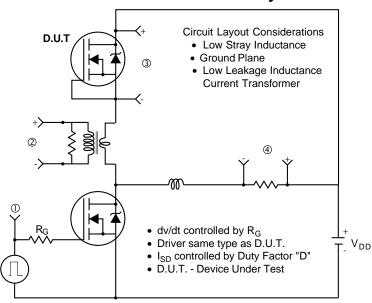


Fig 13b. Gate Charge Test Circuit

#### Peak Diode Recovery dv/dt Test Circuit



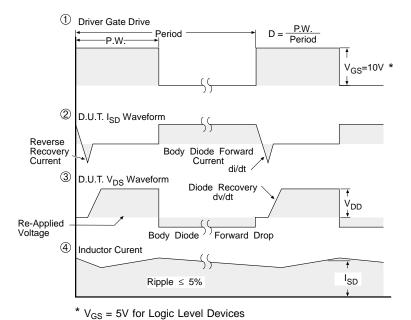


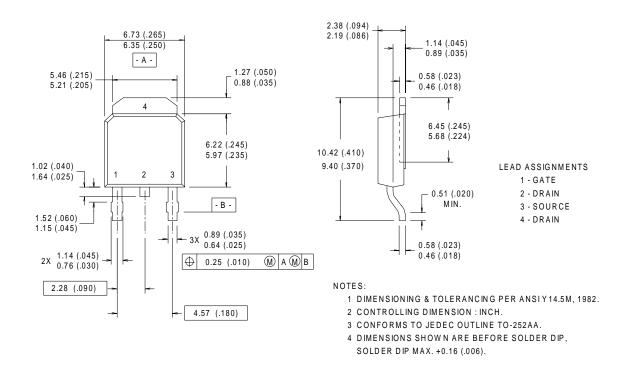
Fig 14. For N-Channel HEXFET® Power MOSFETs

International

TOR Rectifier

#### D-Pak (TO-252AA) Package Outline

Dimensions are shown in millimeters (inches)



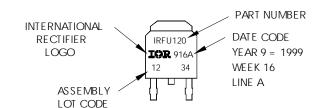
### D-Pak (TO-252AA) Part Marking Information

EXAMPLE: THIS IS AN IRFR120

WITH ASSEMBLY LOT CODE 1234

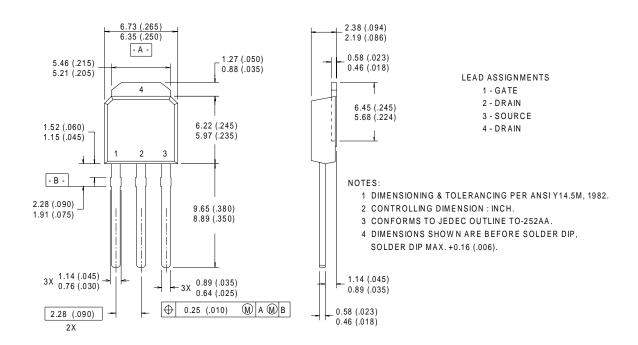
ASSEMBLED ON WW 16, 1999

IN THE ASSEMBLY LINE "A"



#### I-Pak (TO-251AA) Package Outline

Dimensions are shown in millimeters (inches)



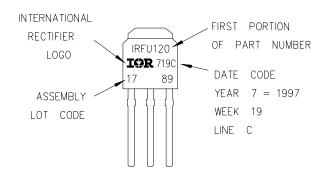
#### I-Pak (TO-251AA) Part Marking Information

EXAMPLE: THIS IS AN IRFU120

LOT CODE 1789

ASSEMBLED ON WW 19, 1997

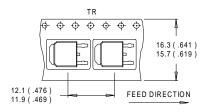
IN THE ASSEMBLY LINE "C"

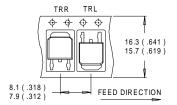


International IOR Rectifier

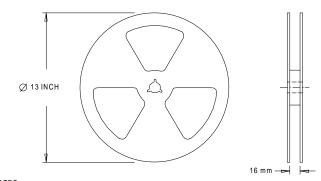
### D-Pak (TO-252AA) Tape & Reel Information

Dimensions are shown in millimeters (inches)





- 1. CONTROLLING DIMENSION : MILLIMETER.
  2. ALL DIMENSIONS ARE SHOWN IN MILLIMETERS (INCHES).
- 3. OUTLINE CONFORMS TO EIA-481 & EIA-541.



NOTES:
1. OUTLINE CONFORMS TO EIA-481.

#### Notes:

- ① Repetitive rating; pulse width limited by max. junction temperature.
- ② Starting T<sub>.1</sub> = 25°C, L = 0.69 mH  $R_G = 25\Omega$ ,  $I_{AS} = 14A$ .
- ③ Pulse width  $\leq 400 \mu s$ ; duty cycle  $\leq 2\%$ .
- 4 When mounted on 1" square PCB (FR-4 or G-10 Material). For recommended footprint and soldering techniques refer to application note #AN-994.
- ⑤ Calculated continuous current based on maximum allowable junction temperature; Package limitation current is 30A

Data and specifications subject to change without notice. These products have been designed and qualified for the Industrial market. Qualification Standards can be found on IR's Web site.



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