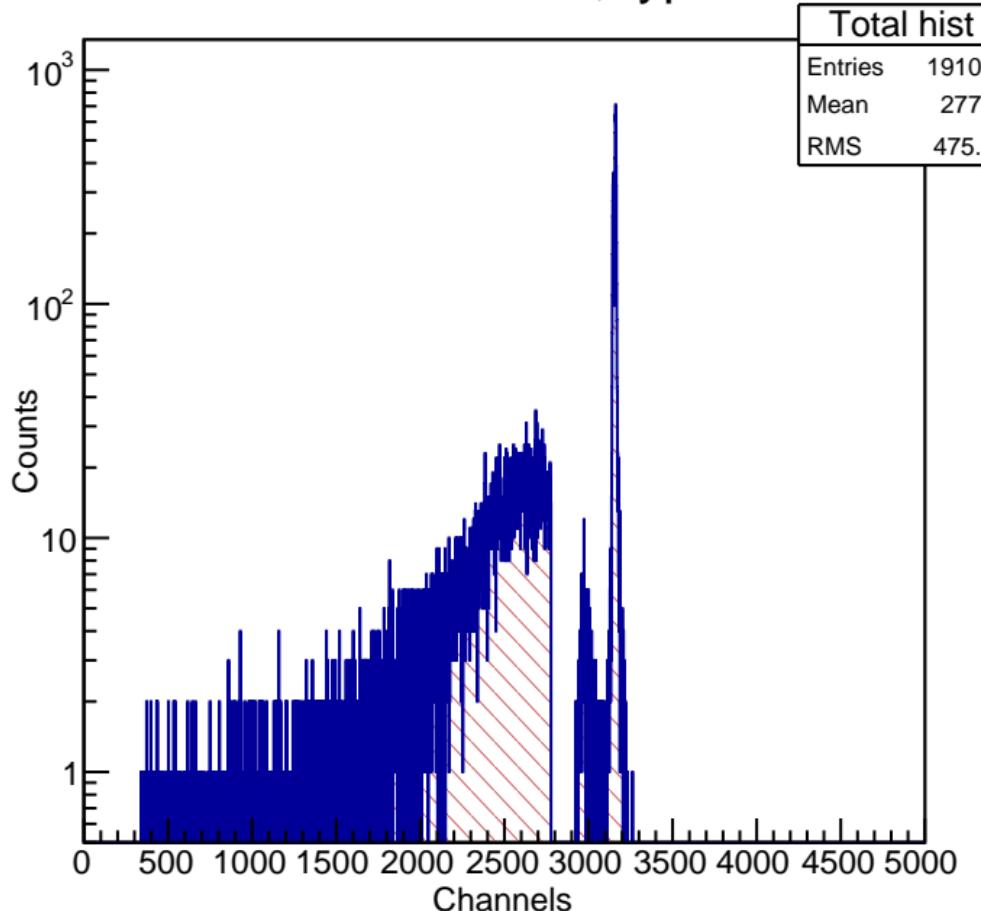
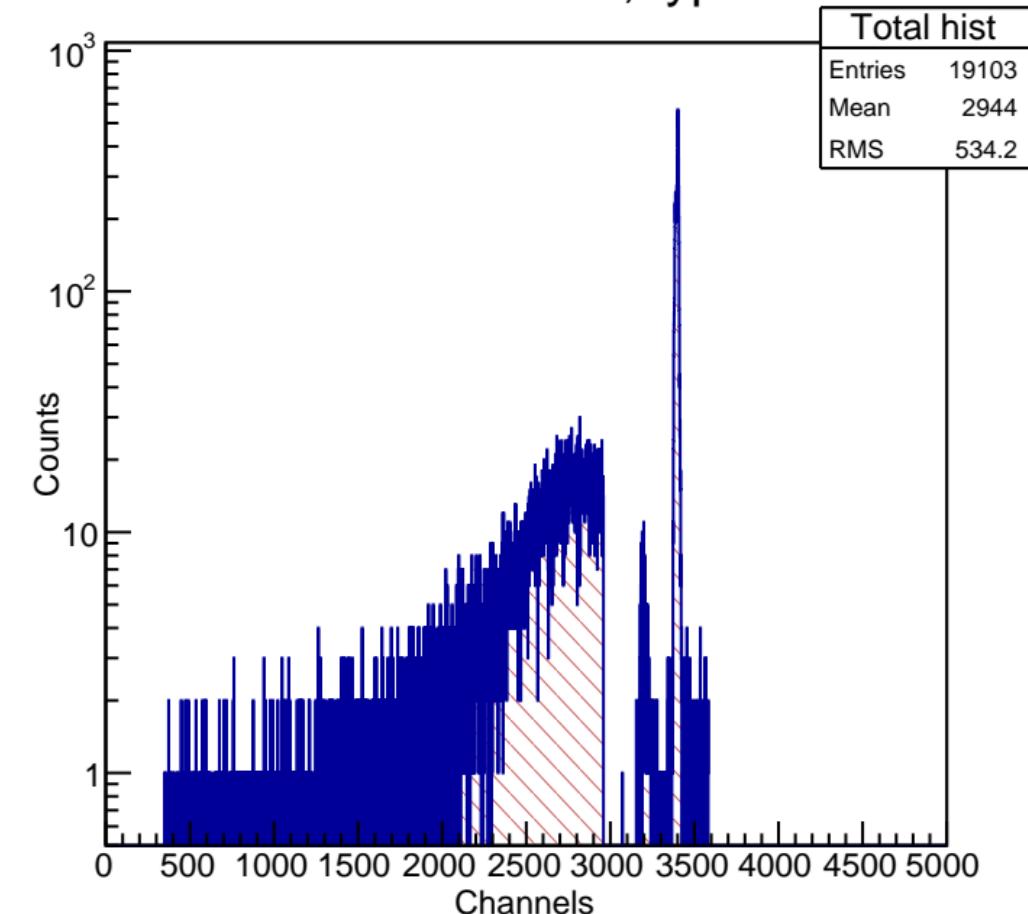


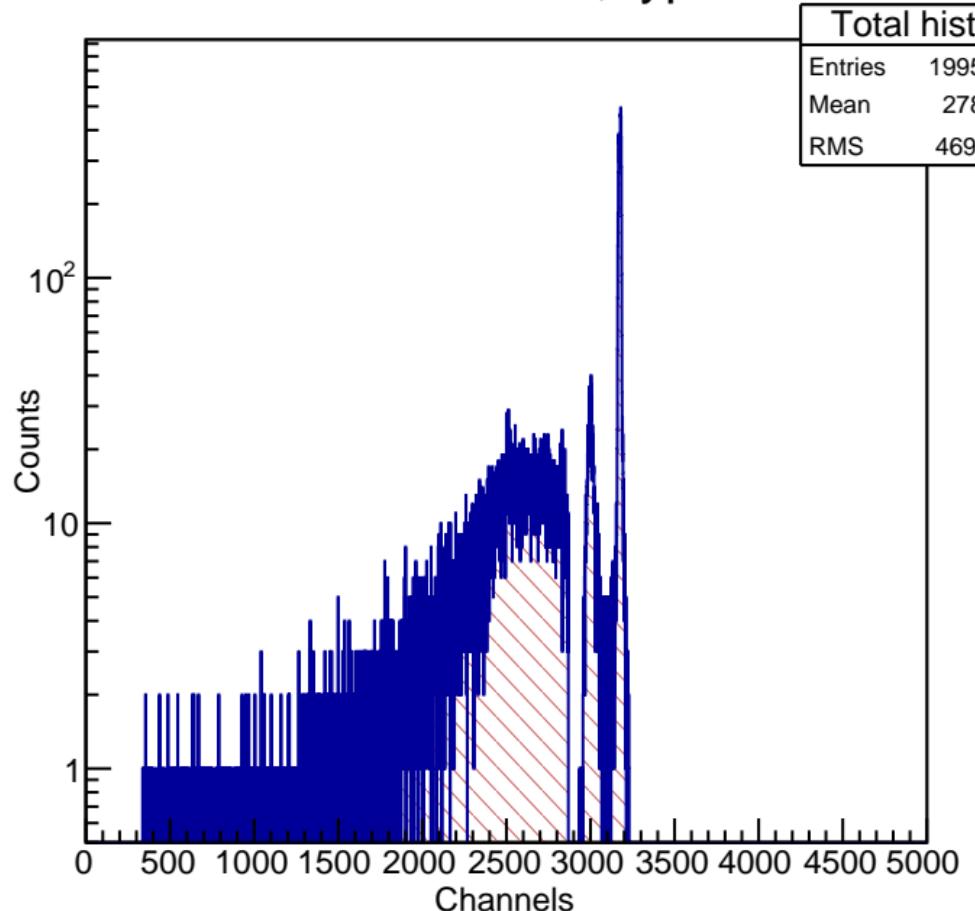
# TDCE Octet 0, type1



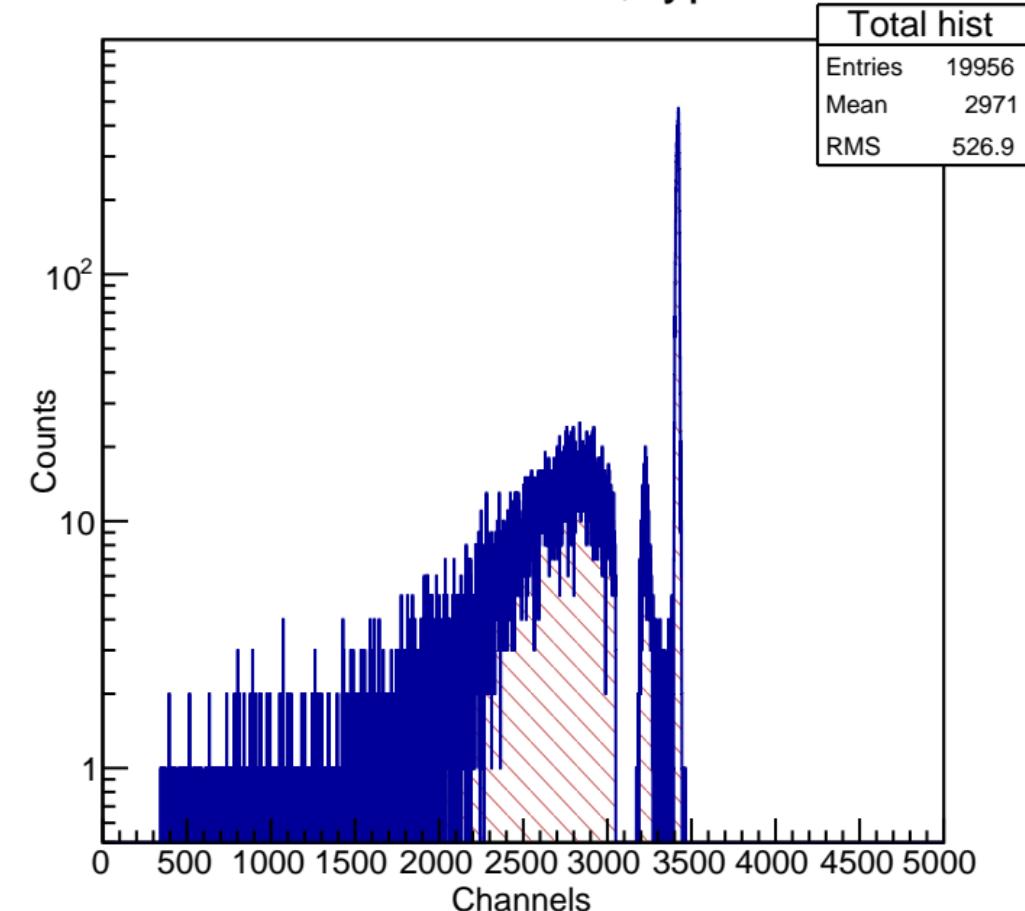
# TDCW Octet 0, type1



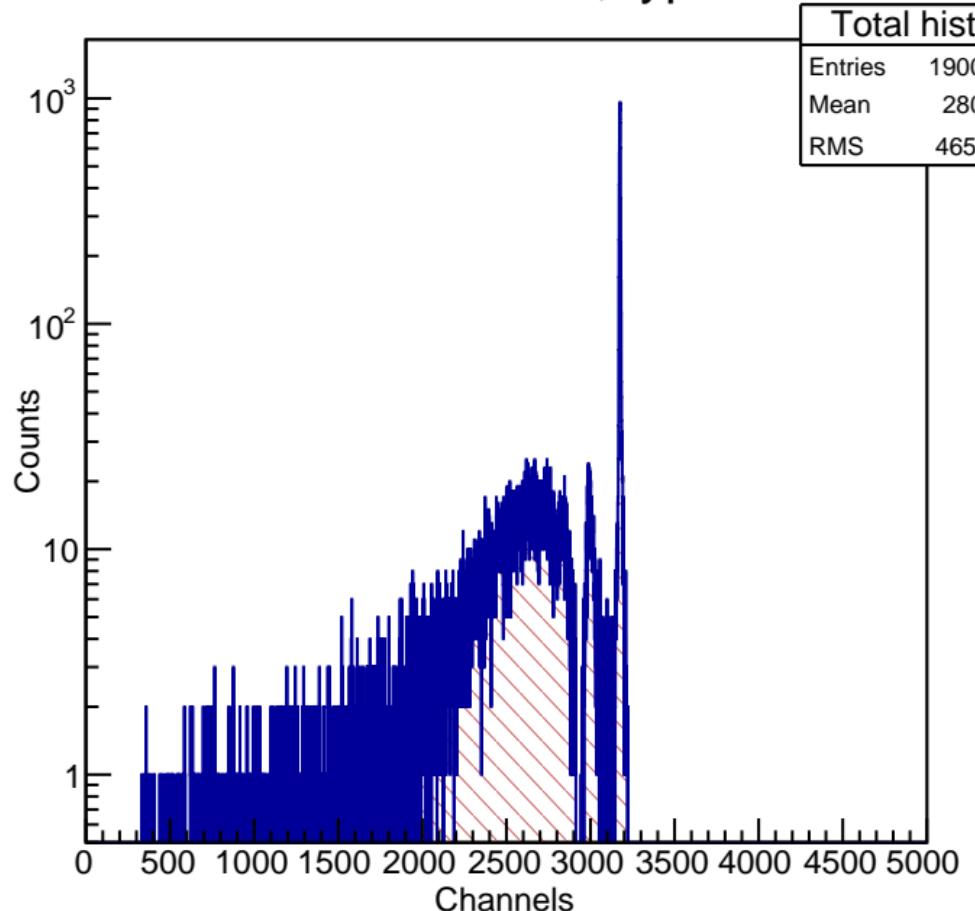
# TDCE Octet 1, type1



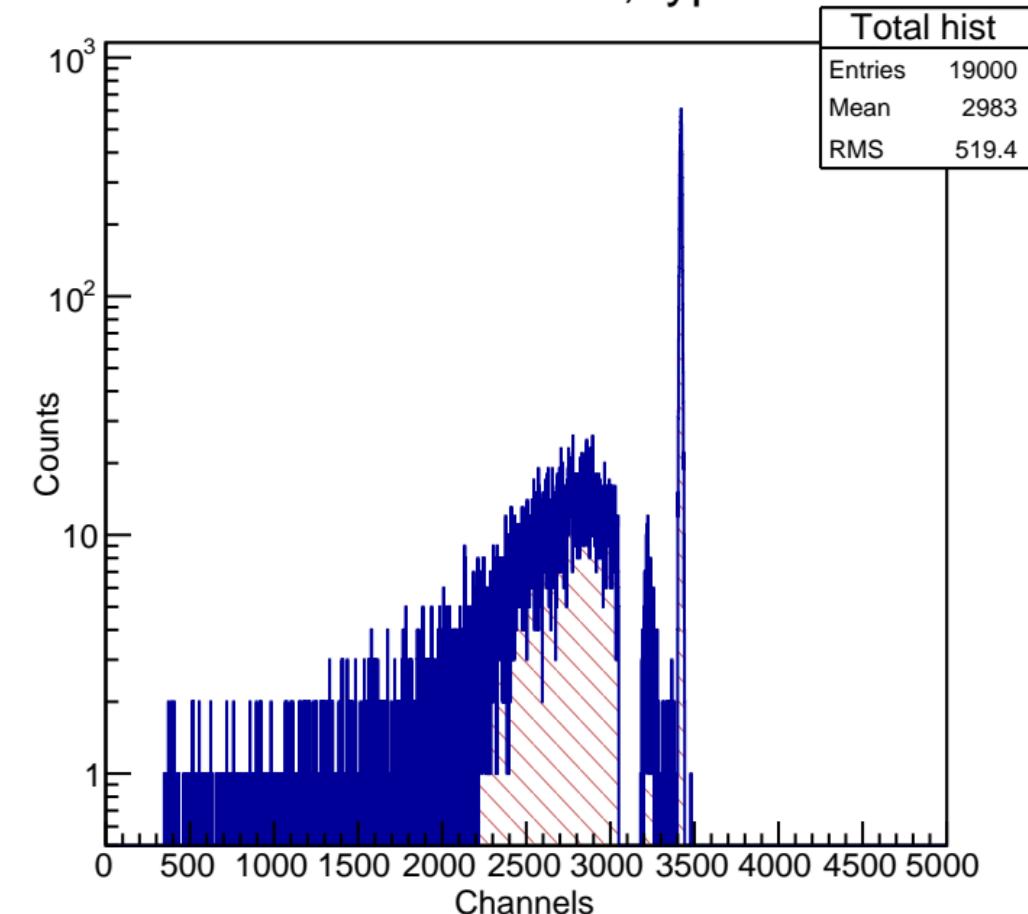
# TDCW Octet 1, type1



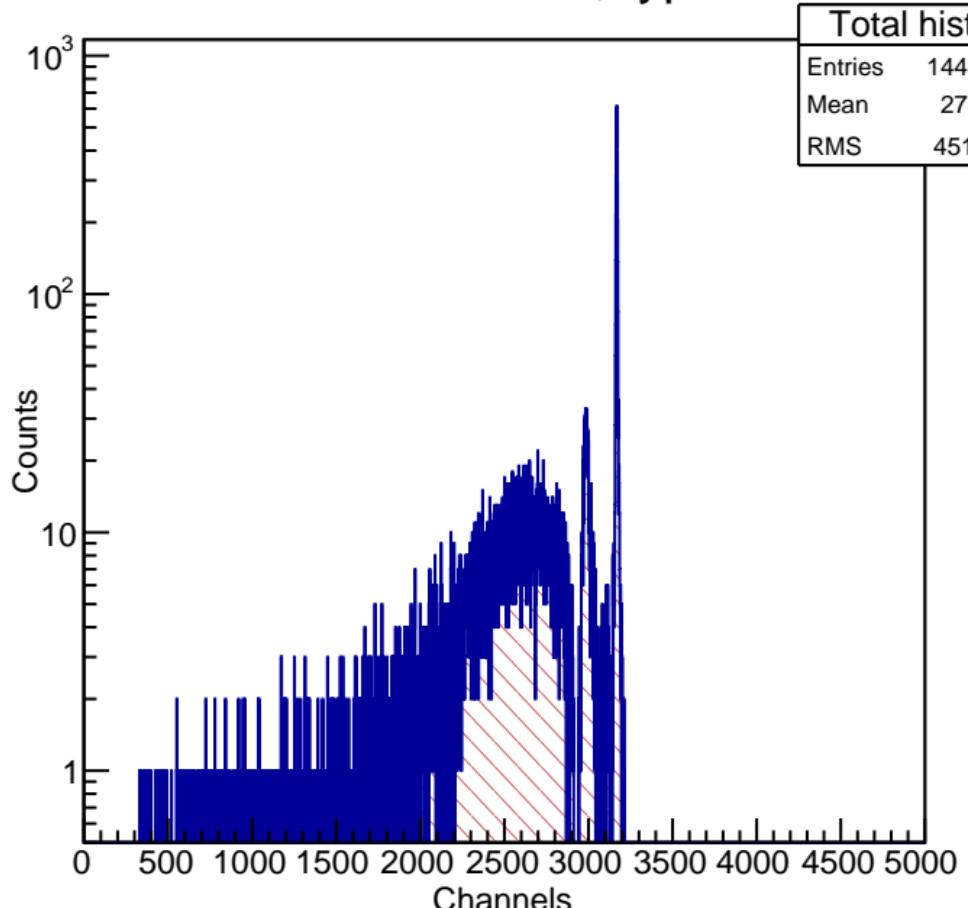
# TDCE Octet 2, type1



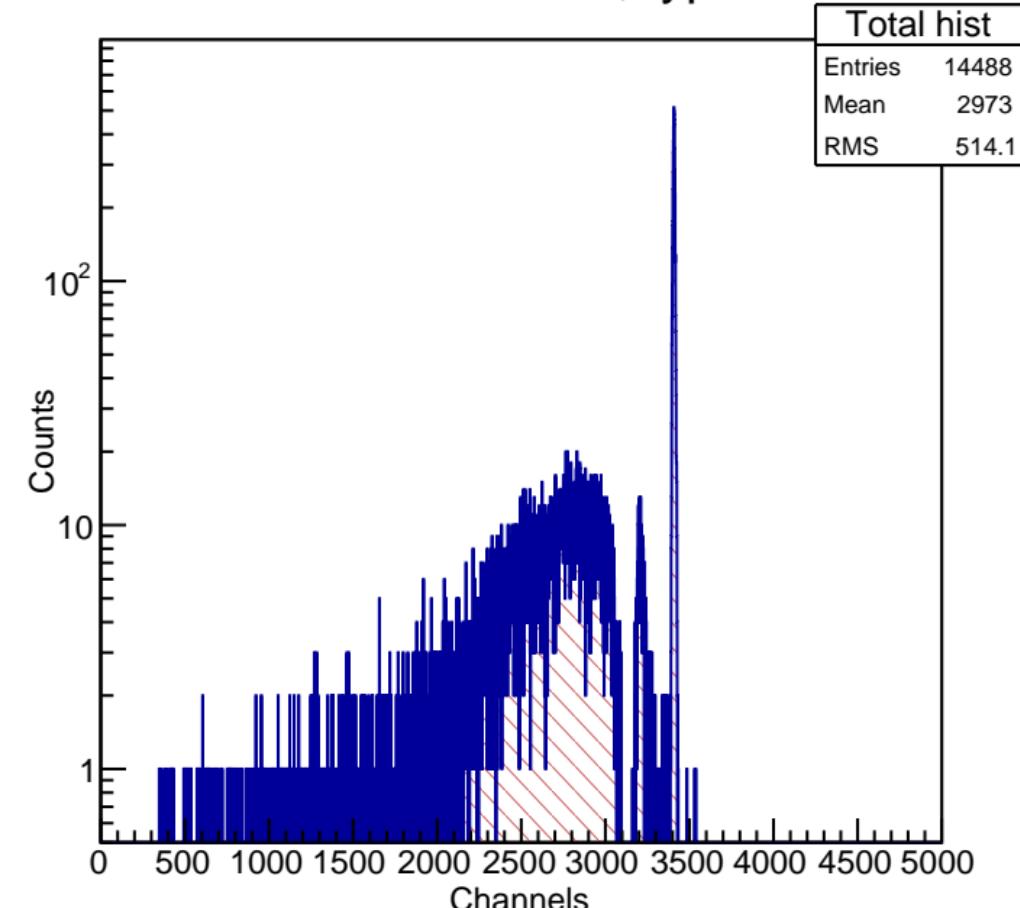
# TDCW Octet 2, type1



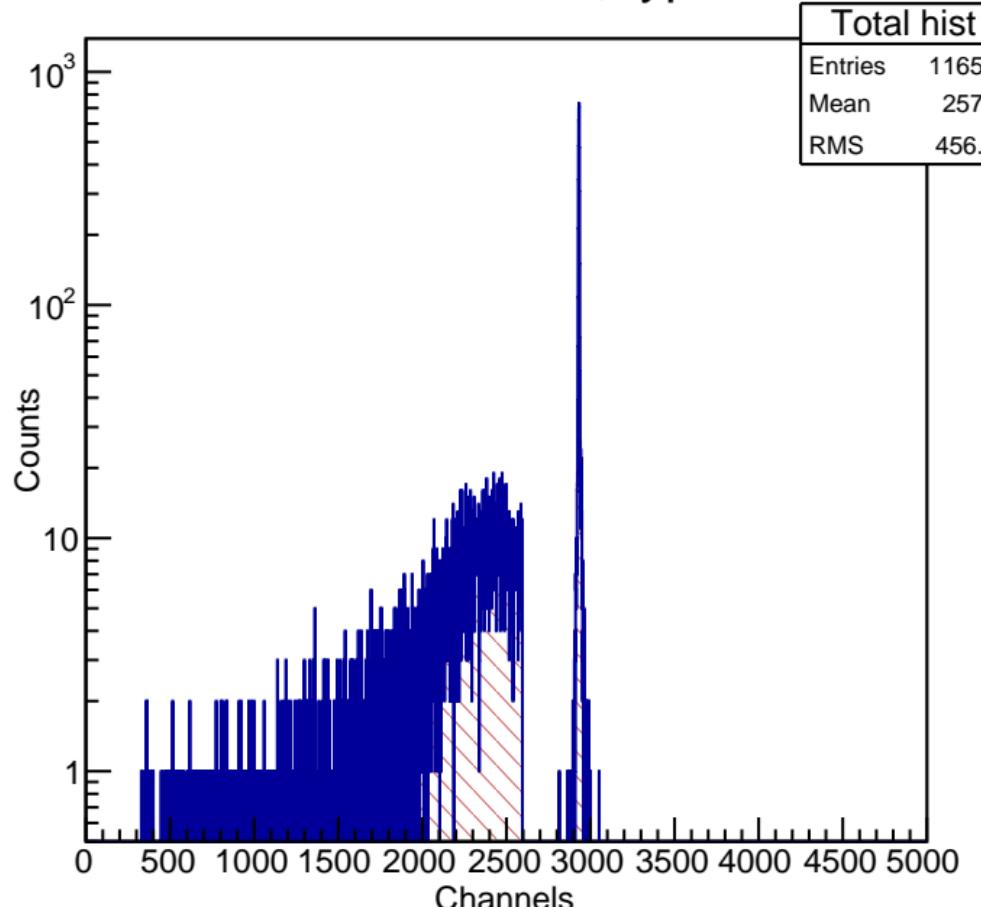
# TDCE Octet 3, type1



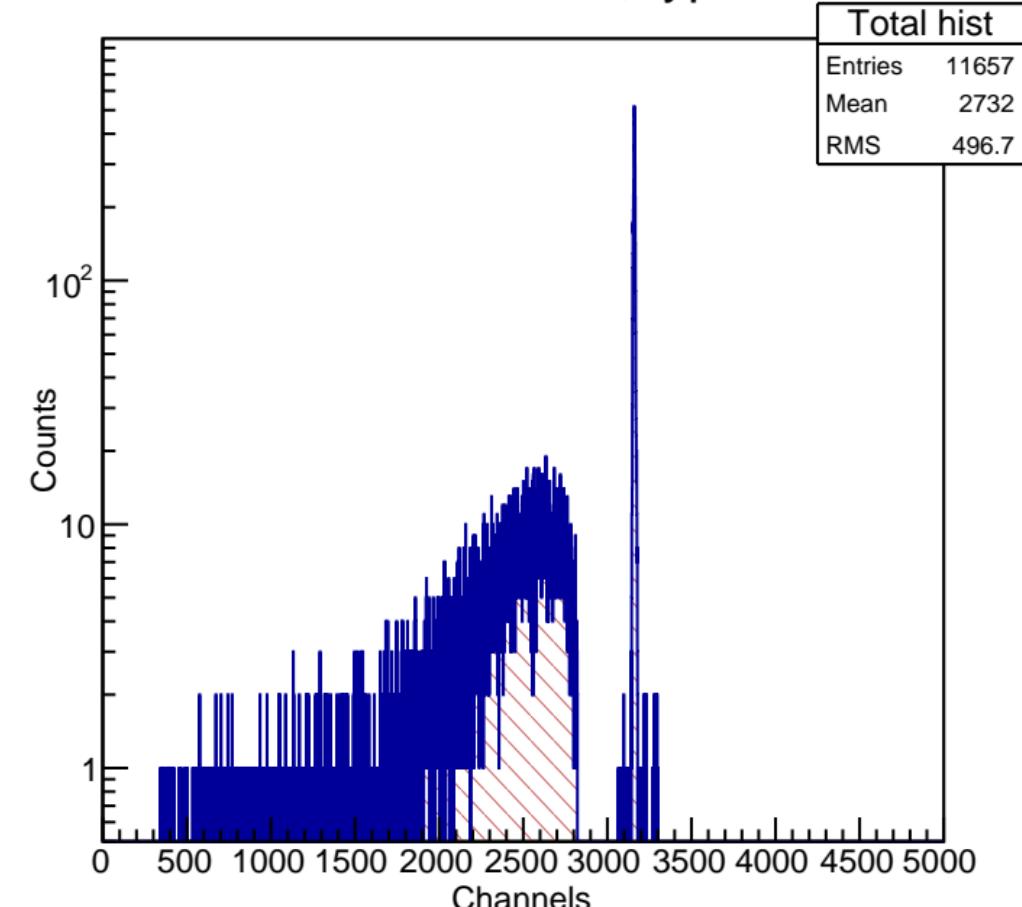
# TDCW Octet 3, type1



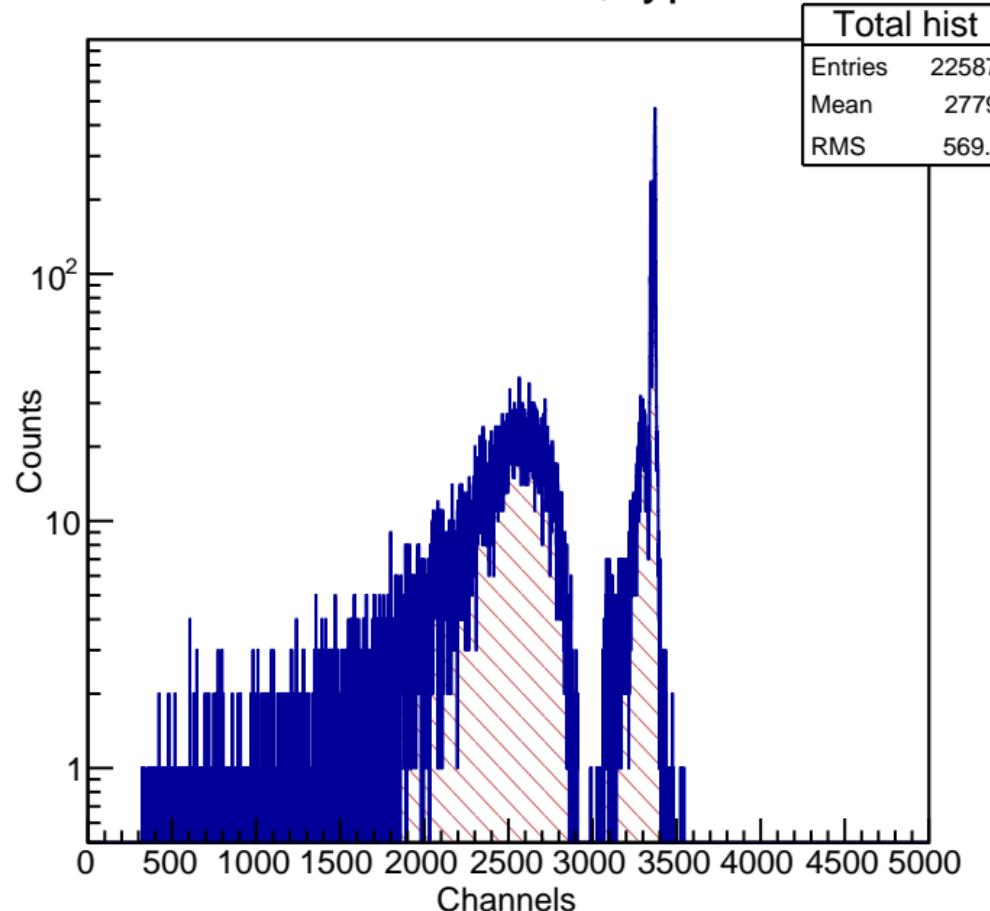
# TDCE Octet 4, type1



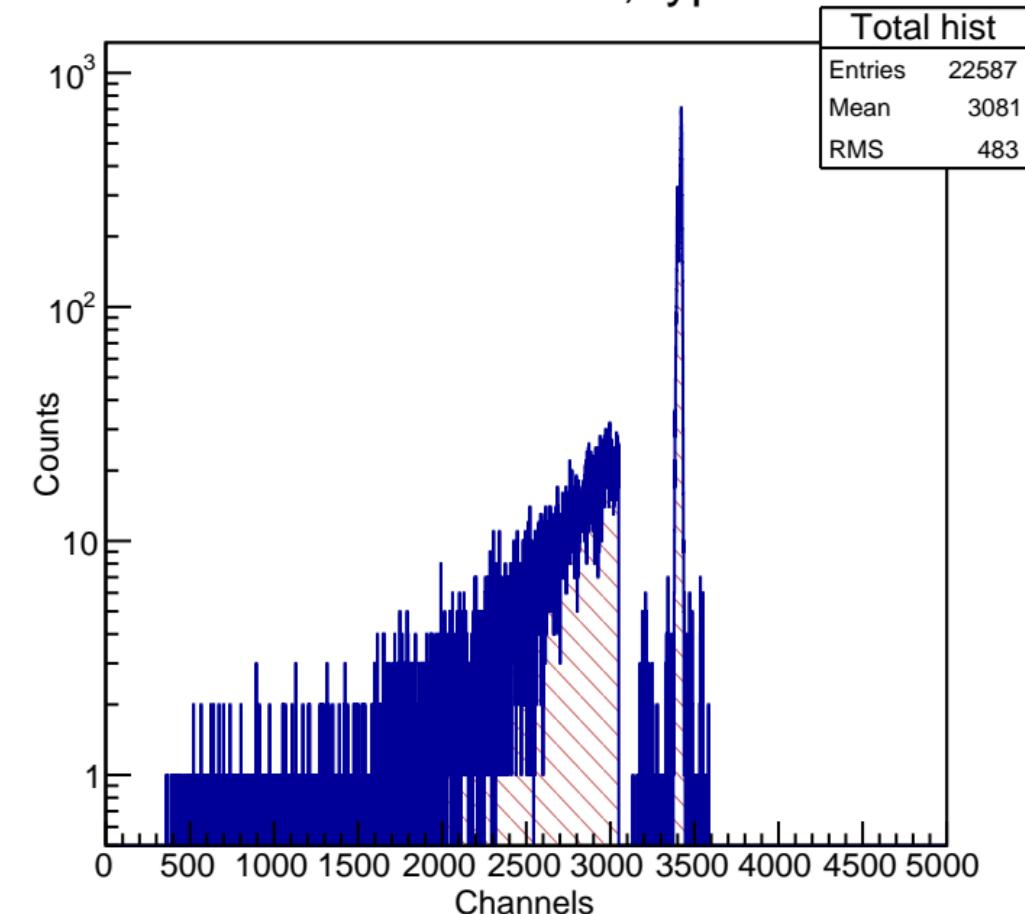
# TDCW Octet 4, type1



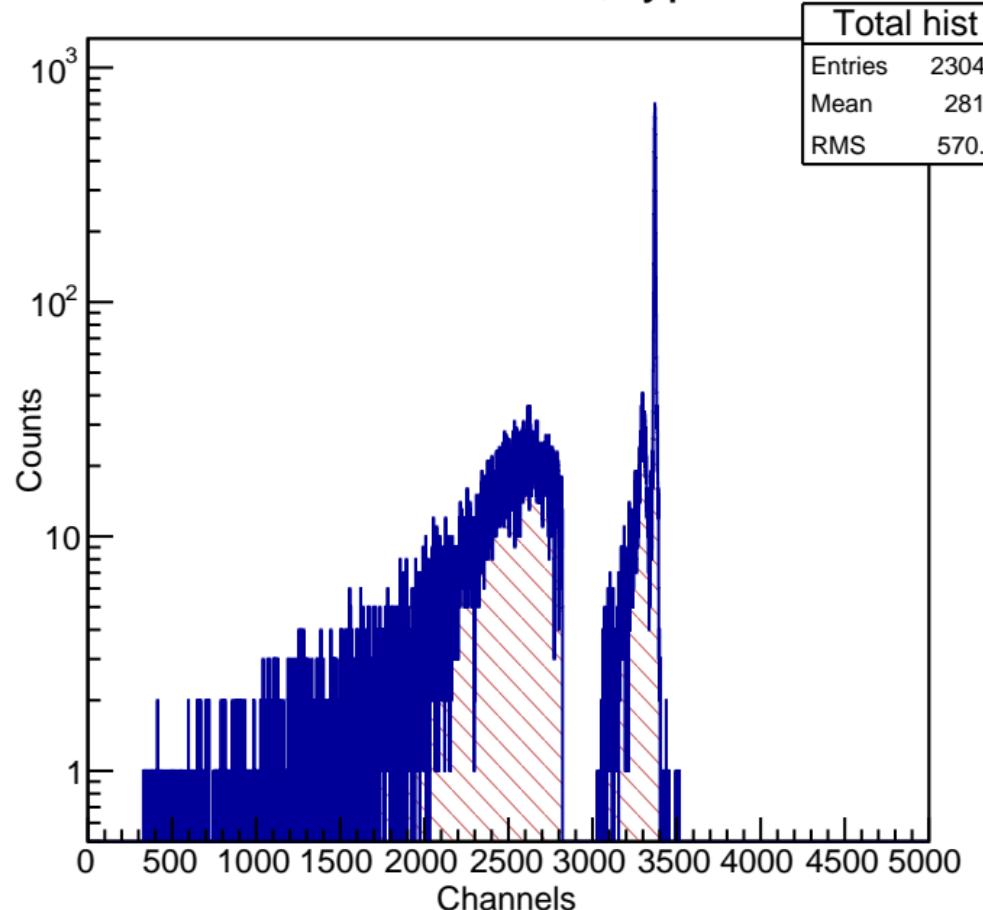
# TDCE Octet 5, type1



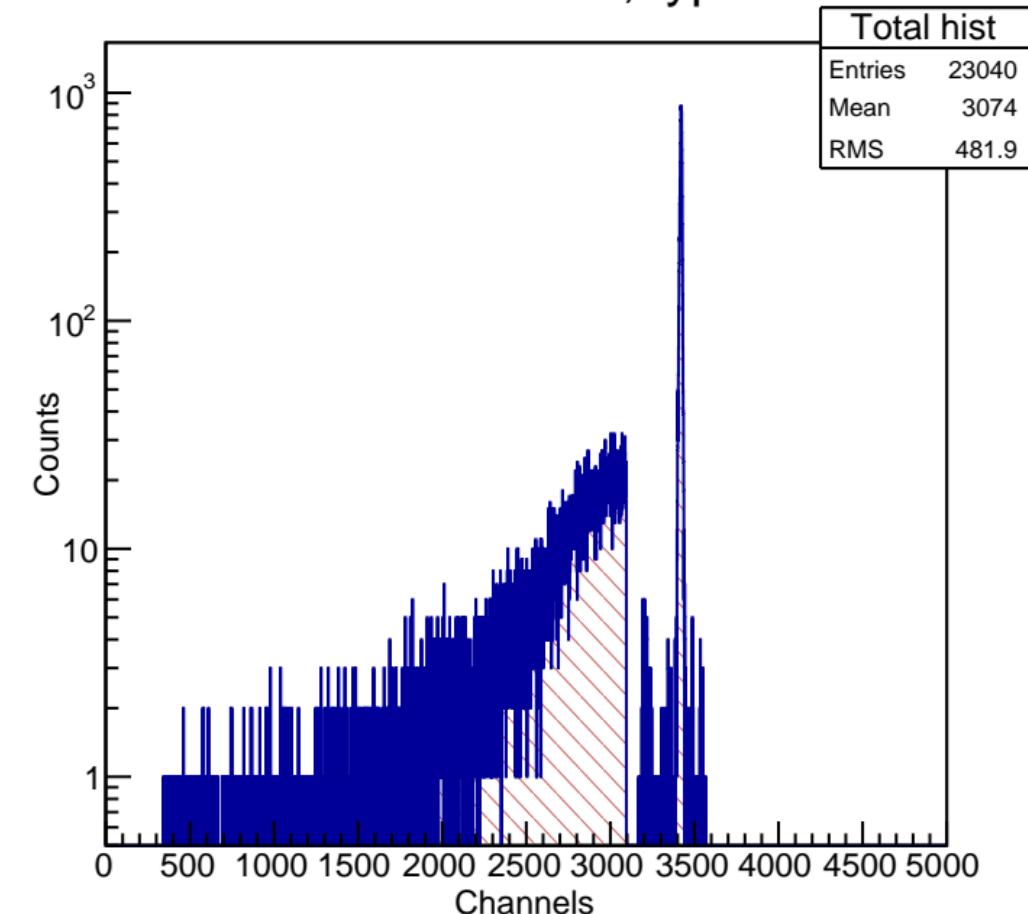
# TDCW Octet 5, type1



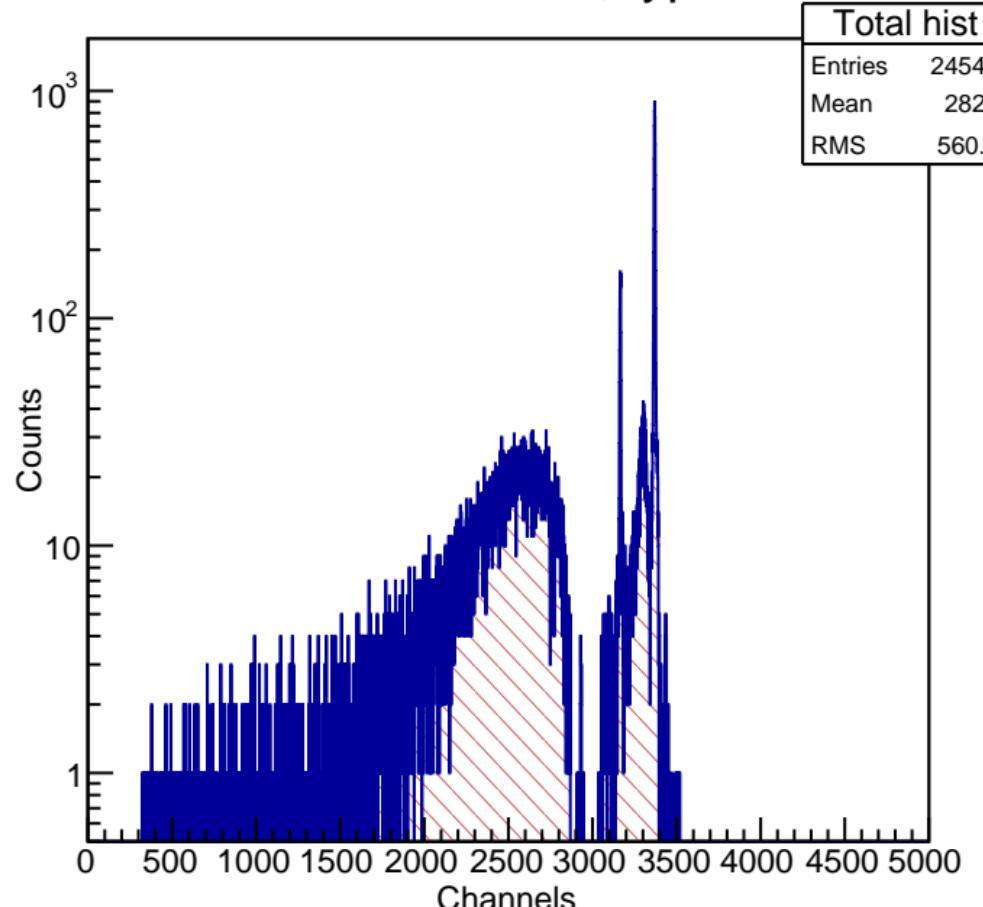
# TDCE Octet 6, type1



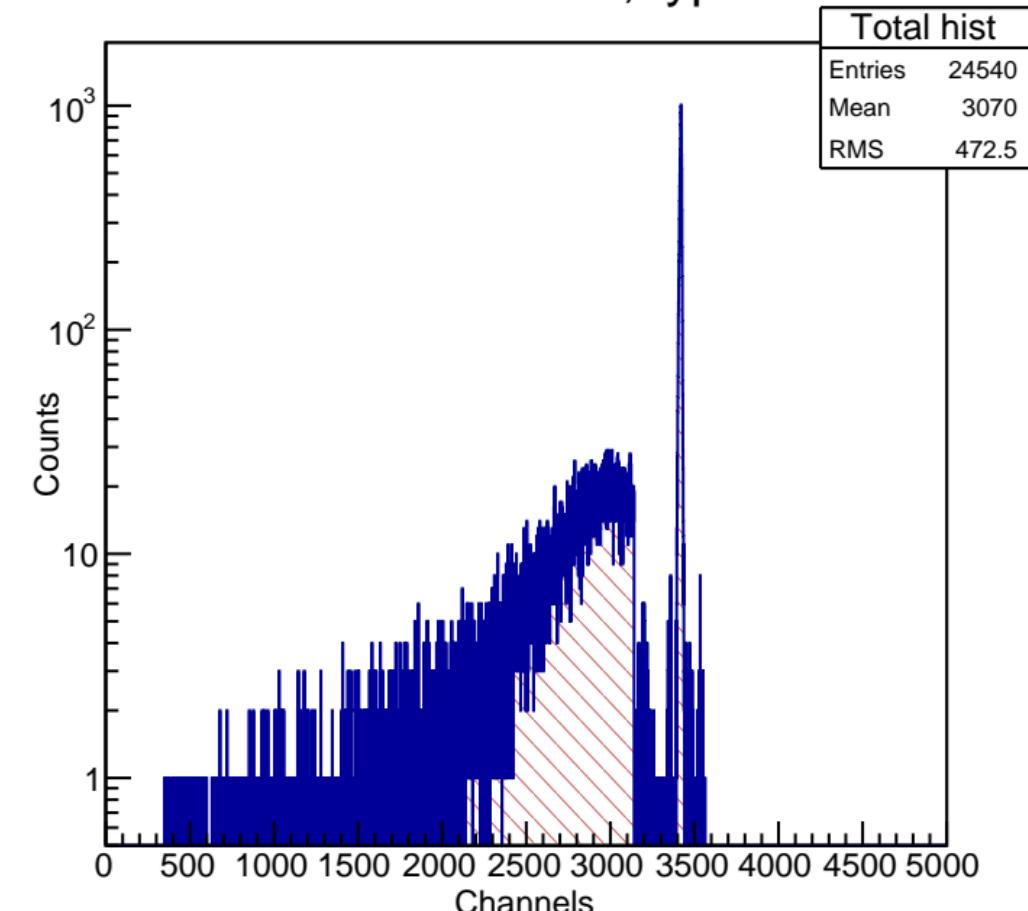
# TDCW Octet 6, type1



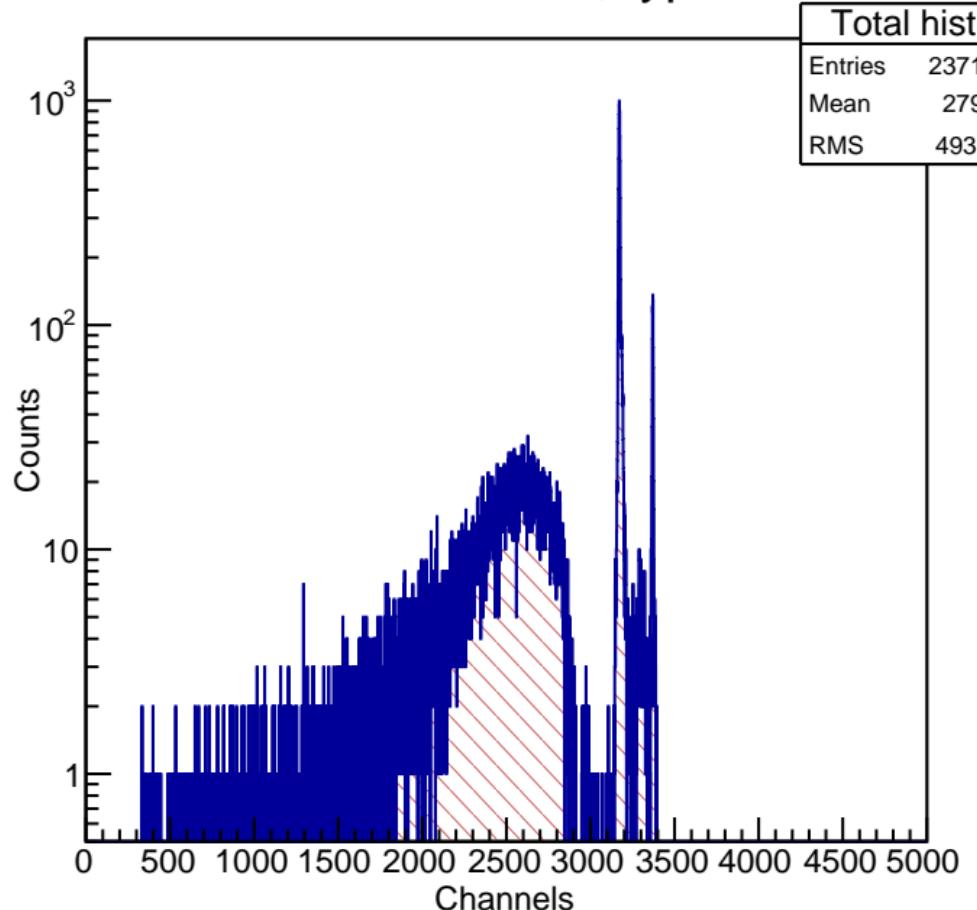
# TDCE Octet 7, type1



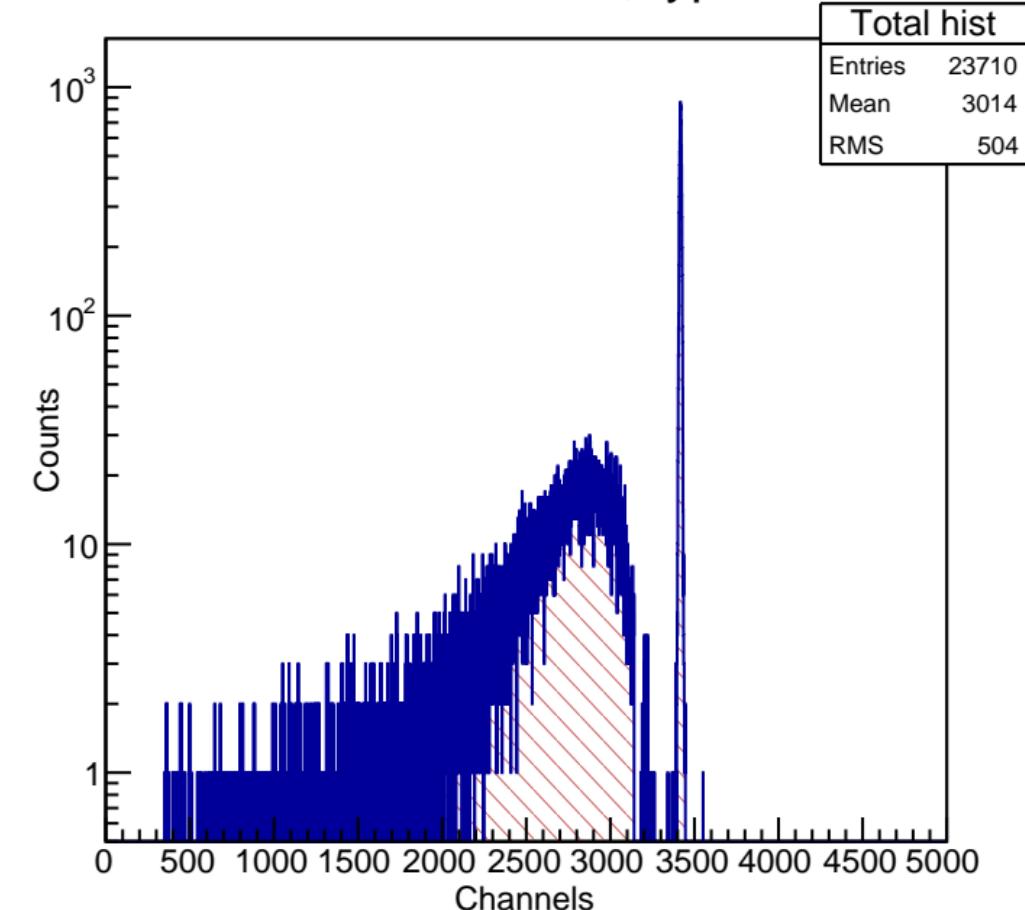
# TDCW Octet 7, type1



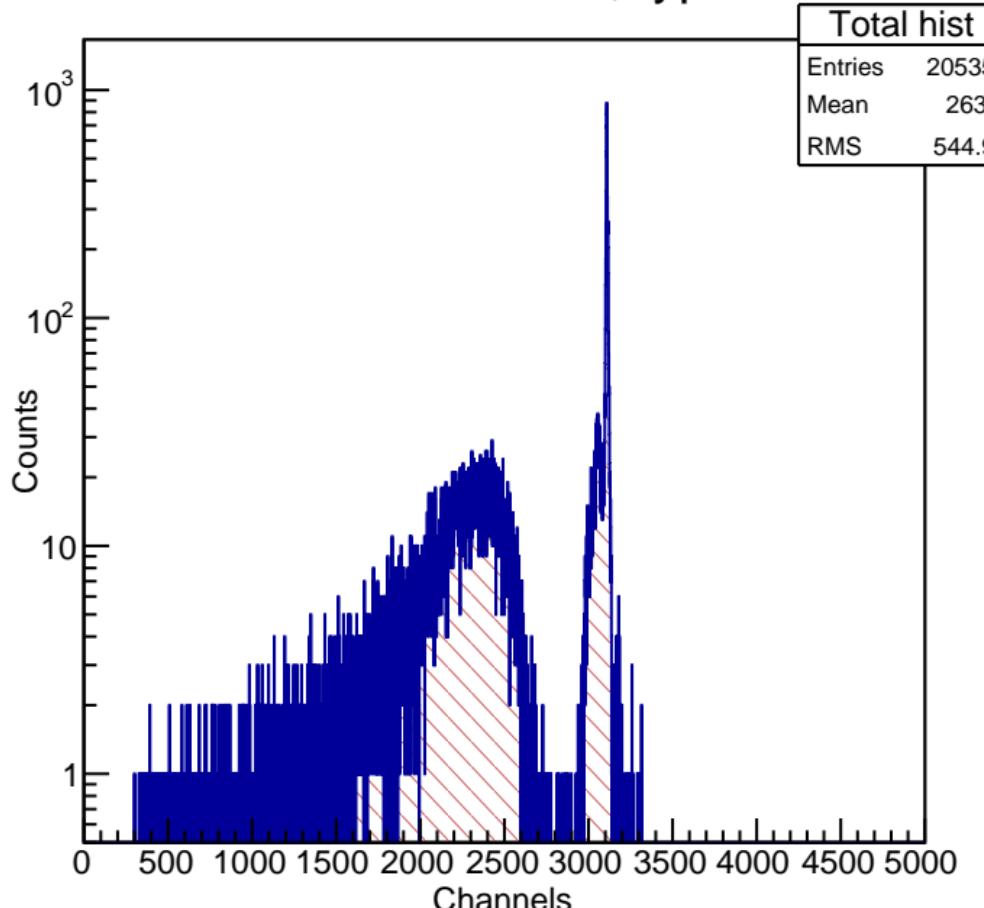
# TDCE Octet 8, type1



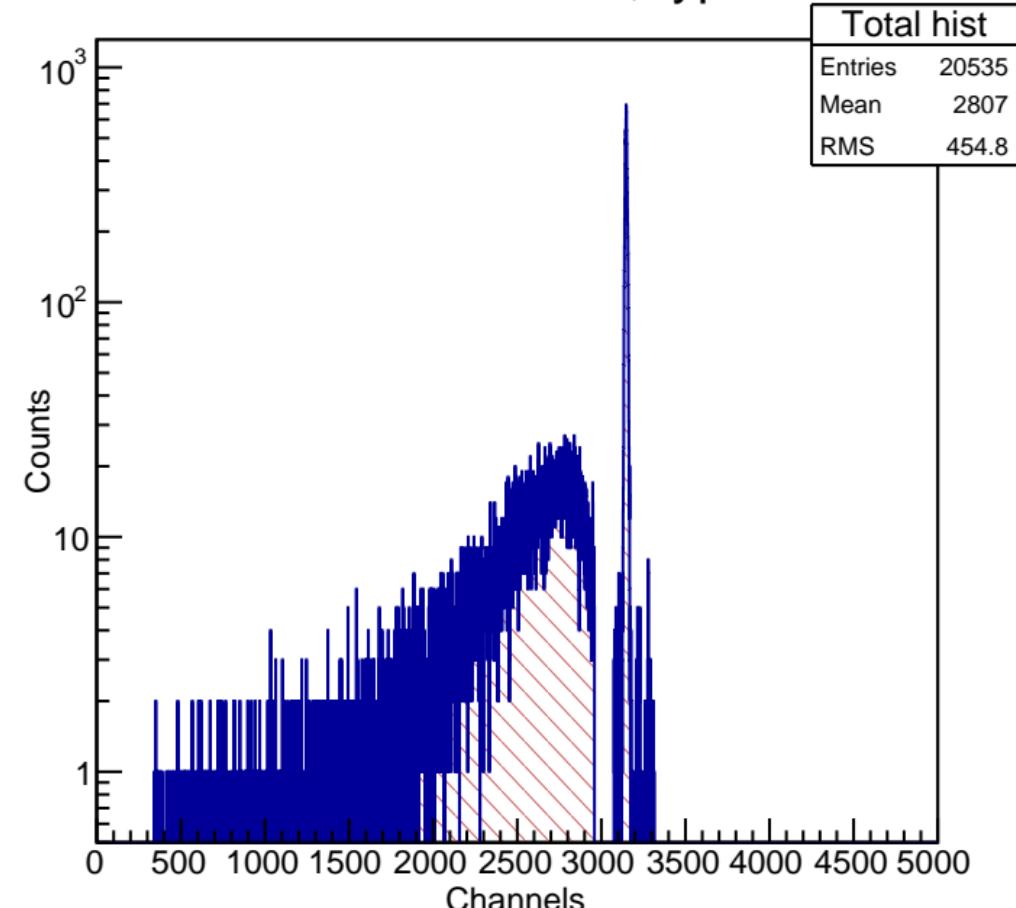
# TDCW Octet 8, type1



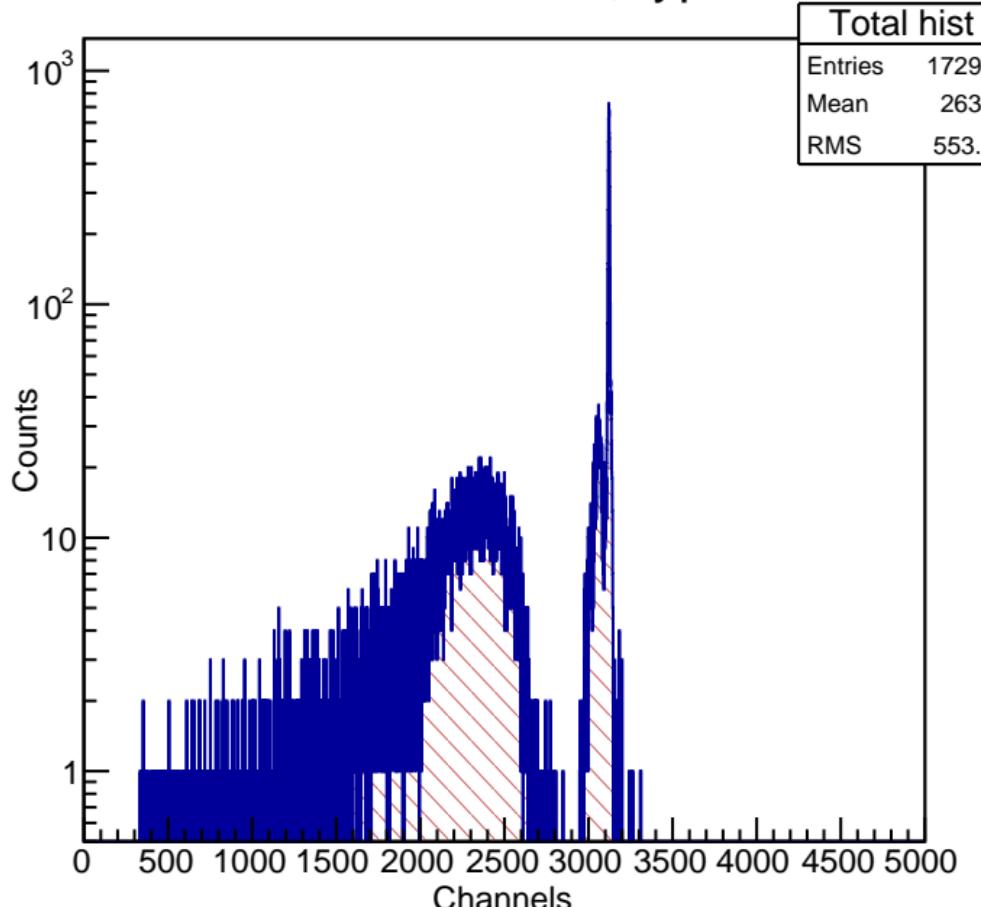
# TDCE Octet 10, type1



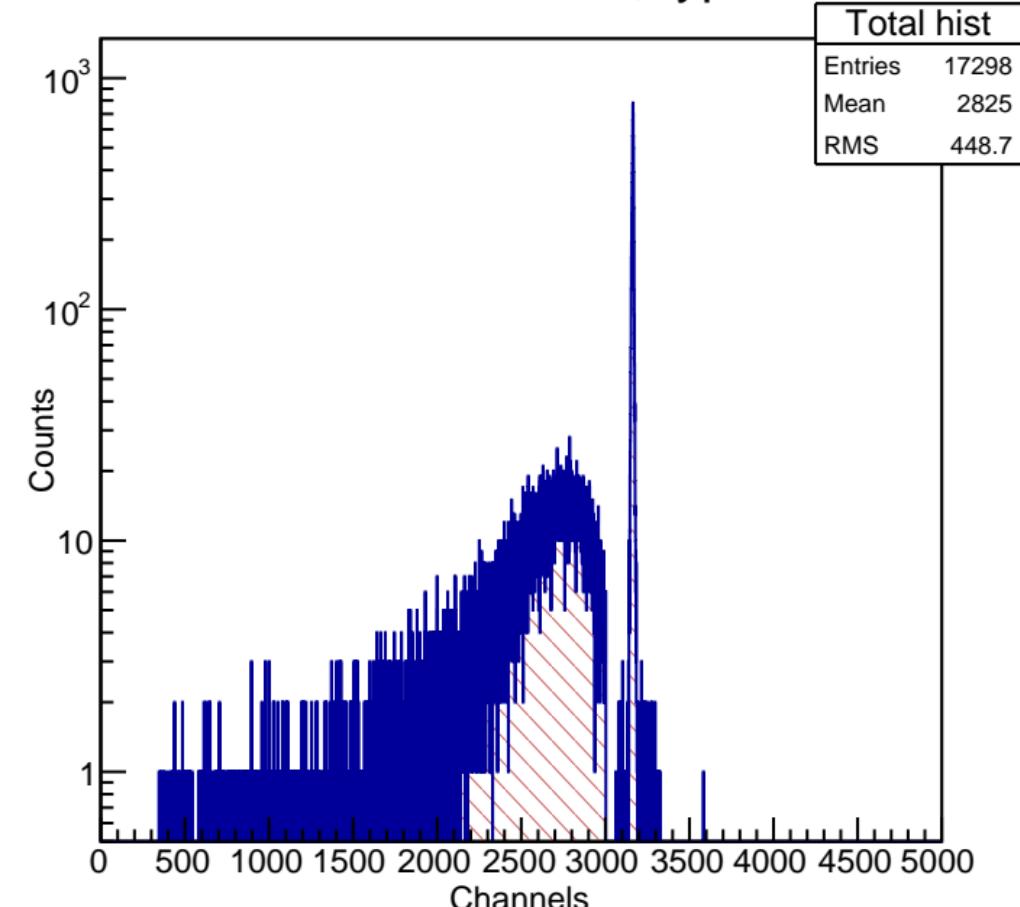
# TDCW Octet 10, type1



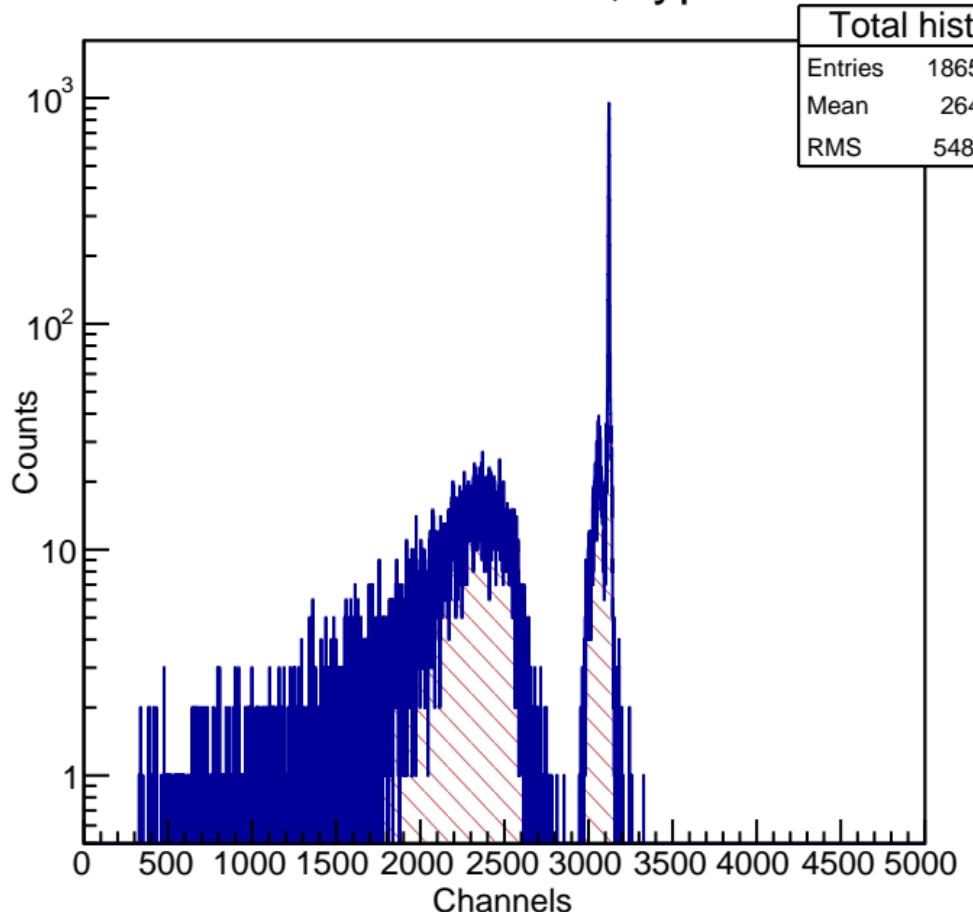
# TDCE Octet 11, type1



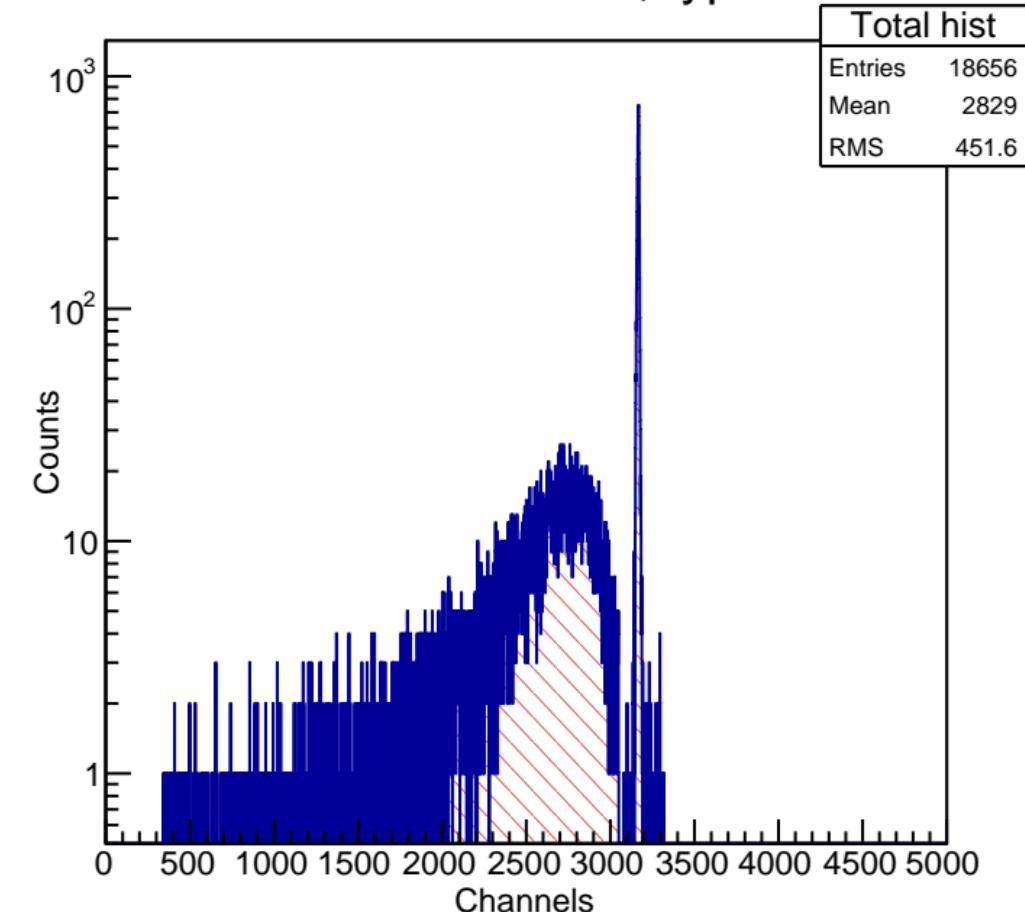
# TDCW Octet 11, type1



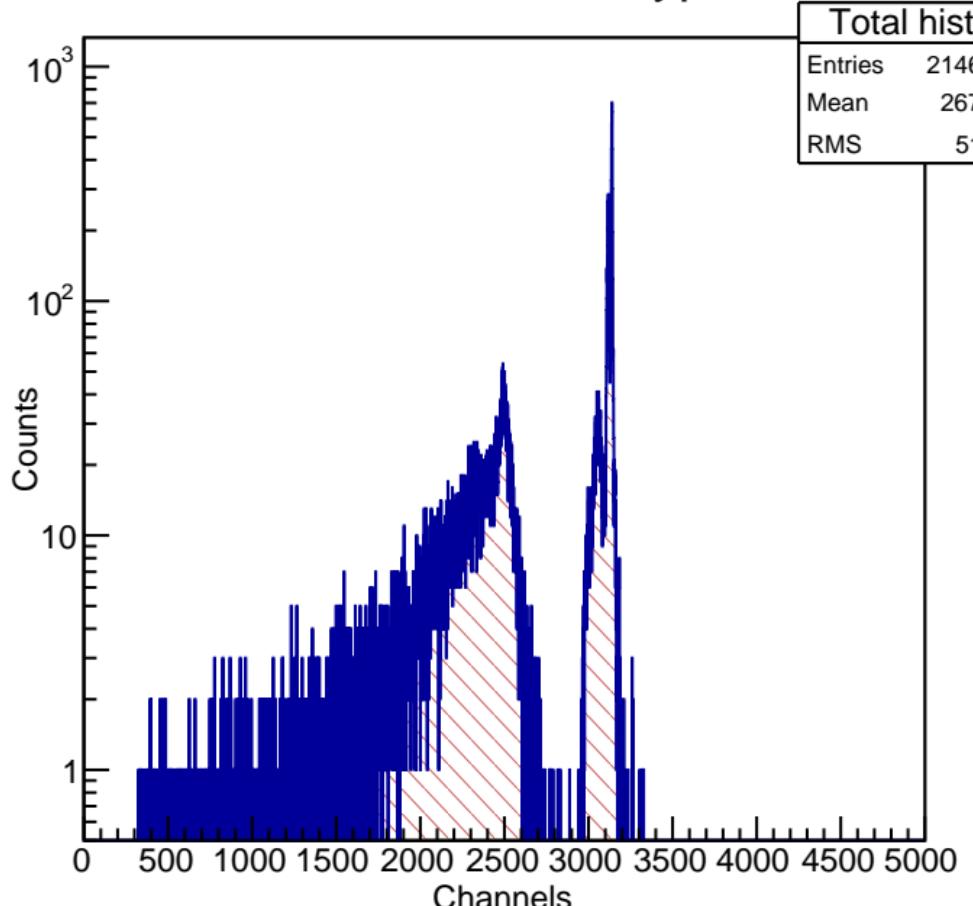
# TDCE Octet 12, type1



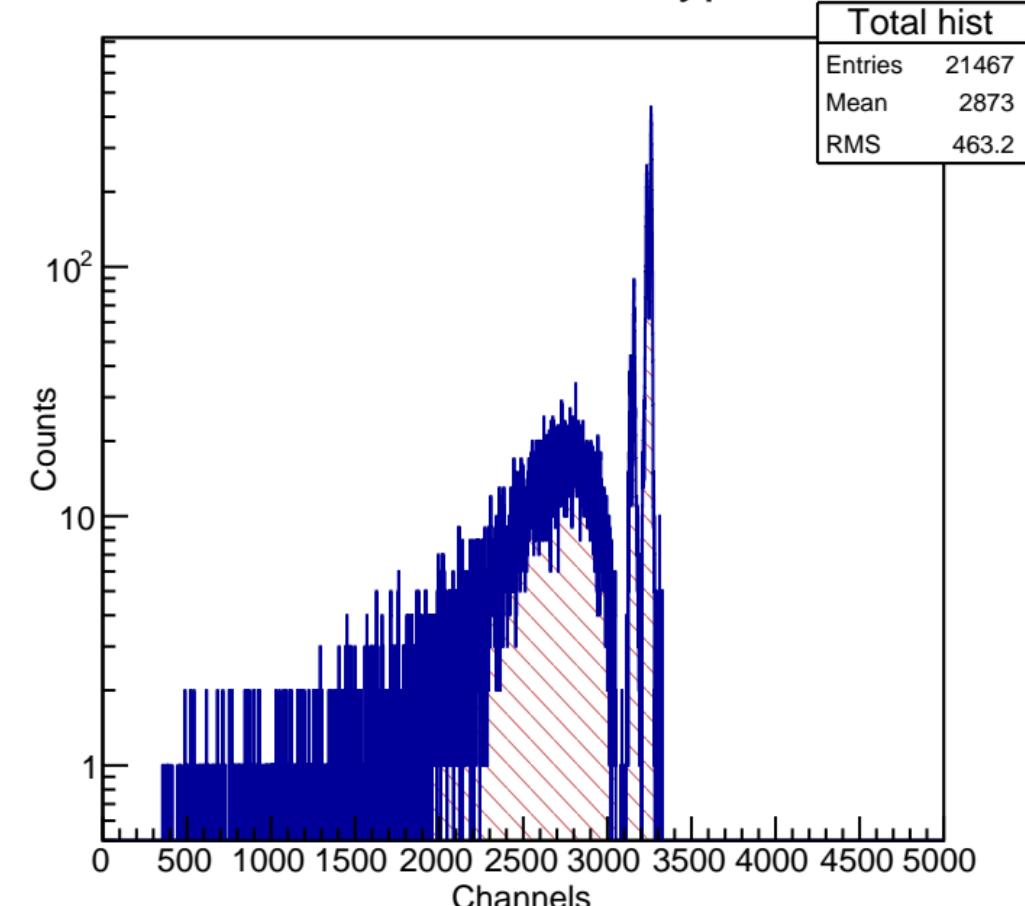
# TDCW Octet 12, type1



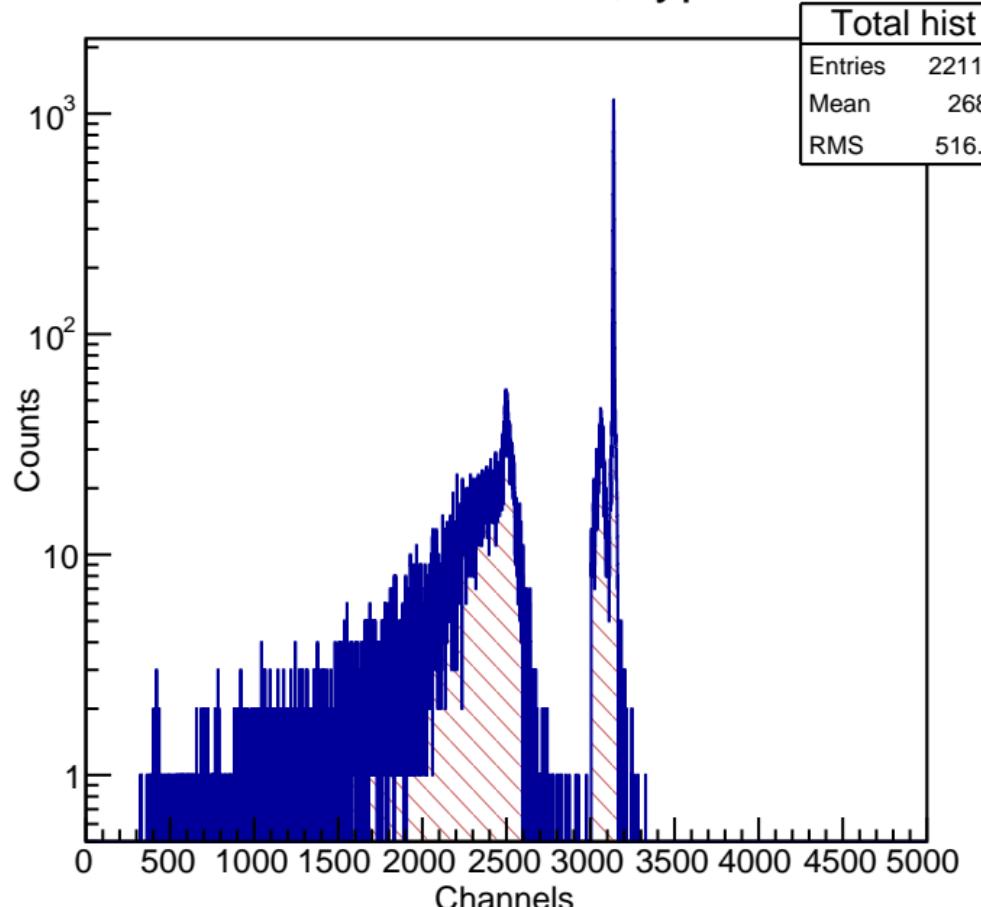
# TDCE Octet 13, type1



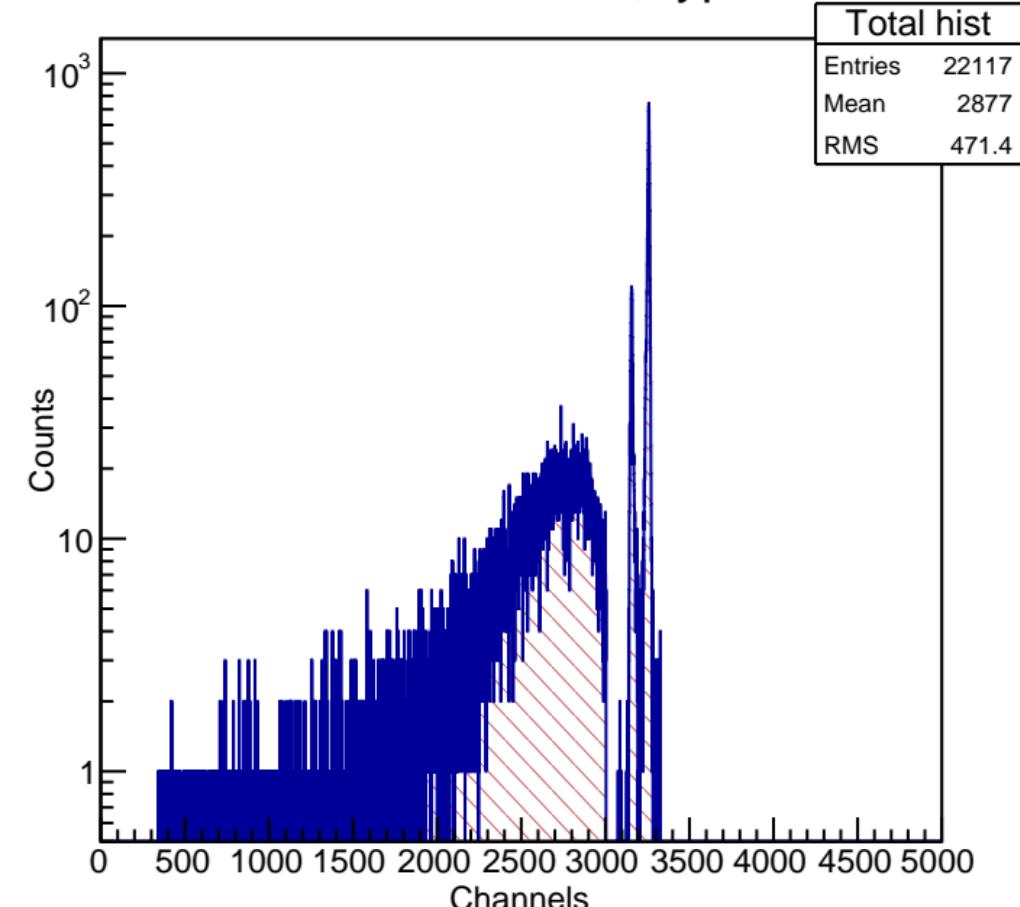
# TDCW Octet 13, type1



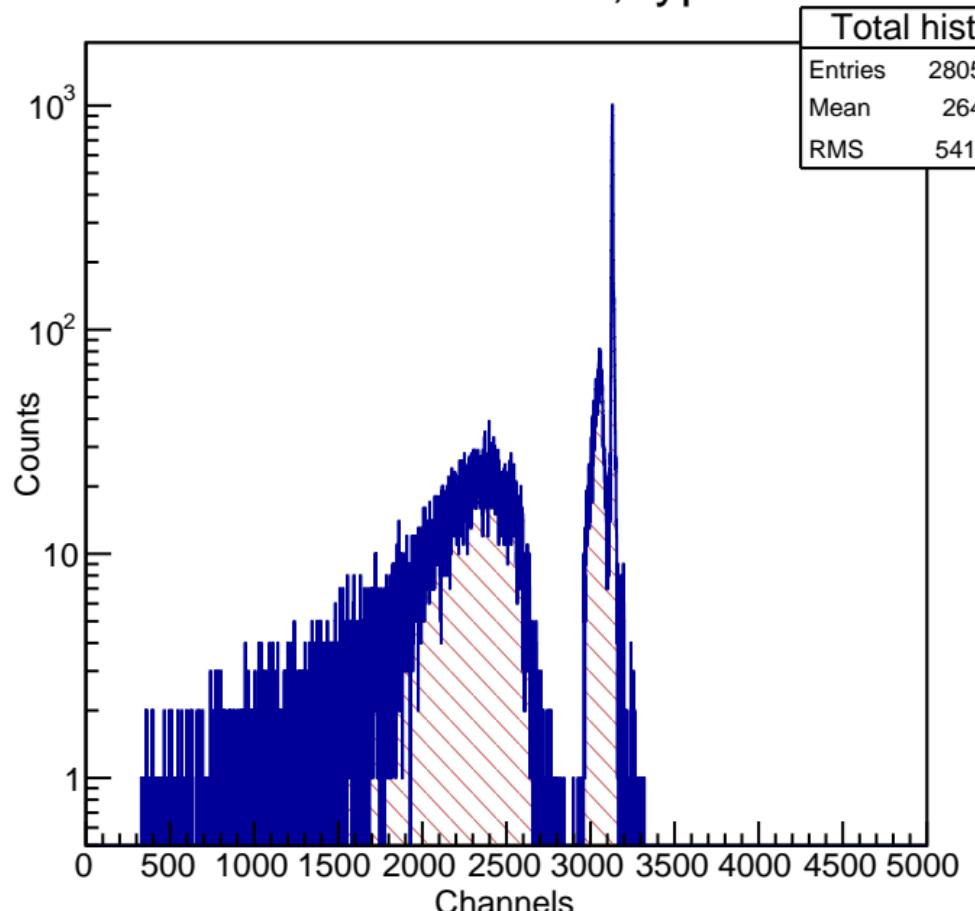
# TDCE Octet 14, type1



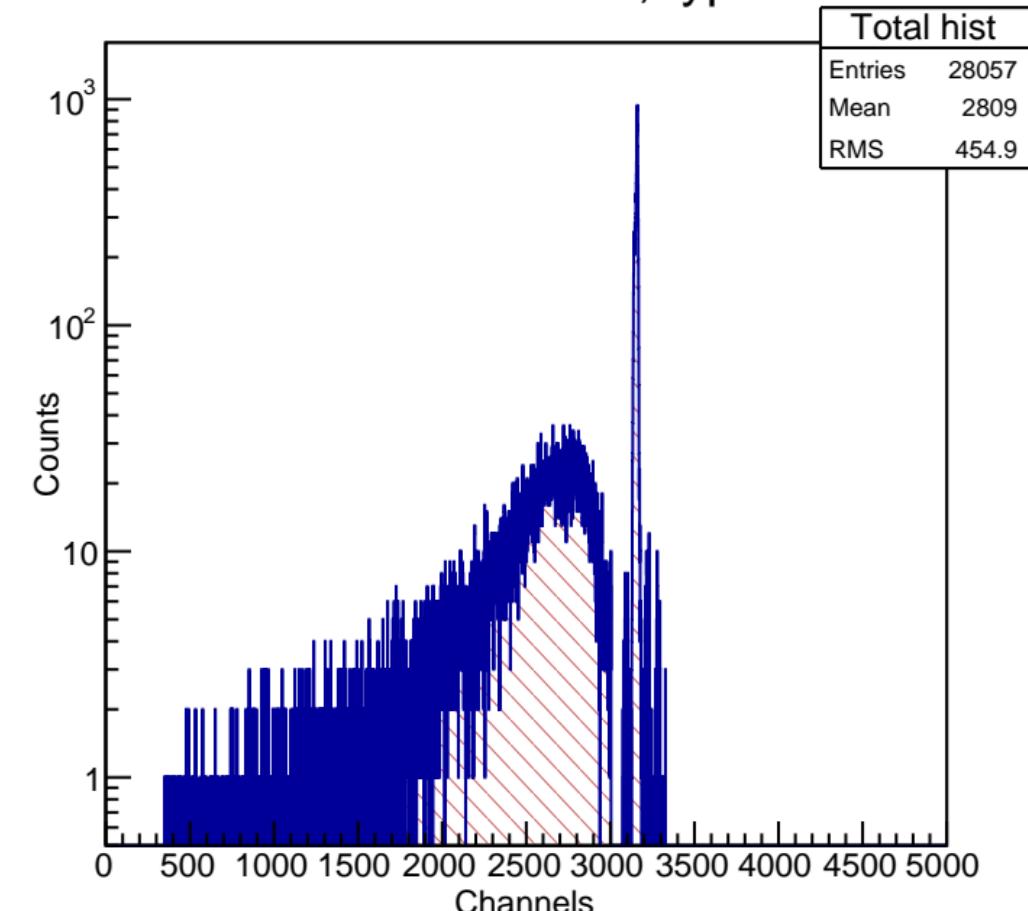
# TDCW Octet 14, type1



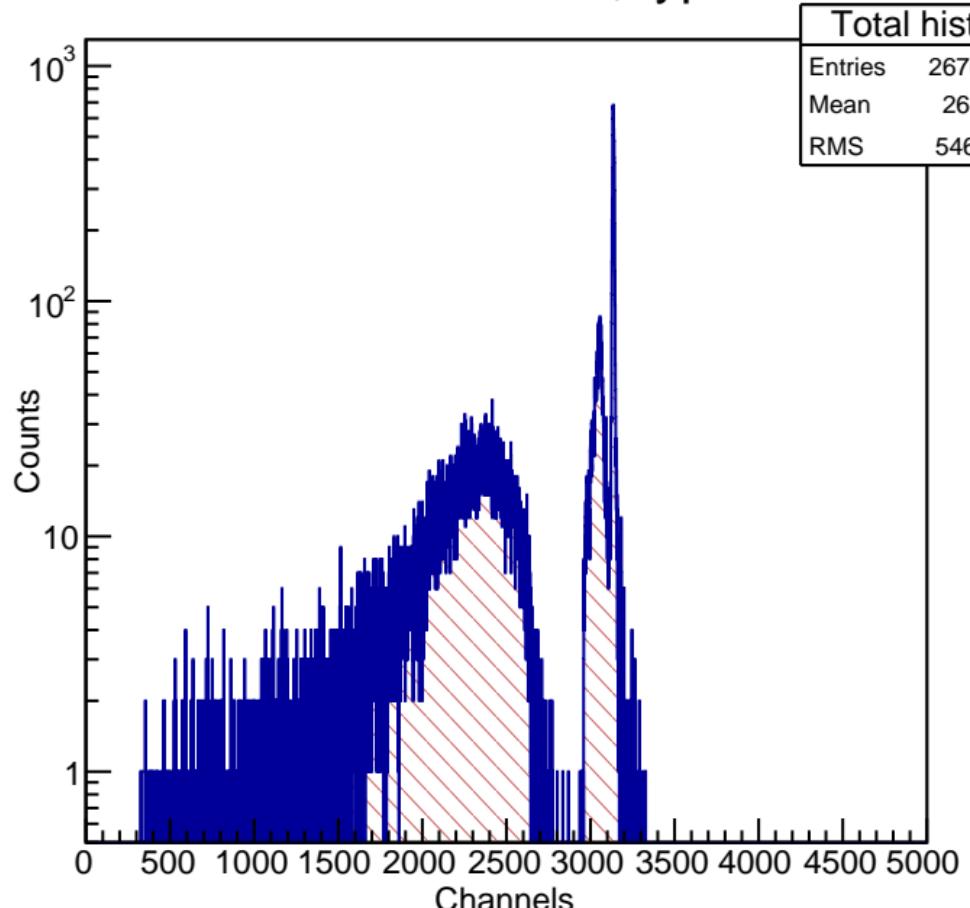
# TDCE Octet 15, type1



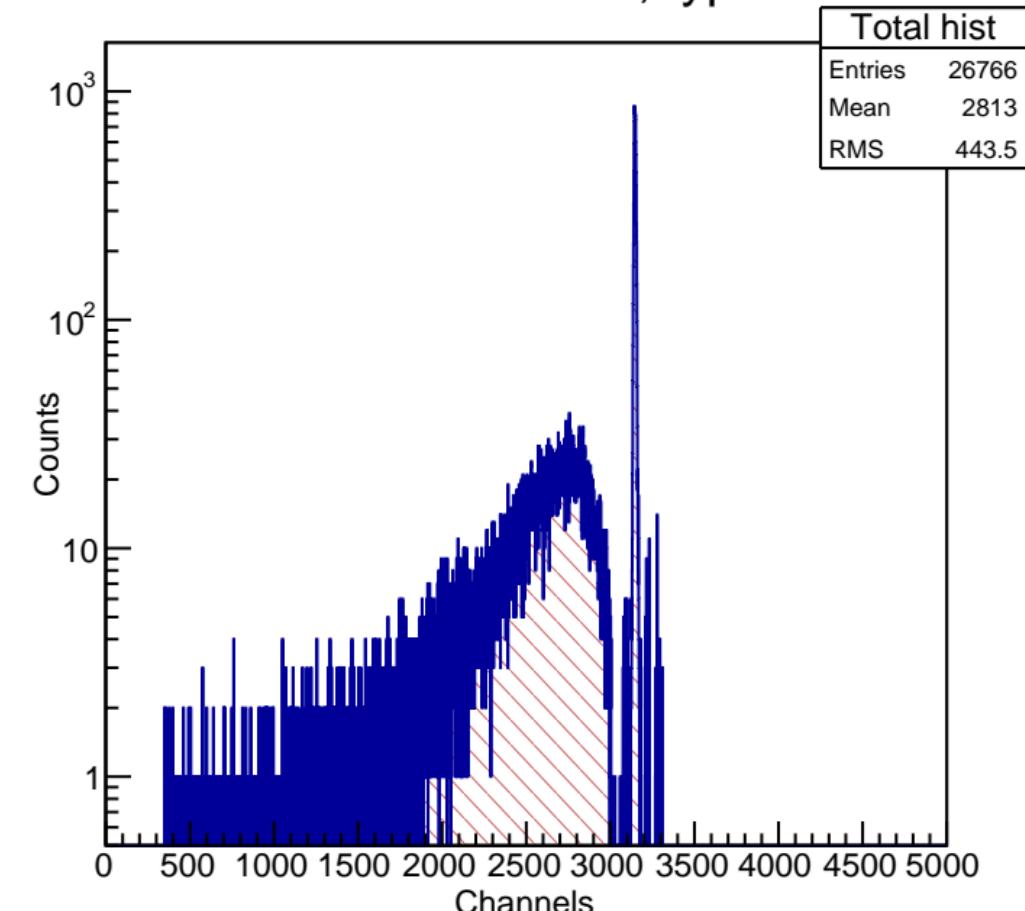
# TDCW Octet 15, type1



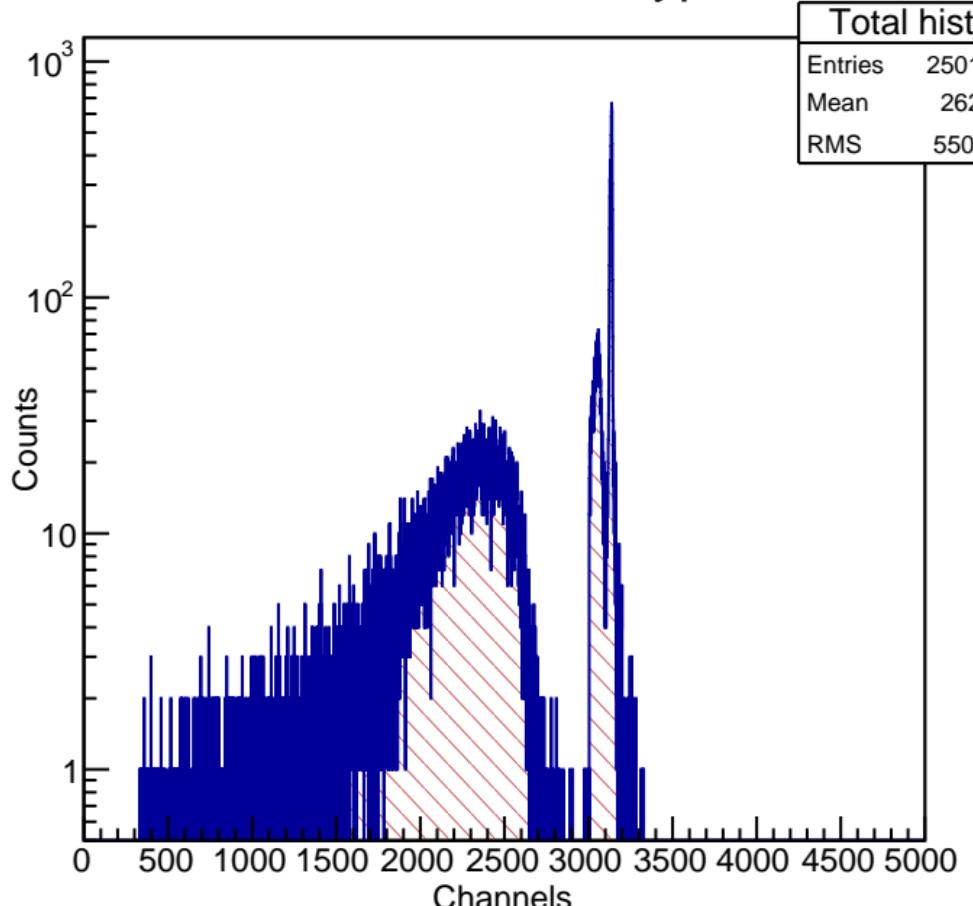
# TDCE Octet 16, type1



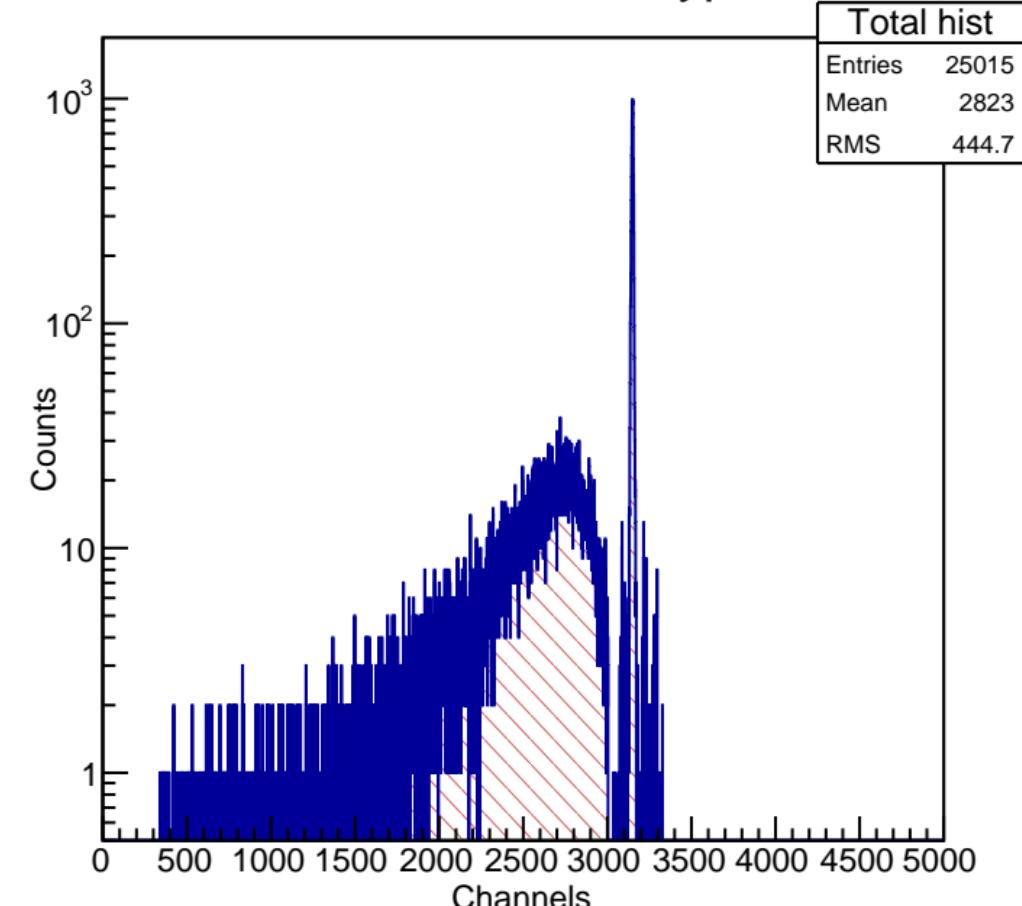
# TDCW Octet 16, type1



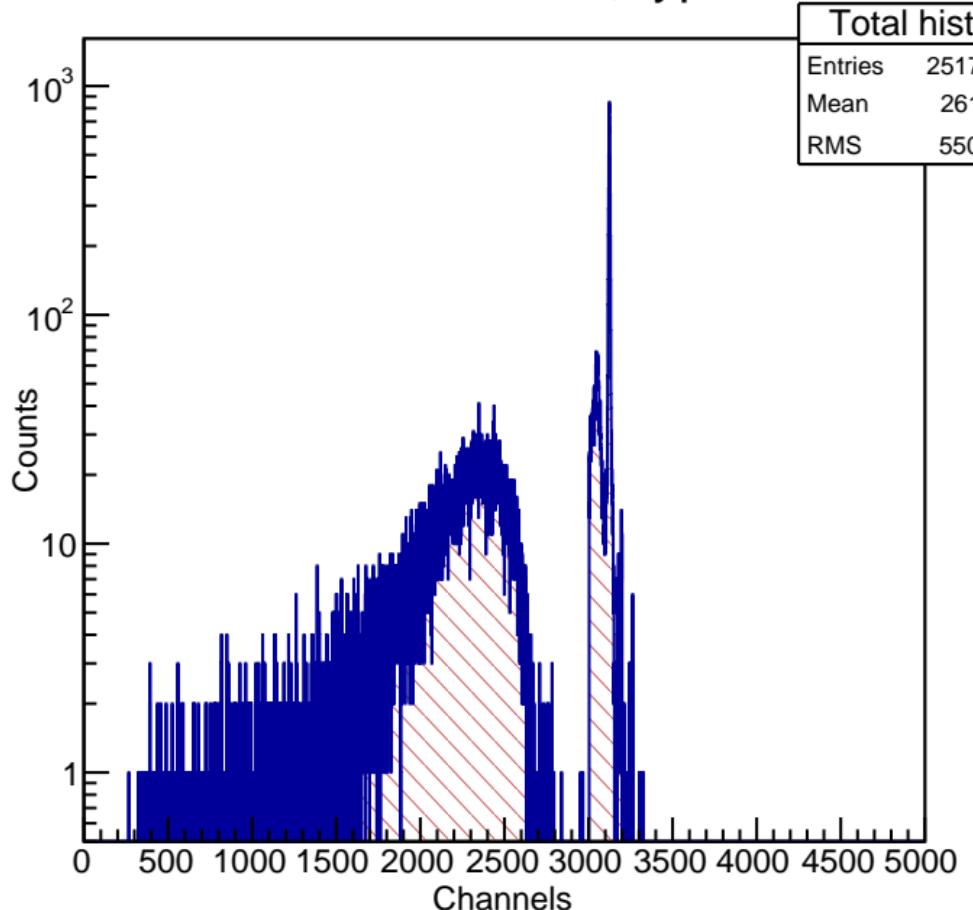
# TDCE Octet 17, type1



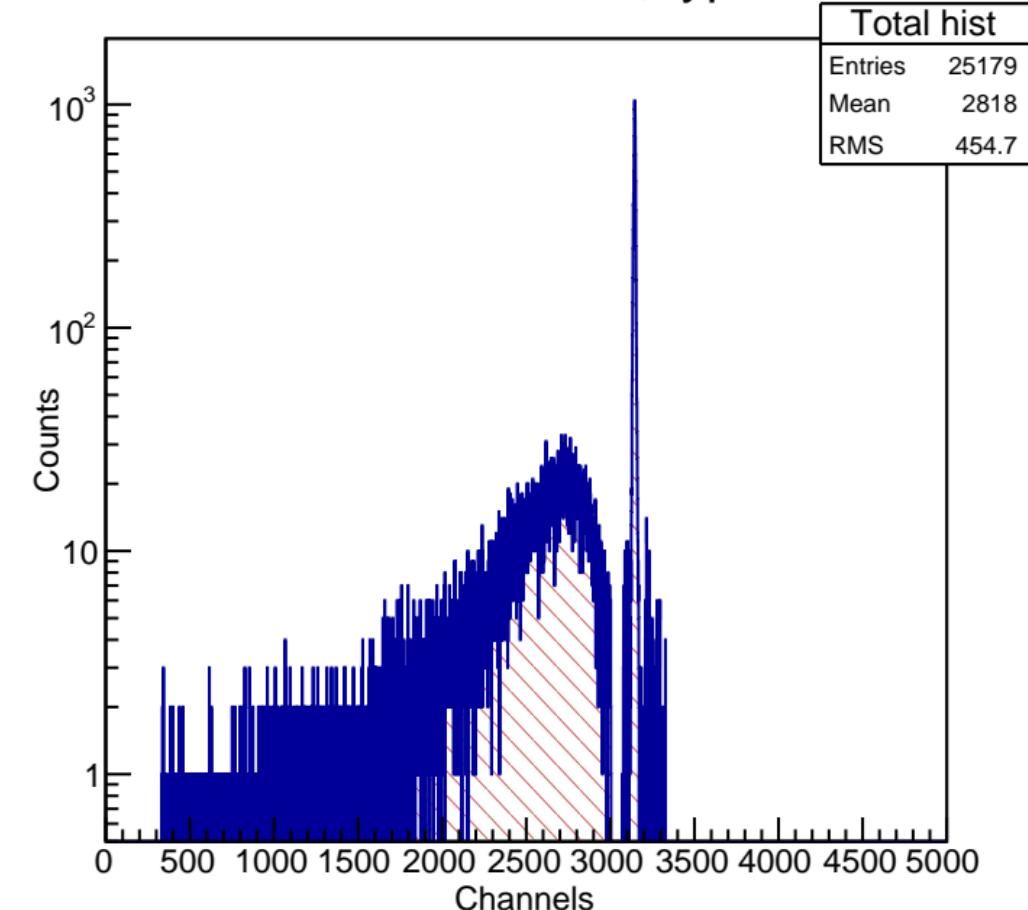
# TDCW Octet 17, type1



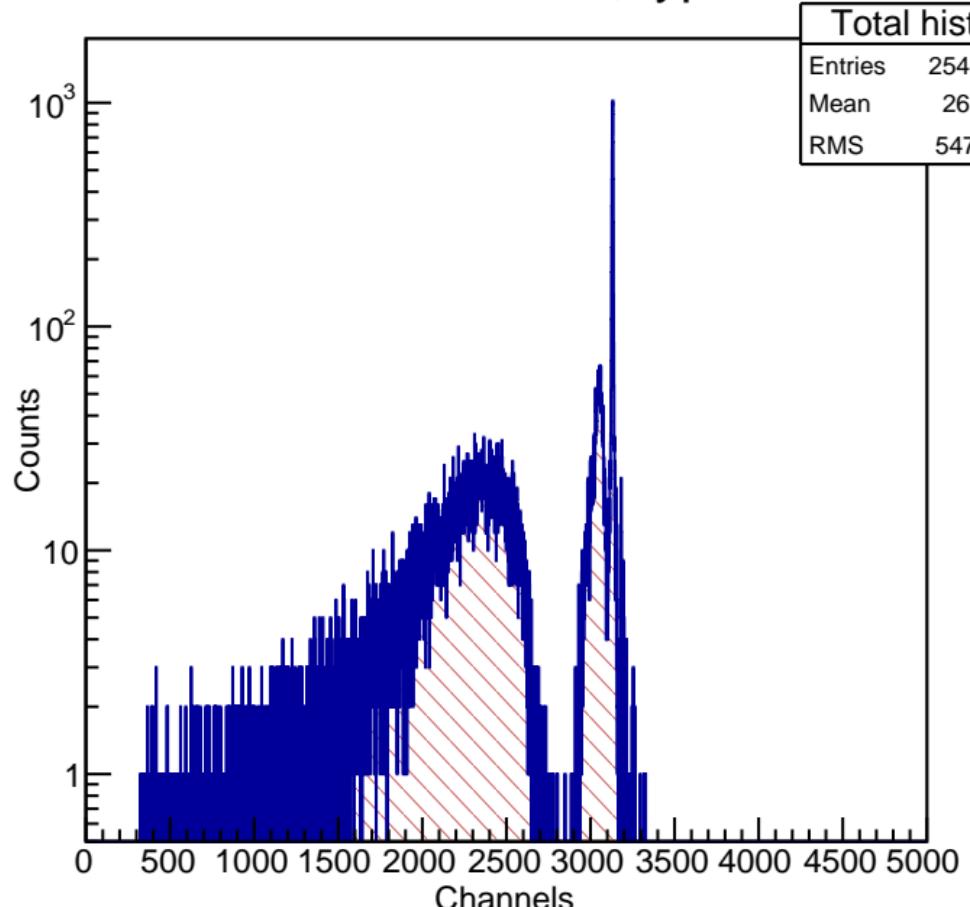
# TDCE Octet 18, type1



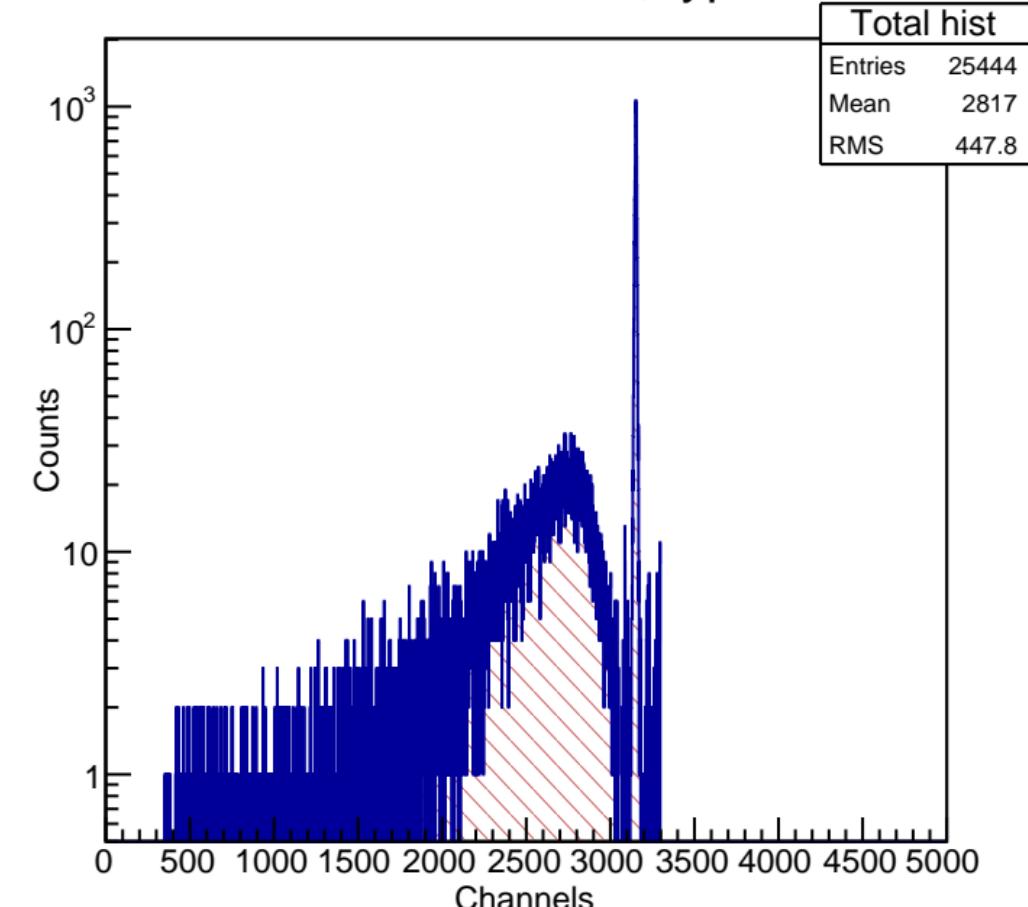
# TDCW Octet 18, type1



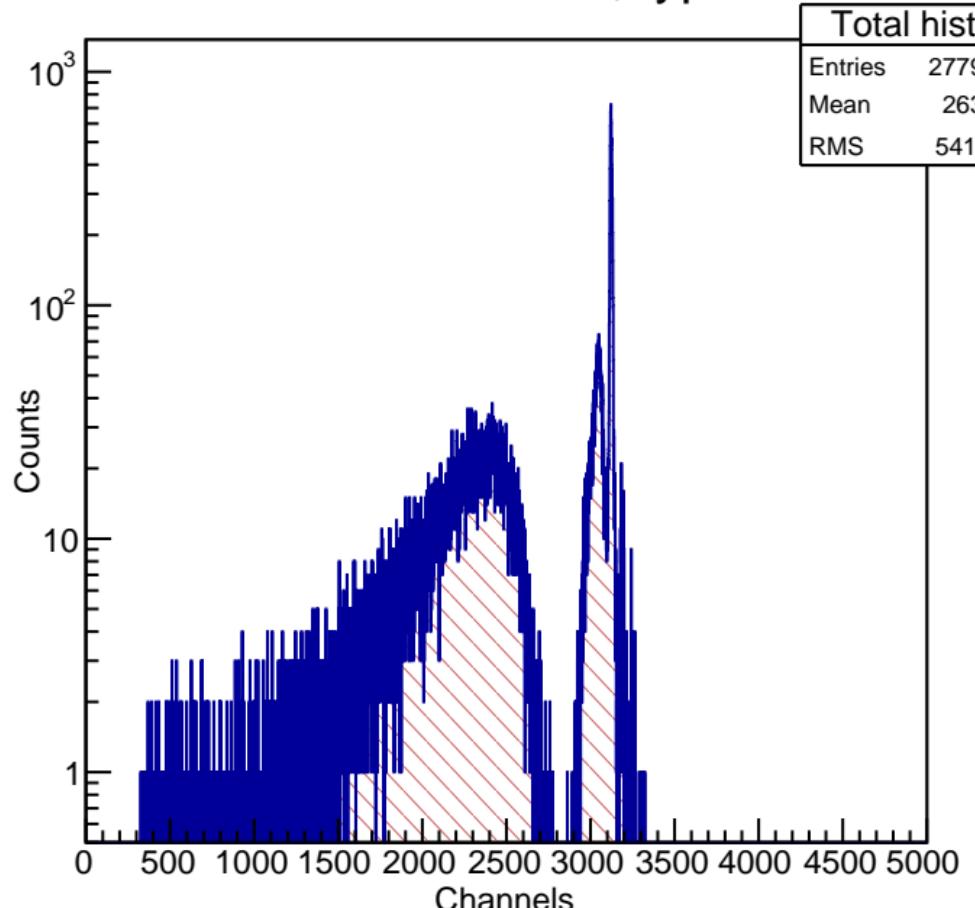
# TDCE Octet 19, type1



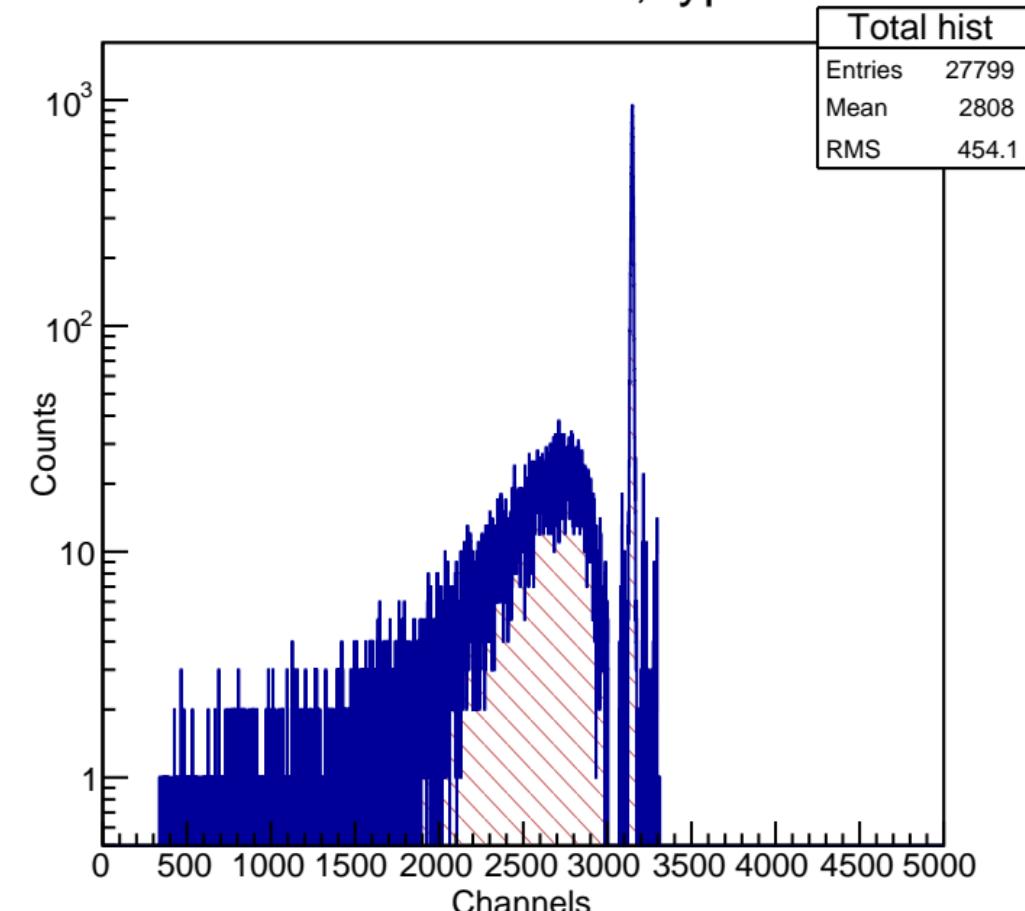
# TDCW Octet 19, type1



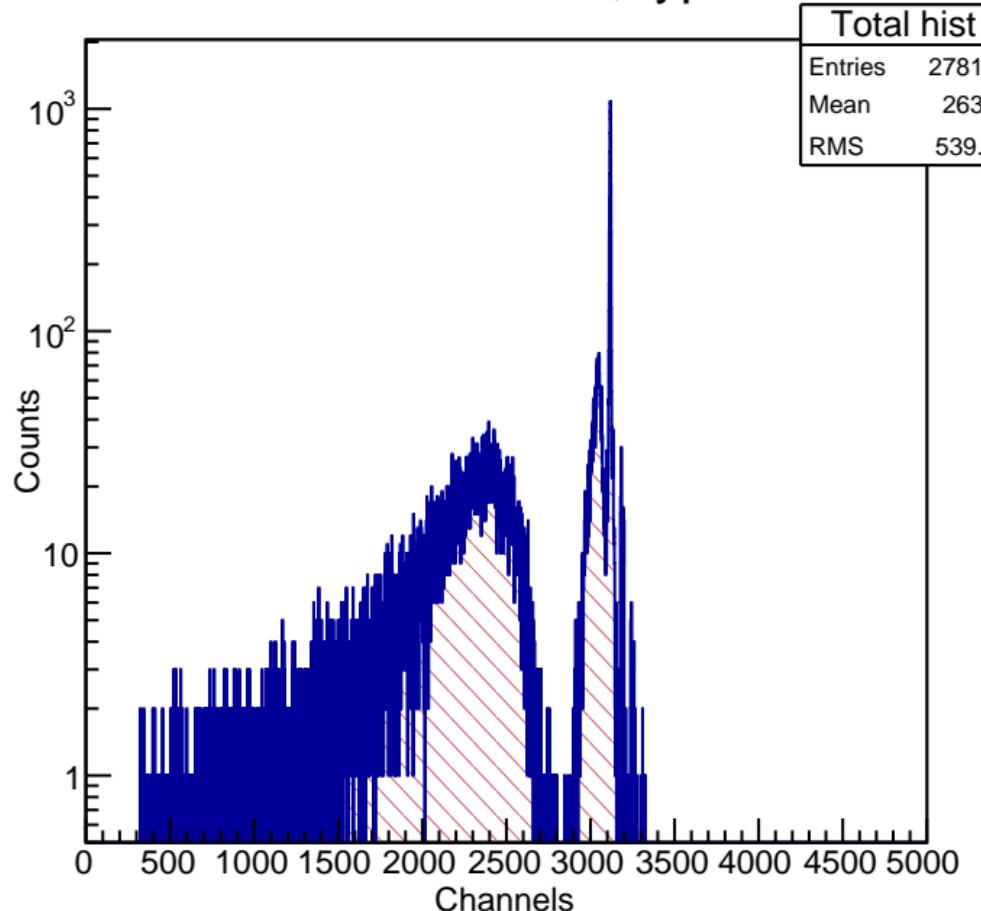
# TDCE Octet 20, type1



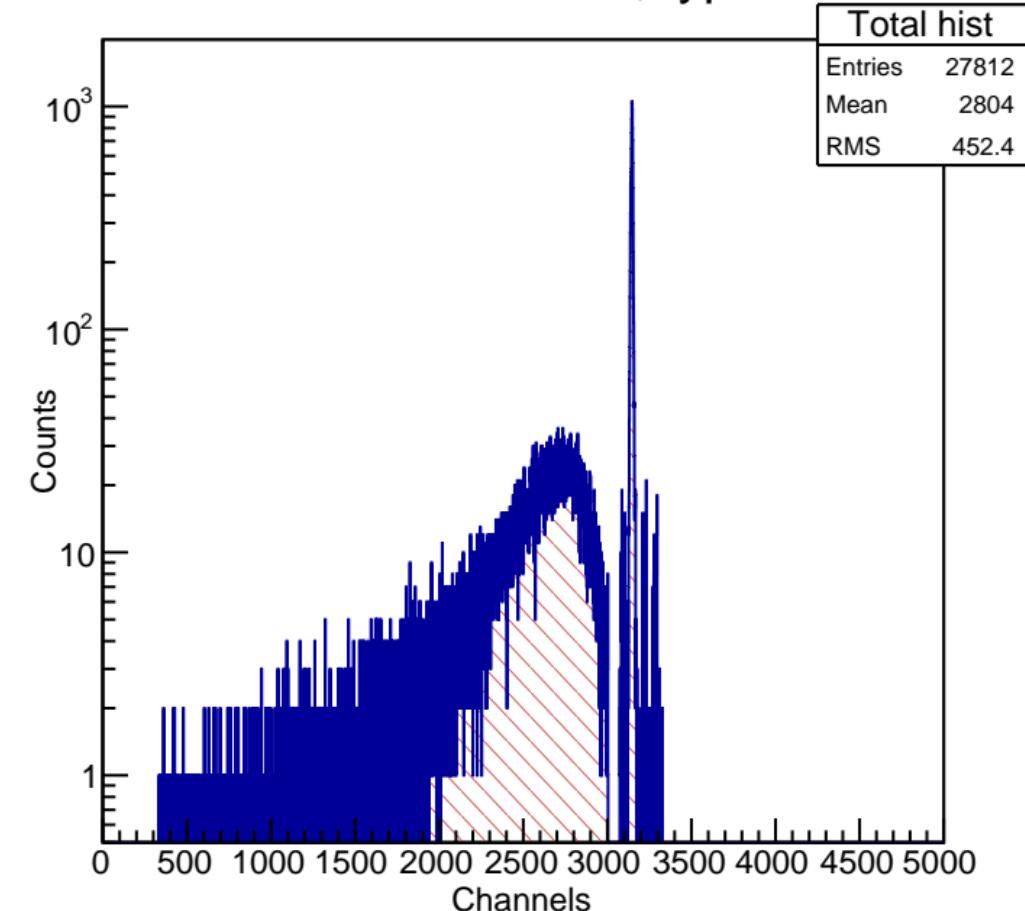
# TDCW Octet 20, type1



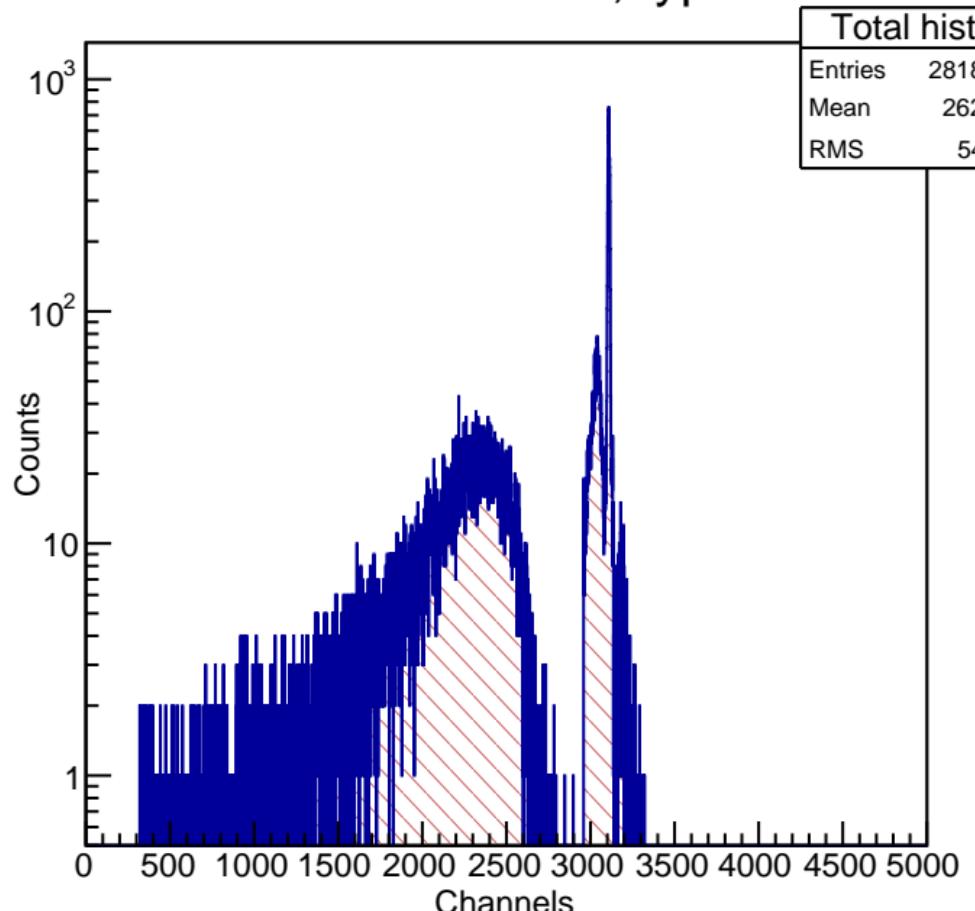
# TDCE Octet 21, type1



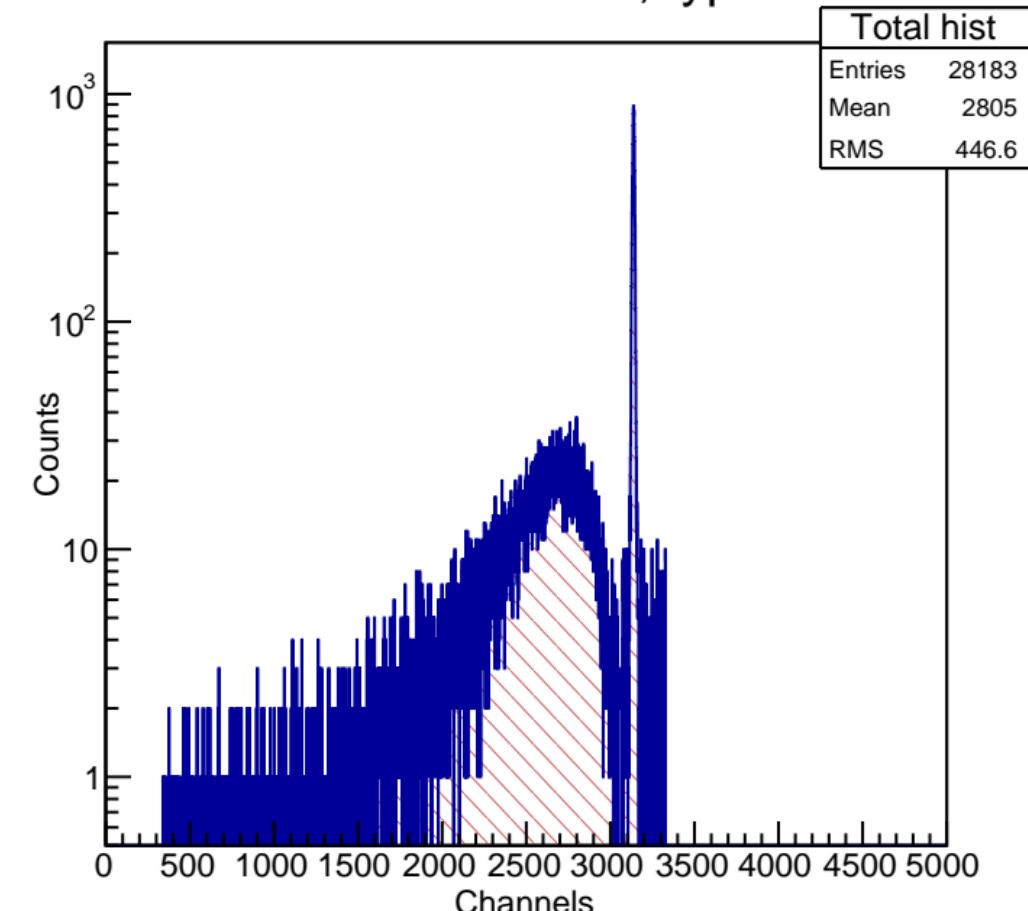
# TDCW Octet 21, type1



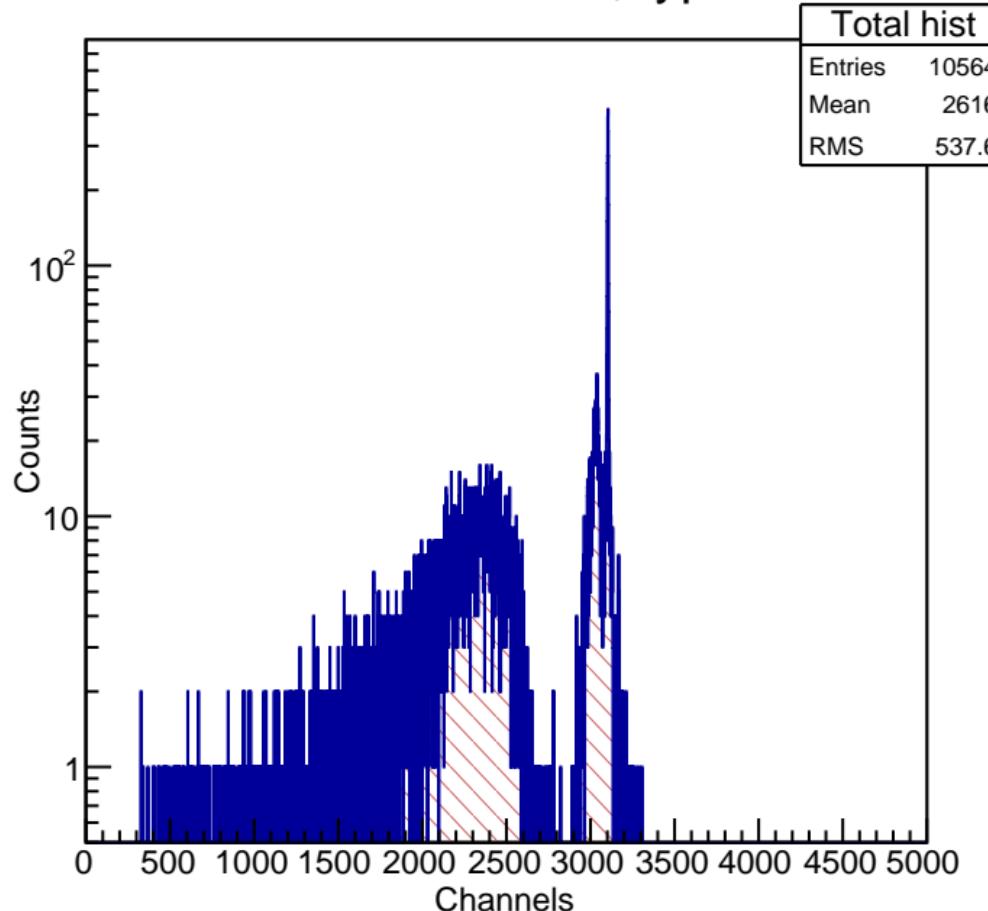
# TDCE Octet 22, type1



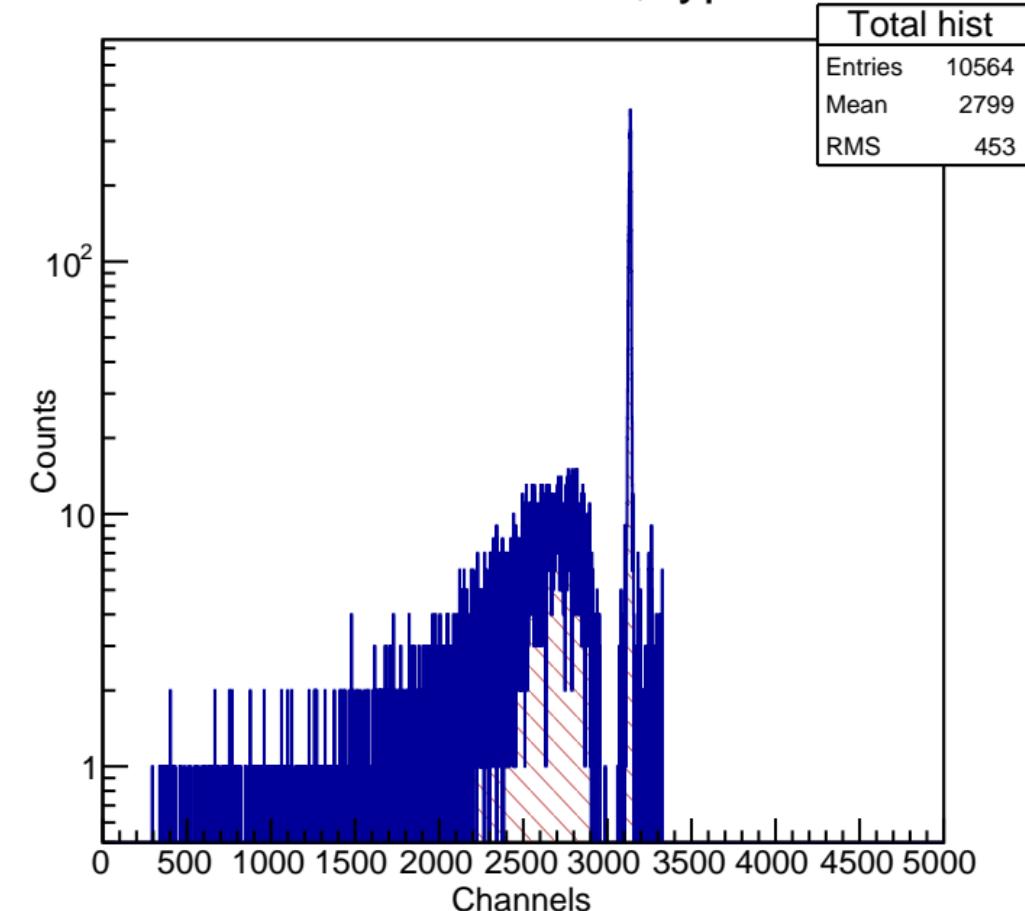
# TDCW Octet 22, type1



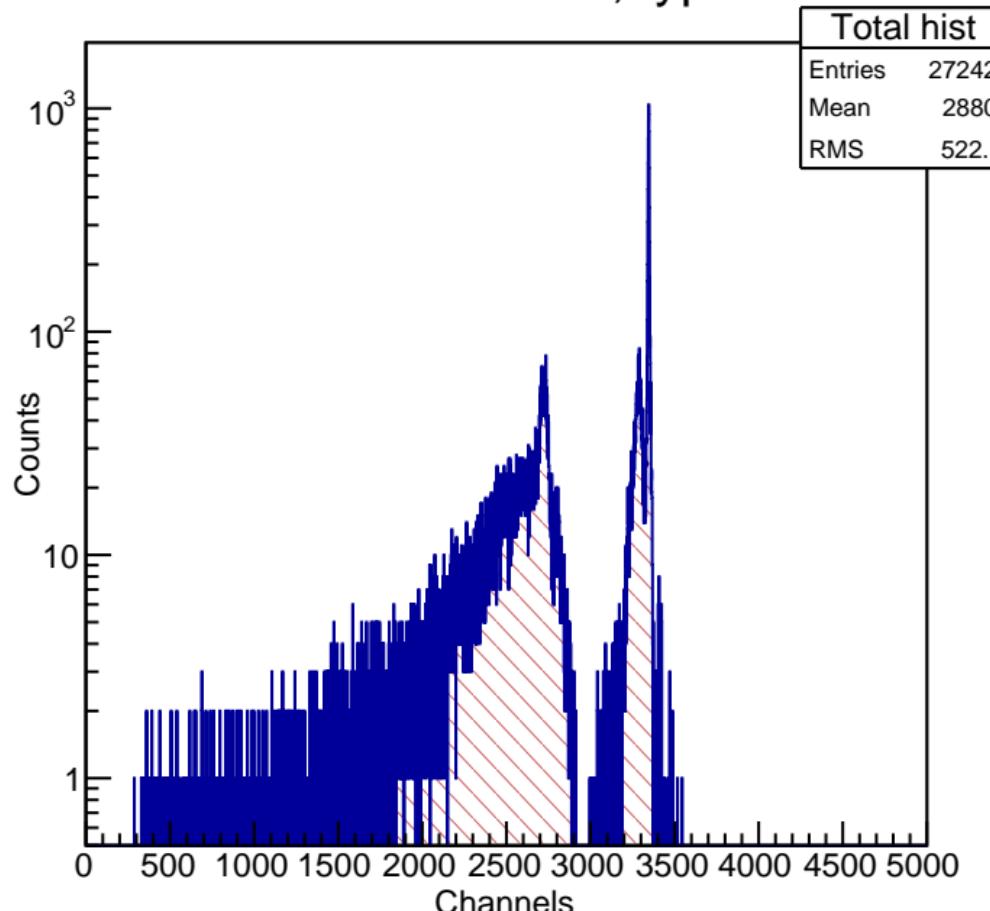
# TDCE Octet 23, type1



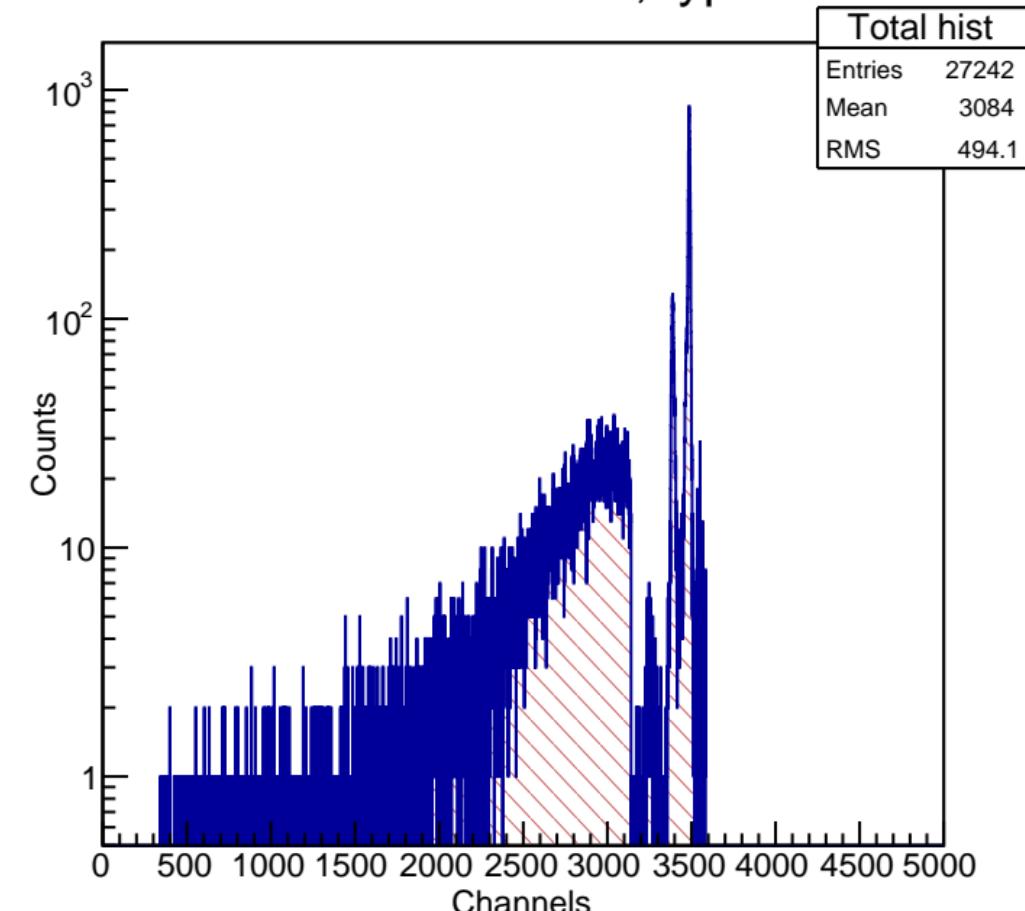
# TDCW Octet 23, type1



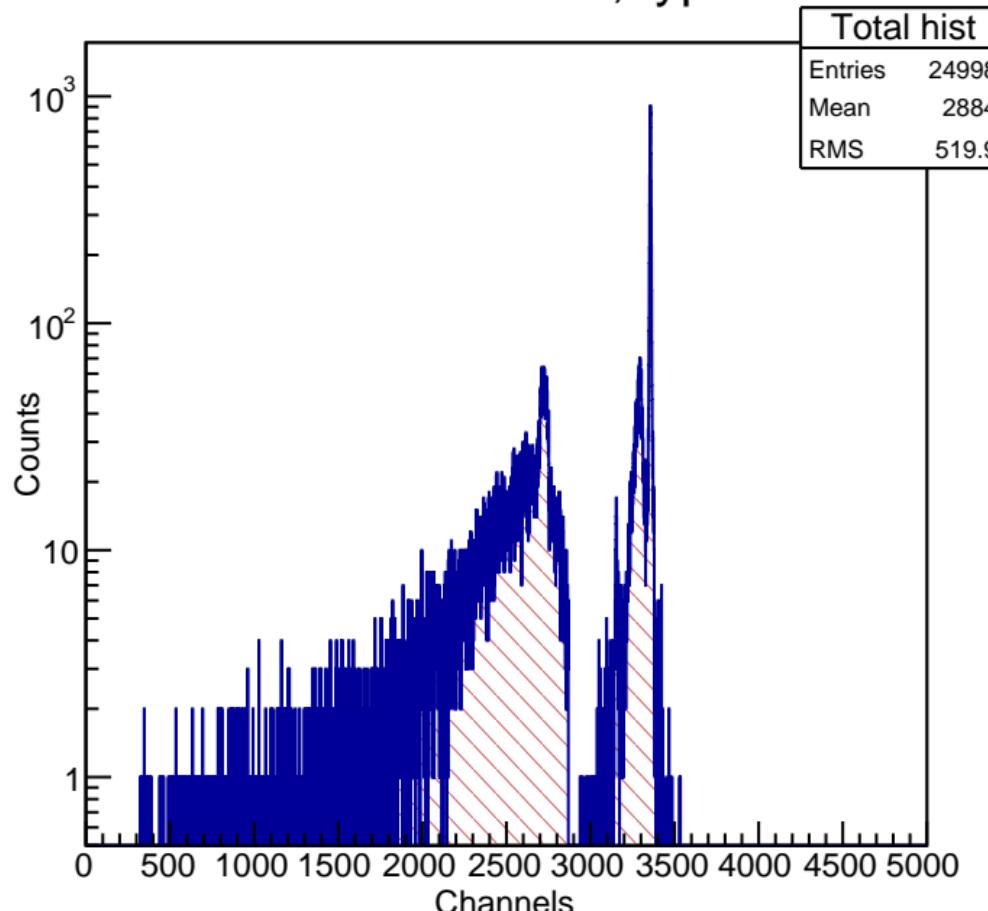
# TDCE Octet 24, type1



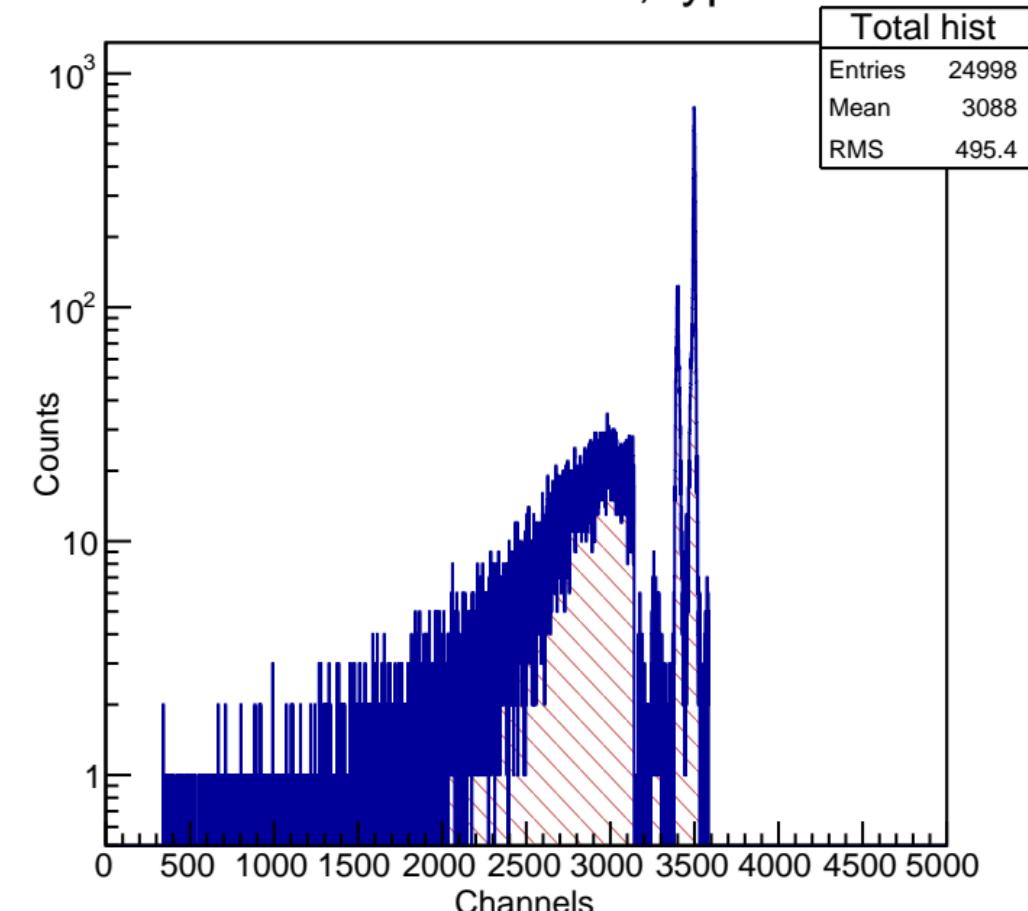
# TDCW Octet 24, type1



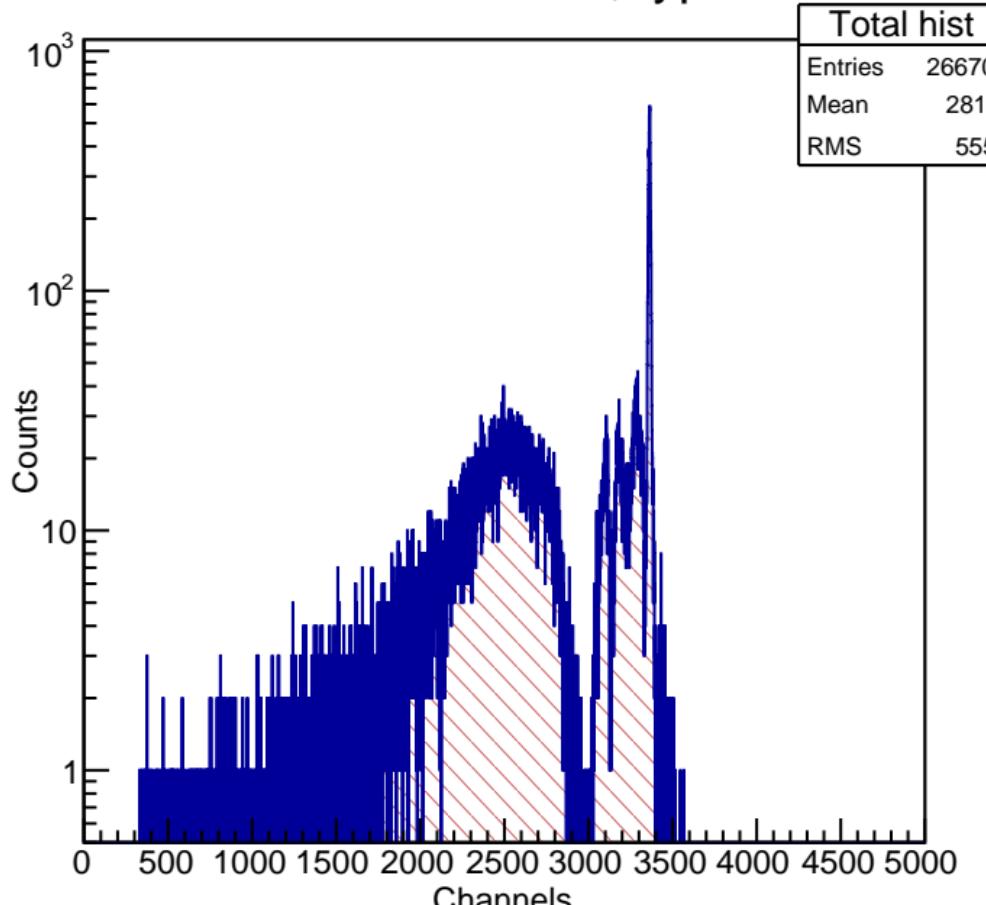
# TDCE Octet 25, type1



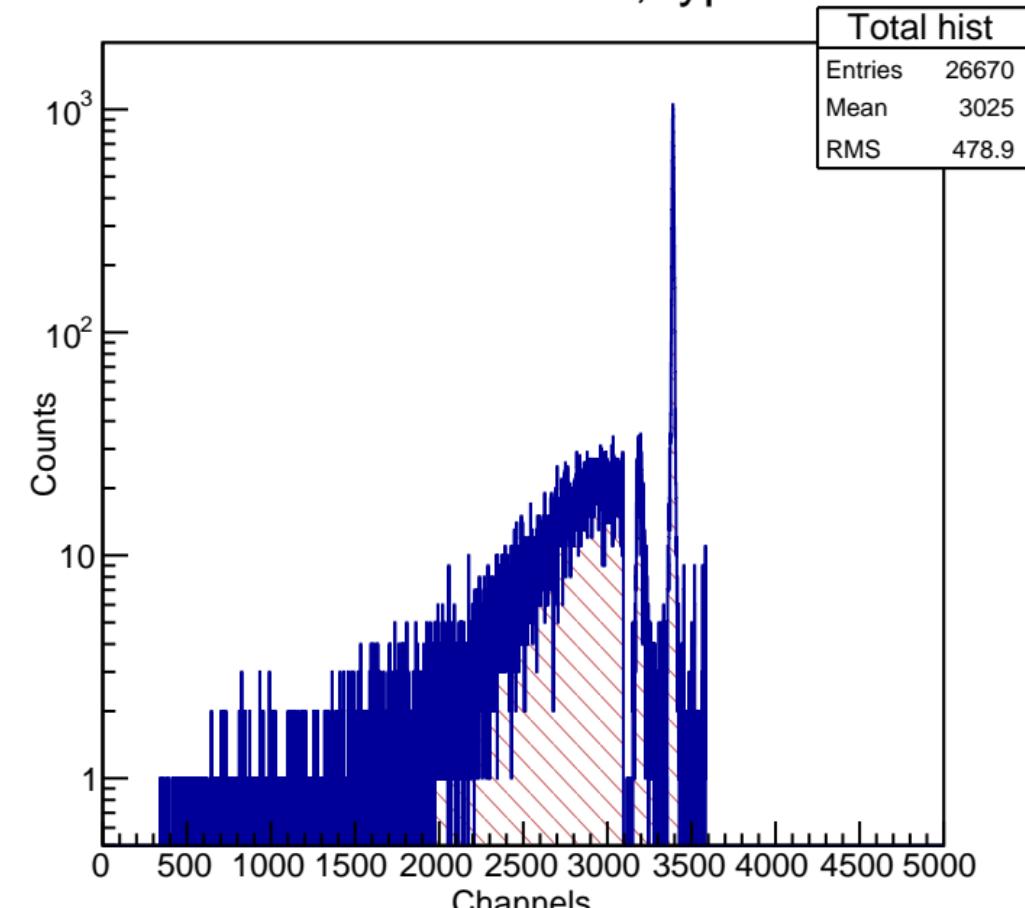
# TDCW Octet 25, type1



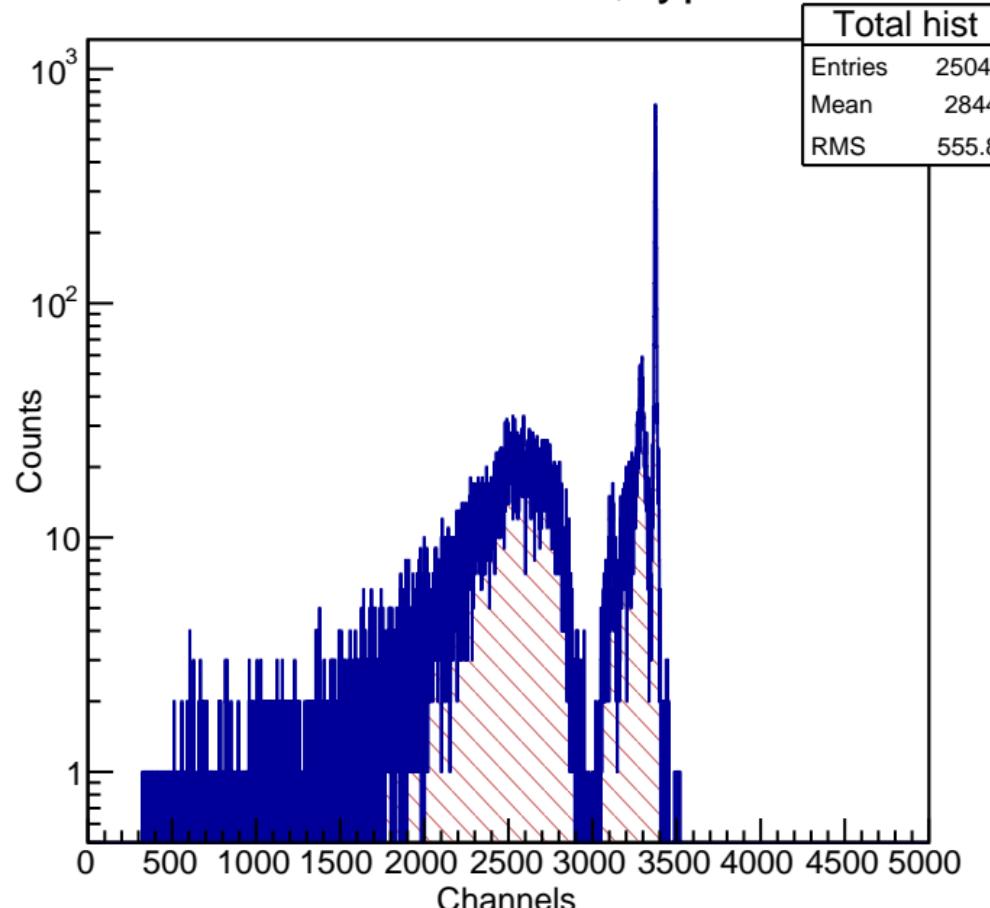
# TDCE Octet 26, type1



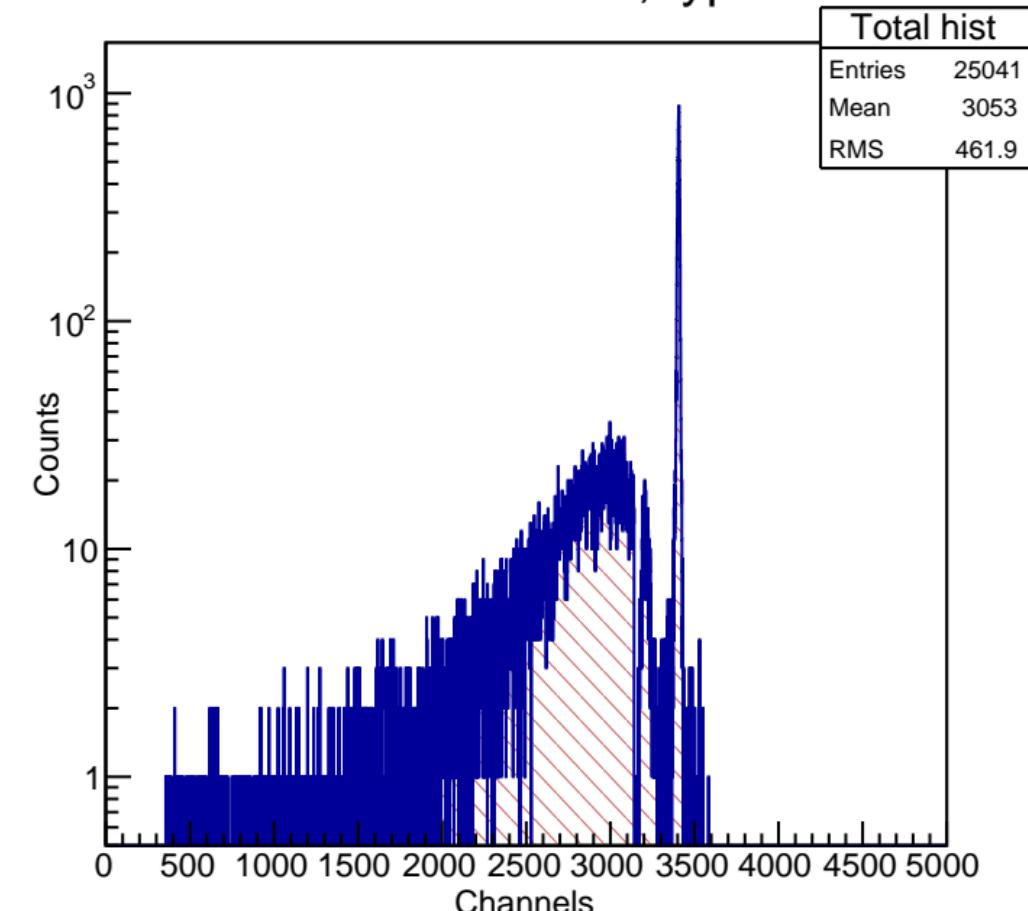
# TDCW Octet 26, type1



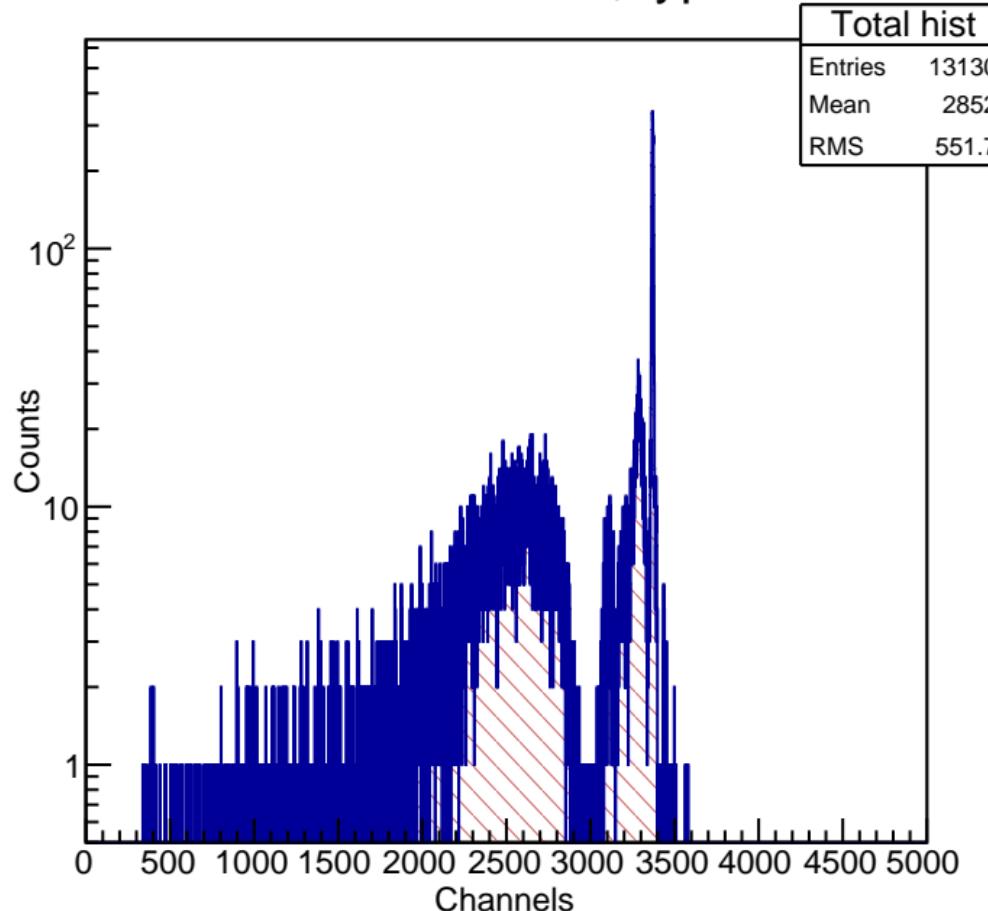
# TDCE Octet 27, type1



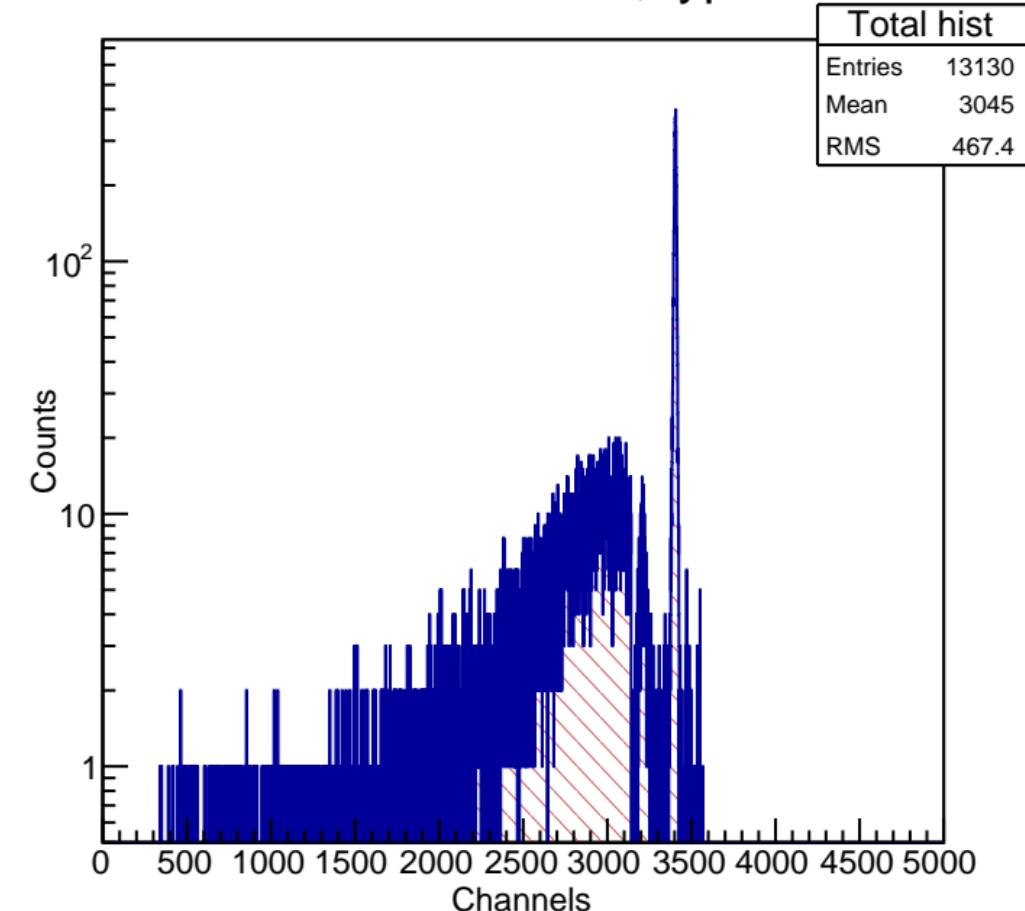
# TDCW Octet 27, type1



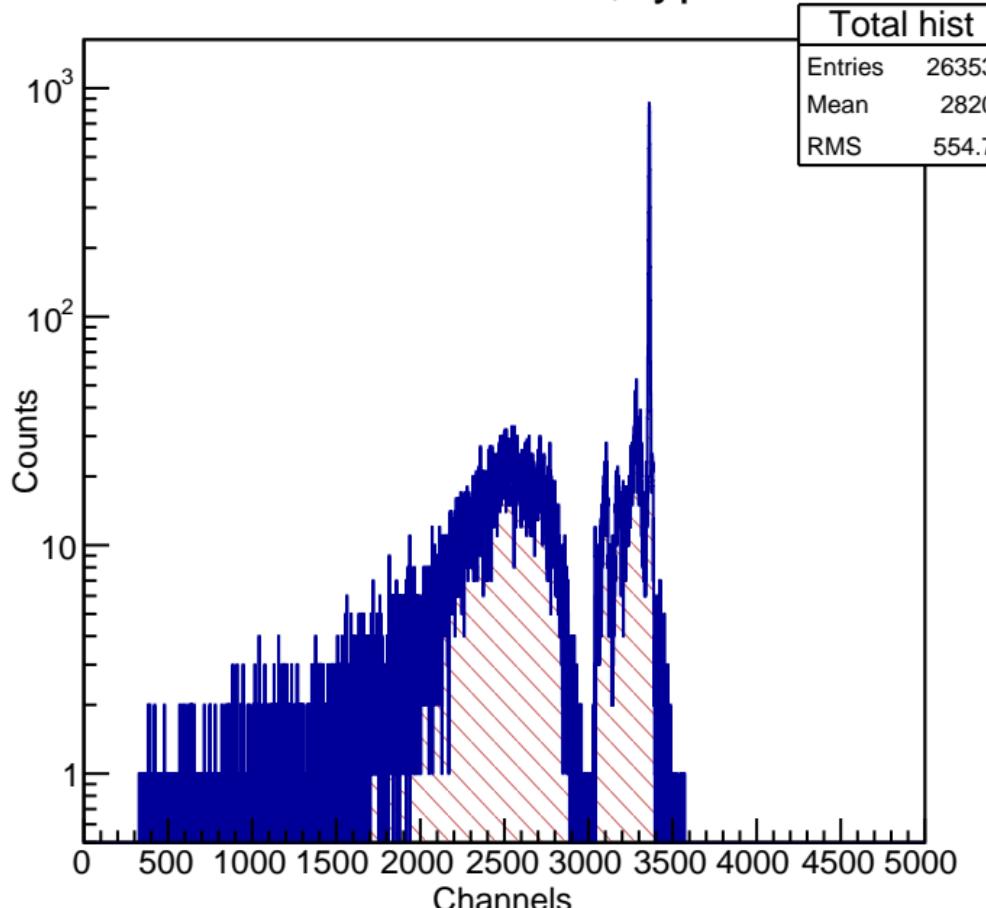
# TDCE Octet 28, type1



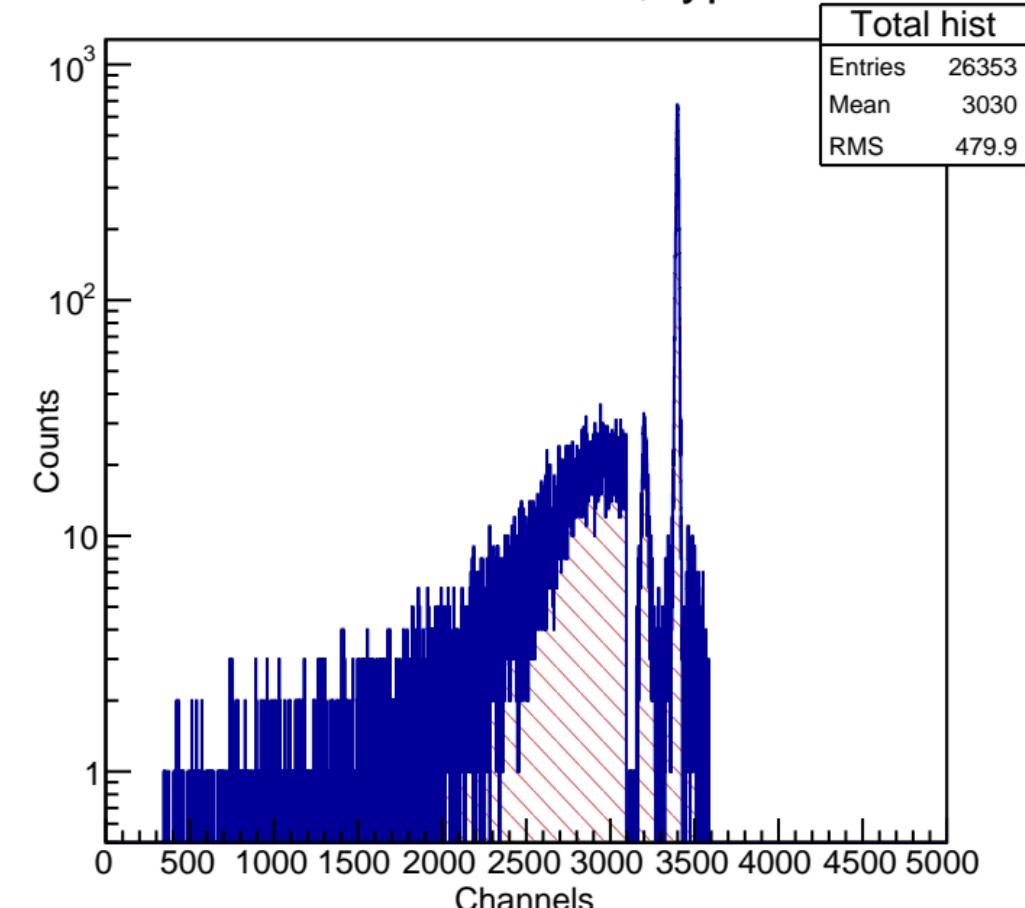
# TDCW Octet 28, type1



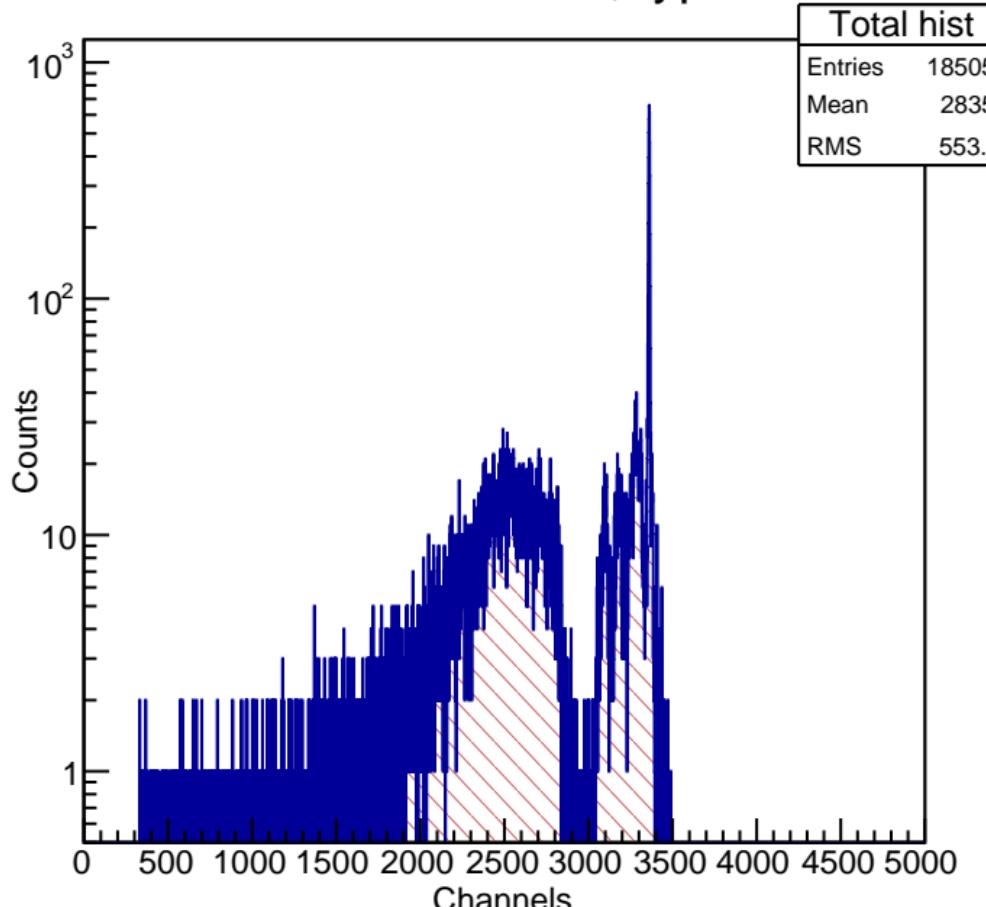
# TDCE Octet 29, type1



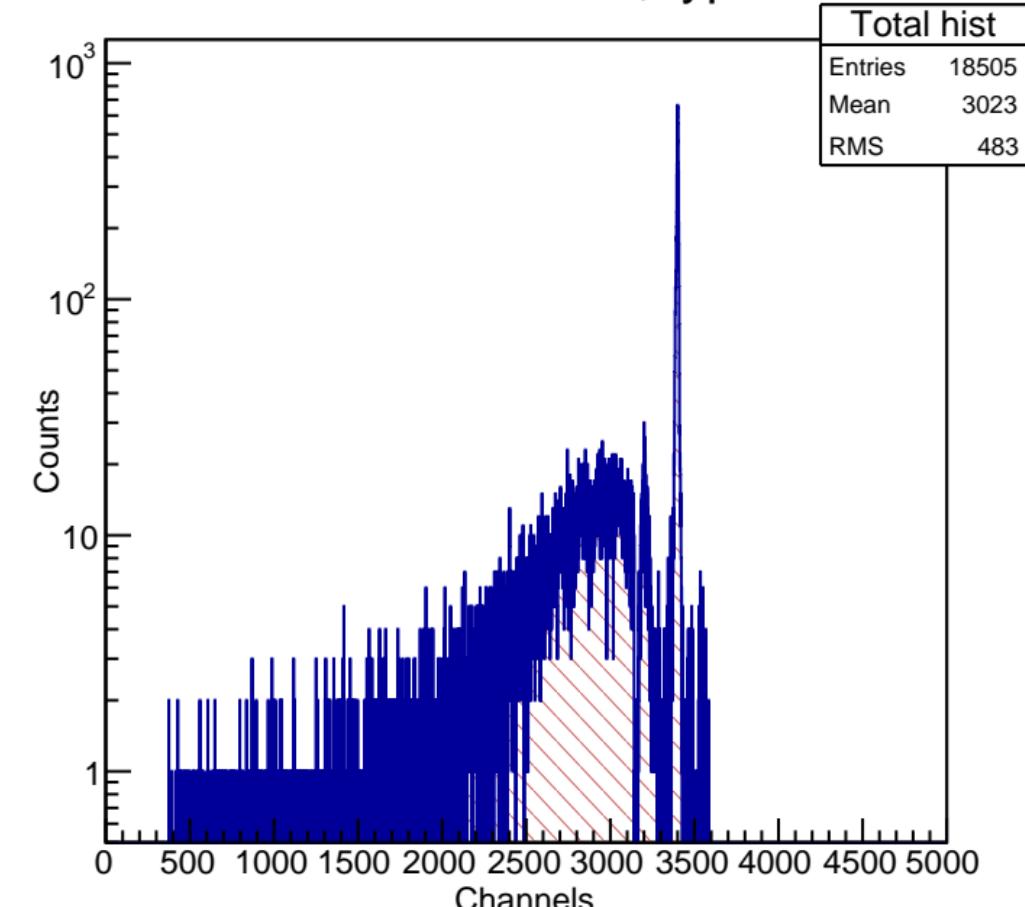
# TDCW Octet 29, type1



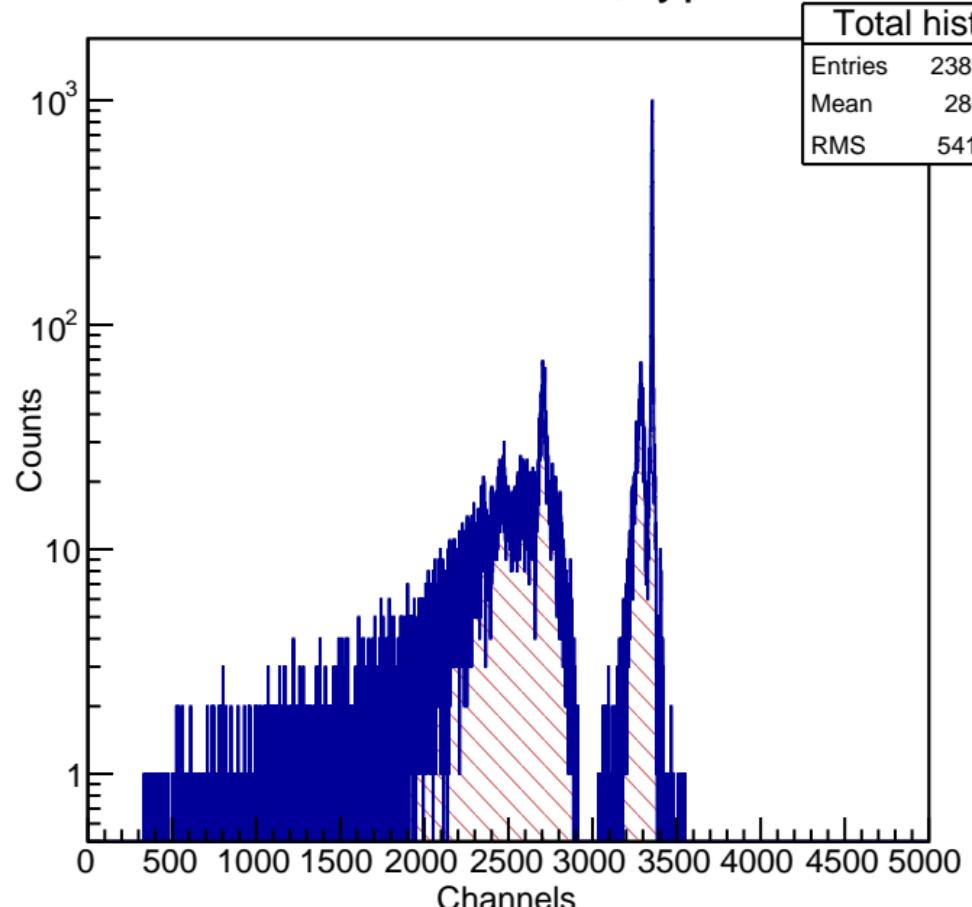
# TDCE Octet 30, type1



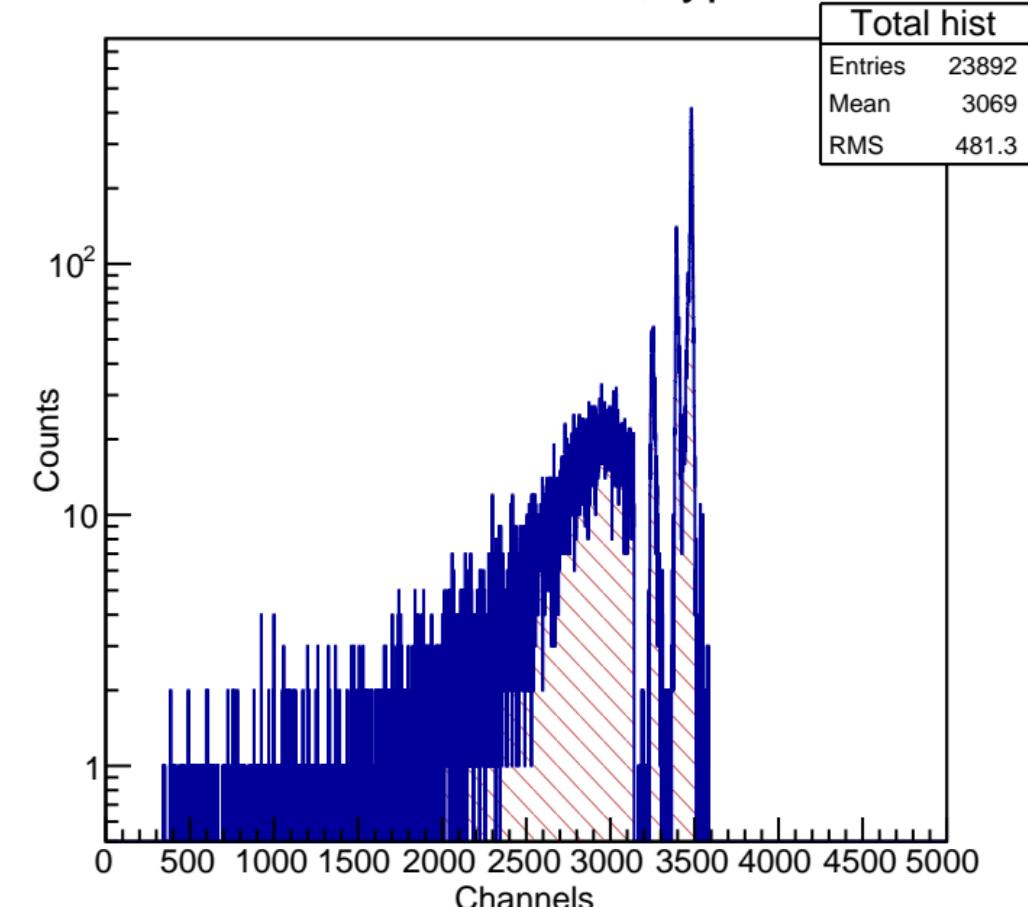
# TDCW Octet 30, type1



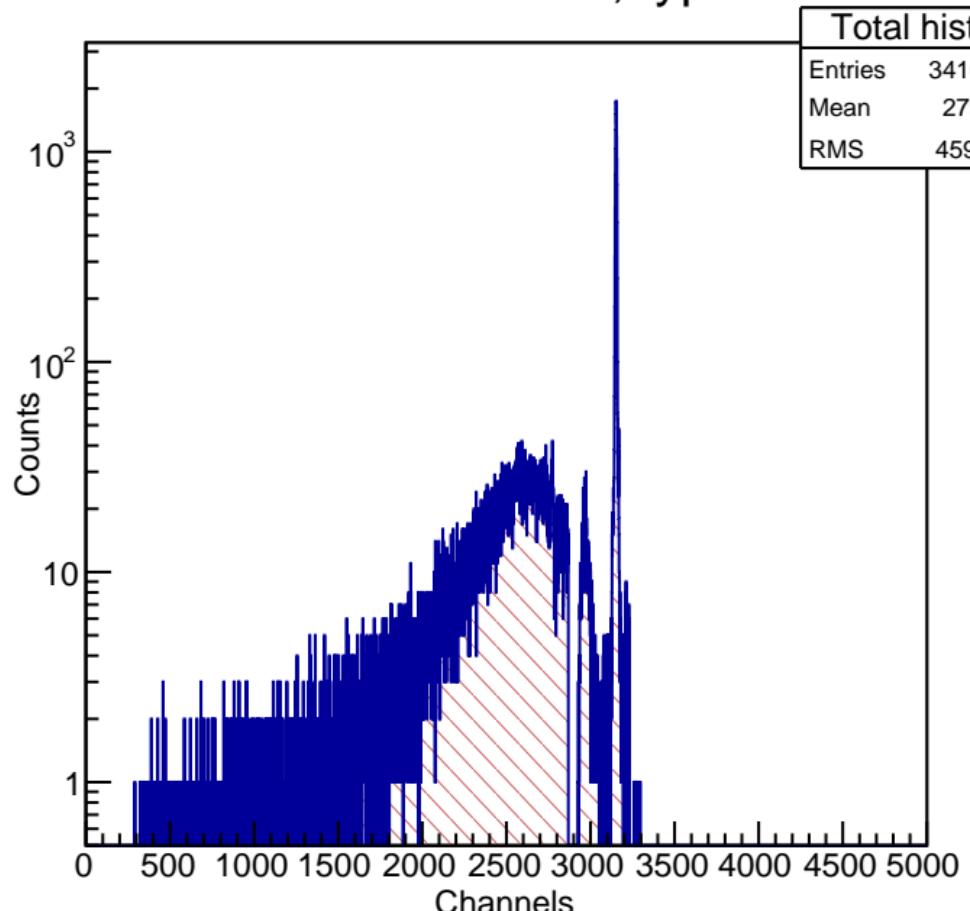
# TDCE Octet 31, type1



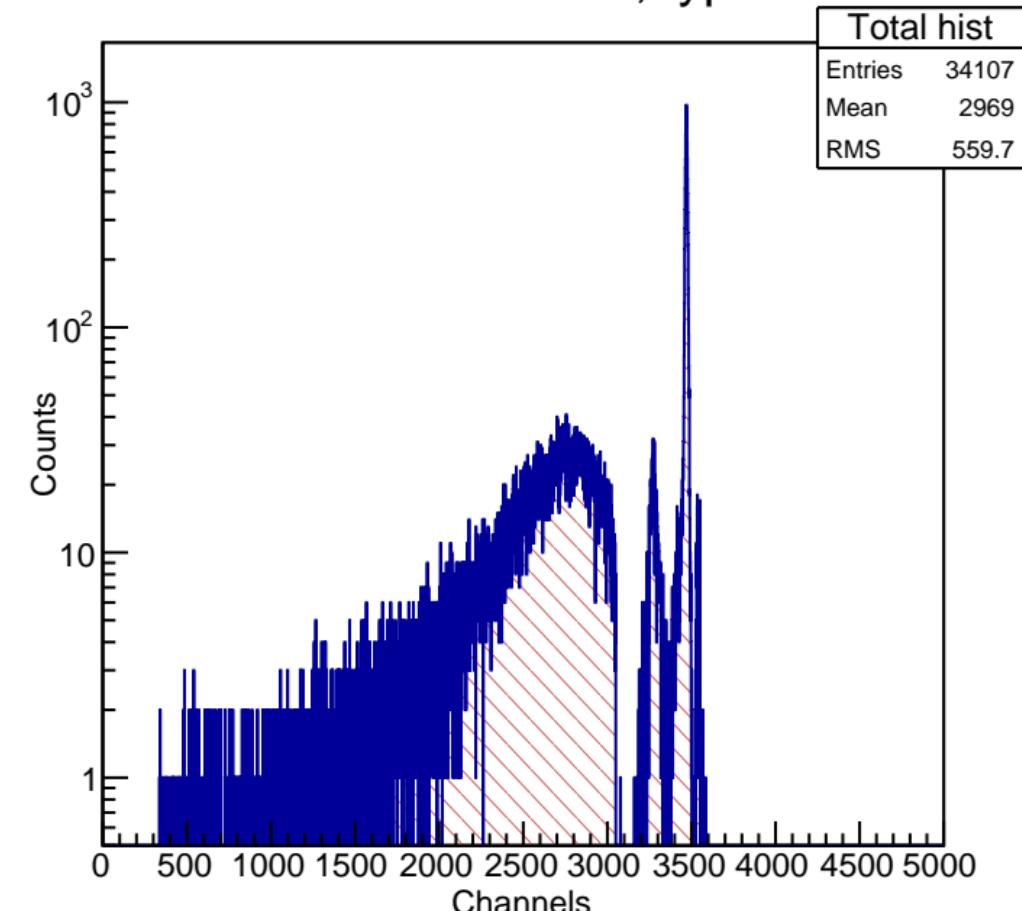
# TDCW Octet 31, type1



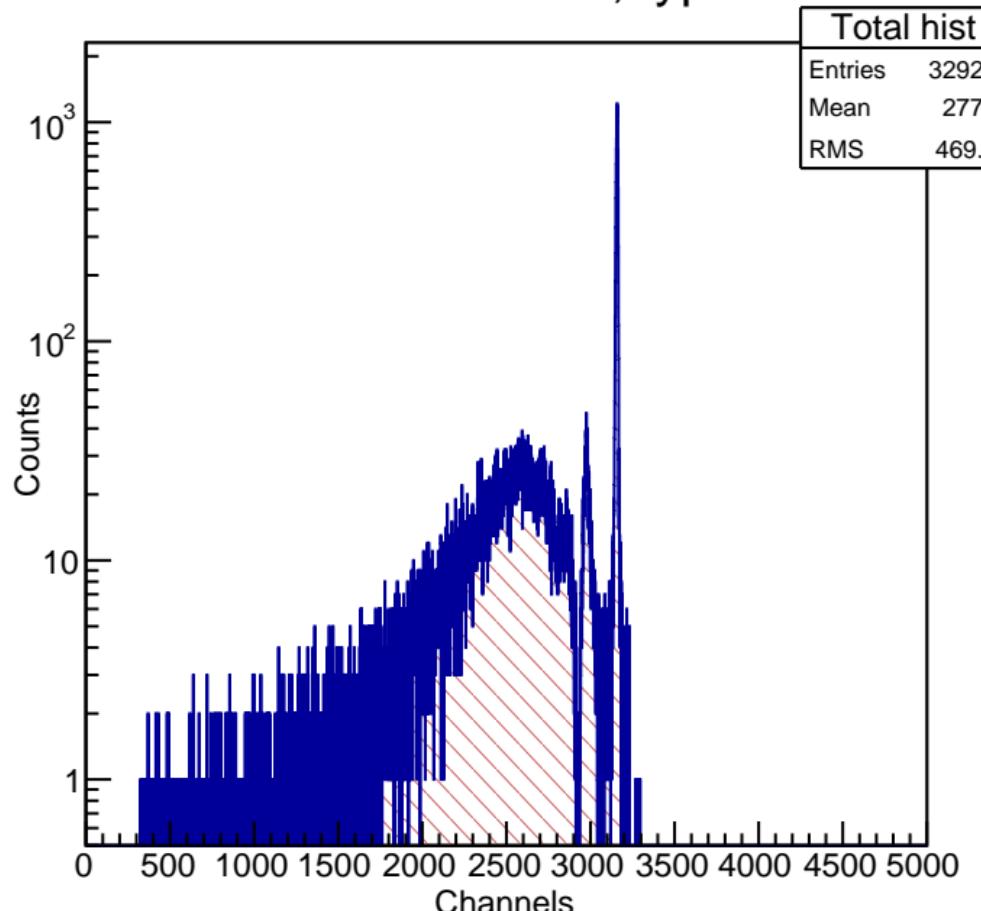
# TDCE Octet 32, type1



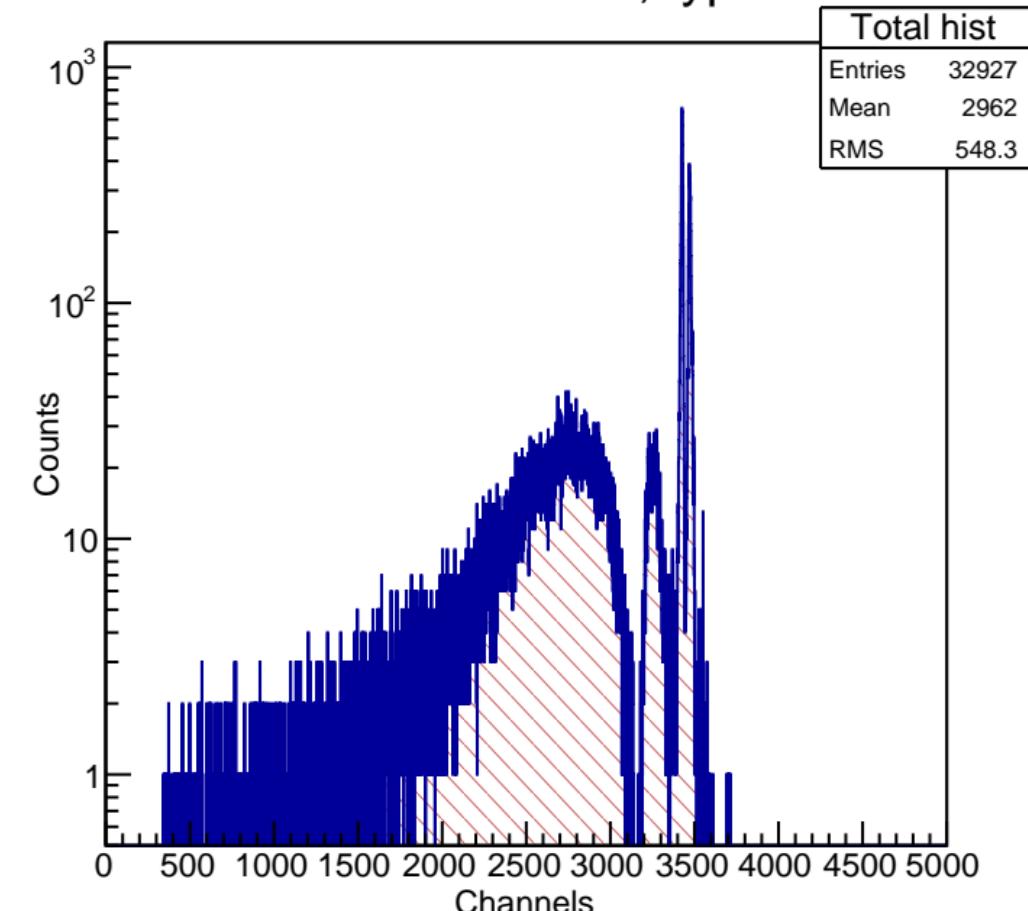
# TDCW Octet 32, type1



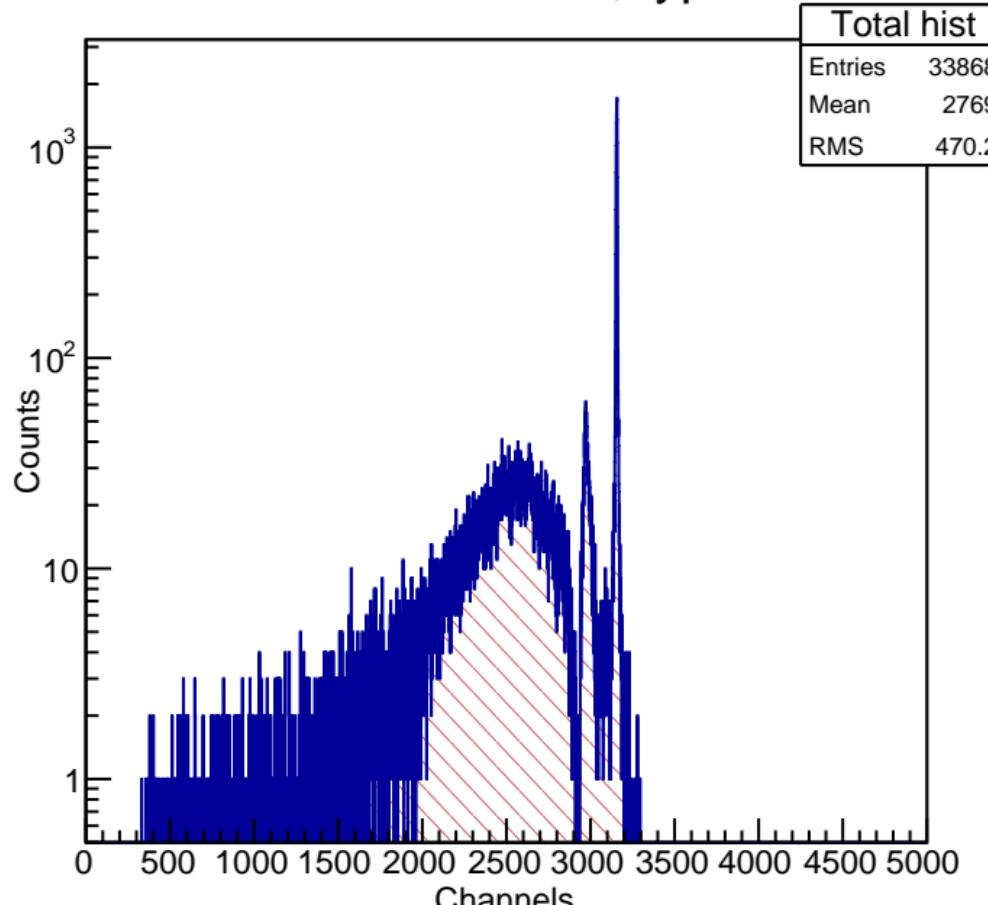
# TDCE Octet 33, type1



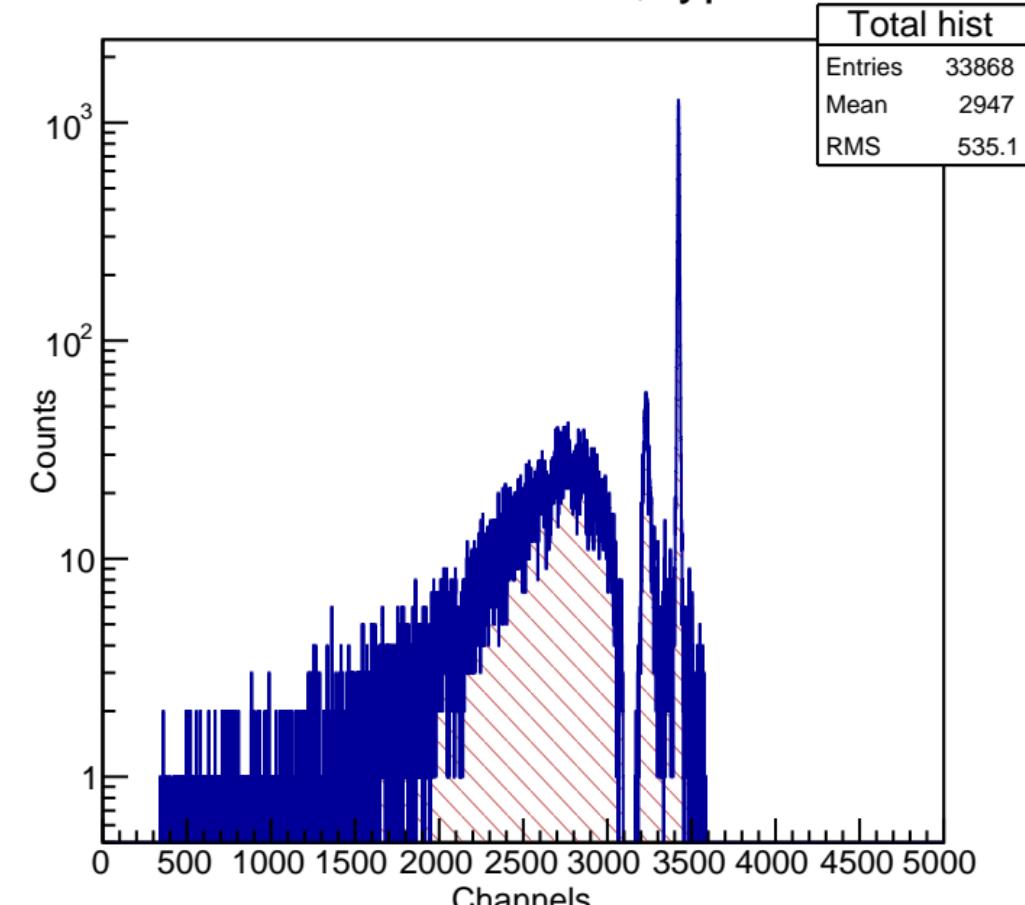
# TDCW Octet 33, type1



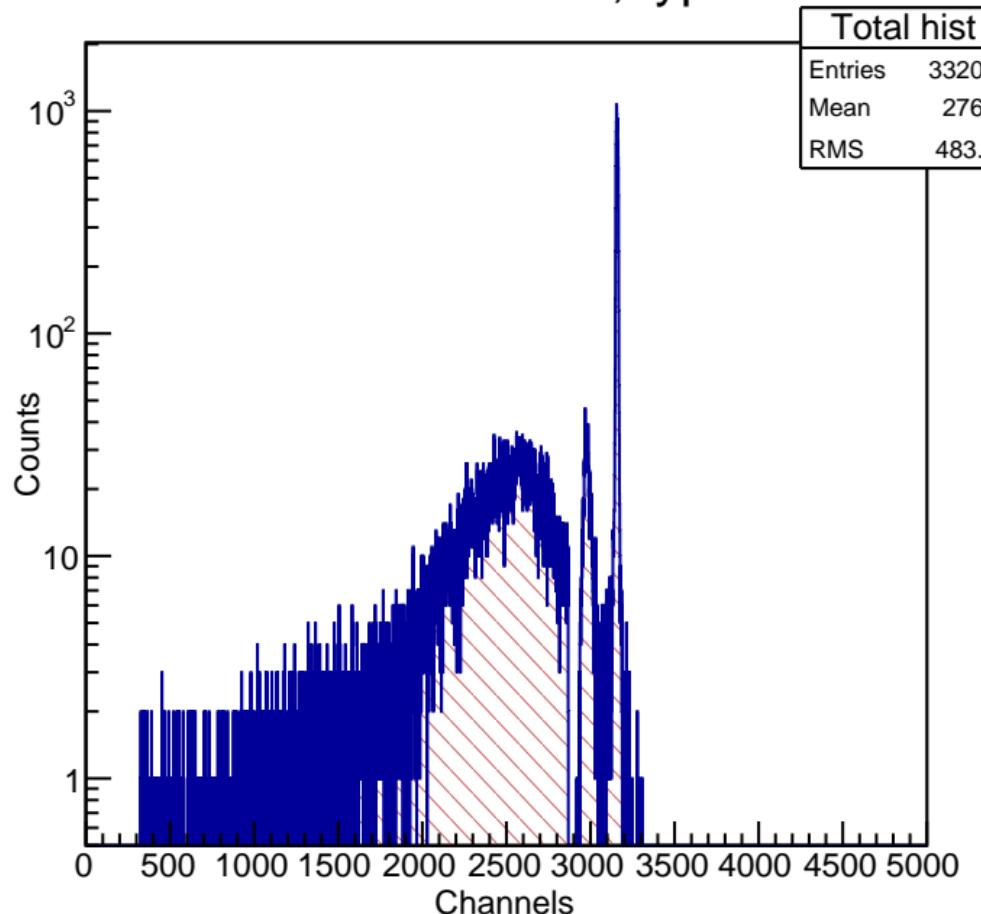
# TDCE Octet 34, type1



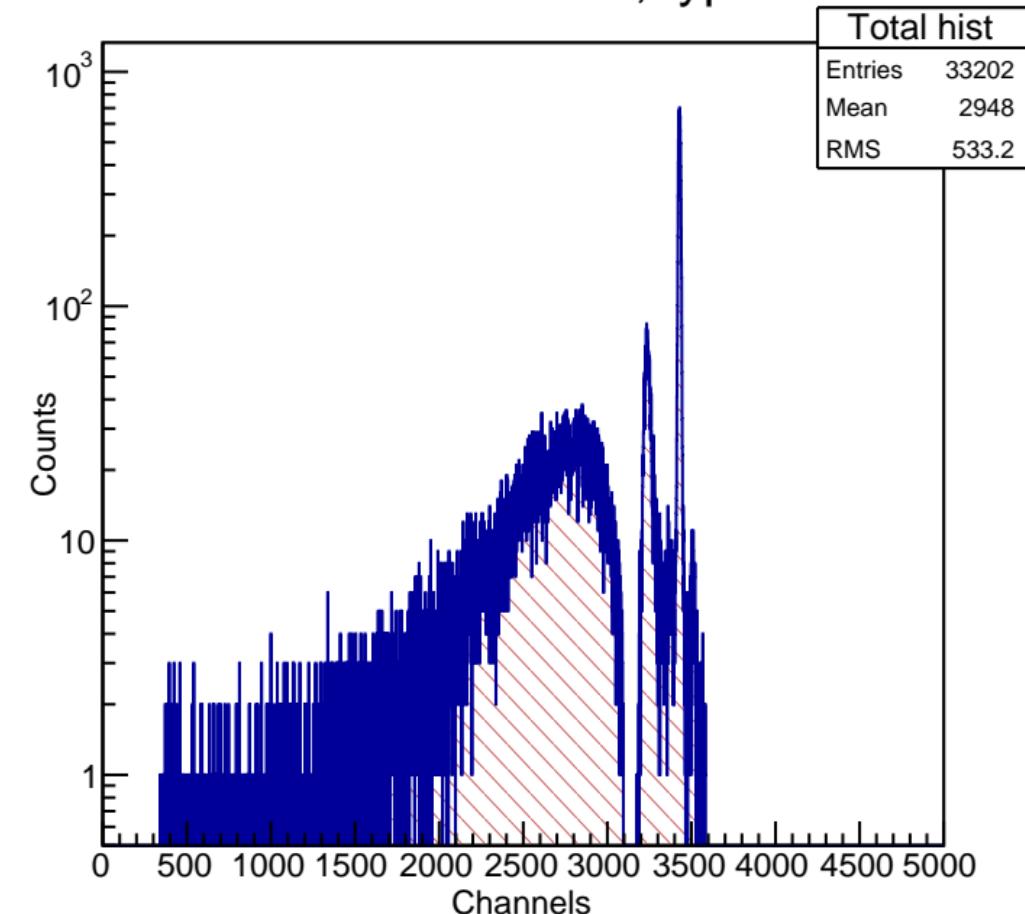
# TDCW Octet 34, type1



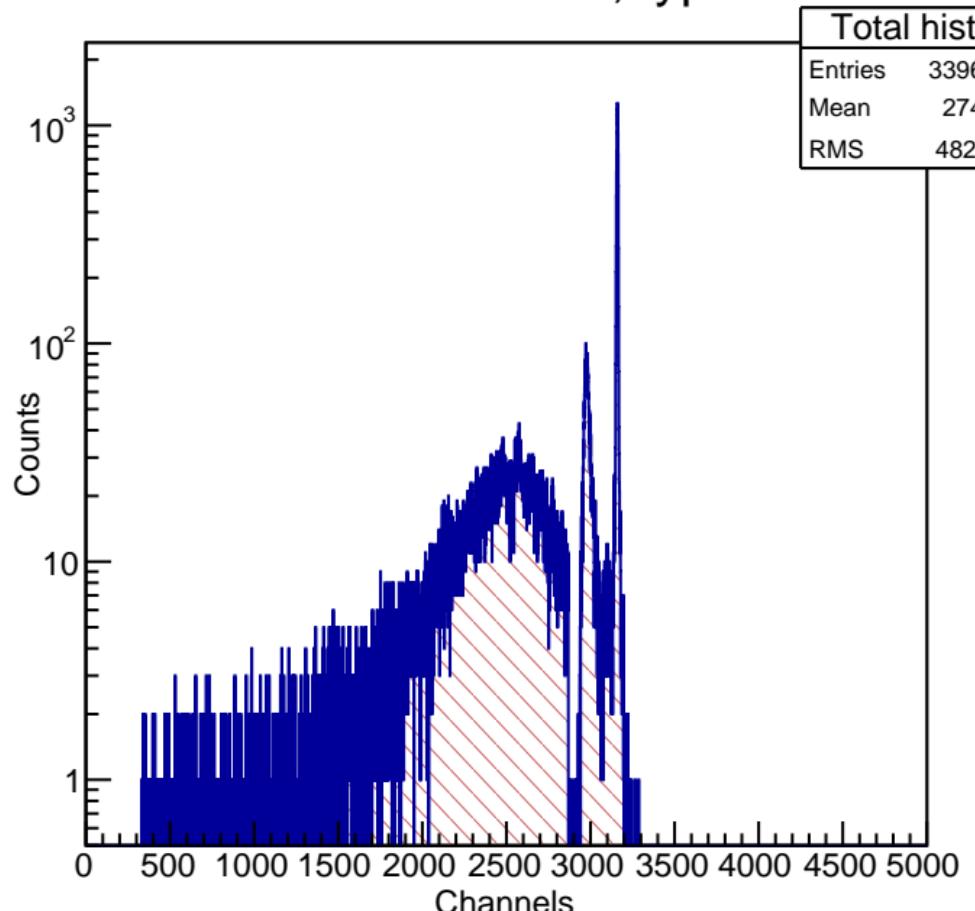
# TDCE Octet 35, type1



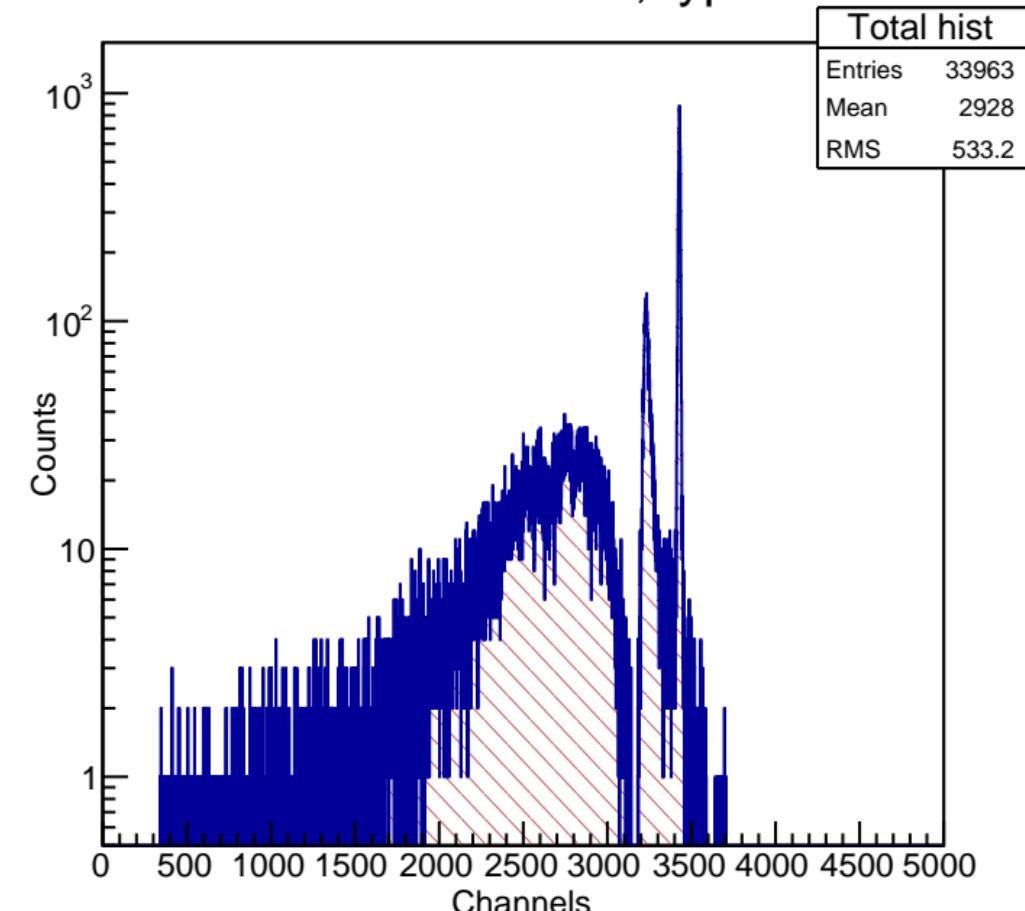
# TDCW Octet 35, type1



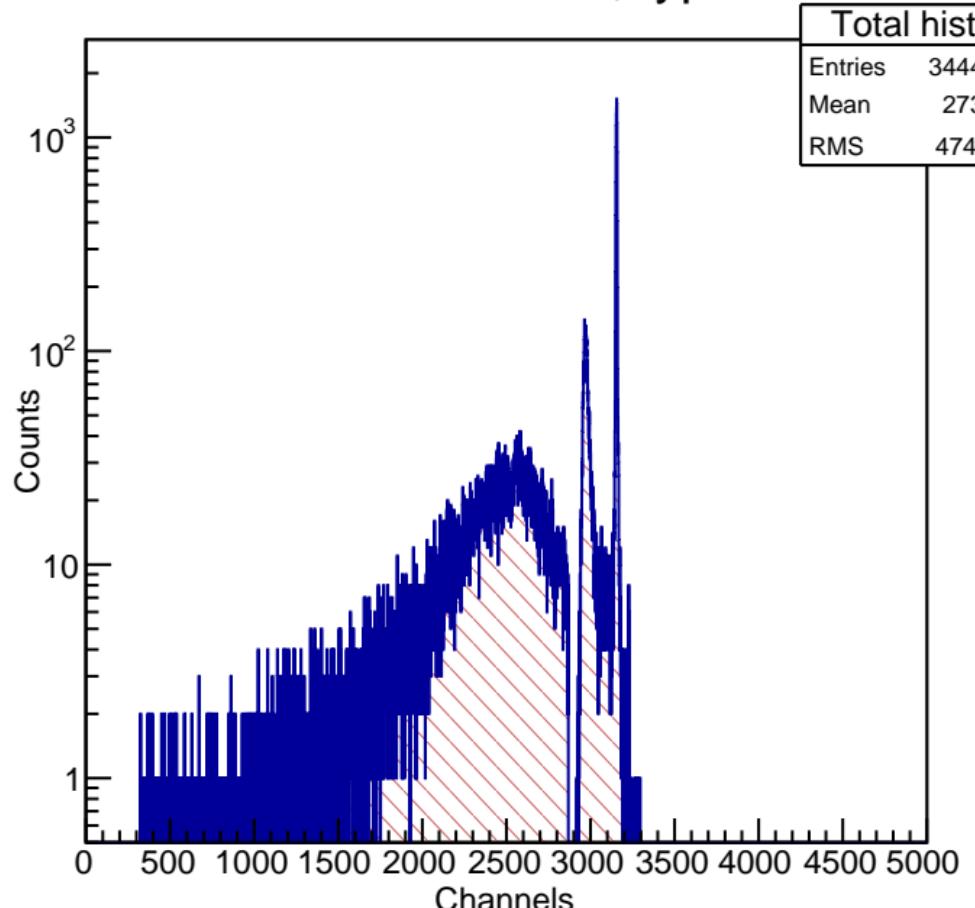
# TDCE Octet 36, type1



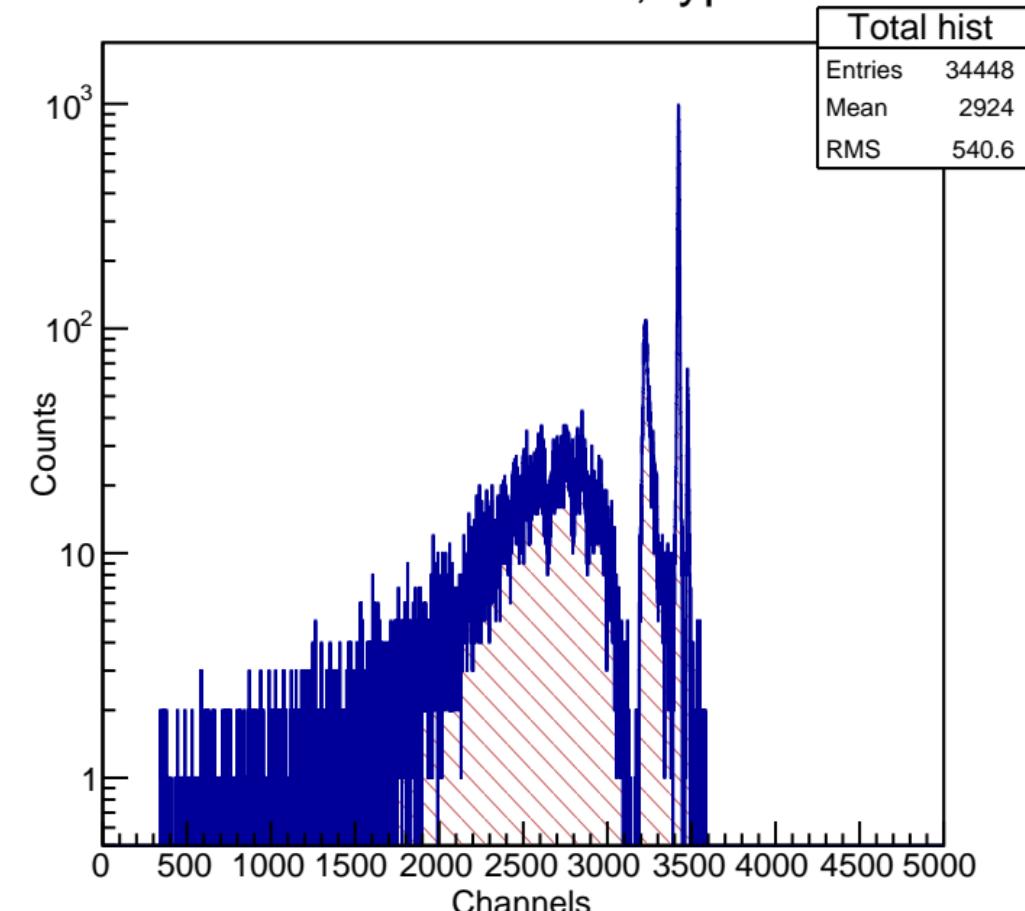
# TDCW Octet 36, type1



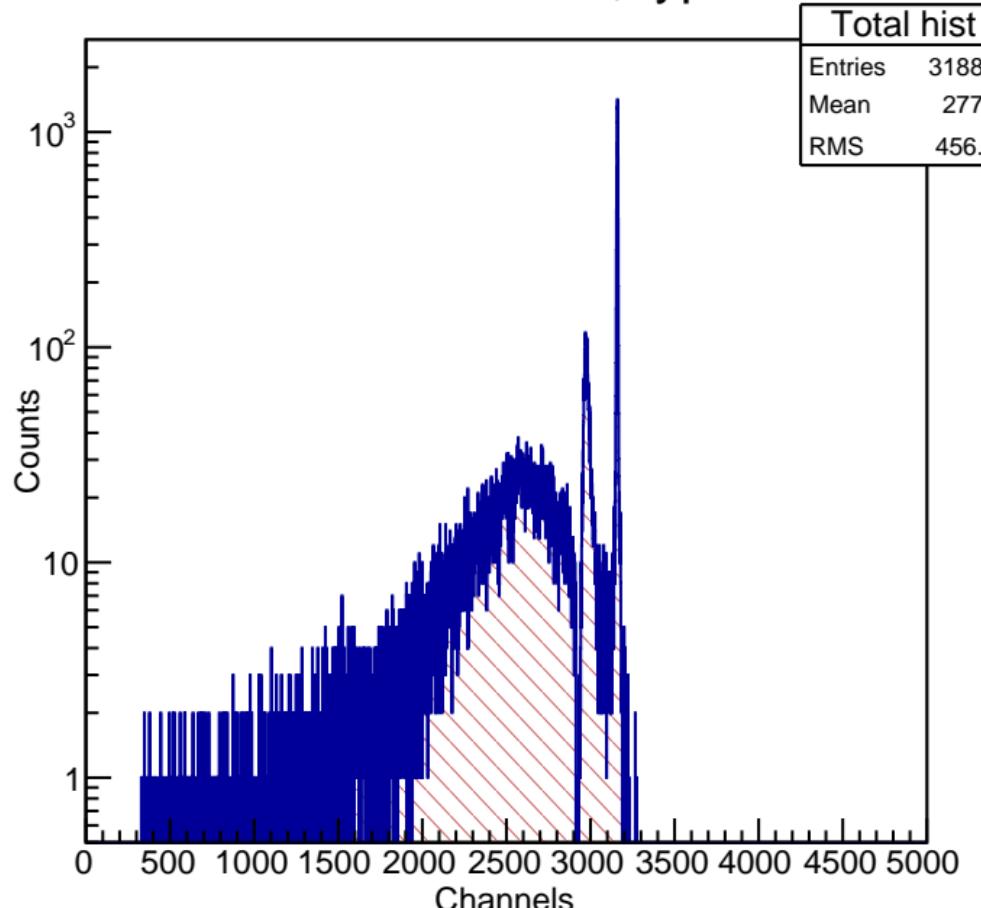
# TDCE Octet 37, type1



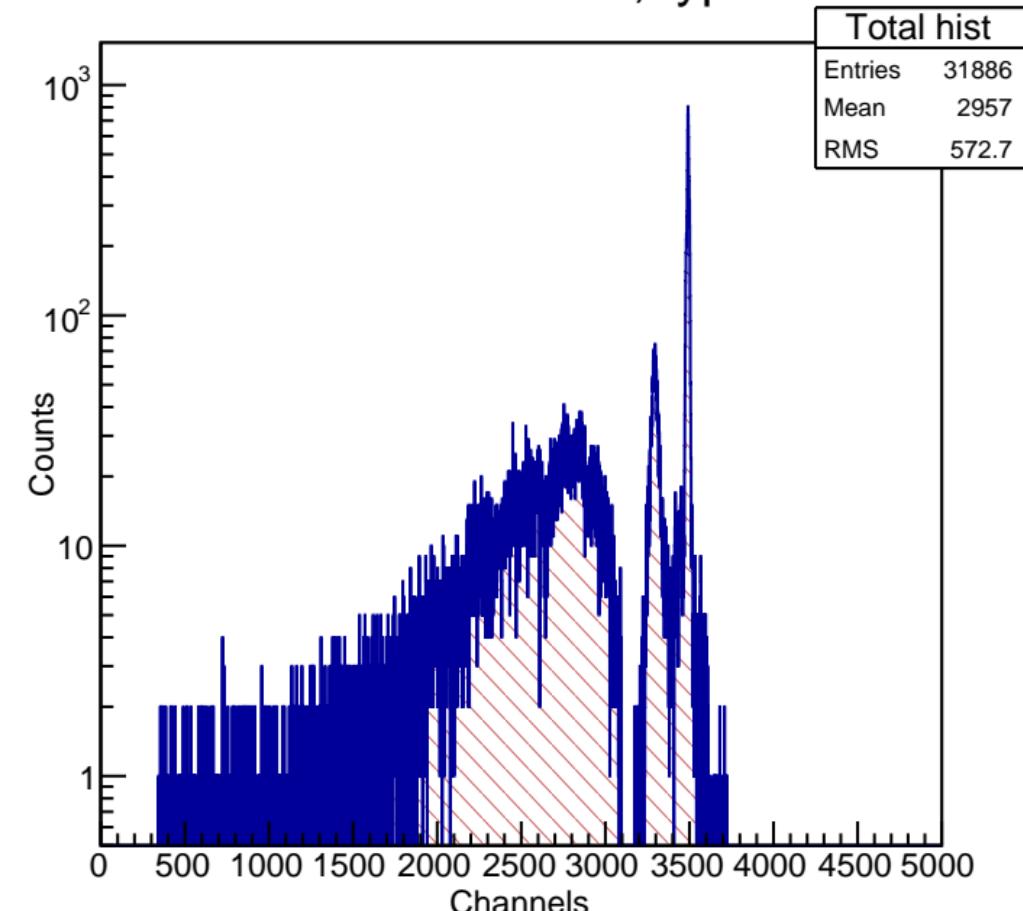
# TDCW Octet 37, type1



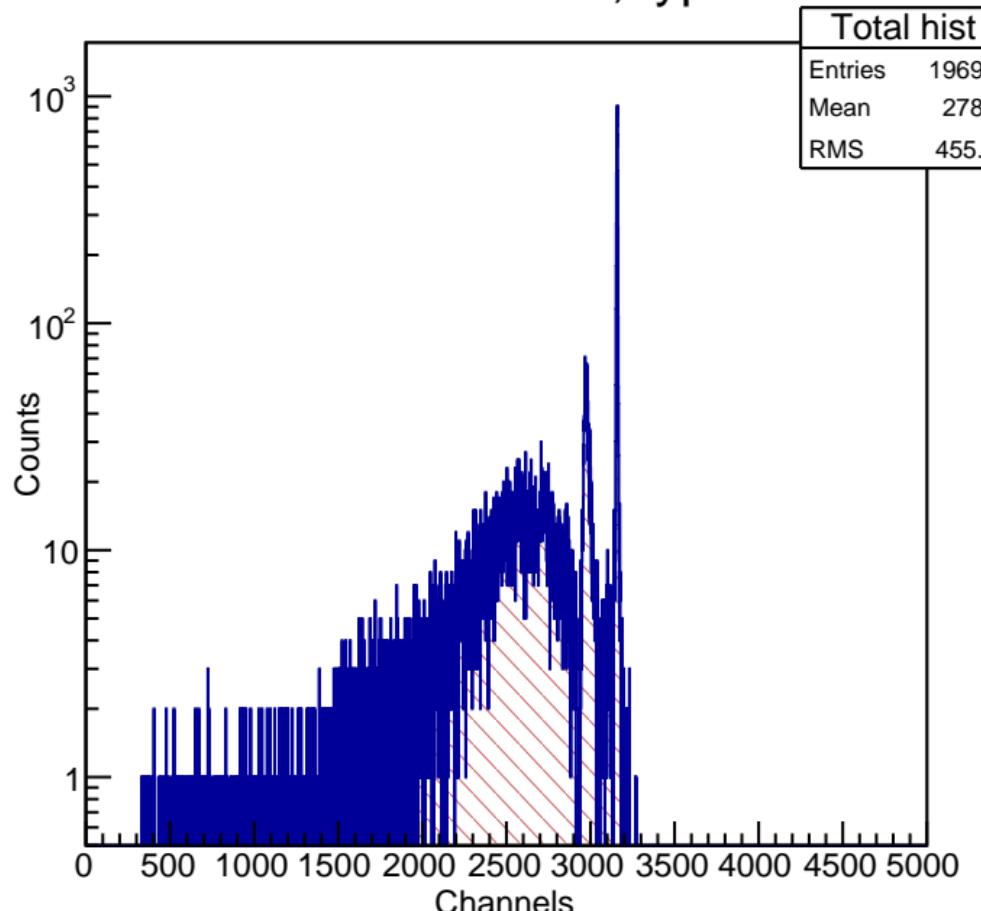
# TDCE Octet 38, type1



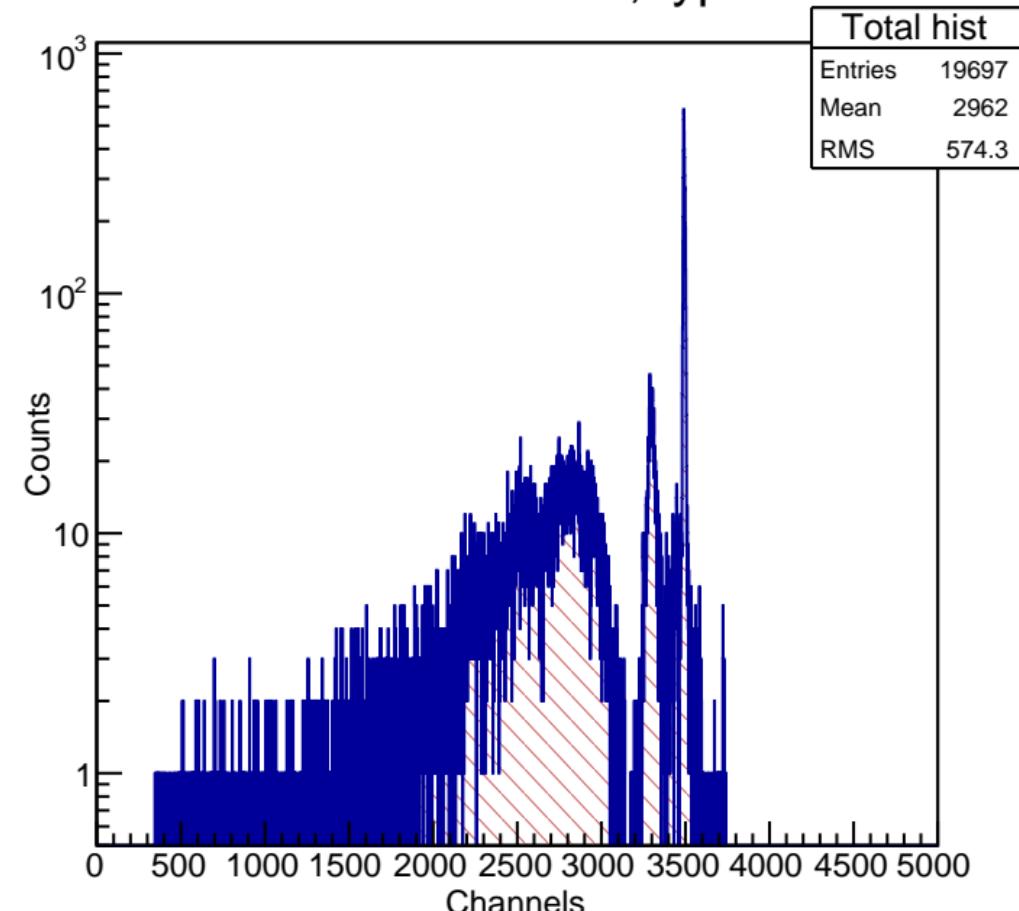
# TDCW Octet 38, type1



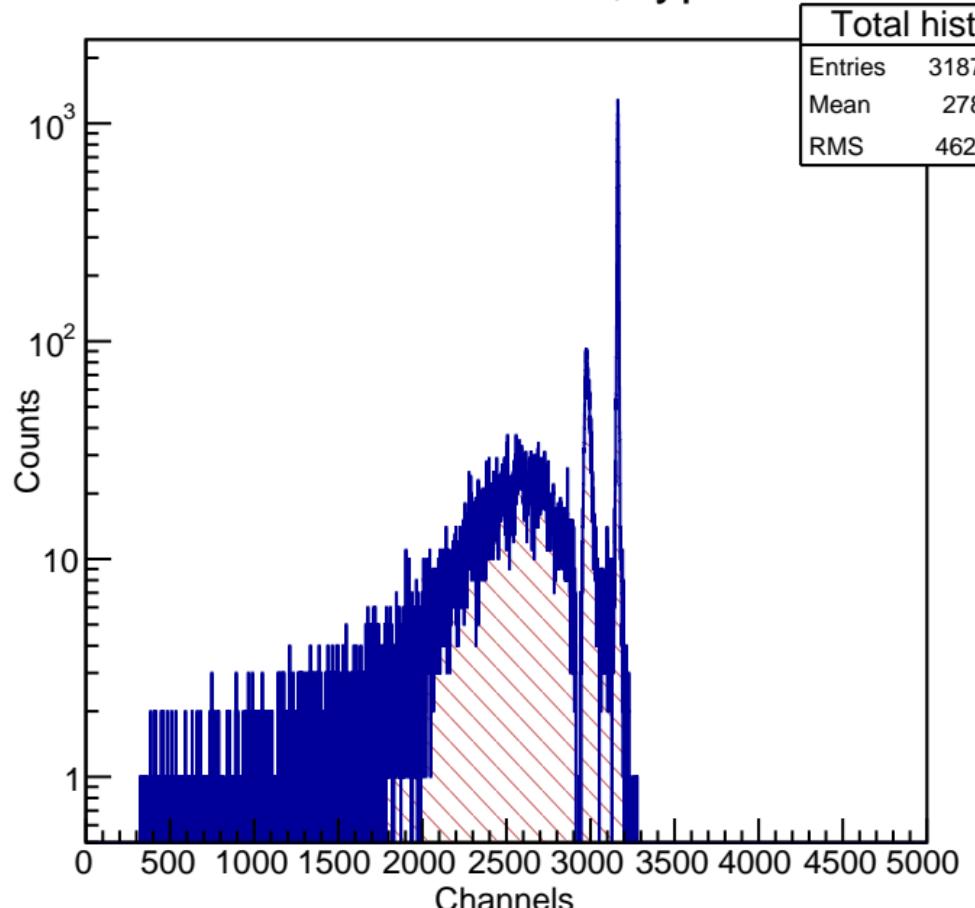
# TDCE Octet 39, type1



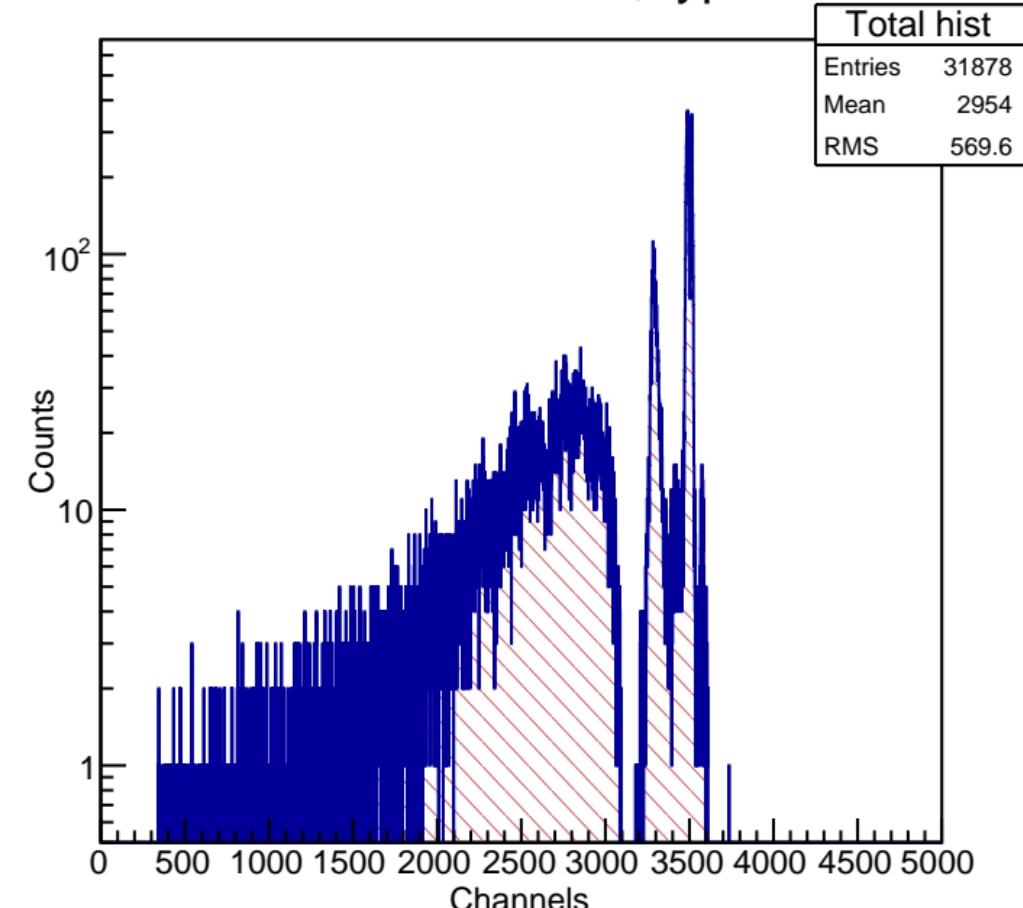
# TDCW Octet 39, type1



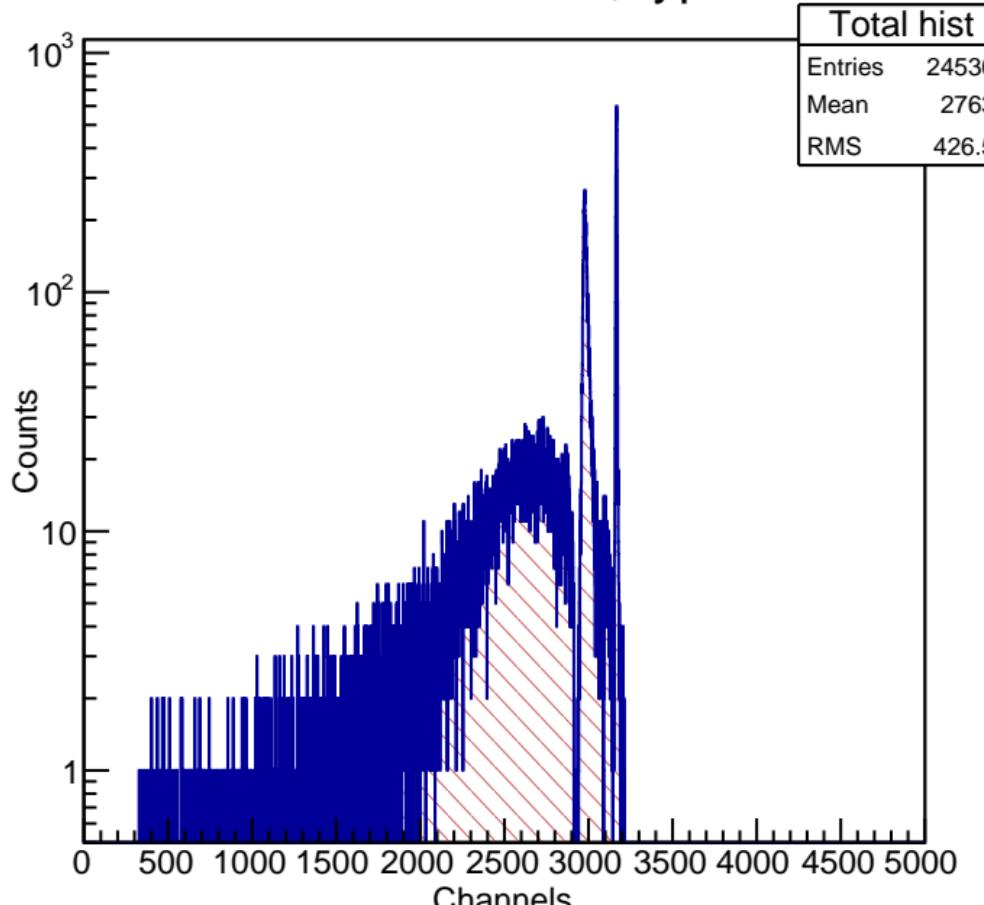
# TDCE Octet 40, type1



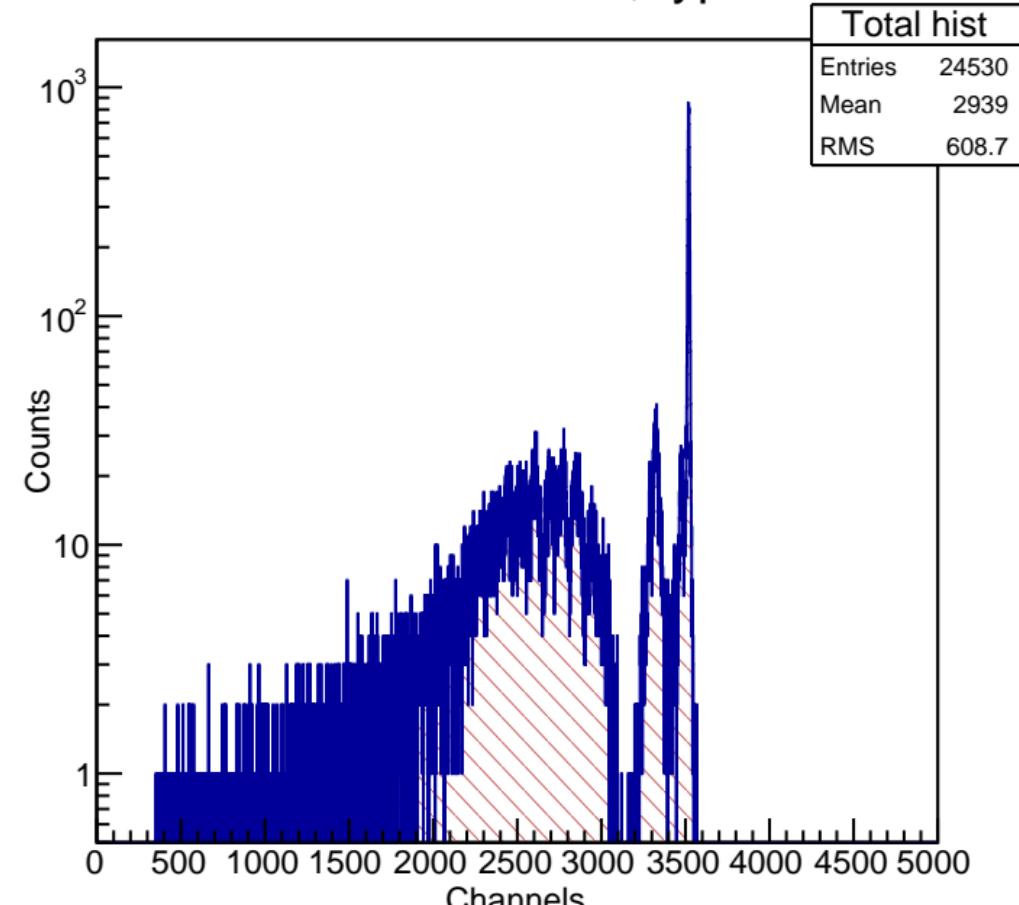
# TDCW Octet 40, type1



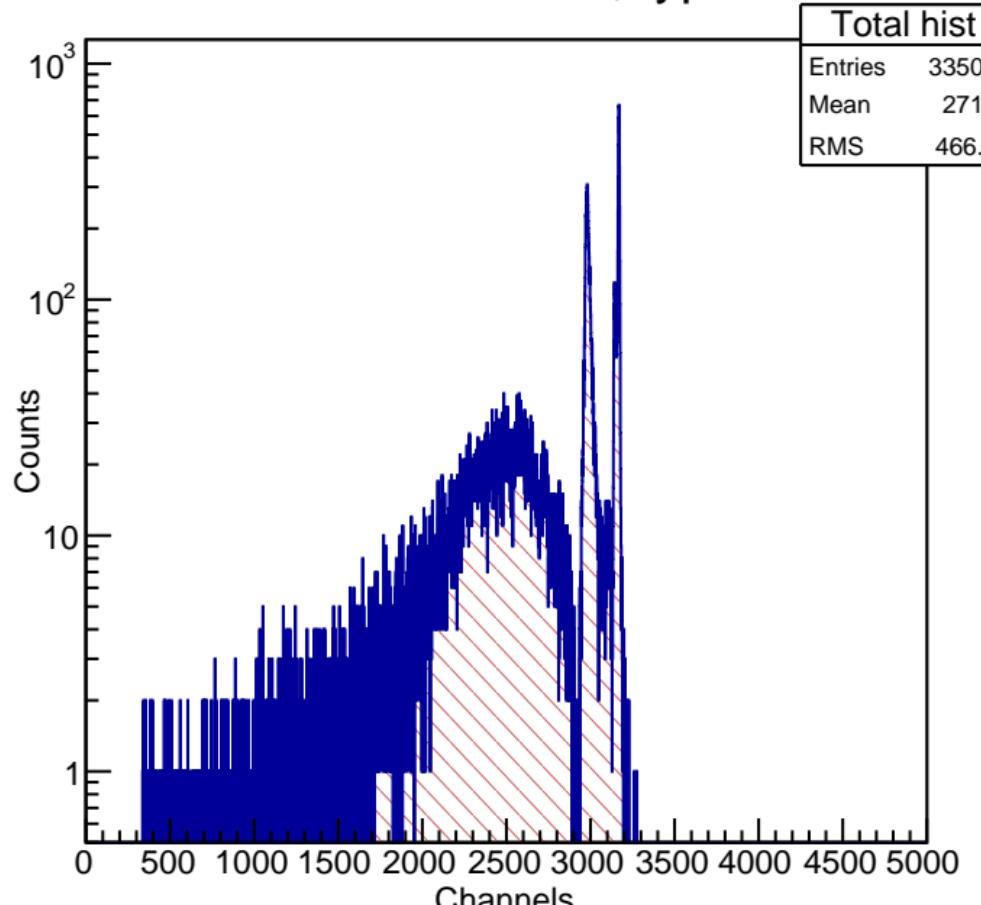
# TDCE Octet 41, type1



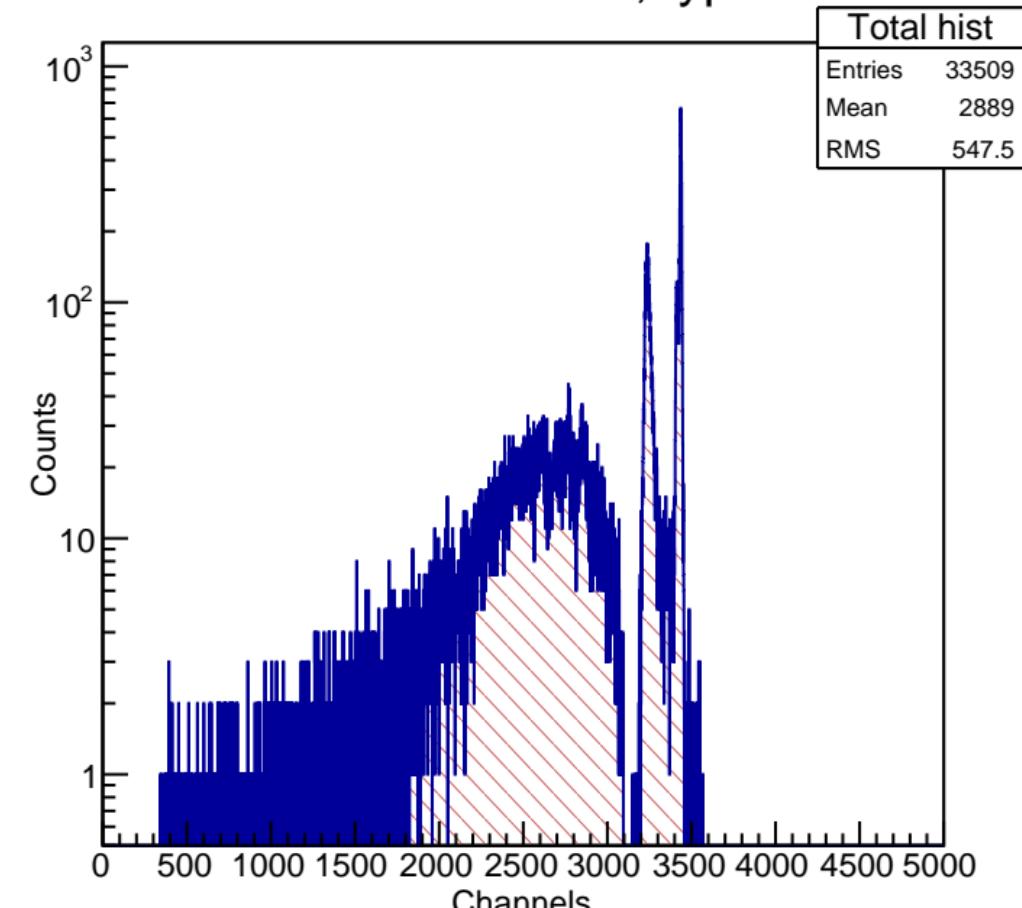
# TDCW Octet 41, type1



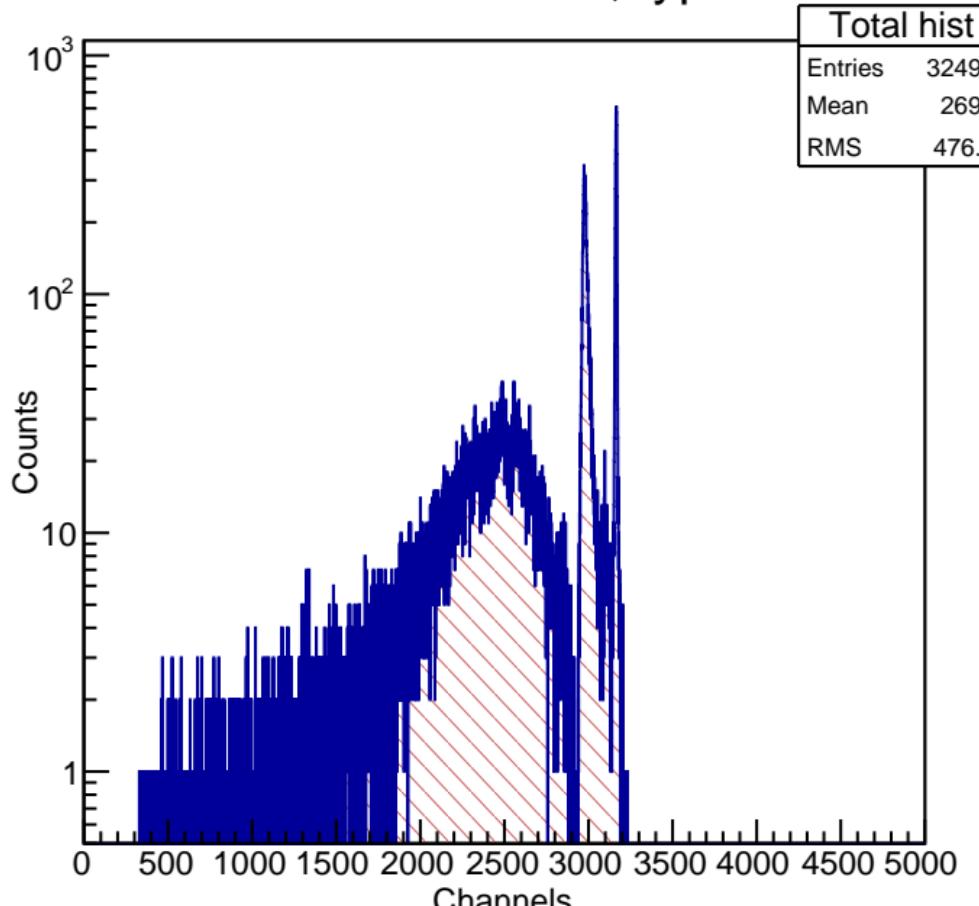
# TDCE Octet 42, type1



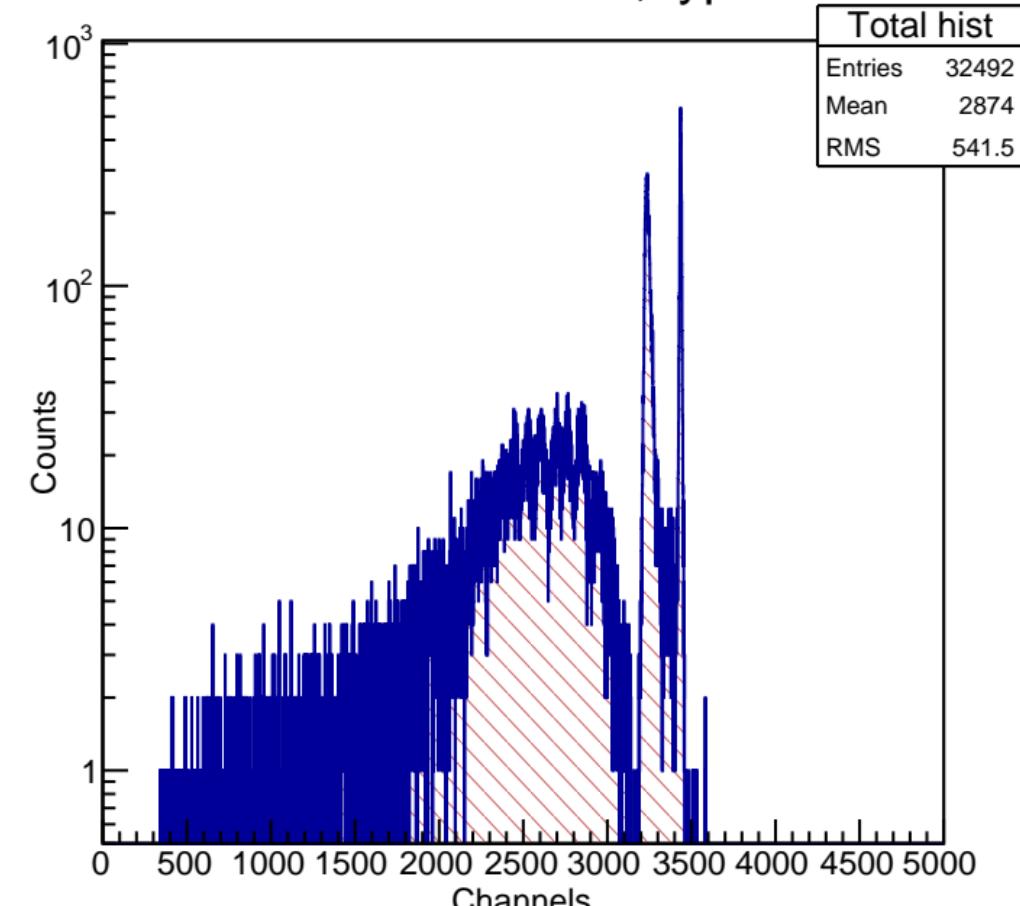
# TDCW Octet 42, type1



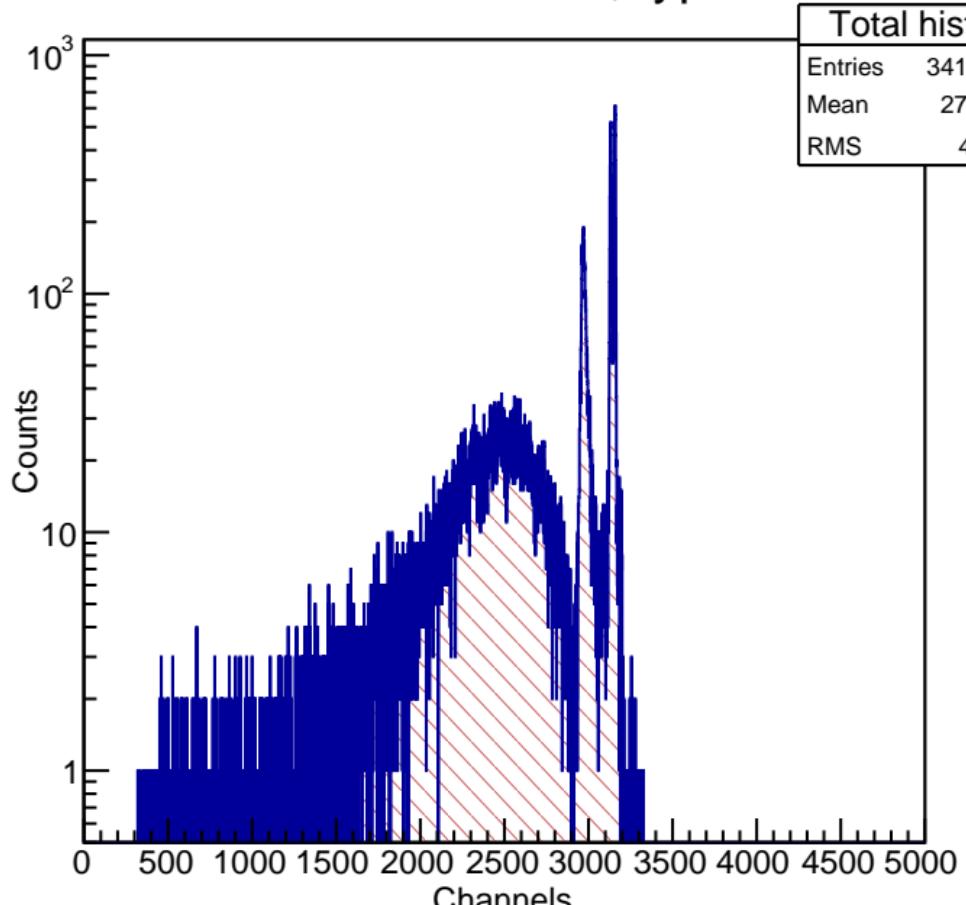
# TDCE Octet 43, type1



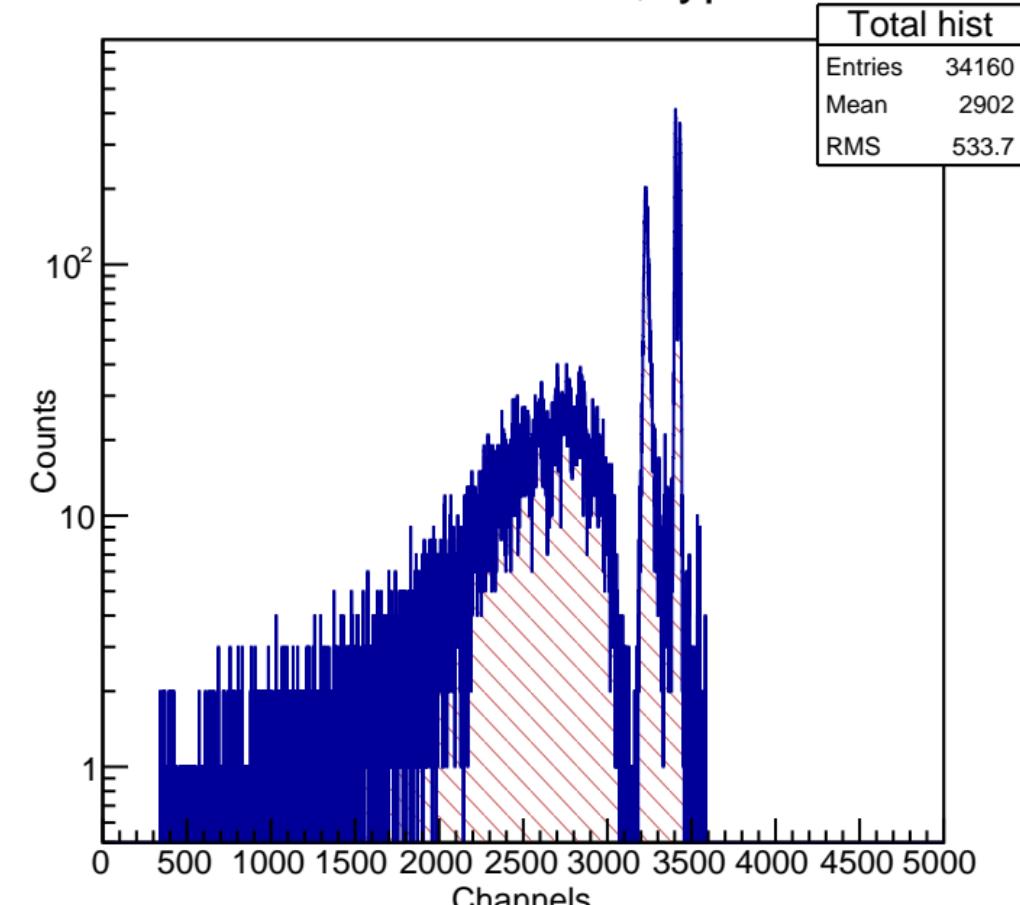
# TDCW Octet 43, type1



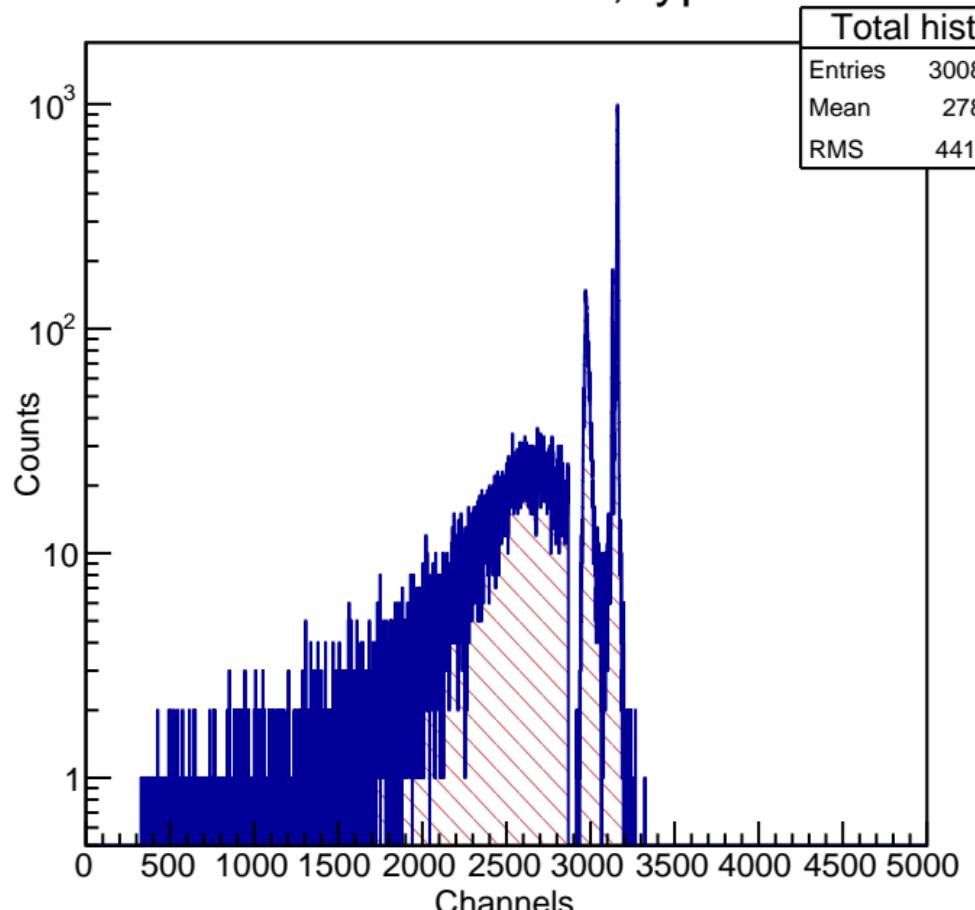
# TDCE Octet 44, type1



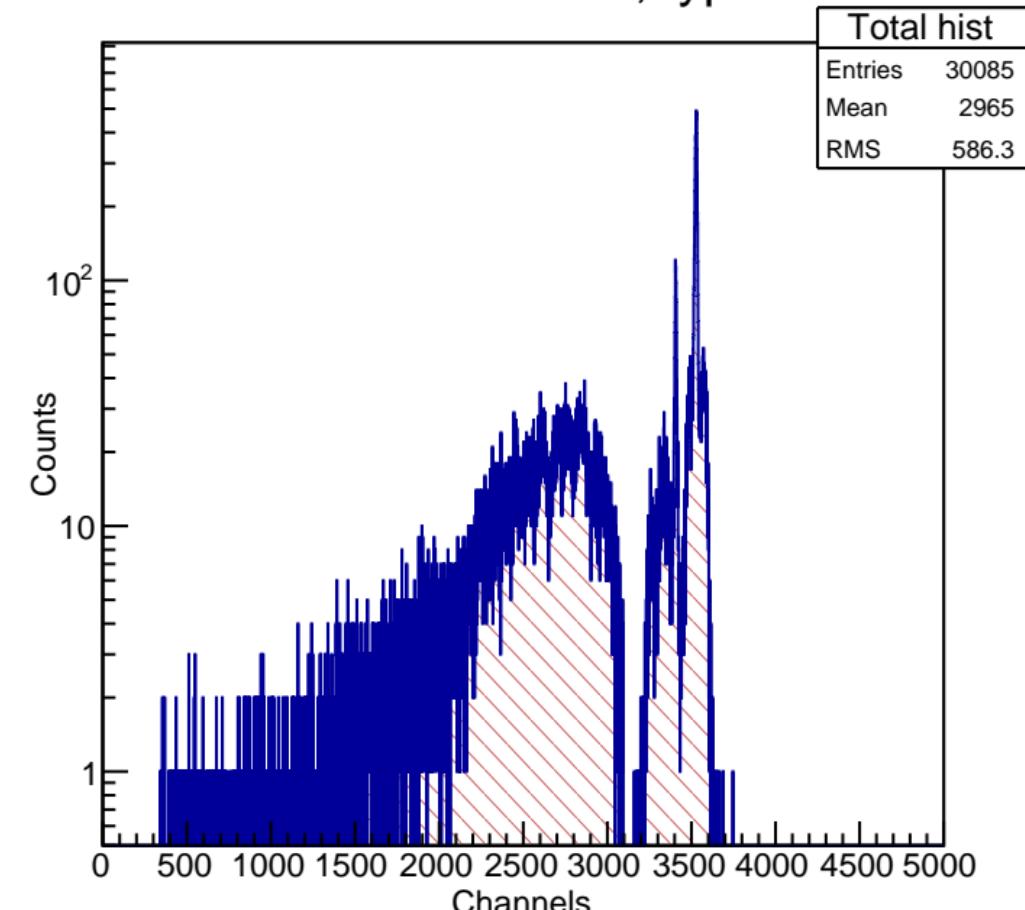
# TDCW Octet 44, type1



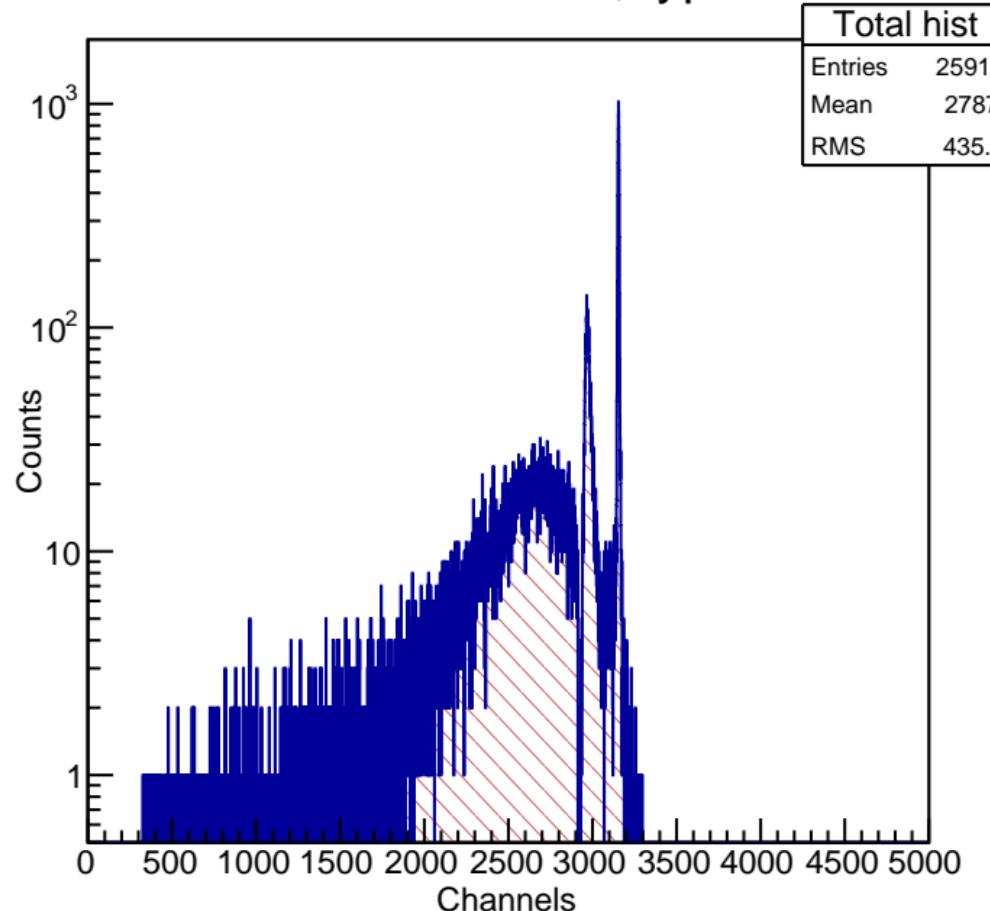
# TDCE Octet 45, type1



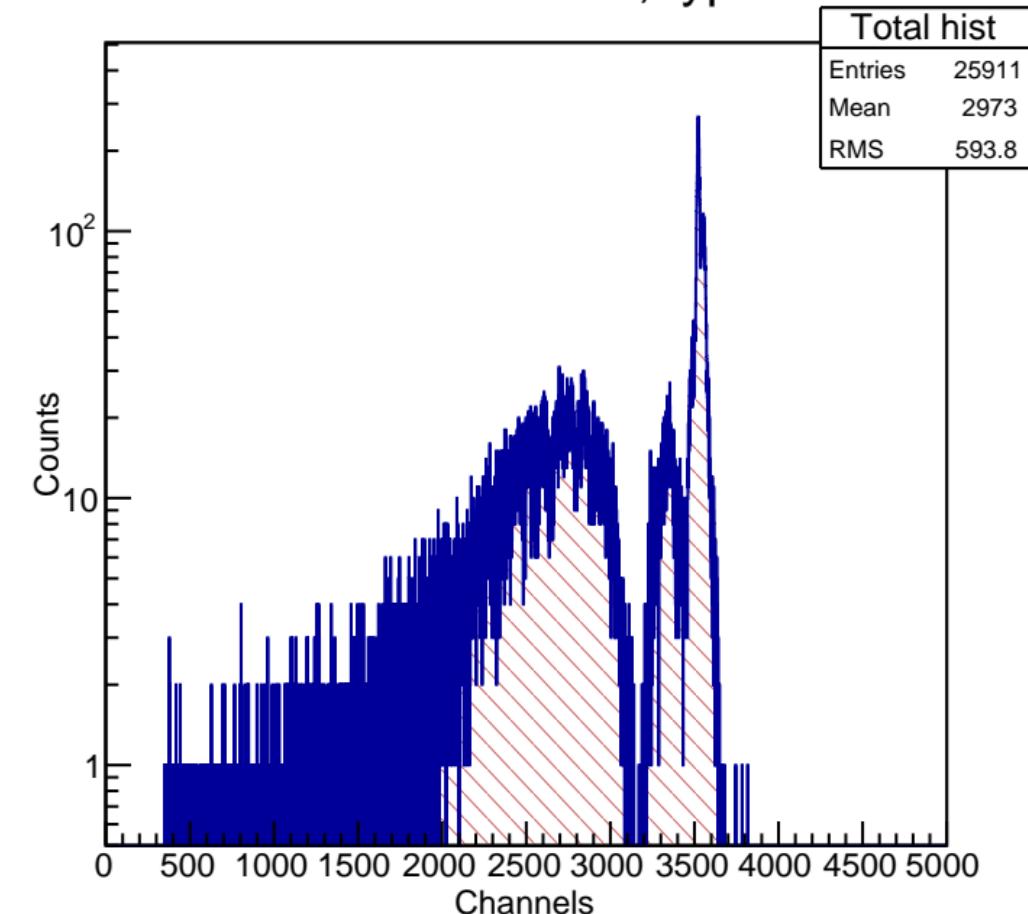
# TDCW Octet 45, type1



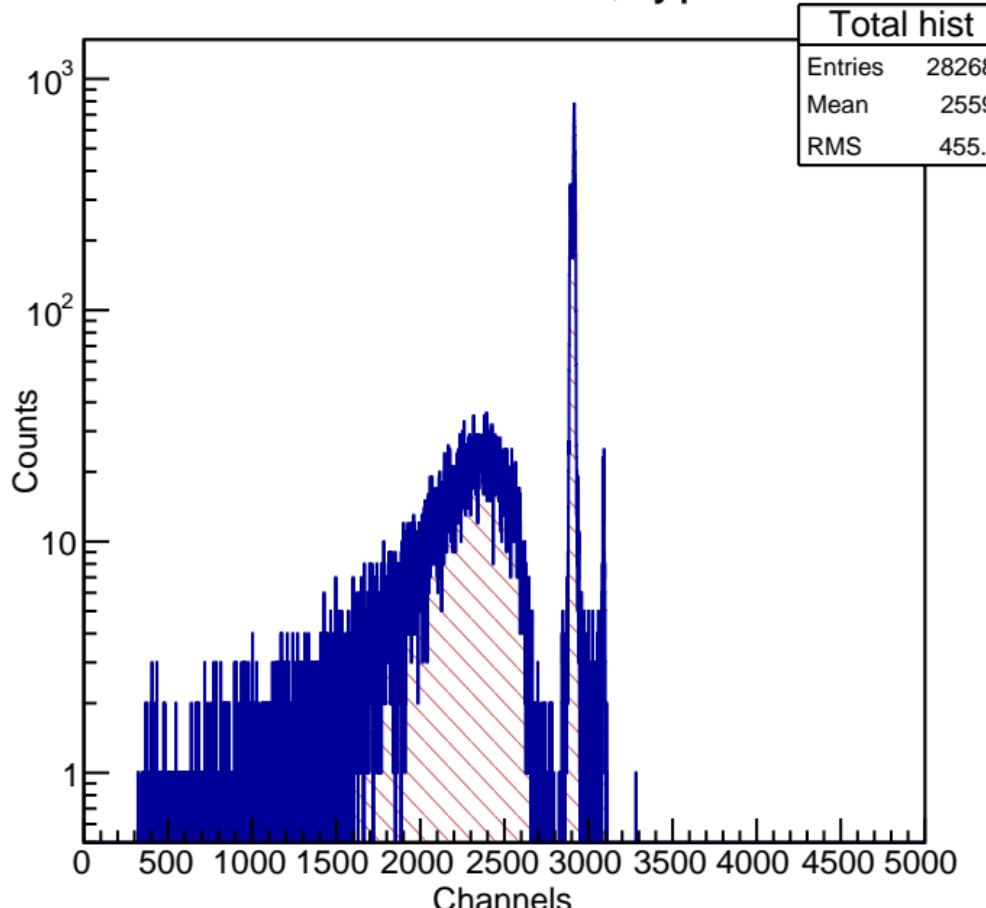
# TDCE Octet 46, type1



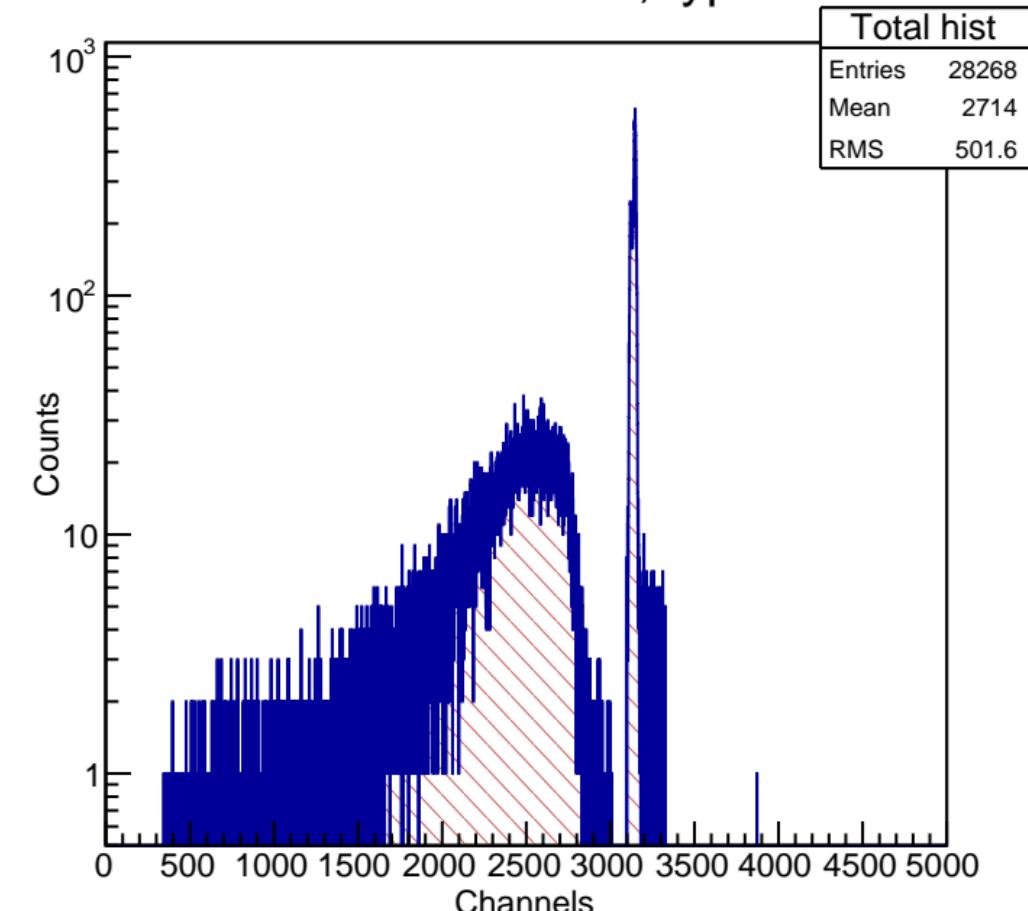
# TDCW Octet 46, type1



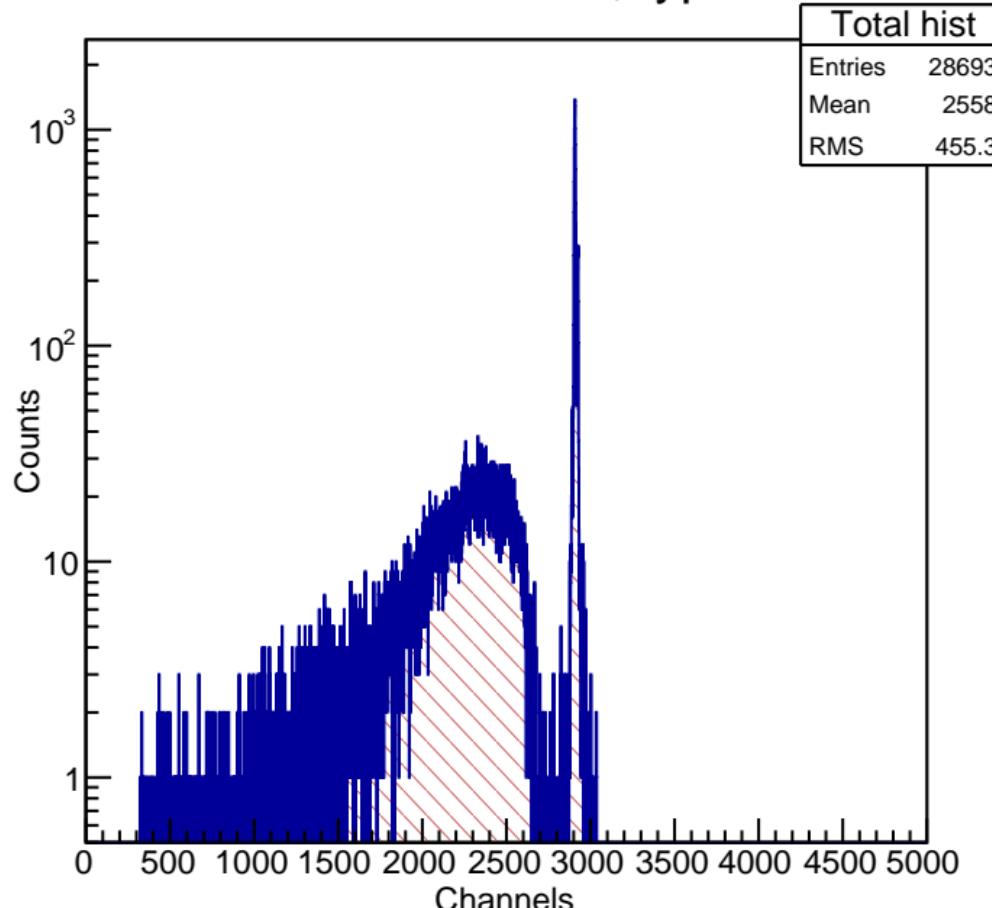
# TDCE Octet 47, type1



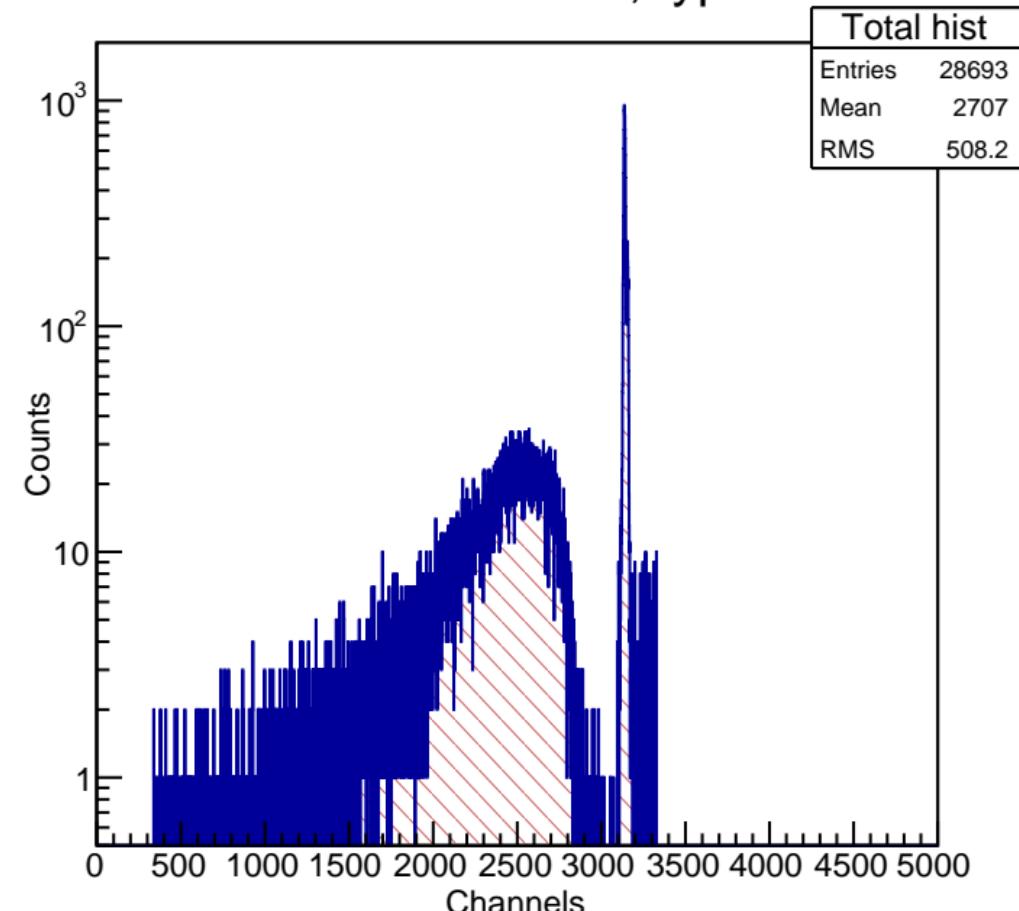
# TDCW Octet 47, type1



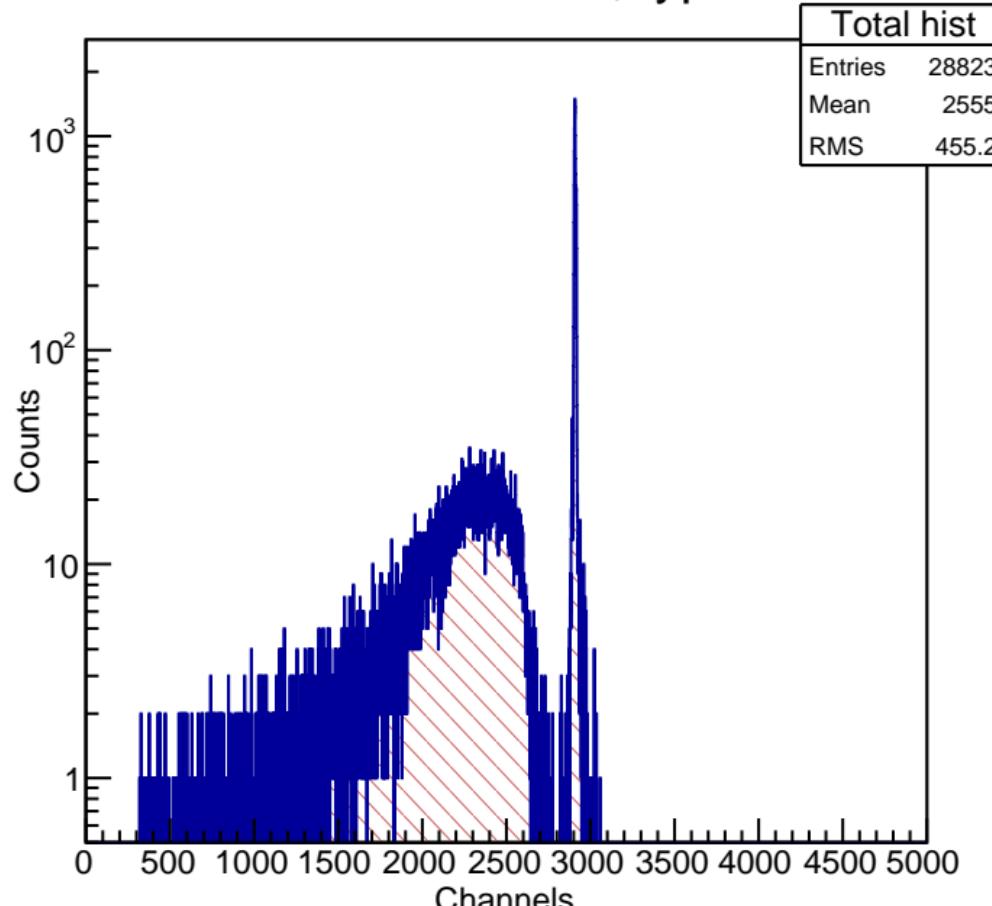
# TDCE Octet 48, type1



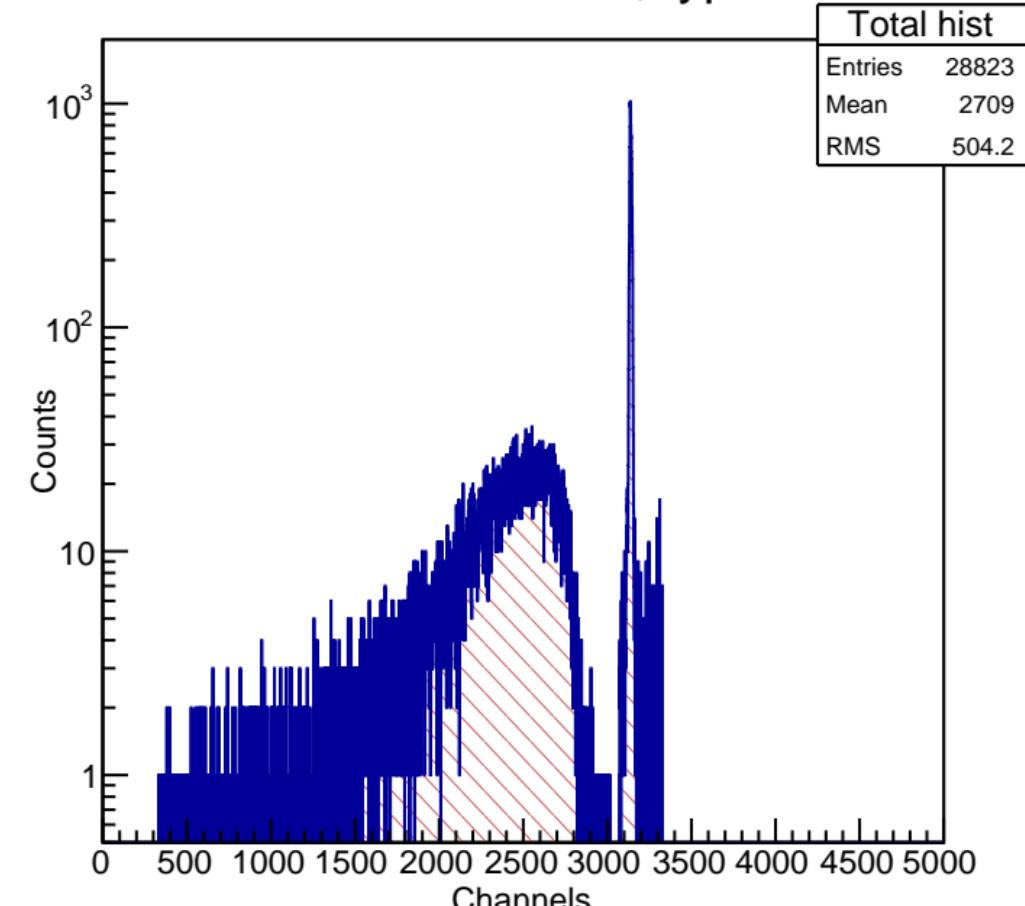
# TDCW Octet 48, type1



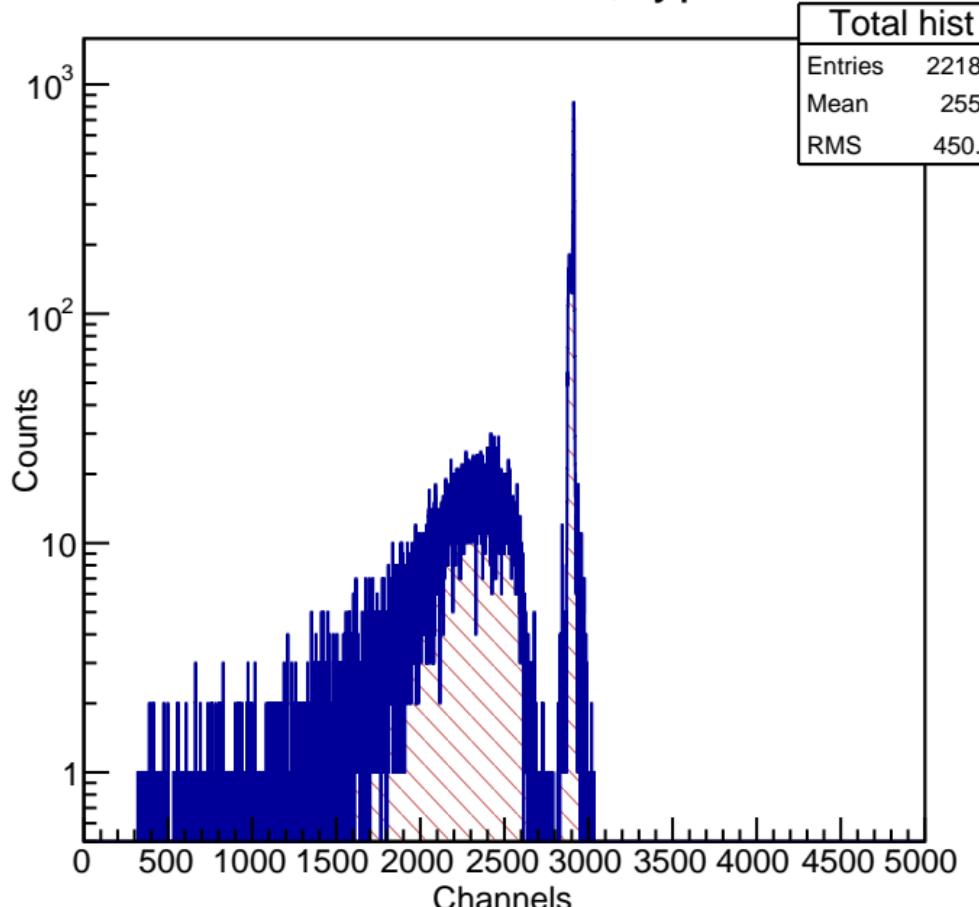
# TDCE Octet 49, type1



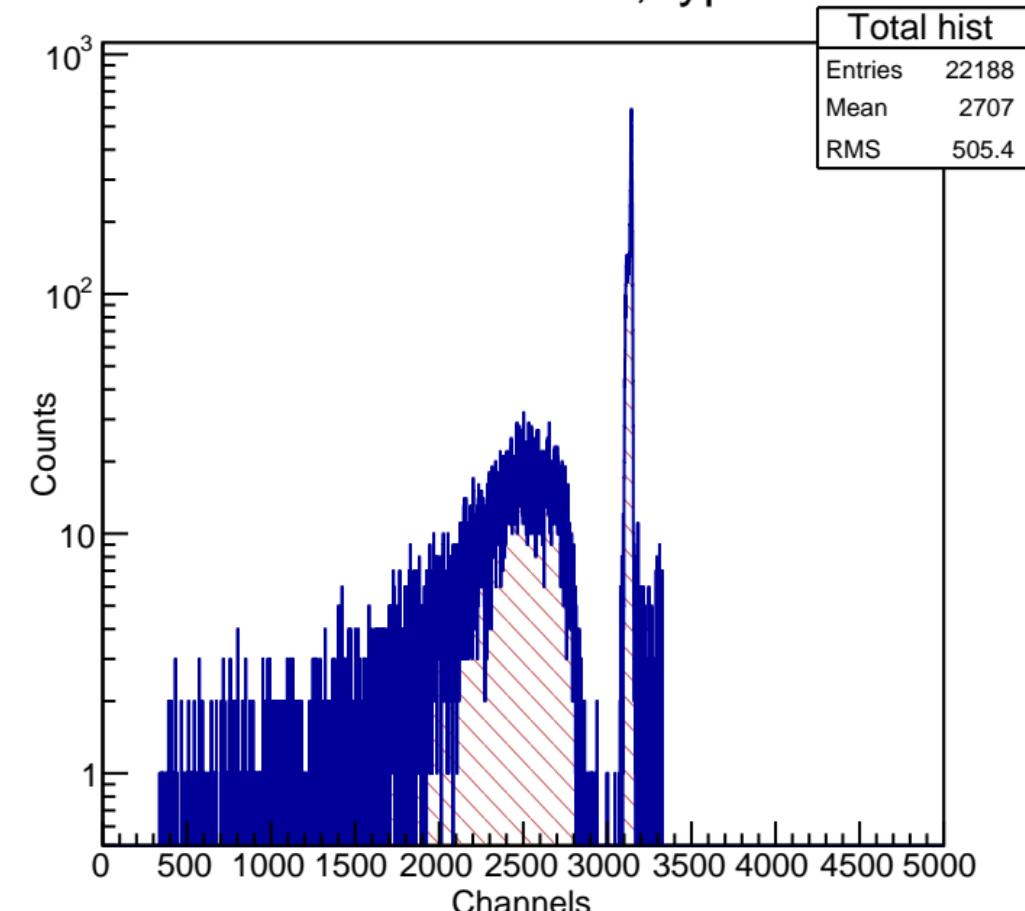
# TDCW Octet 49, type1



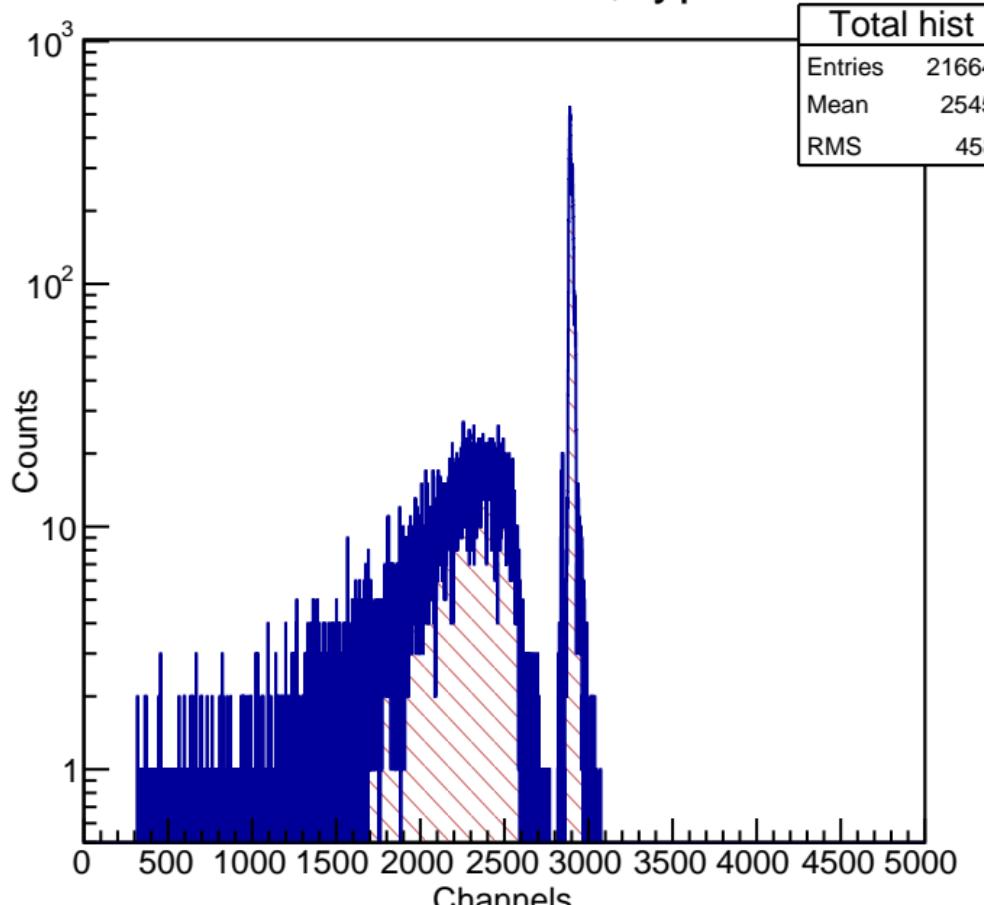
# TDCE Octet 50, type1



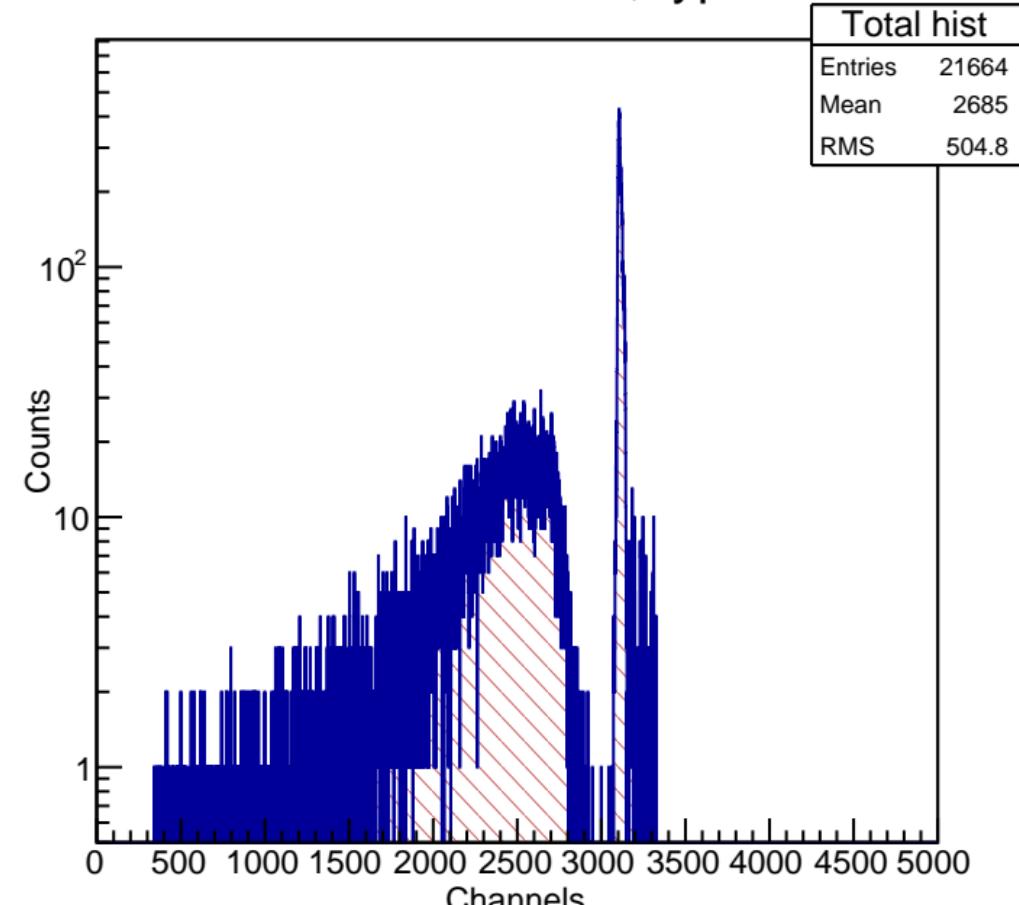
# TDCW Octet 50, type1



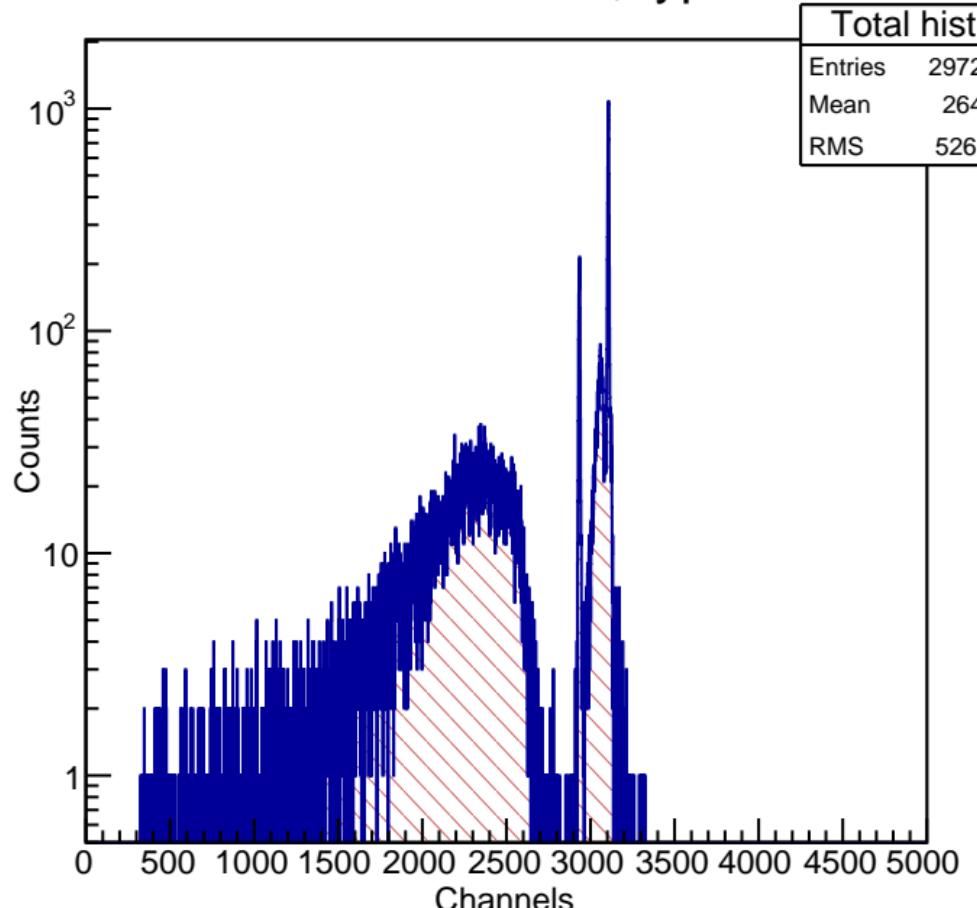
# TDCE Octet 51, type1



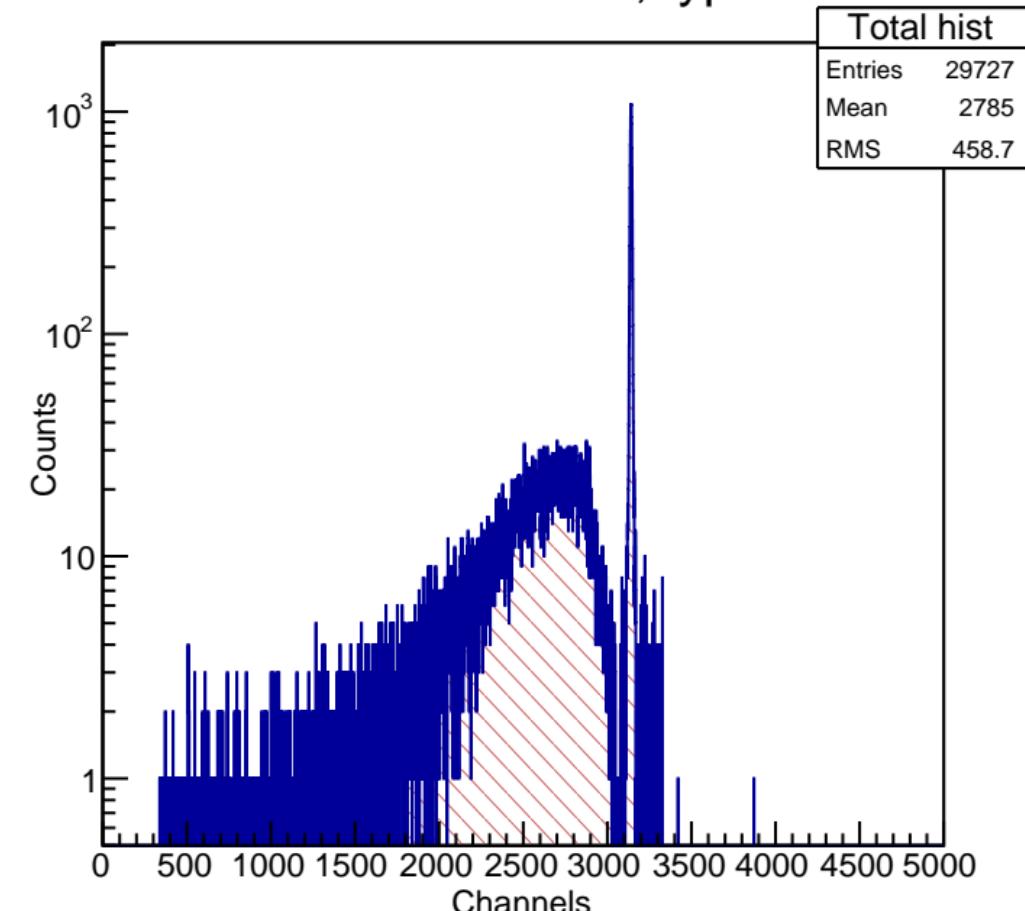
# TDCW Octet 51, type1



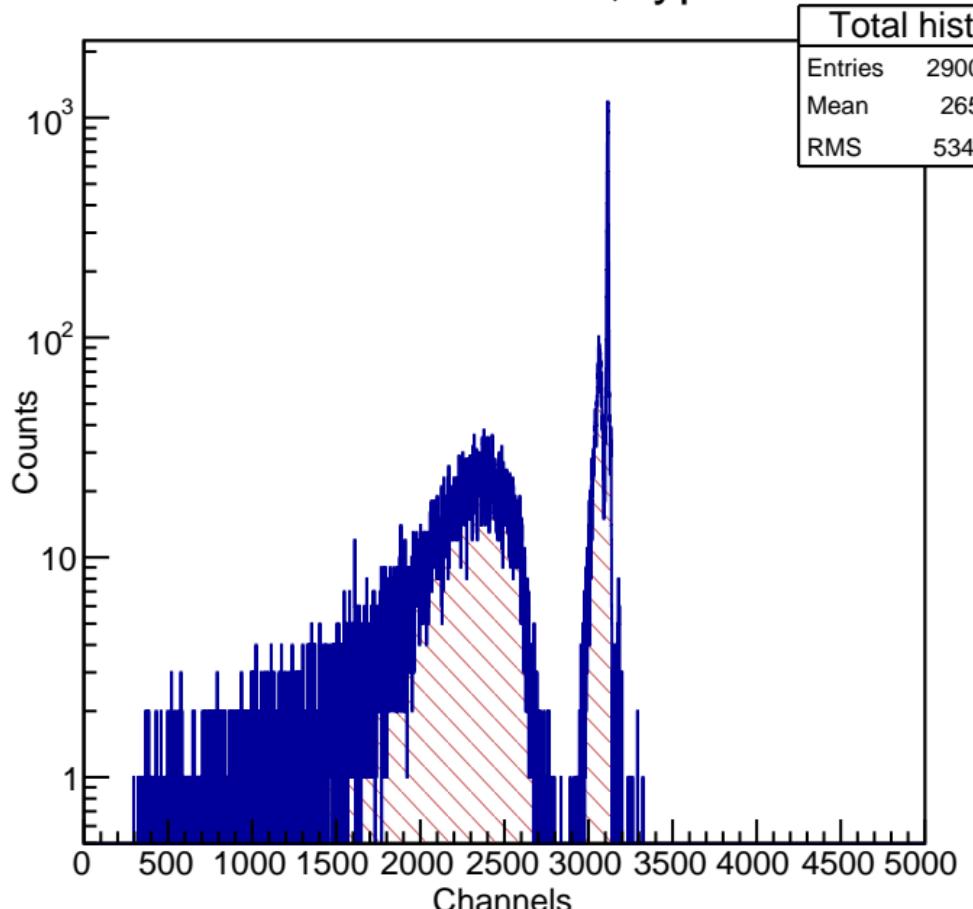
# TDCE Octet 52, type1



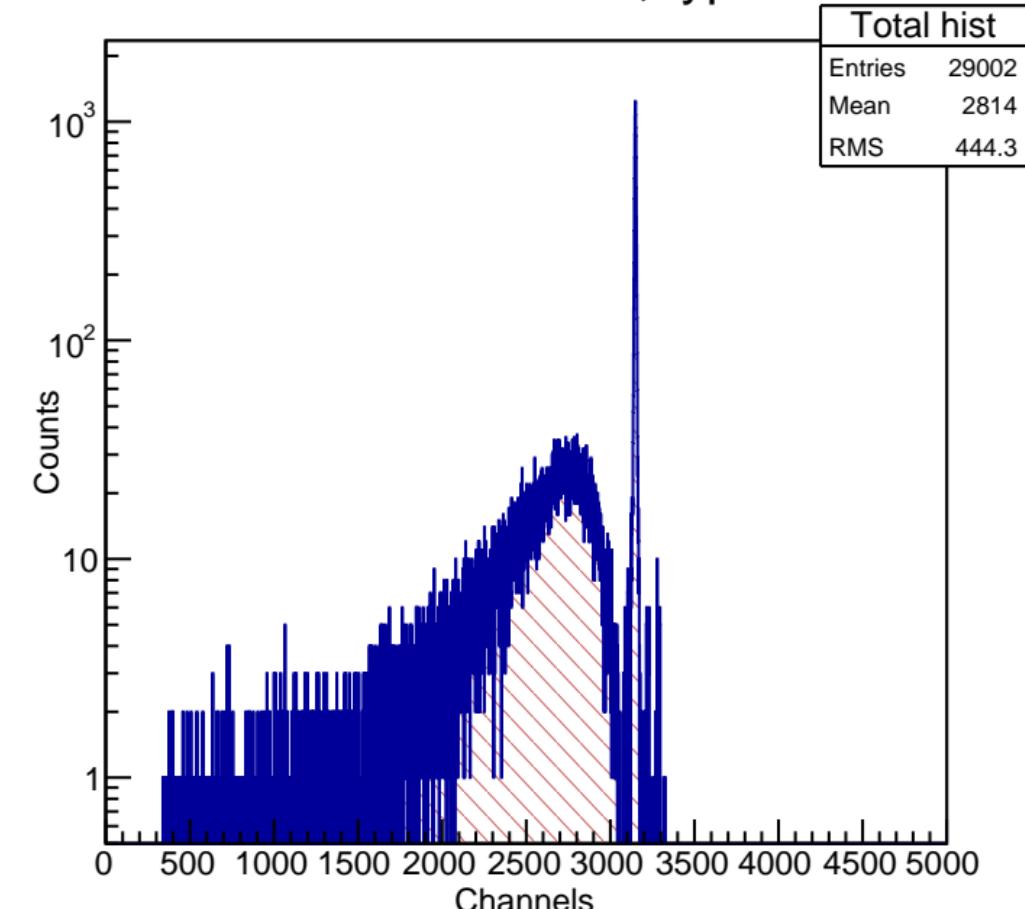
# TDCW Octet 52, type1



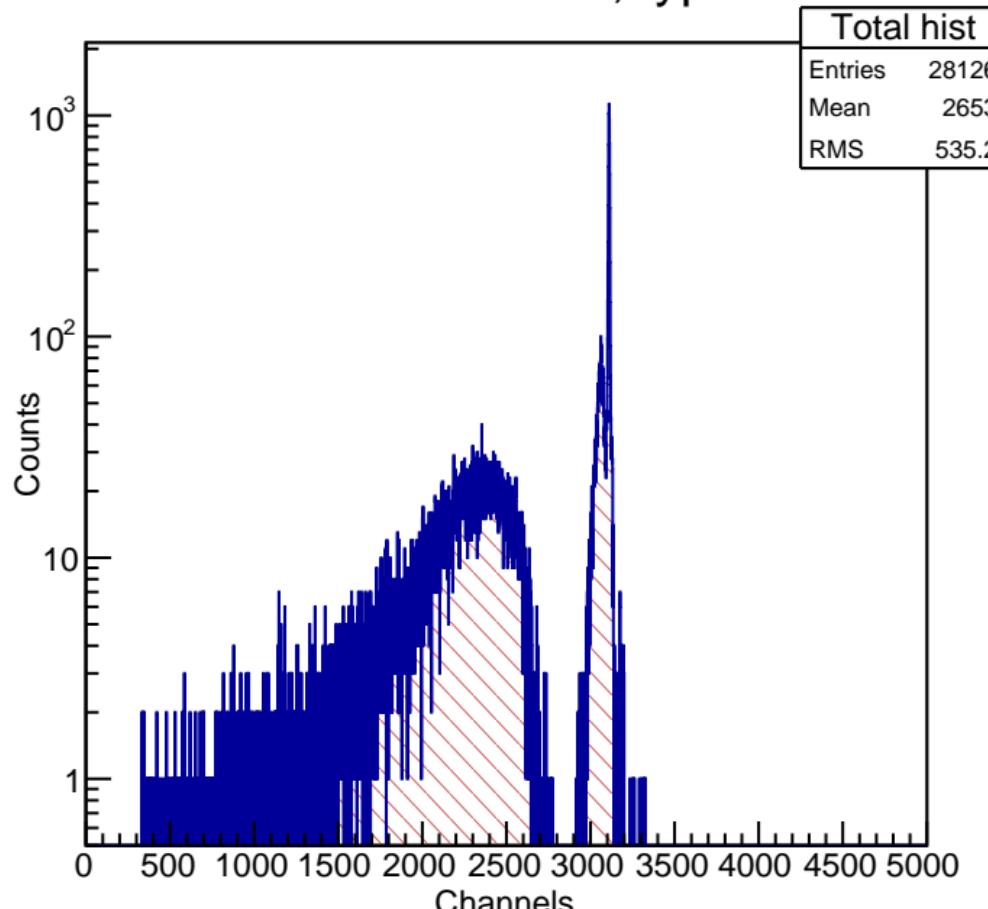
# TDCE Octet 53, type1



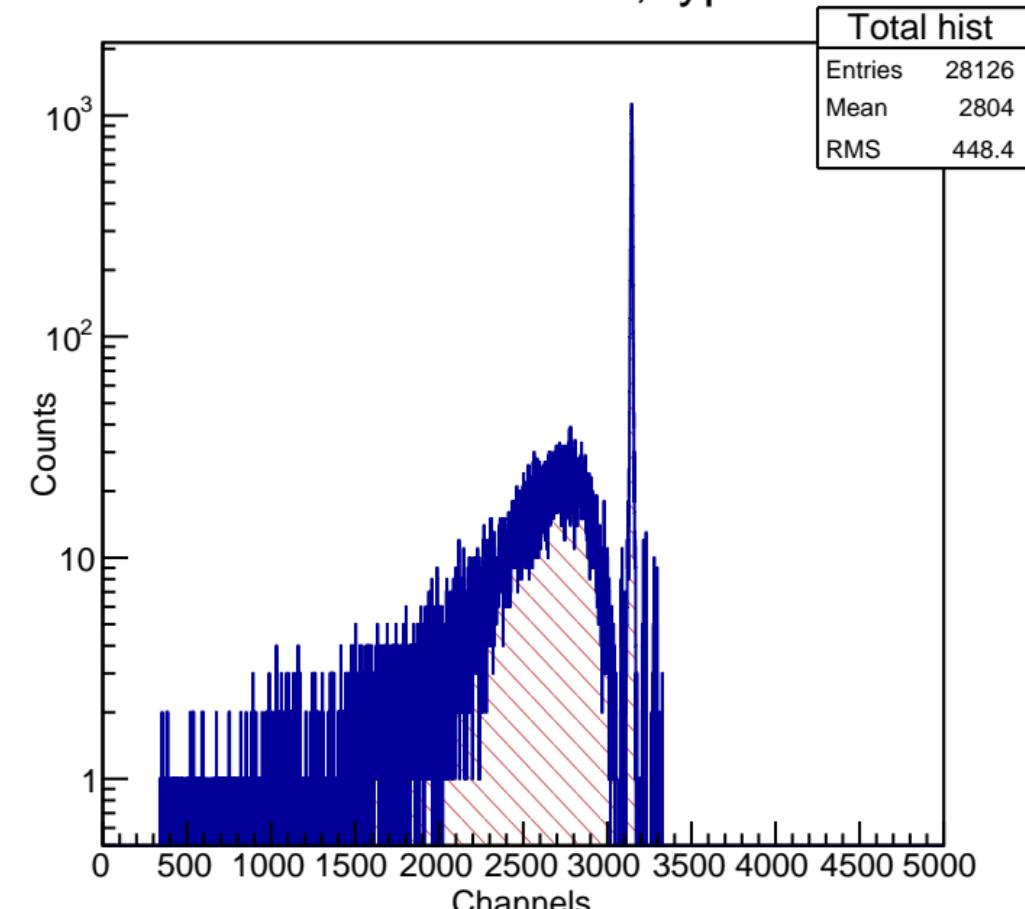
# TDCW Octet 53, type1



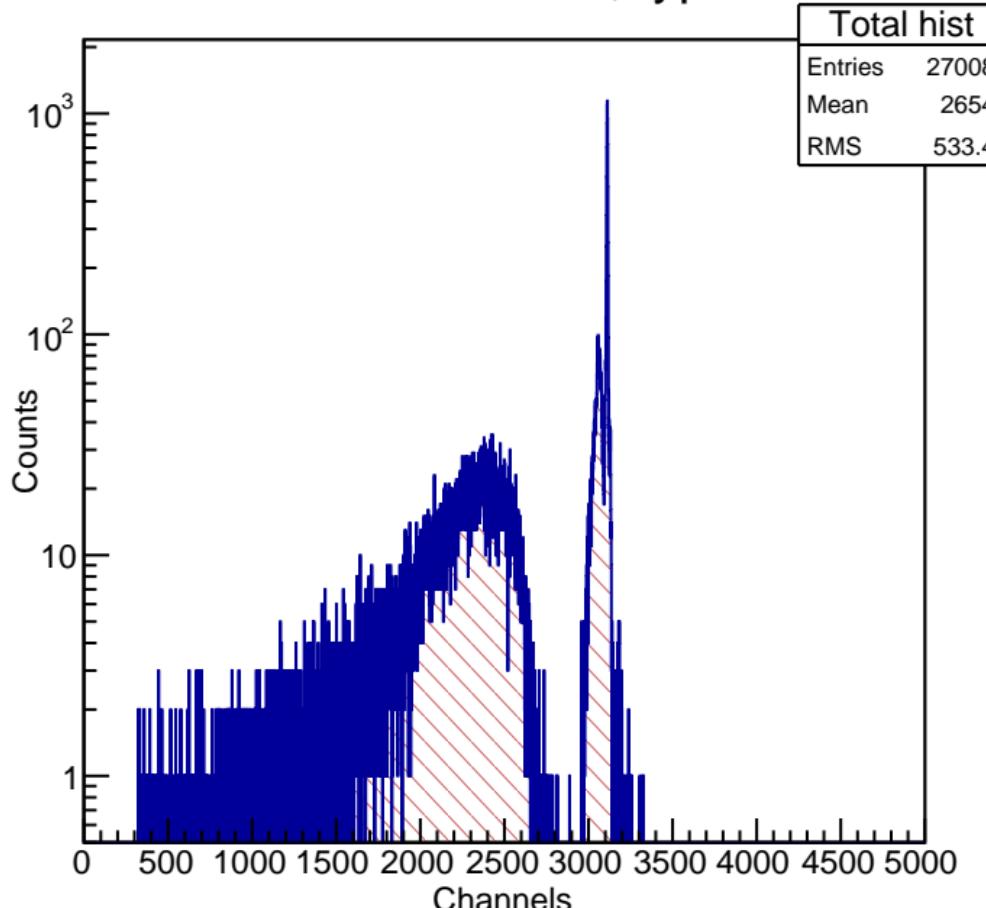
# TDCE Octet 54, type1



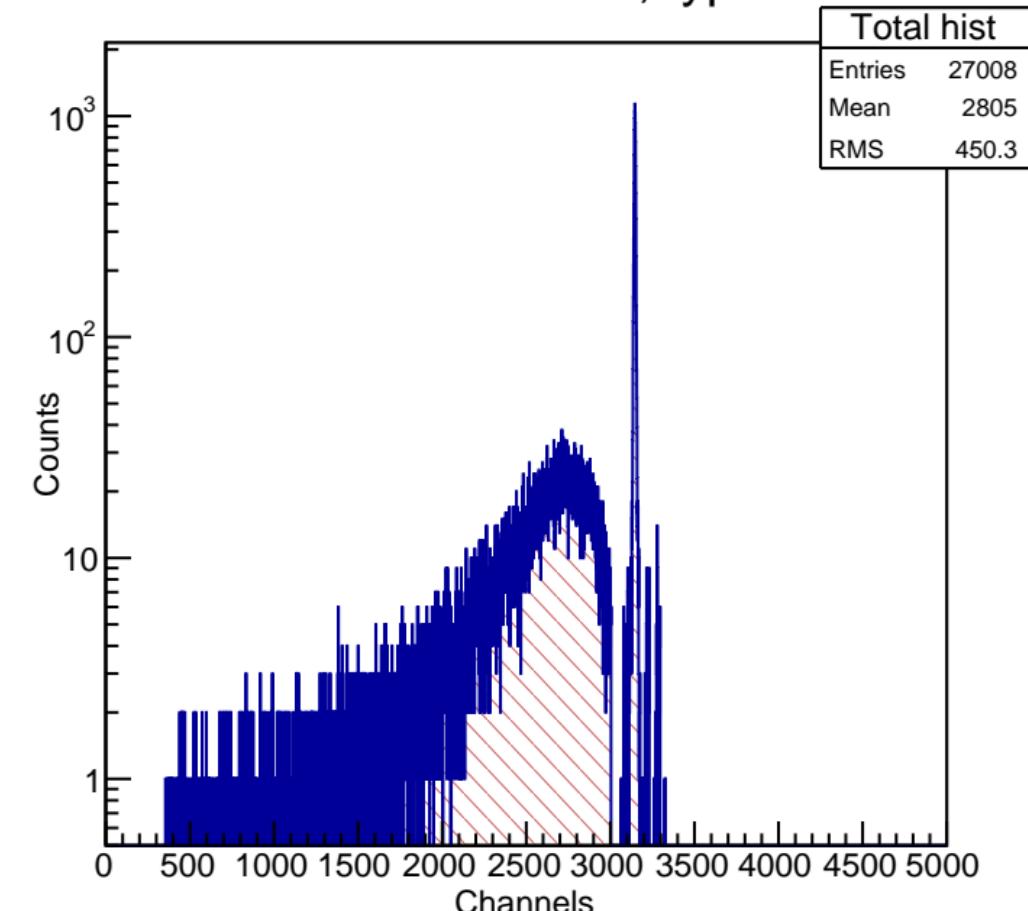
# TDCW Octet 54, type1



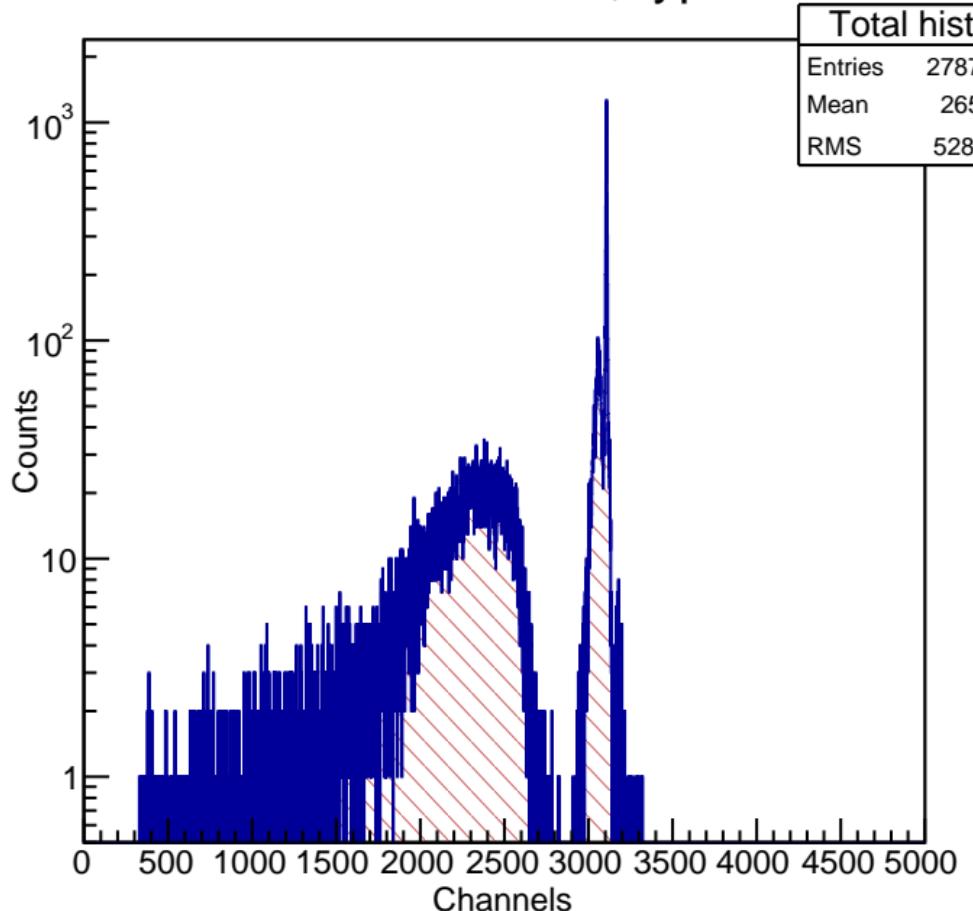
# TDCE Octet 55, type1



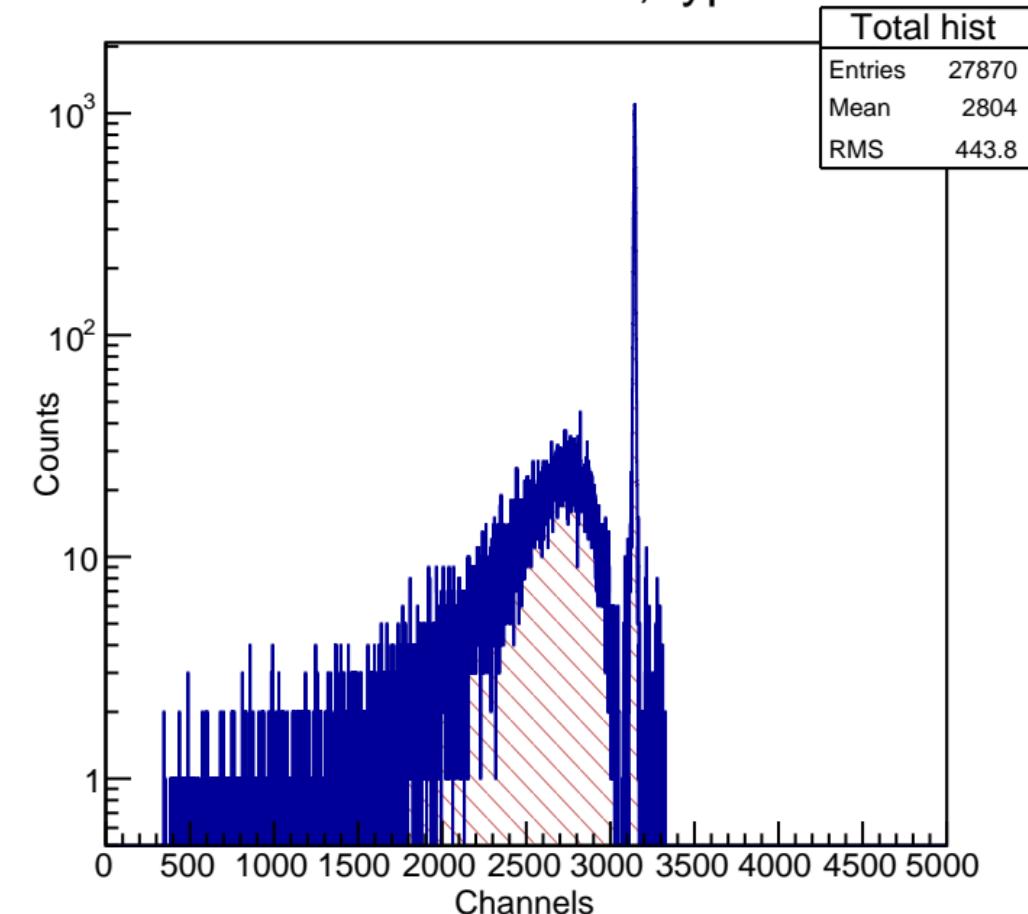
# TDCW Octet 55, type1



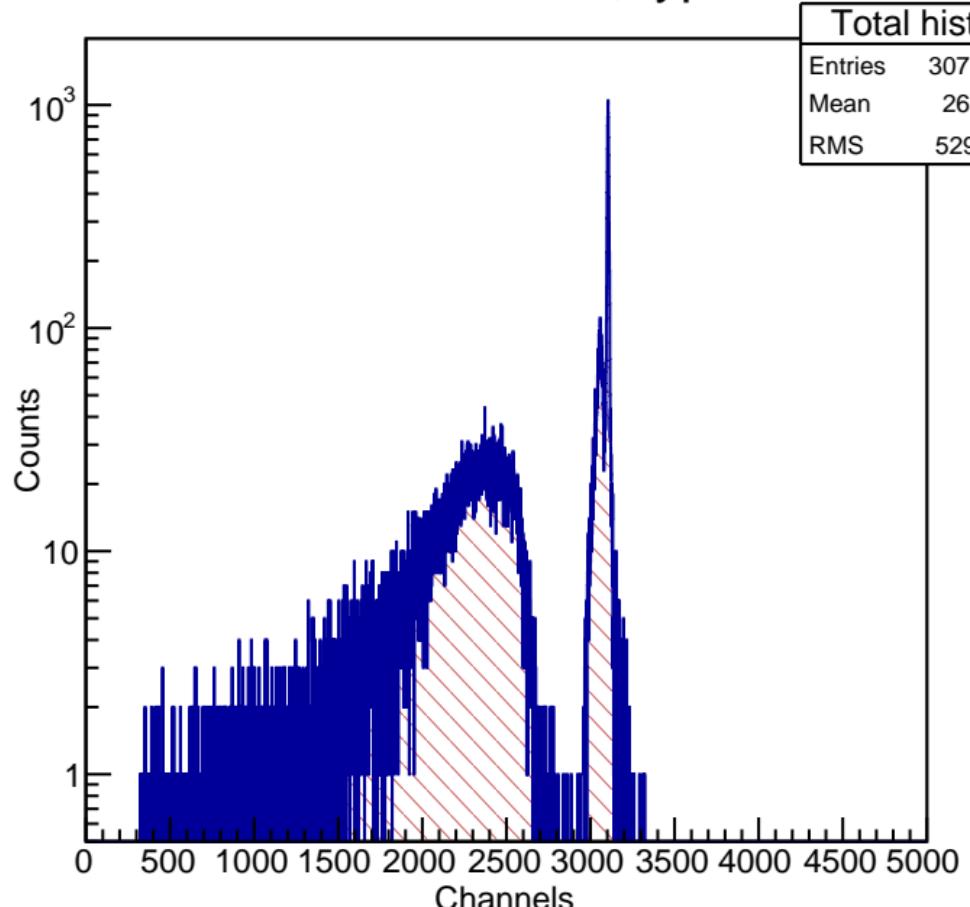
# TDCE Octet 56, type1



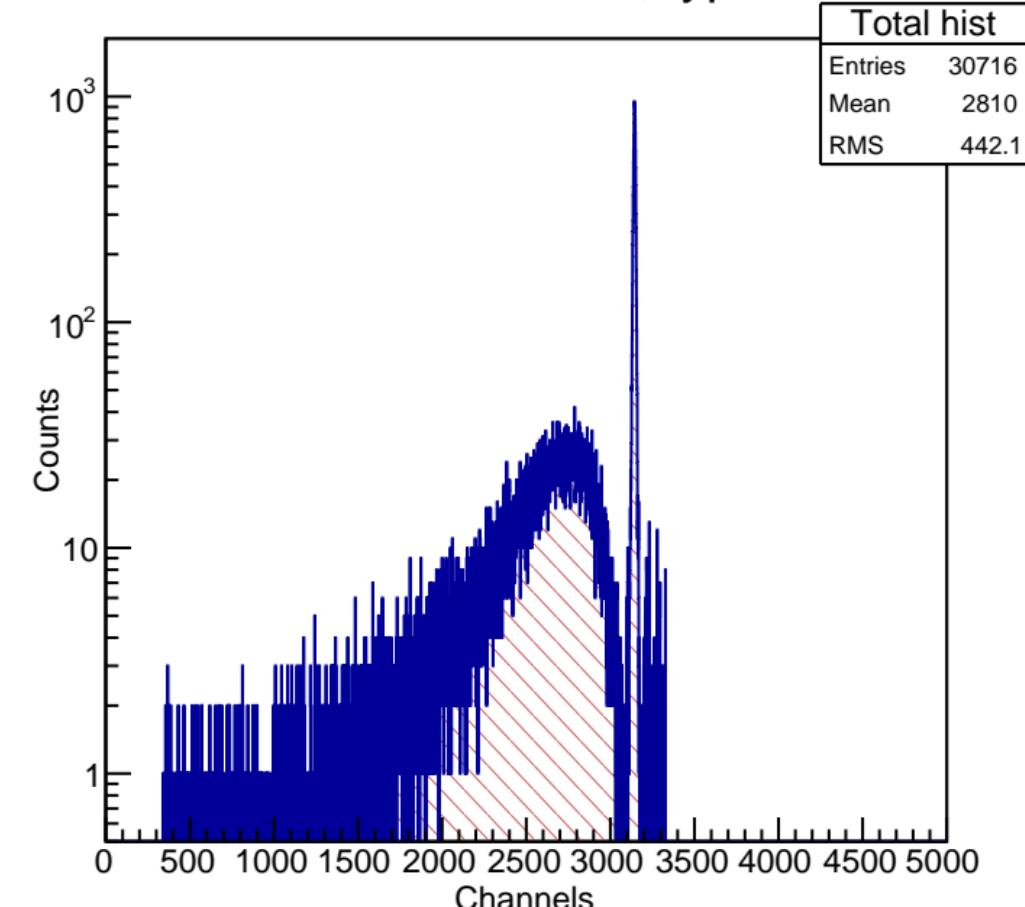
# TDCW Octet 56, type1



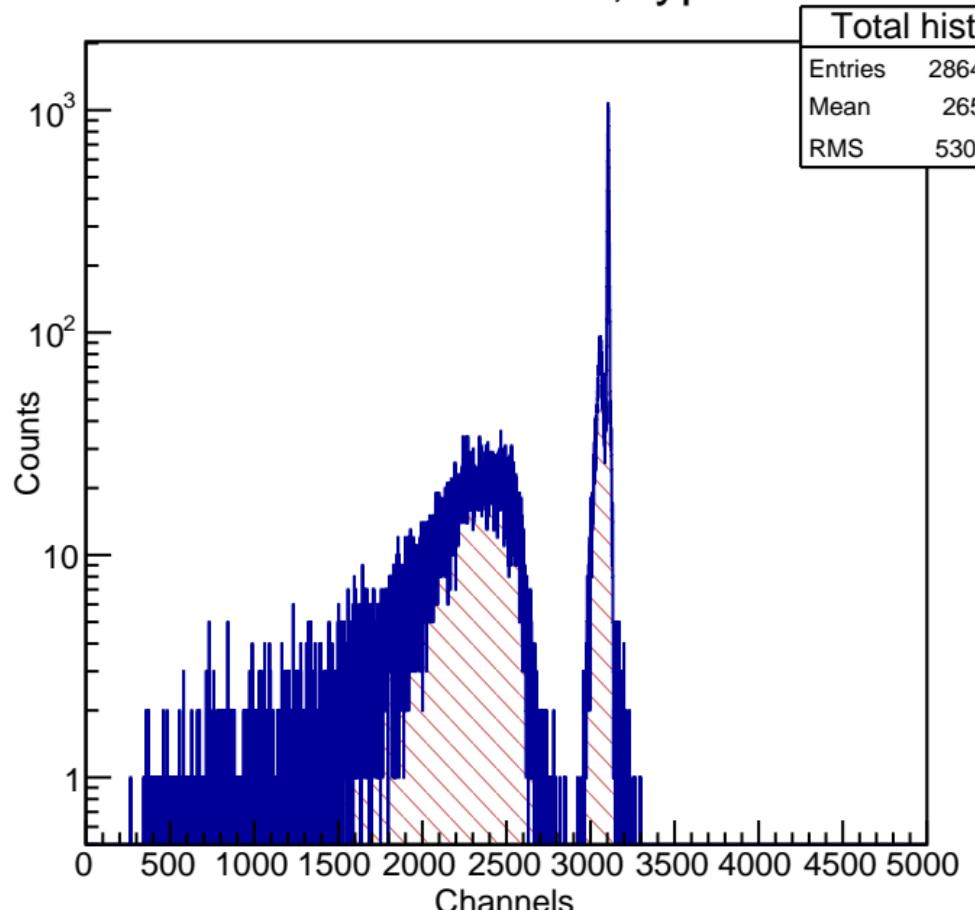
# TDCE Octet 57, type1



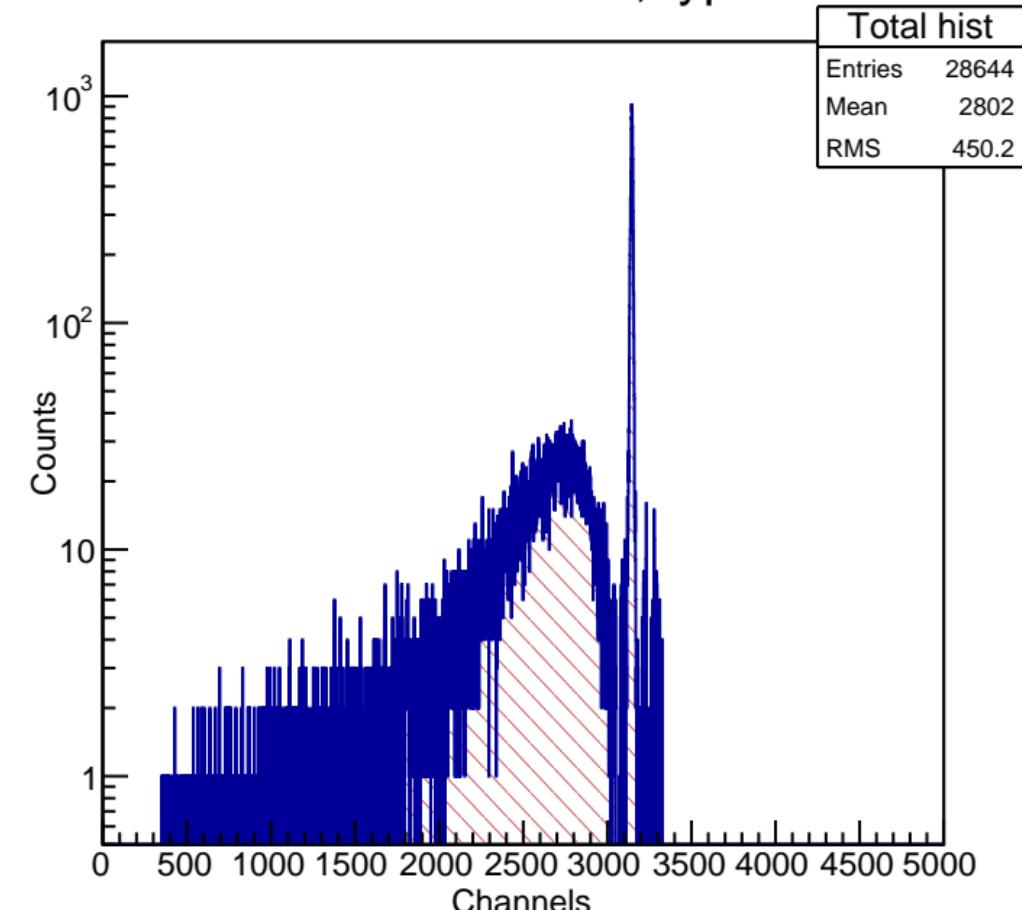
# TDCW Octet 57, type1



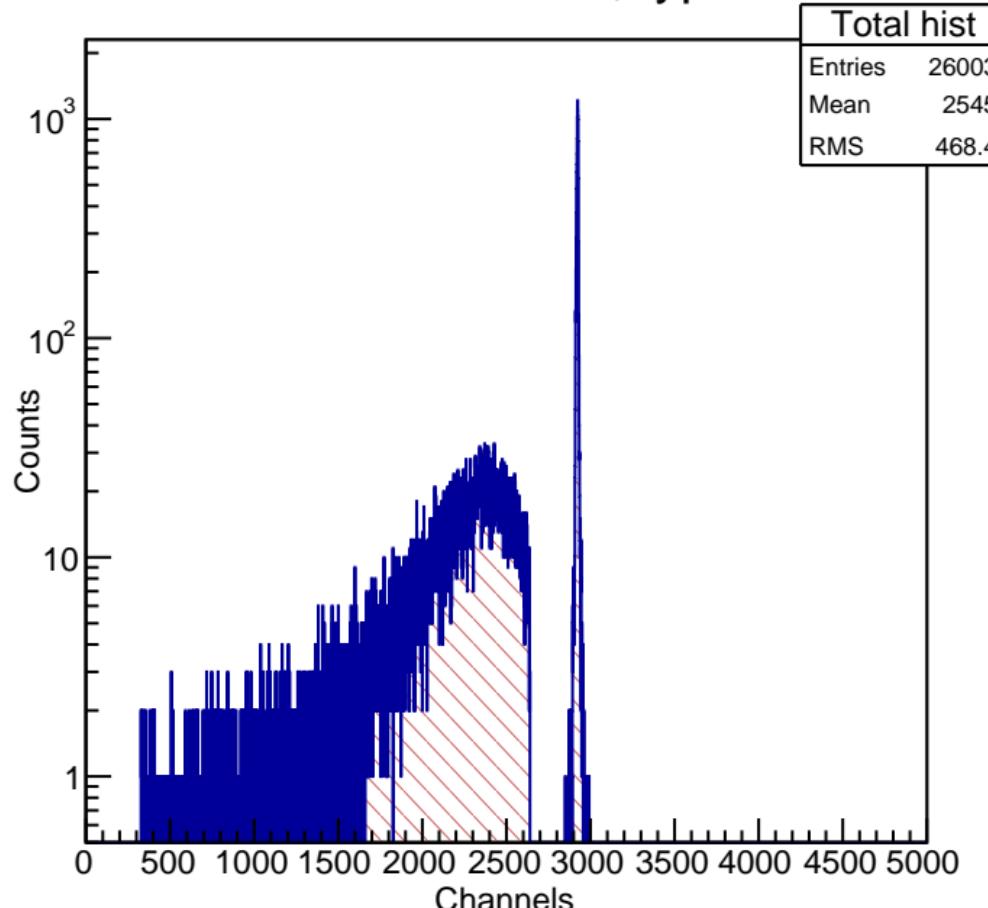
# TDCE Octet 58, type1



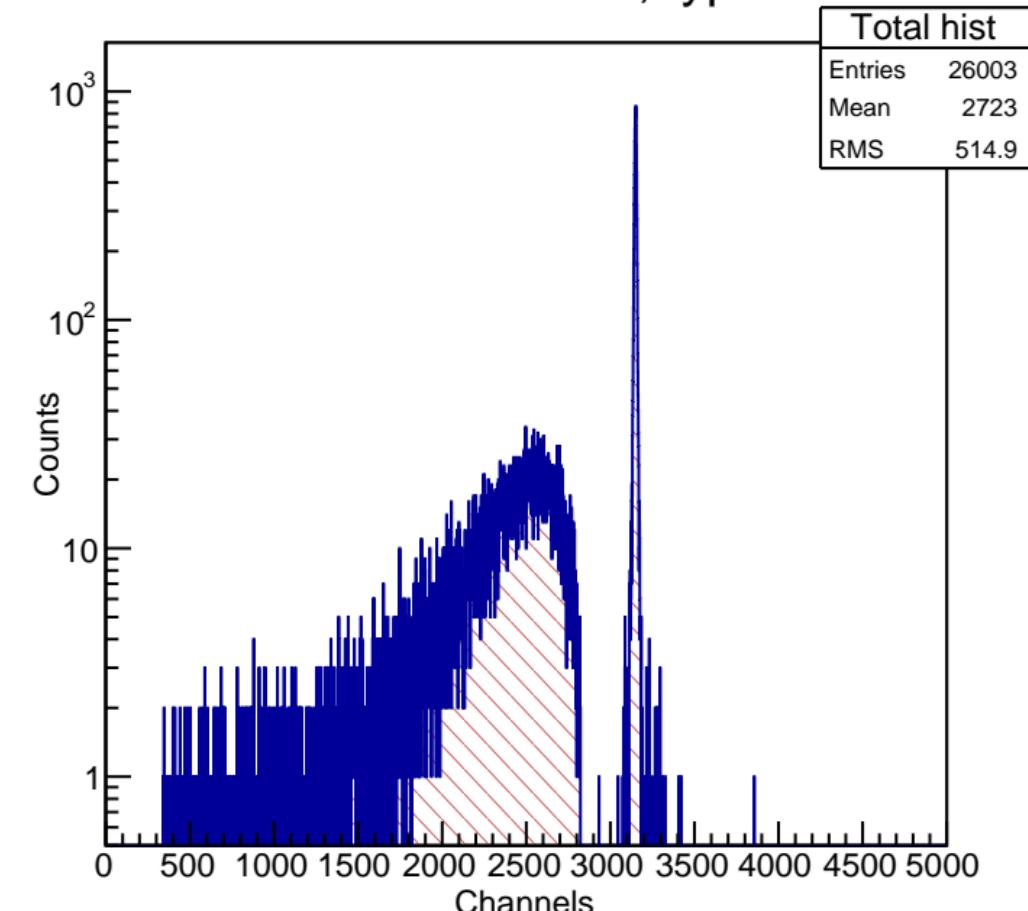
# TDCW Octet 58, type1



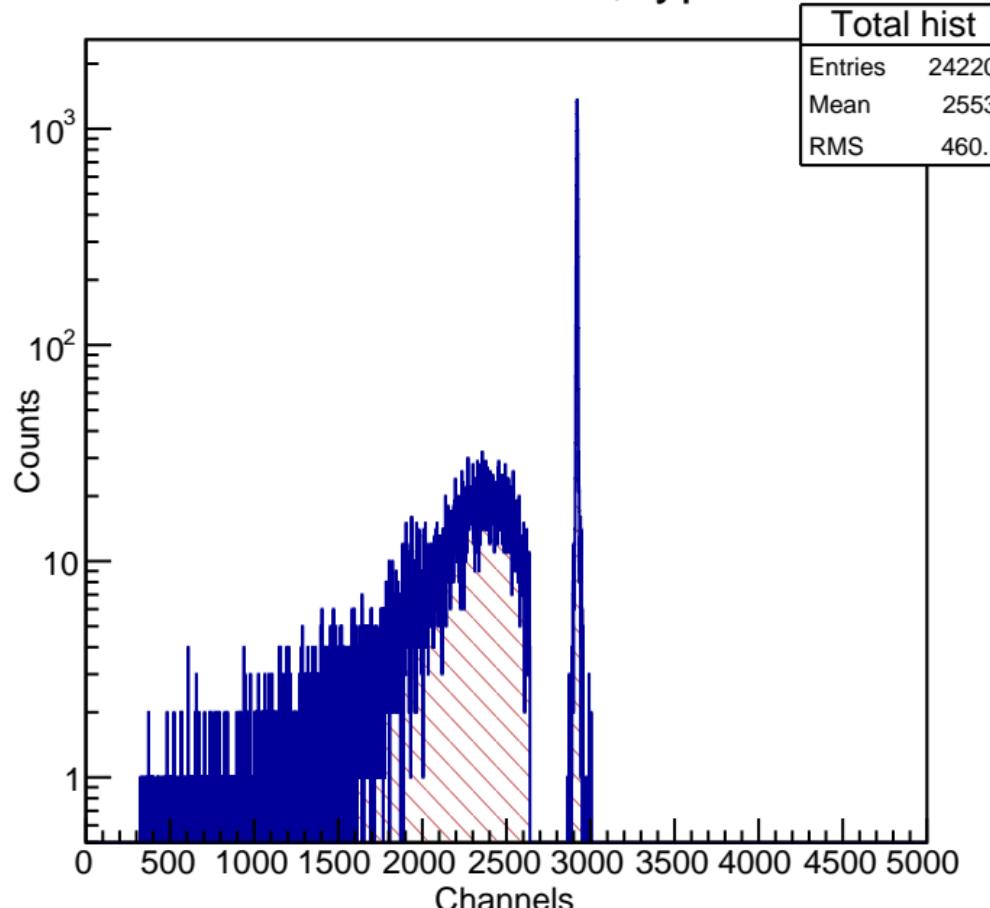
# TDCE Octet 81, type1



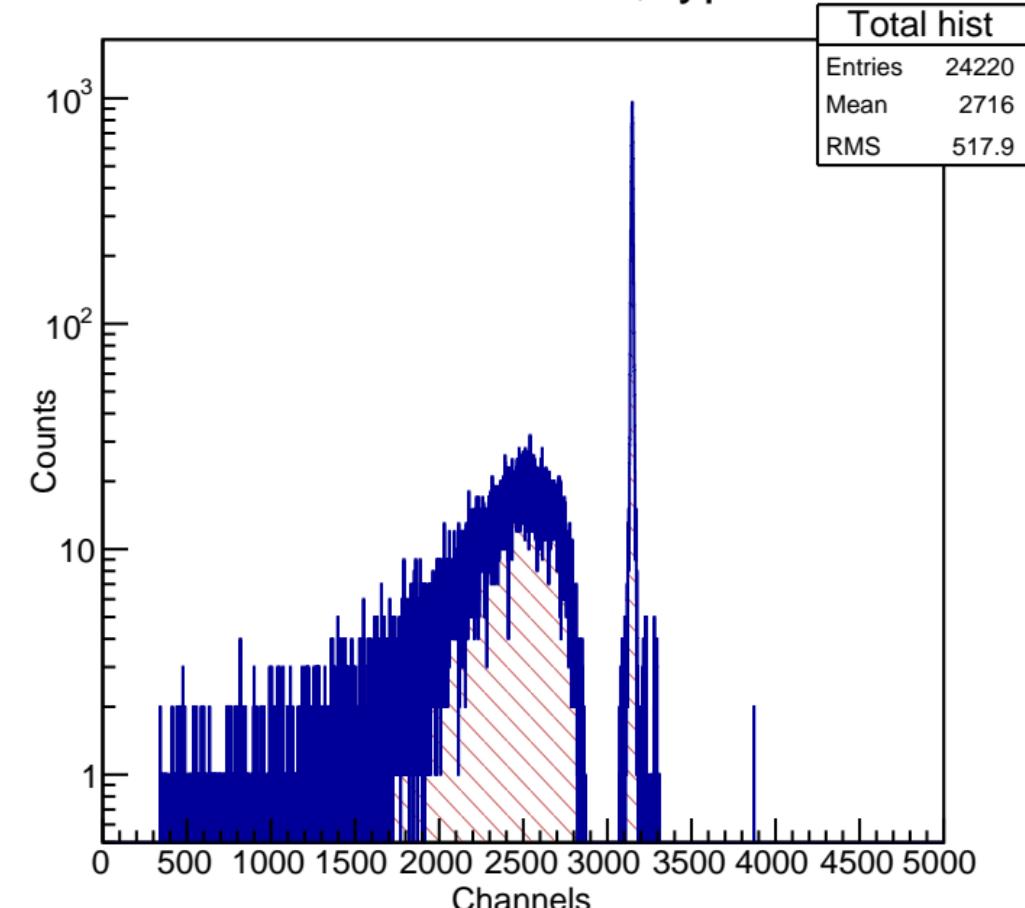
# TDCW Octet 81, type1



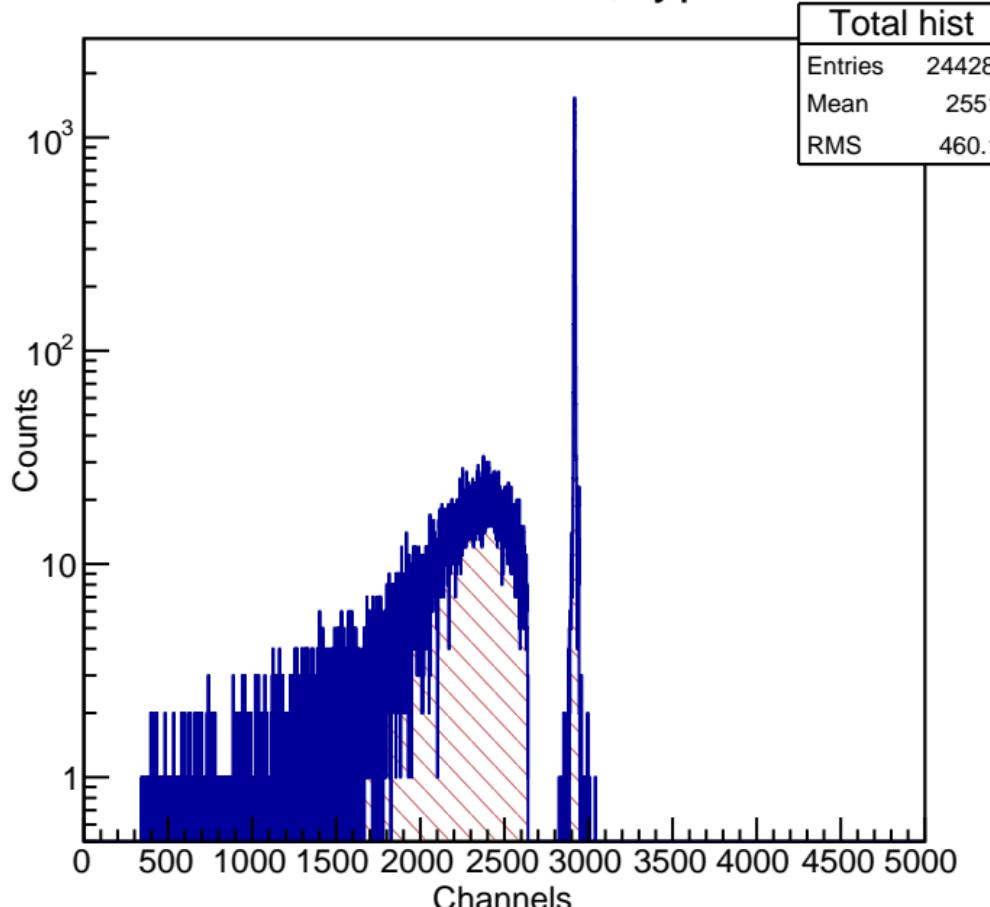
# TDCE Octet 82, type1



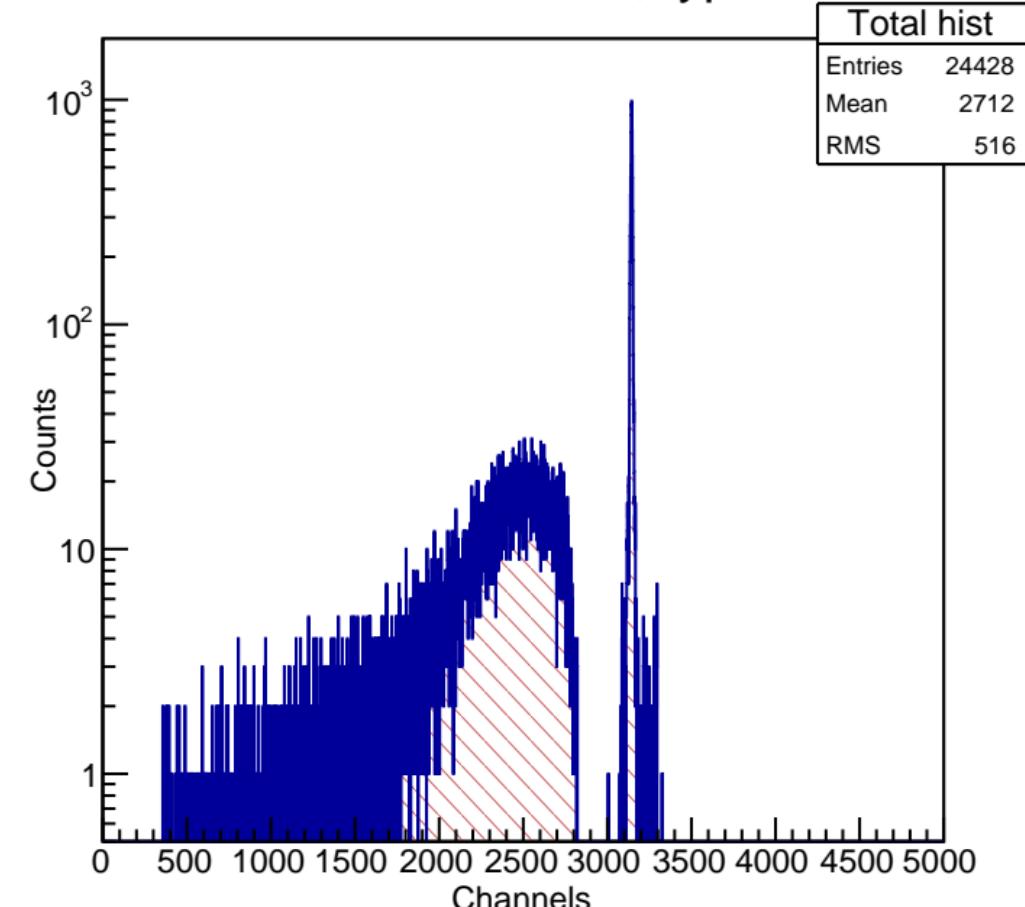
# TDCW Octet 82, type1



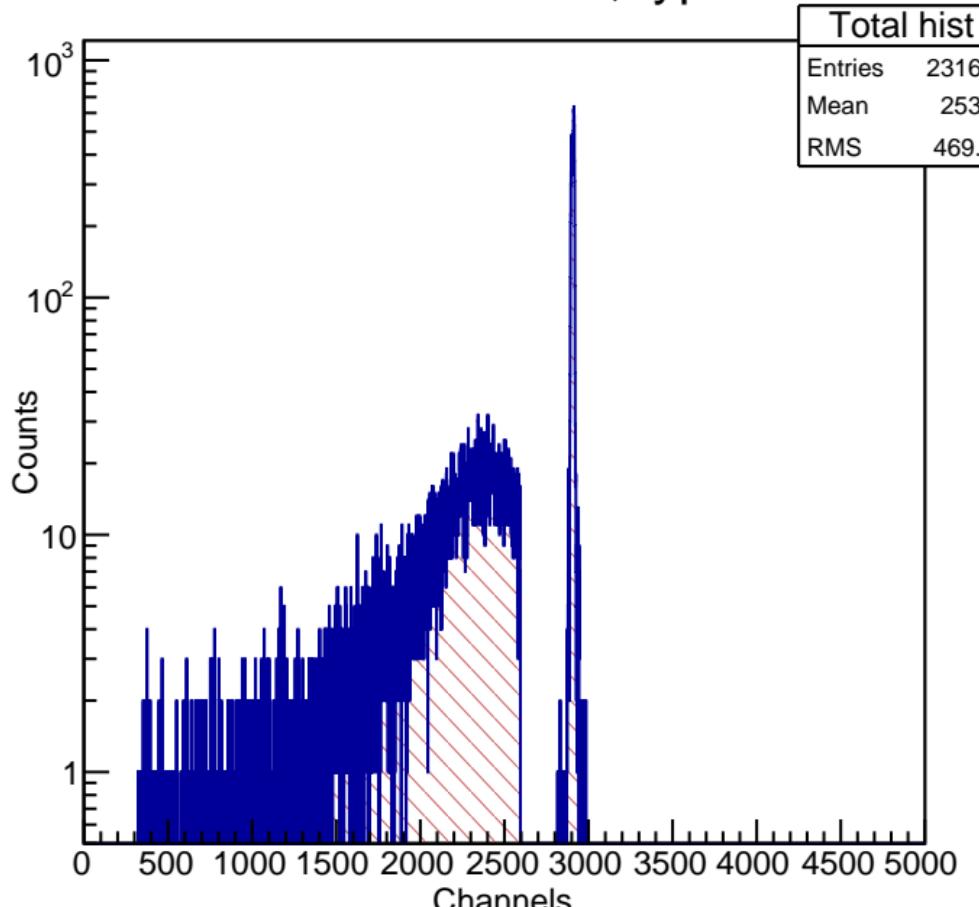
# TDCE Octet 83, type1



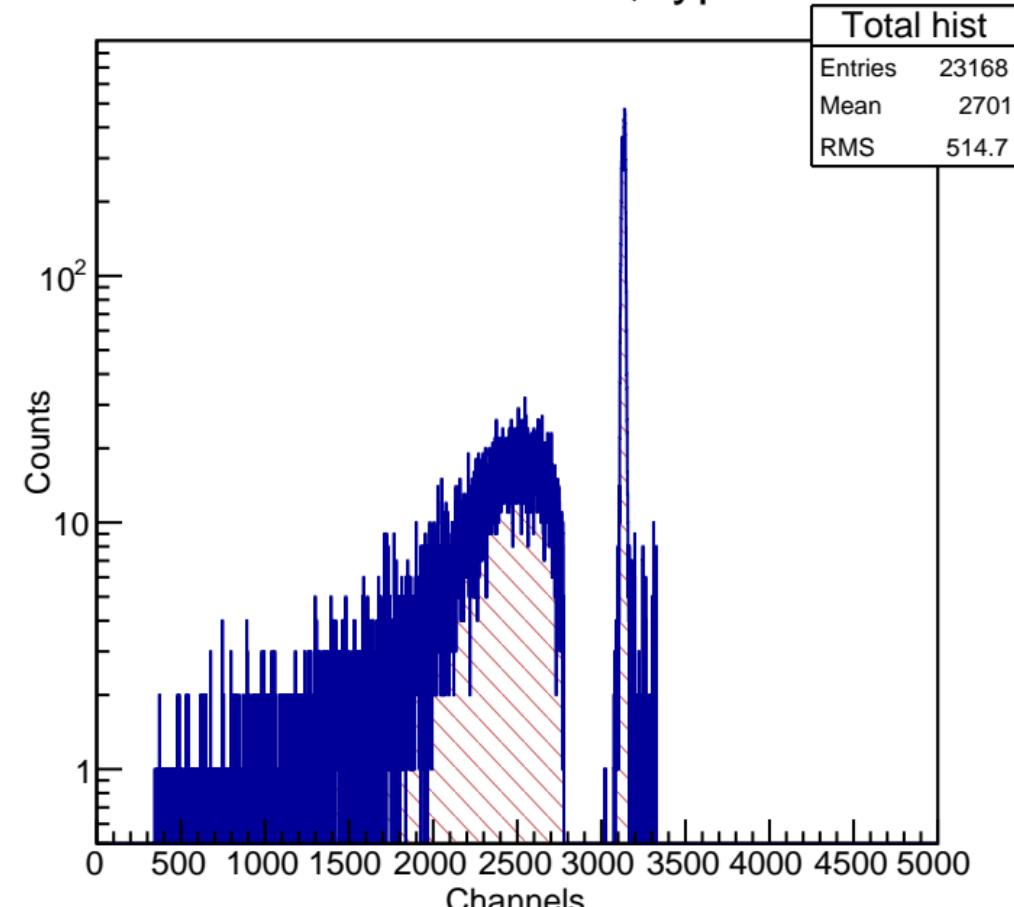
# TDCW Octet 83, type1



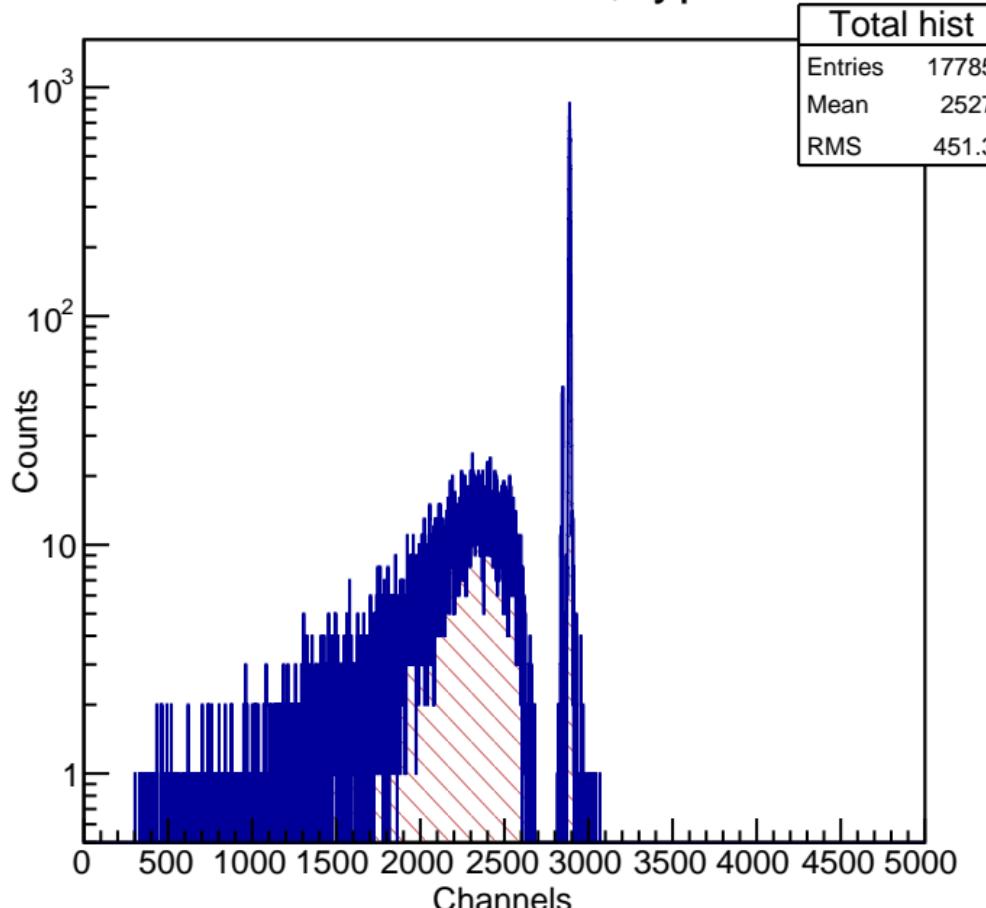
# TDCE Octet 84, type1



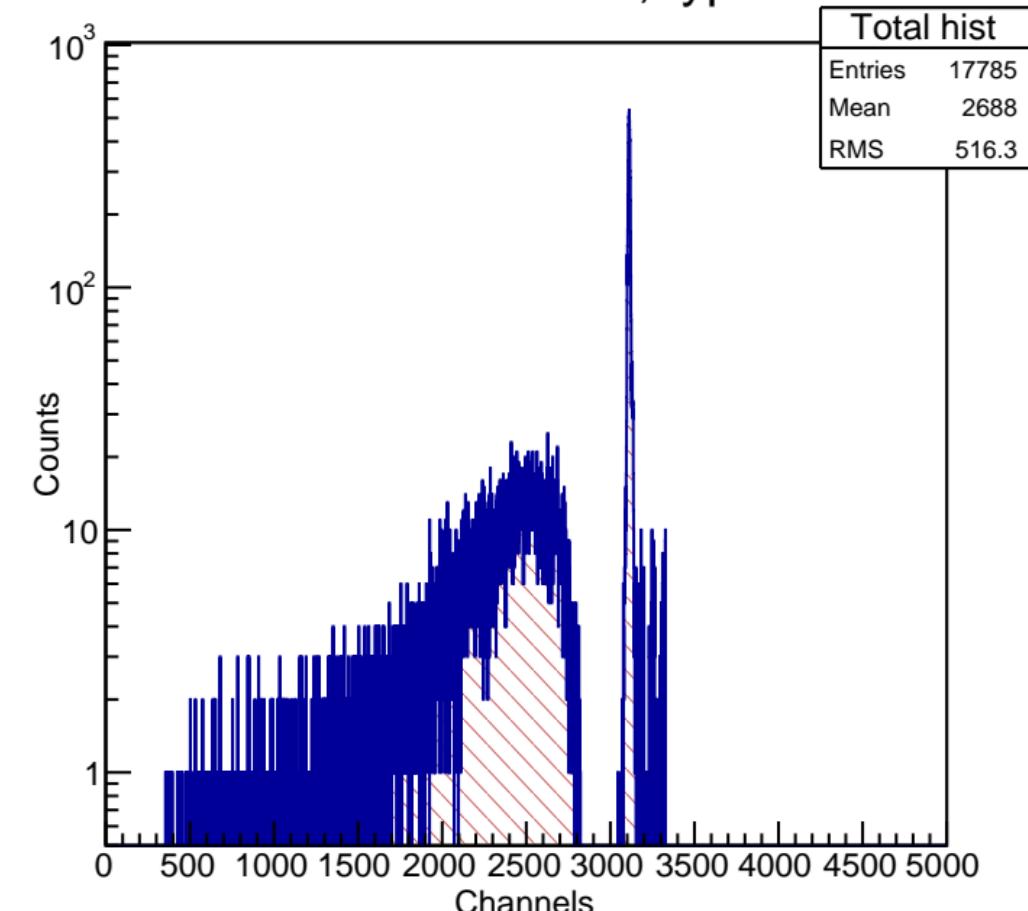
# TDCW Octet 84, type1



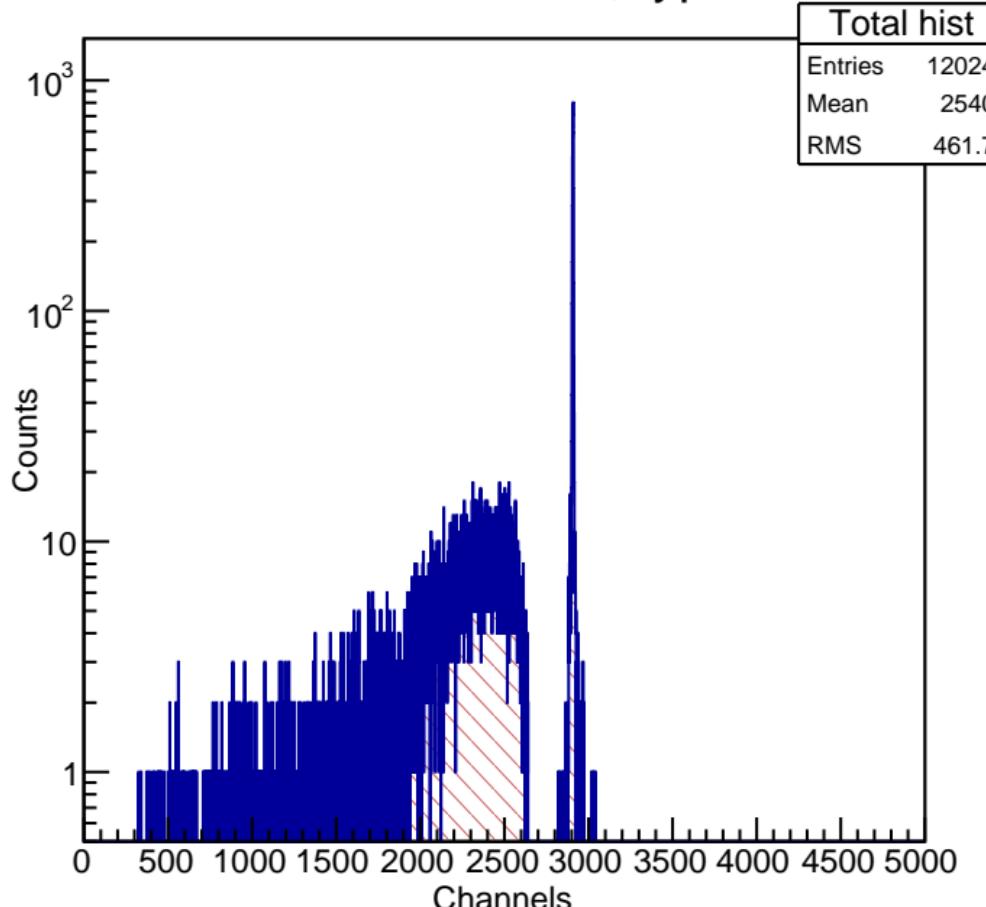
# TDCE Octet 85, type1



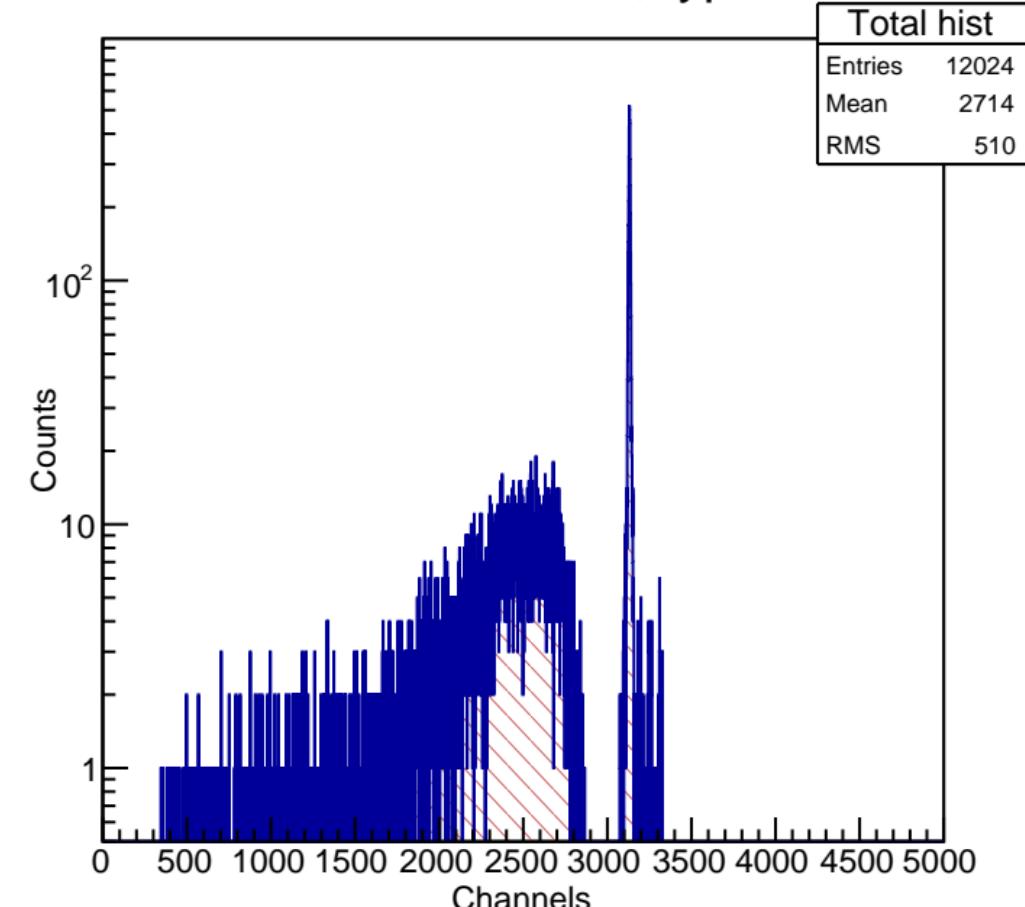
# TDCW Octet 85, type1



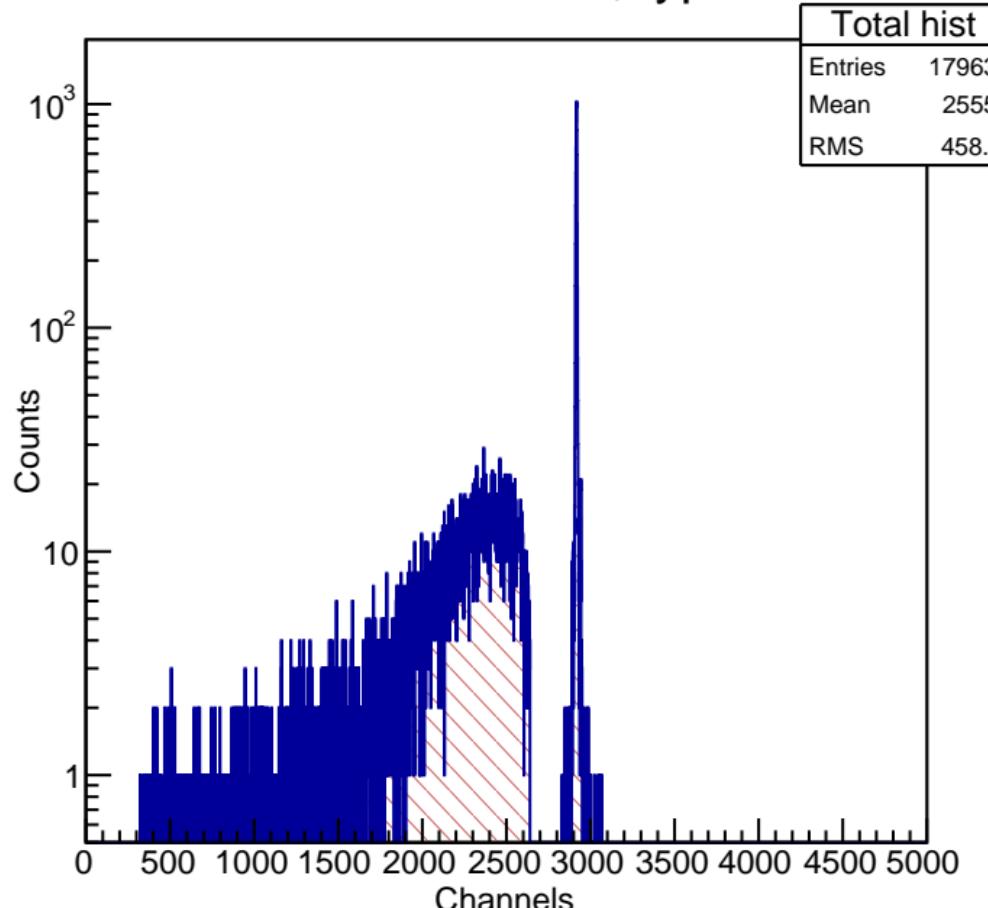
# TDCE Octet 86, type1



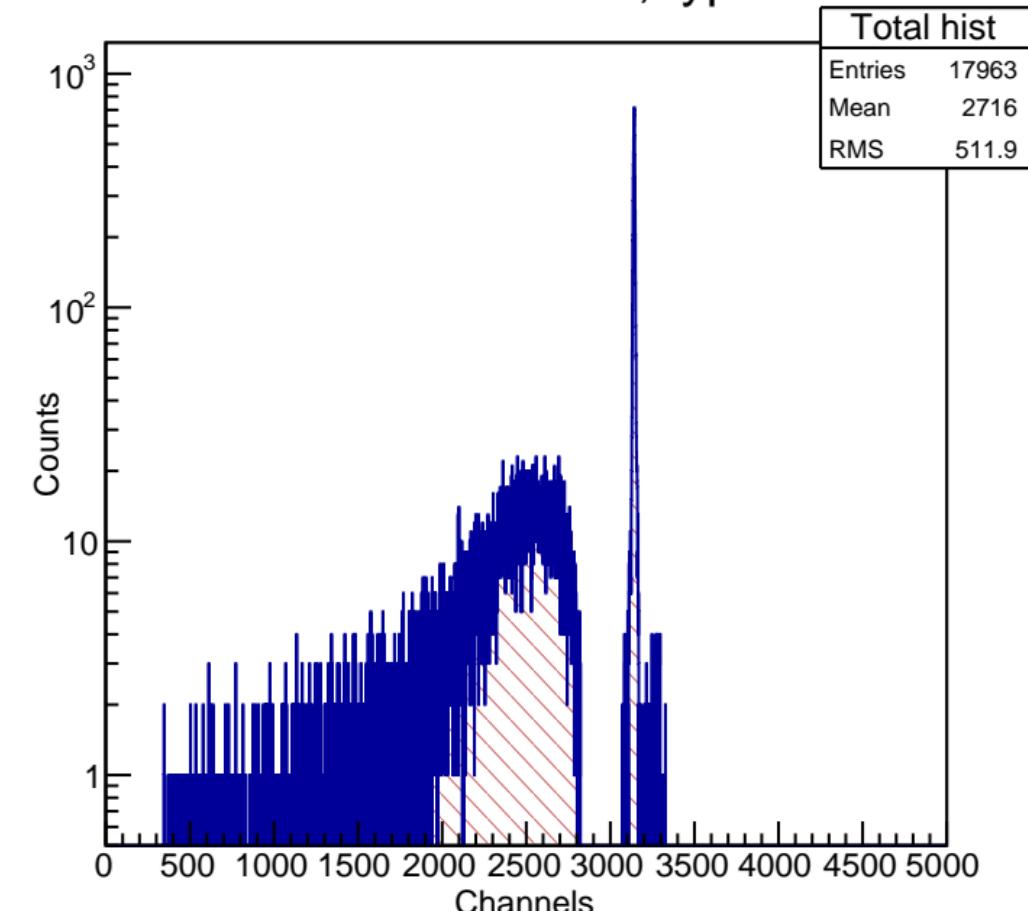
# TDCW Octet 86, type1



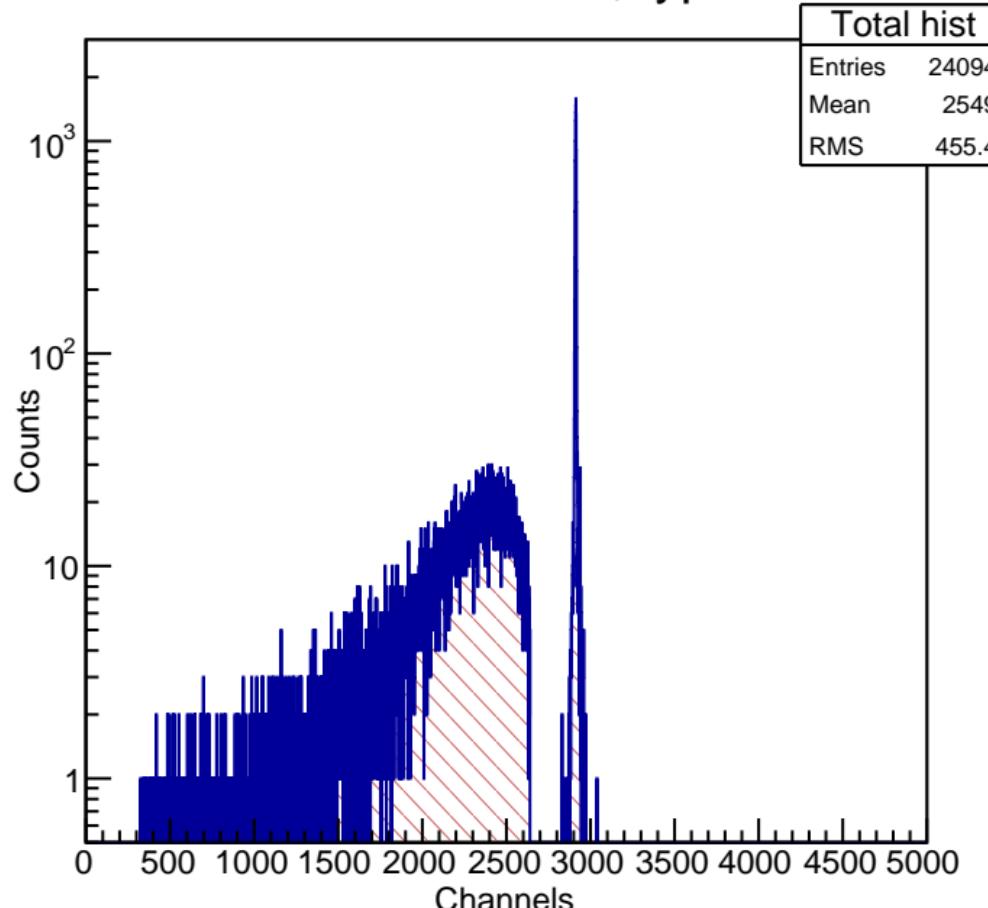
# TDCE Octet 87, type1



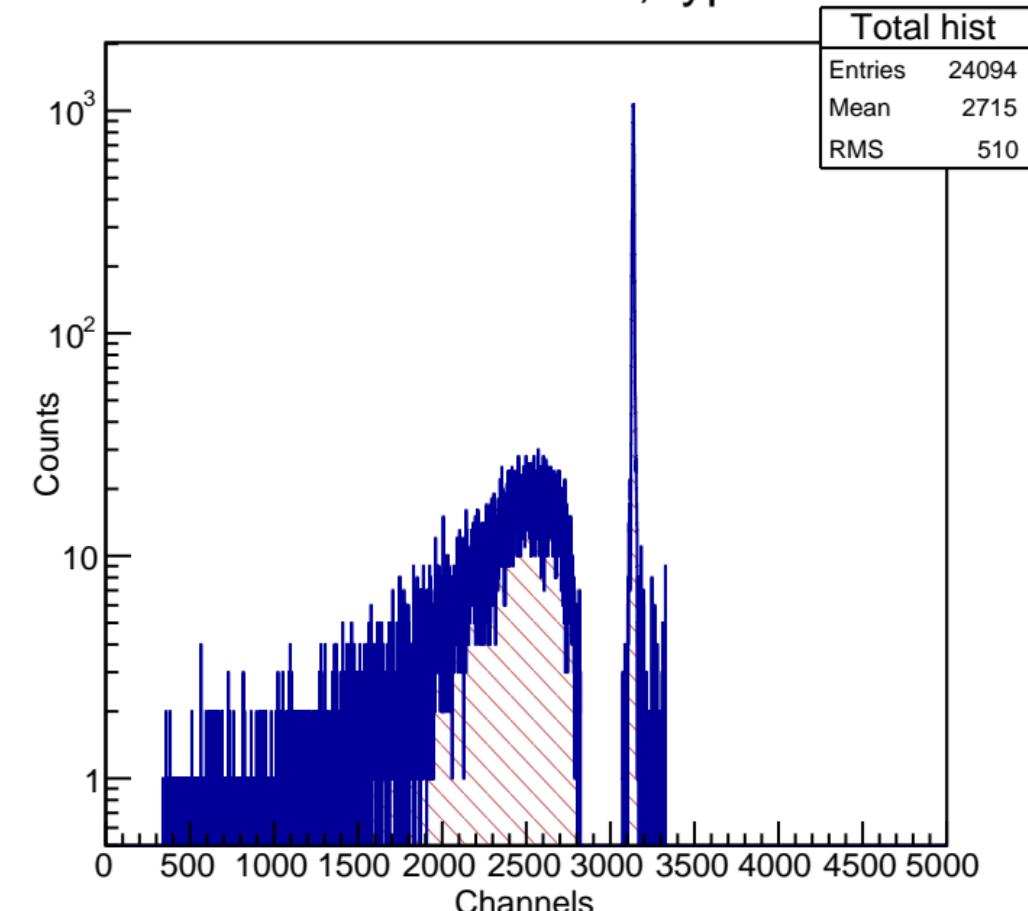
# TDCW Octet 87, type1



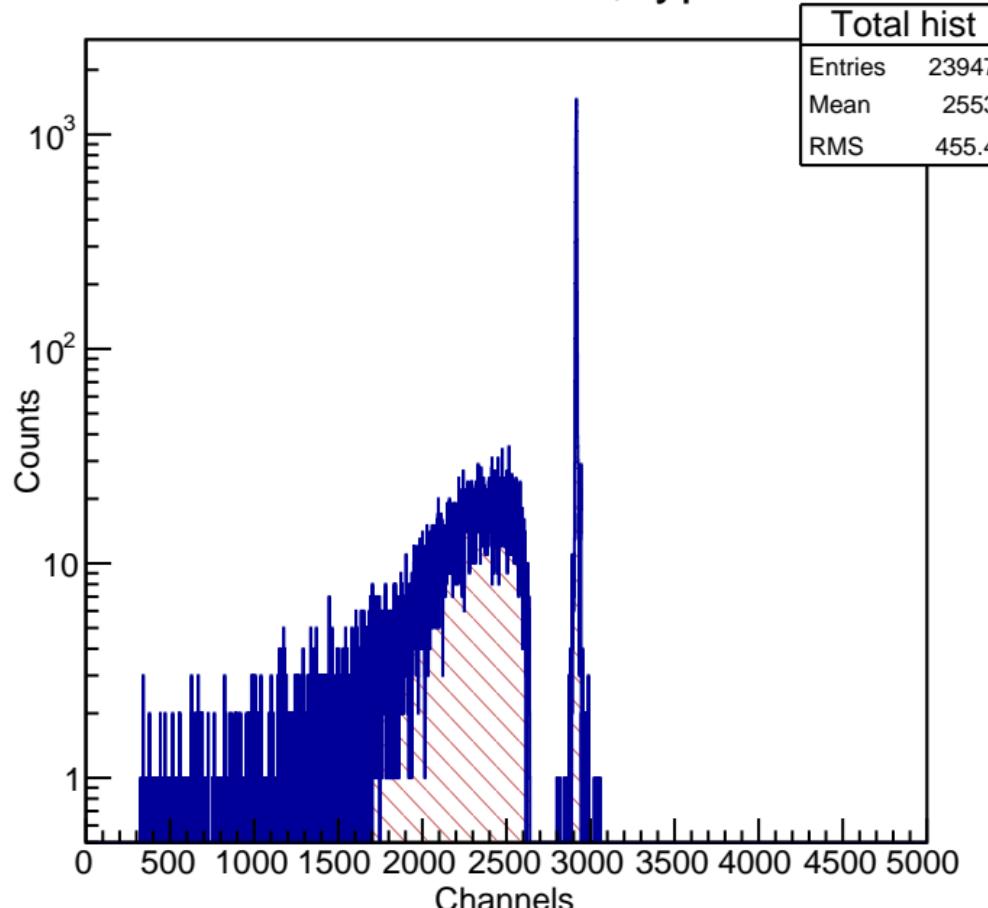
# TDCE Octet 88, type1



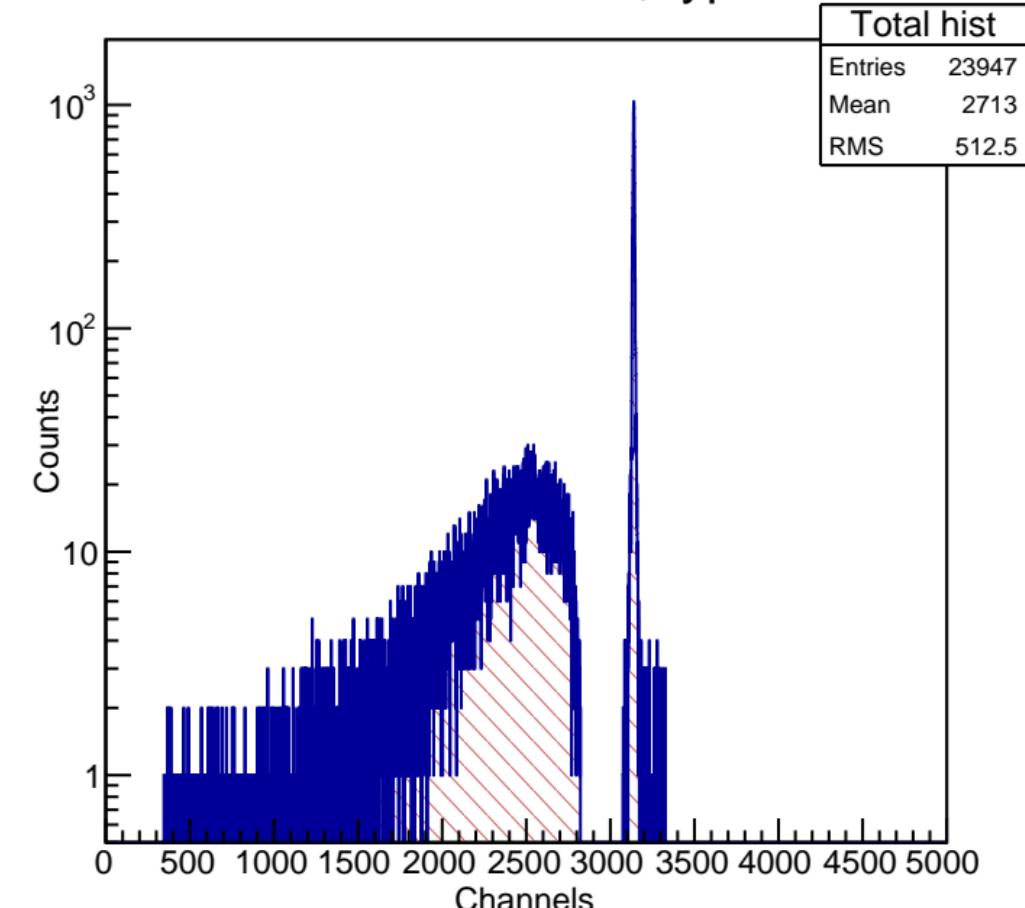
# TDCW Octet 88, type1



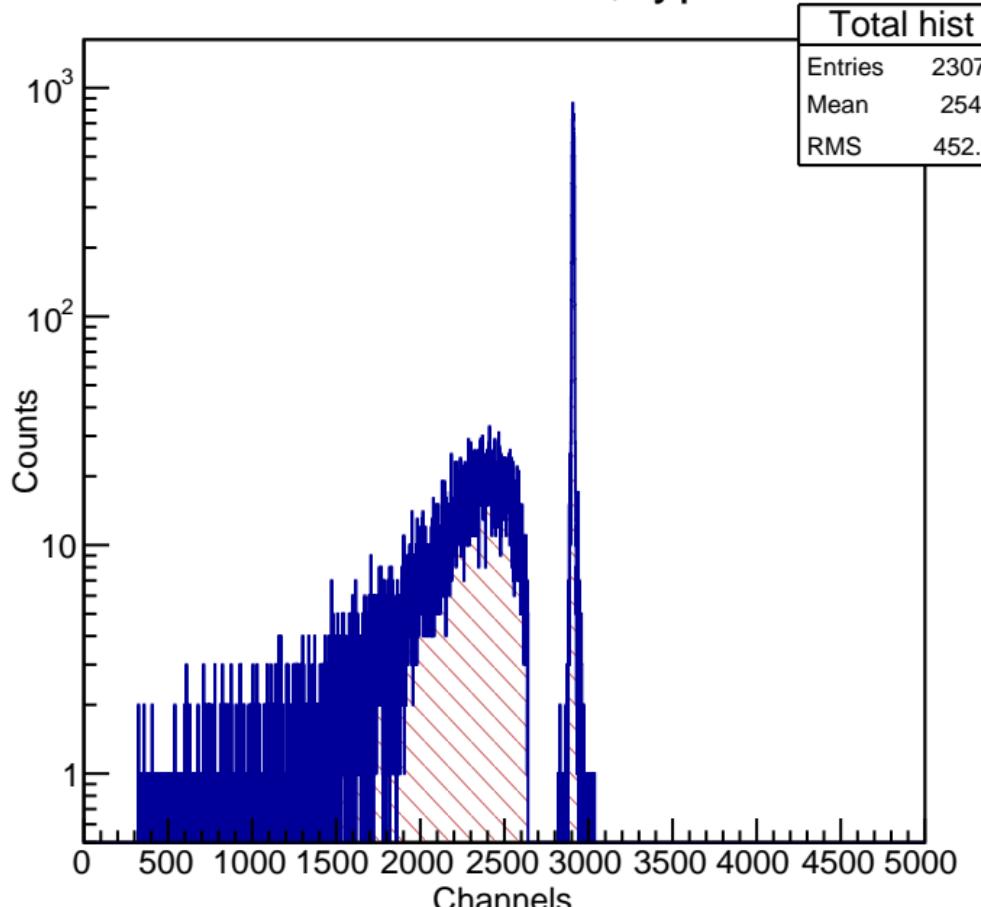
# TDCE Octet 89, type1



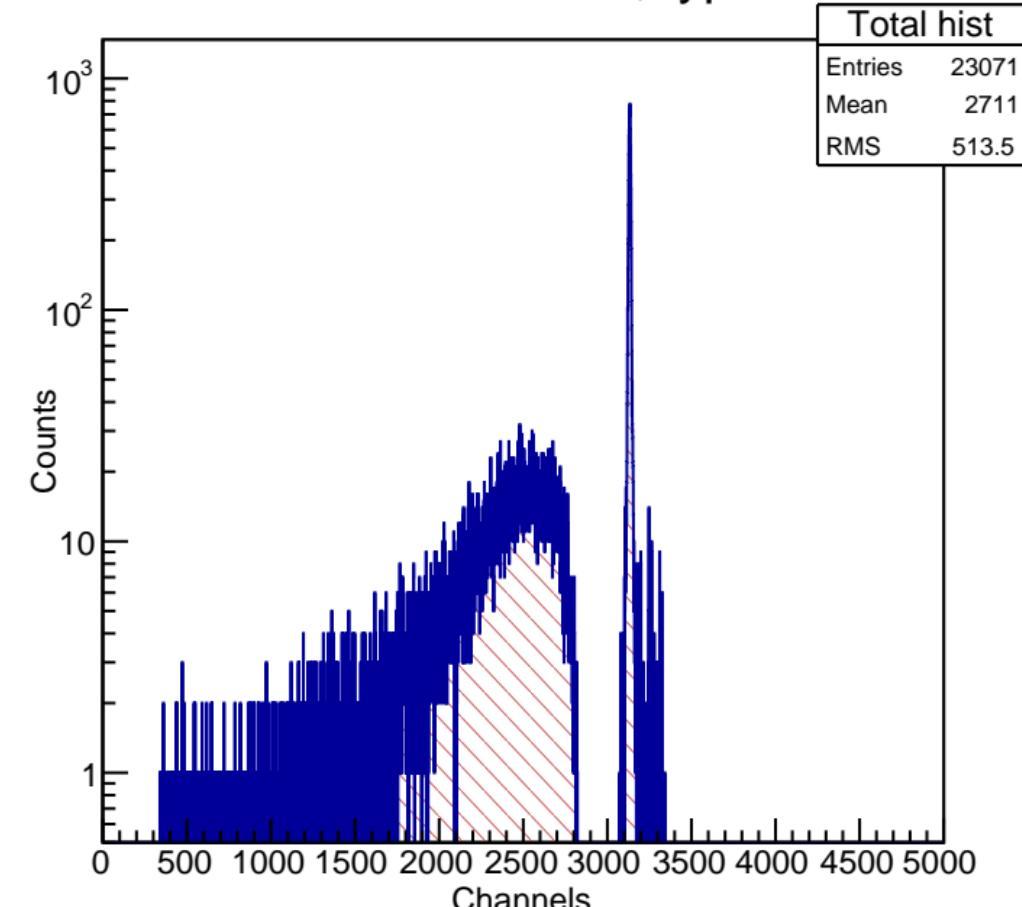
# TDCW Octet 89, type1



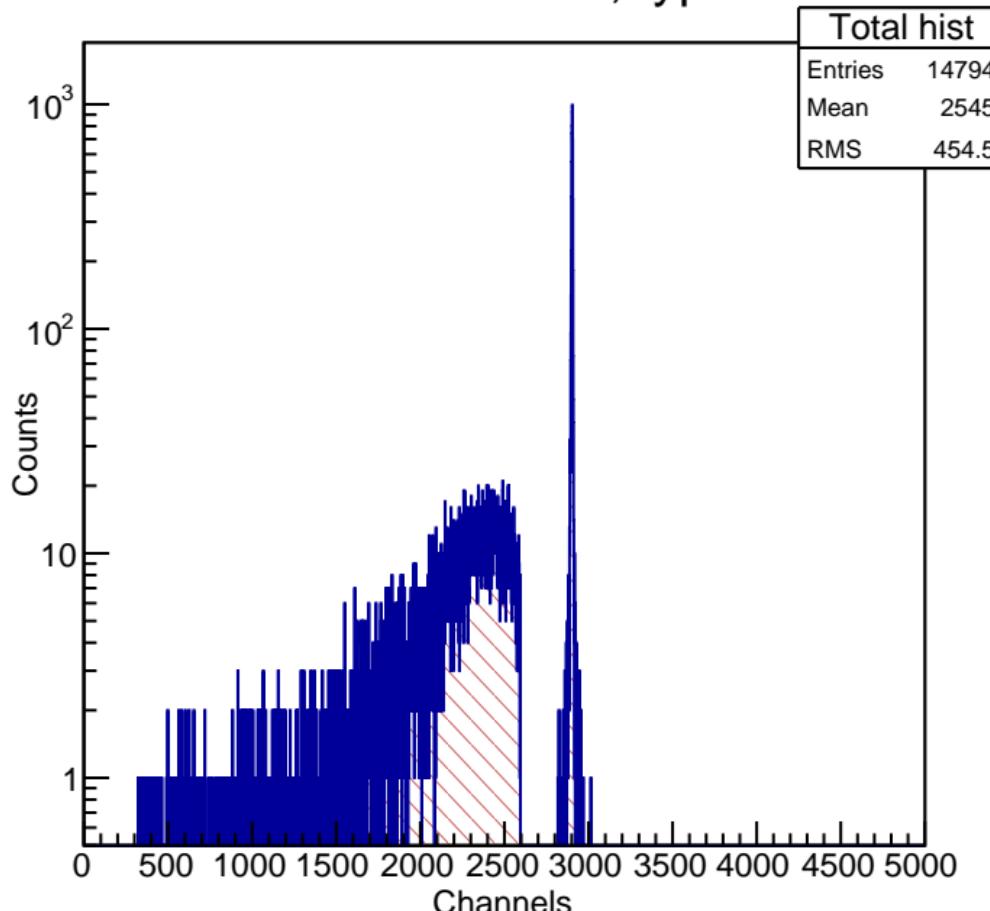
# TDCE Octet 90, type1



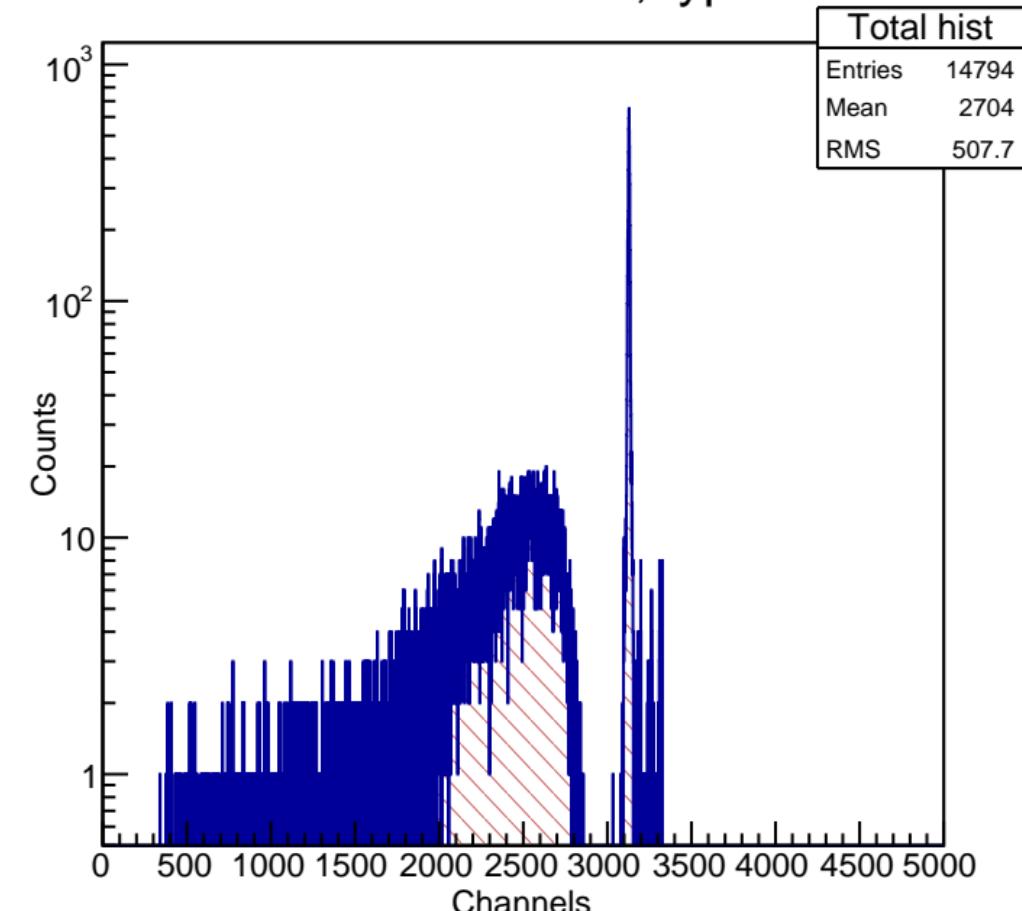
# TDCW Octet 90, type1



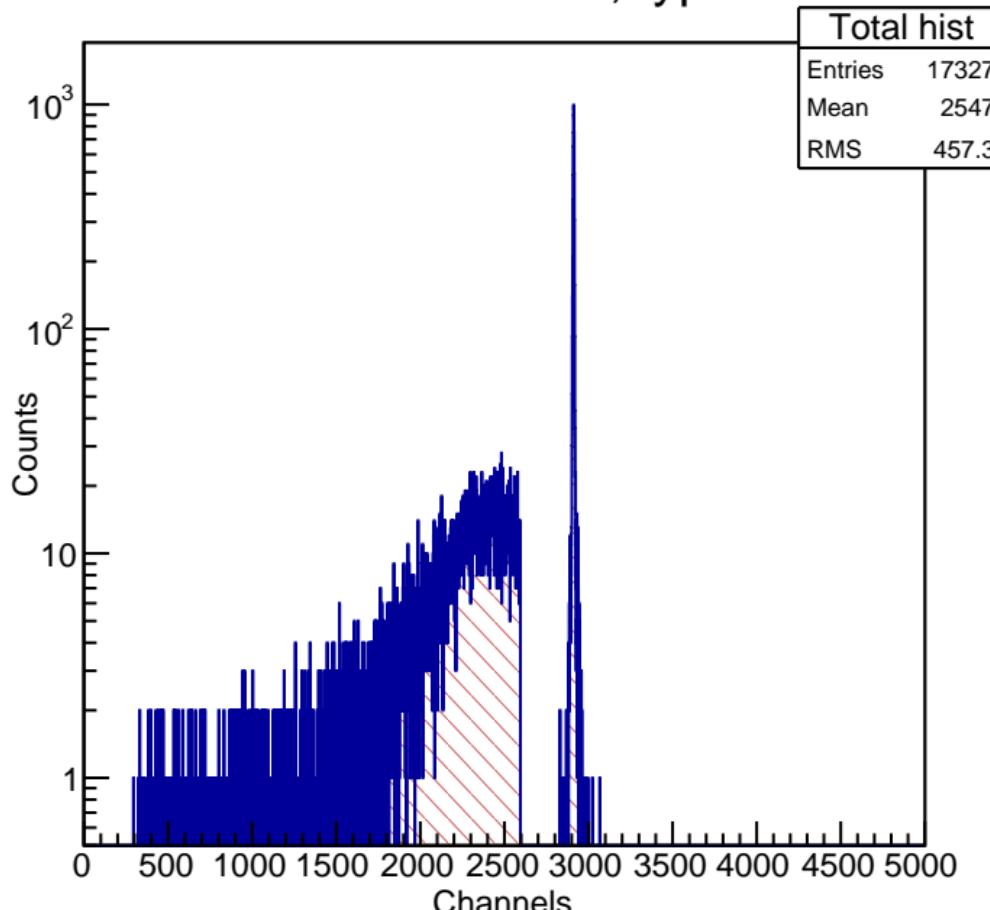
# TDCE Octet 92, type1



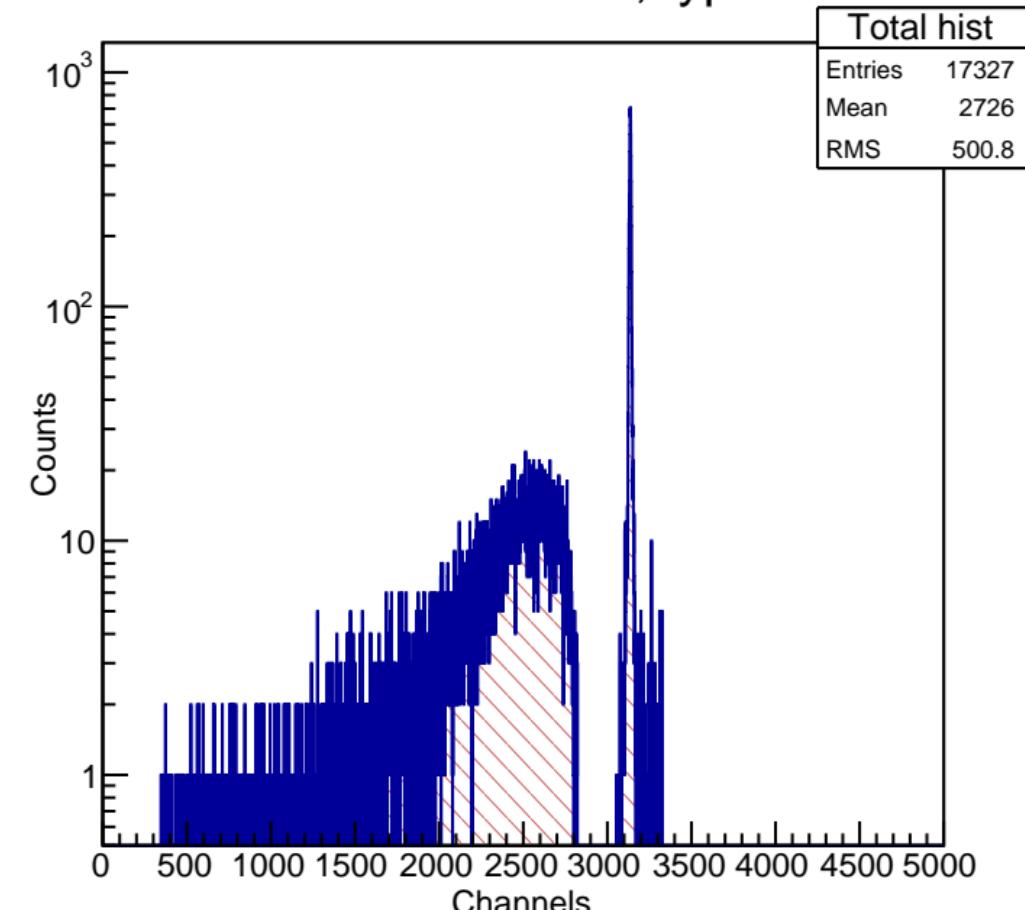
# TDCW Octet 92, type1



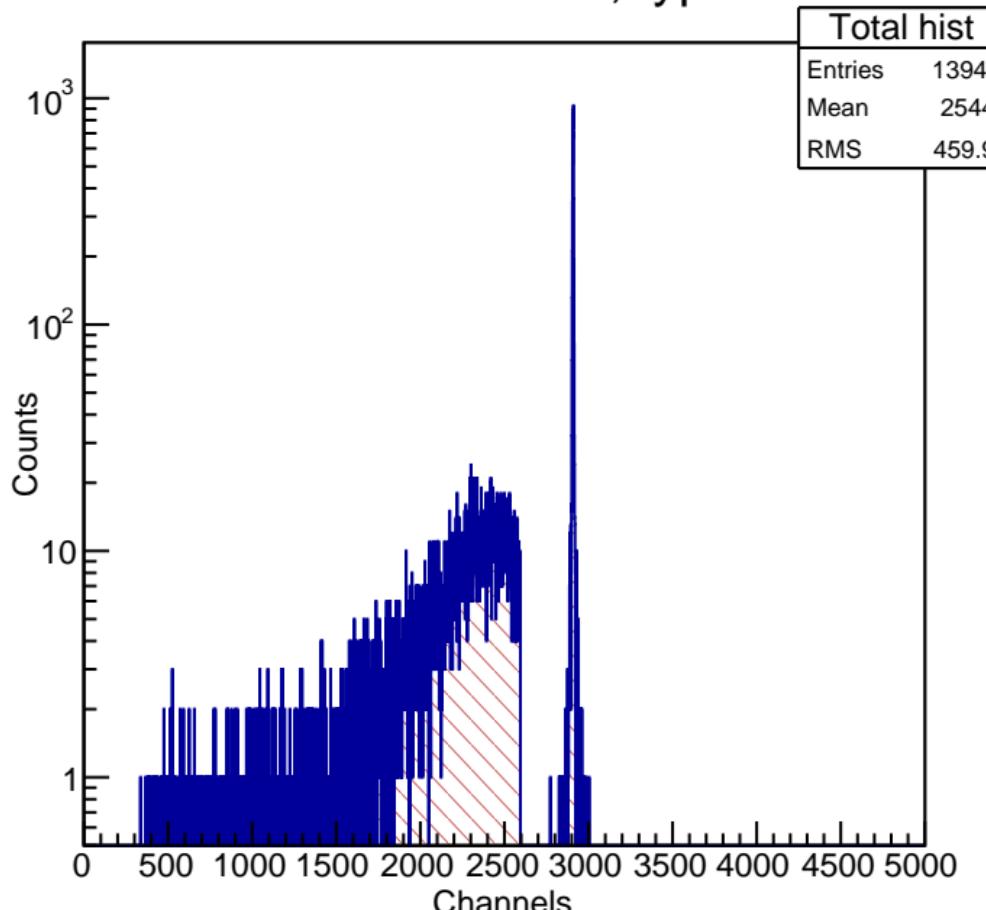
# TDCE Octet 94, type1



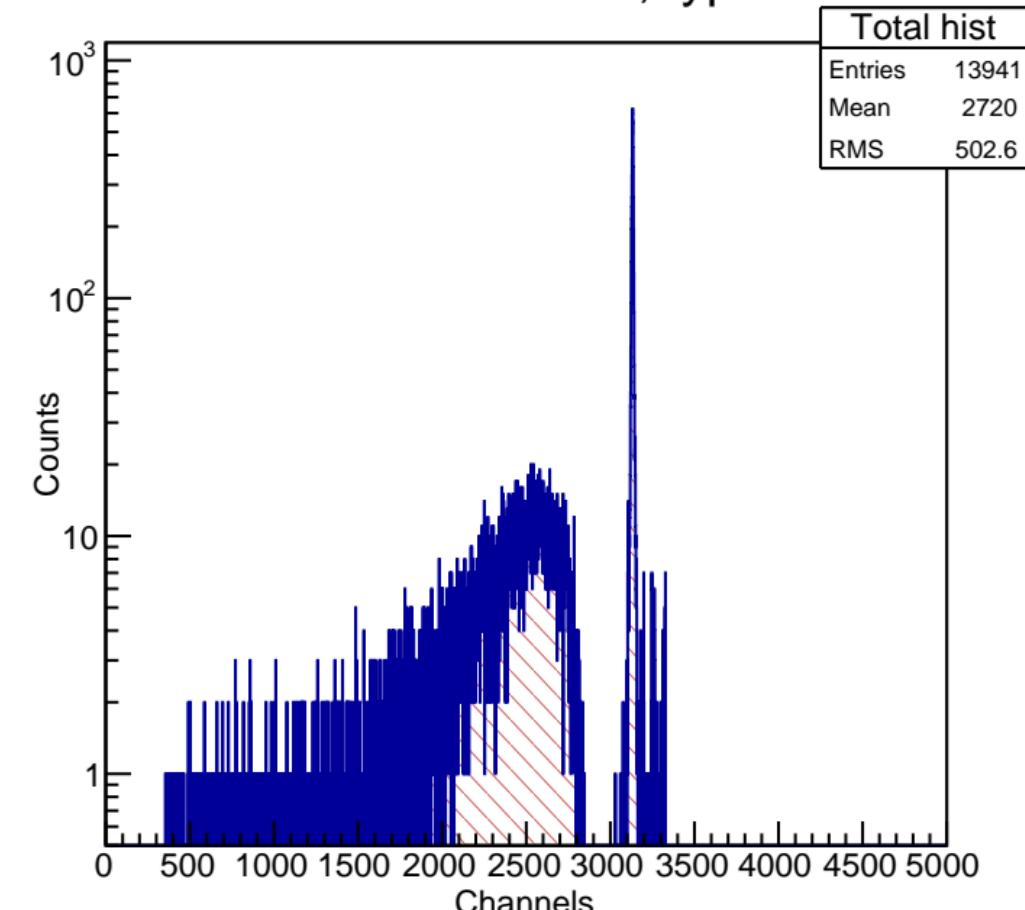
# TDCW Octet 94, type1



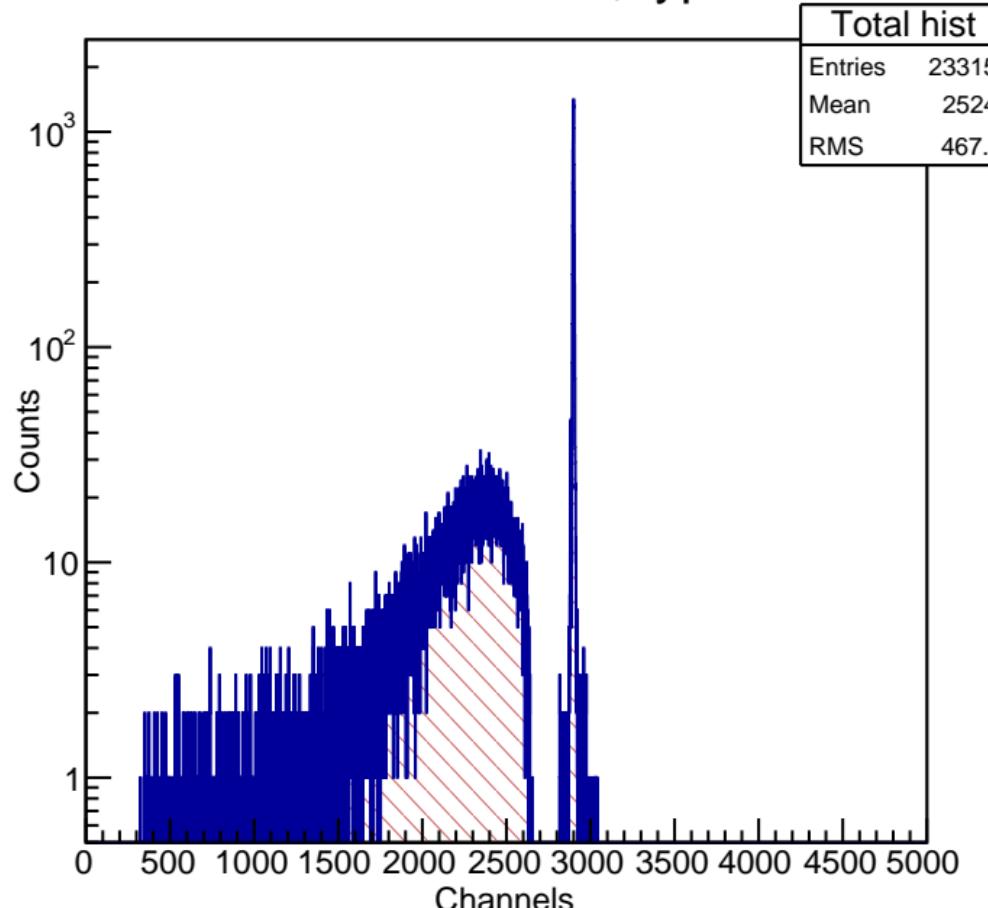
# TDCE Octet 95, type1



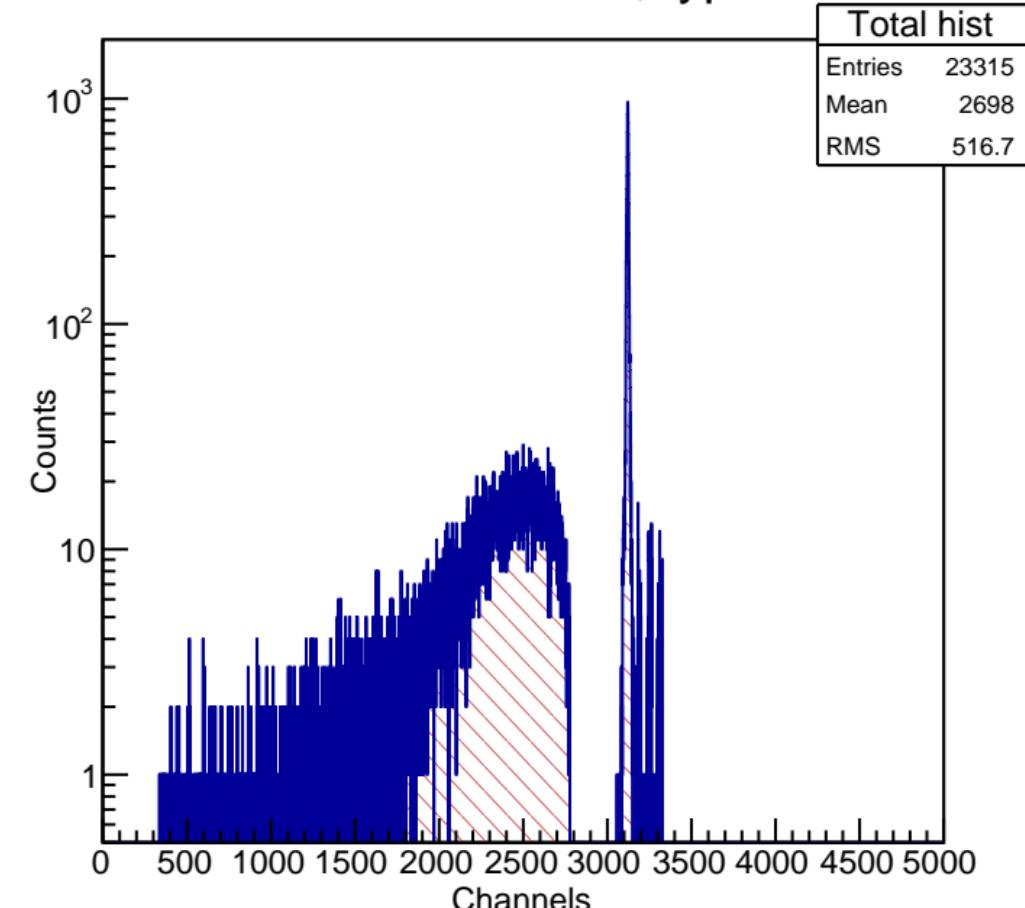
# TDCW Octet 95, type1



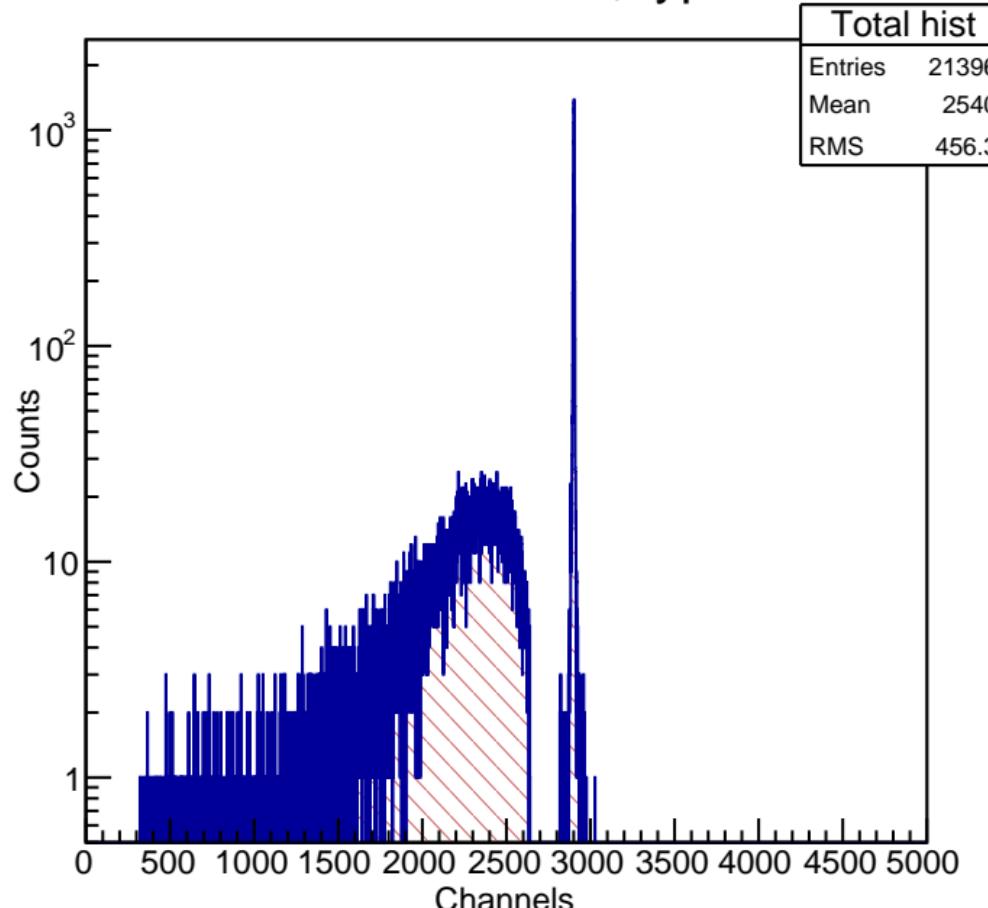
# TDCE Octet 96, type1



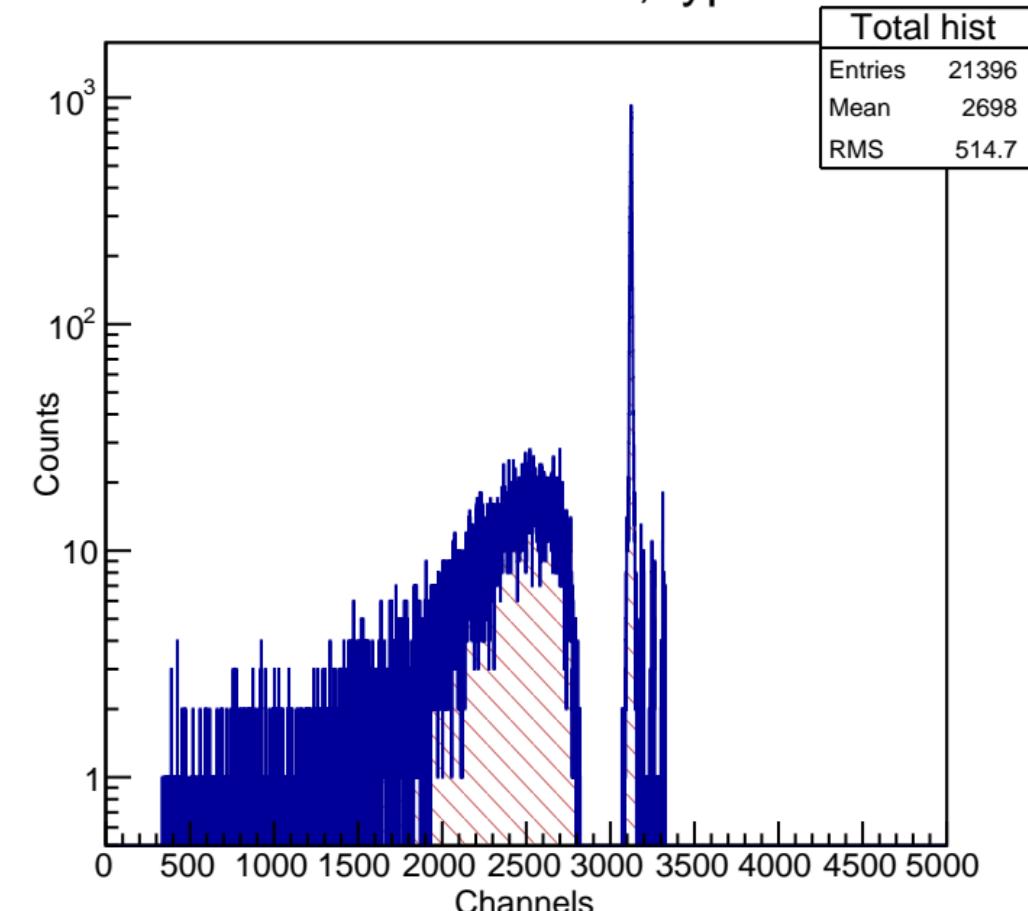
# TDCW Octet 96, type1



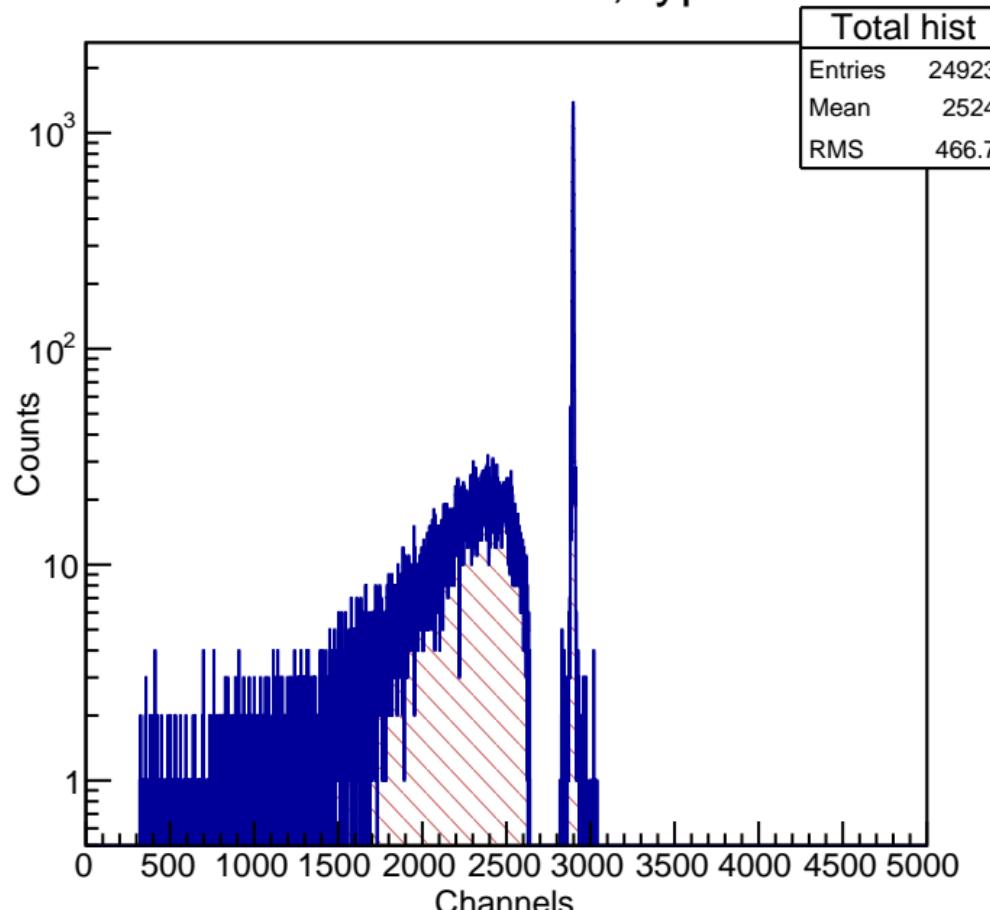
# TDCE Octet 97, type1



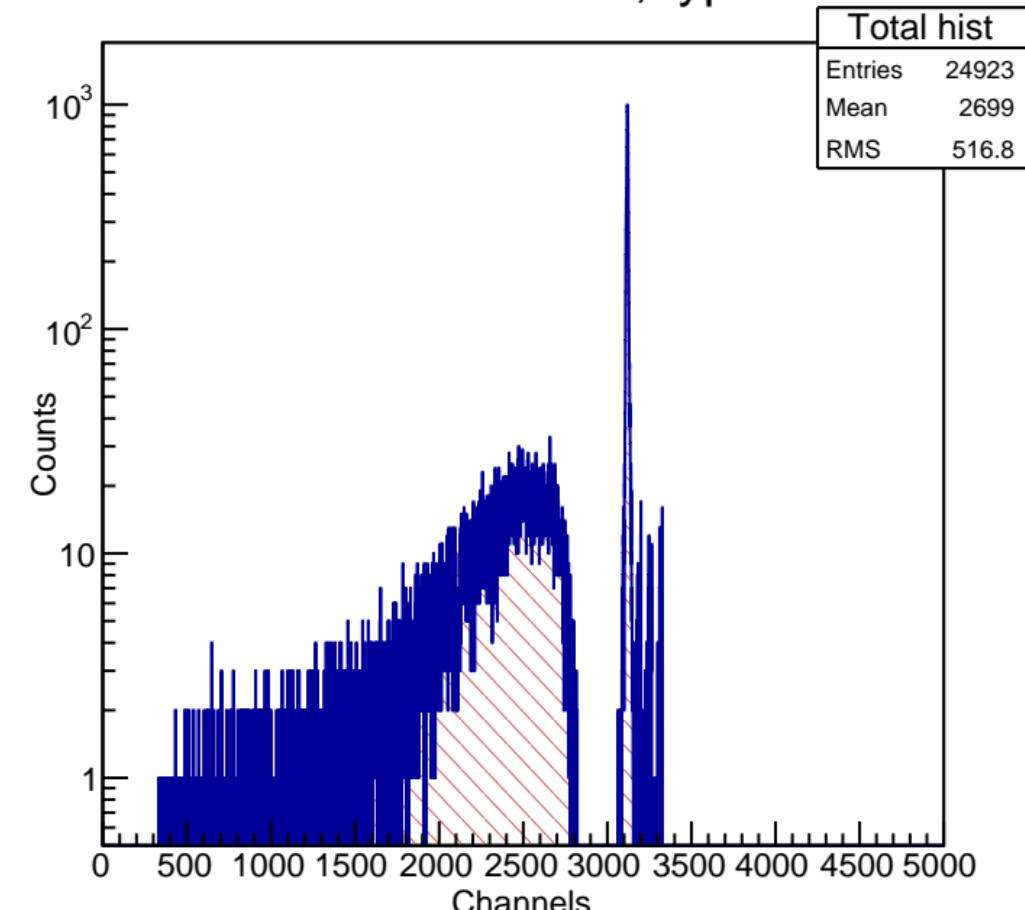
# TDCW Octet 97, type1



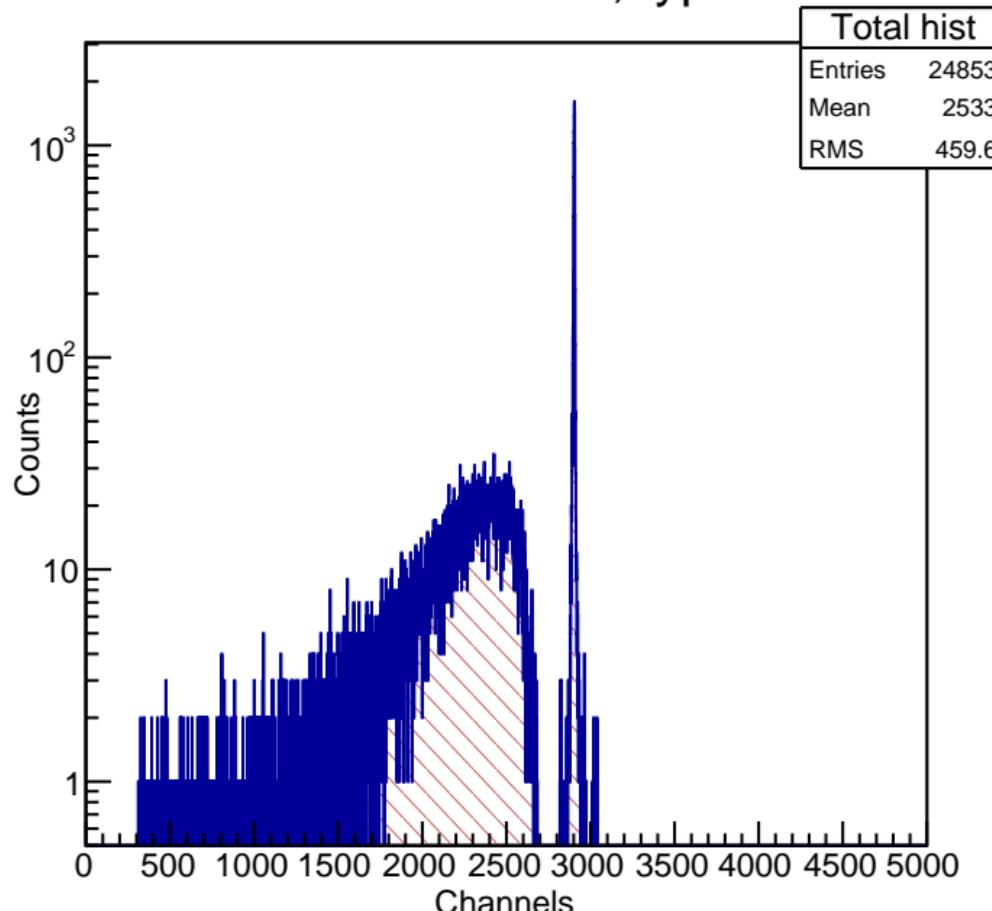
# TDCE Octet 98, type1



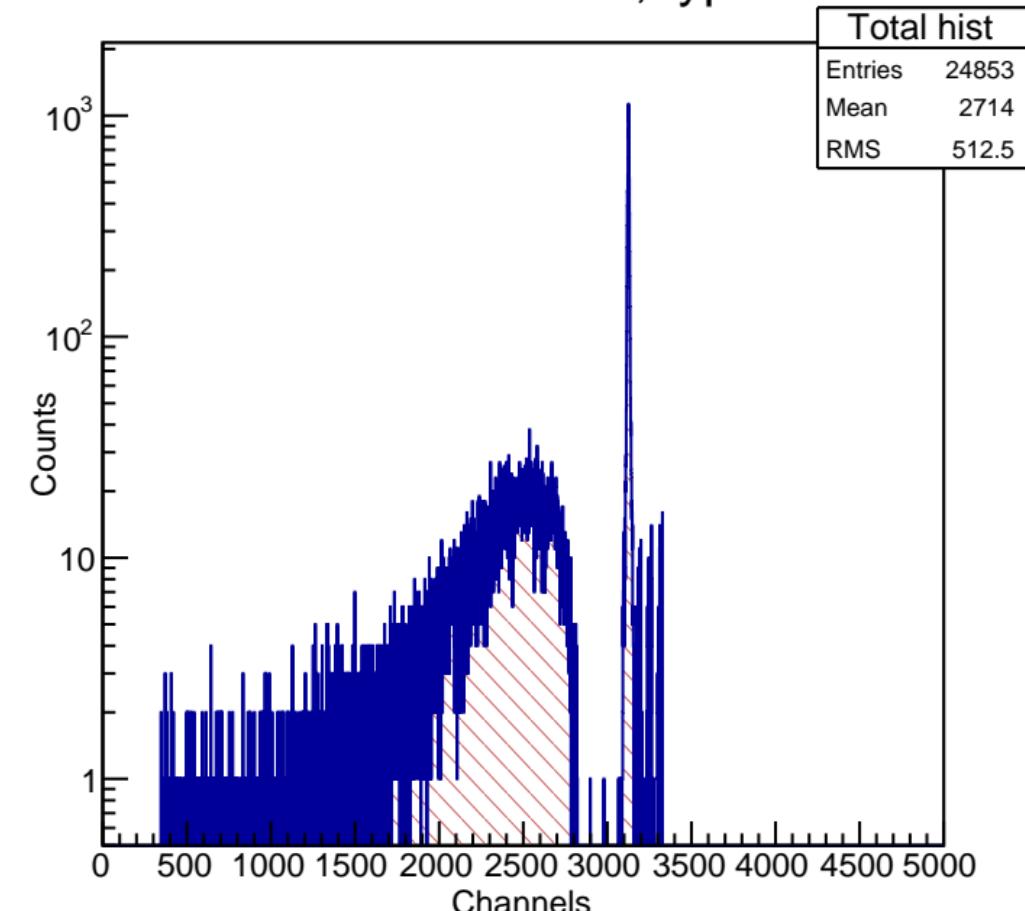
# TDCW Octet 98, type1



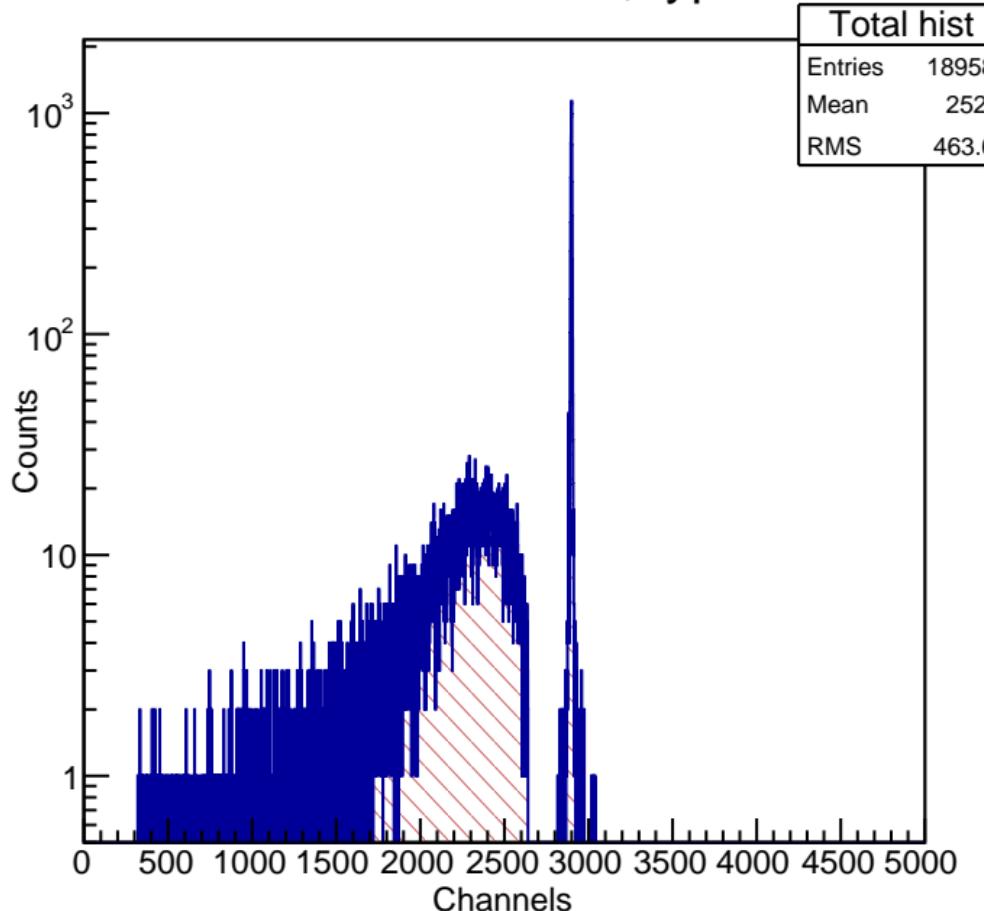
# TDCE Octet 99, type1



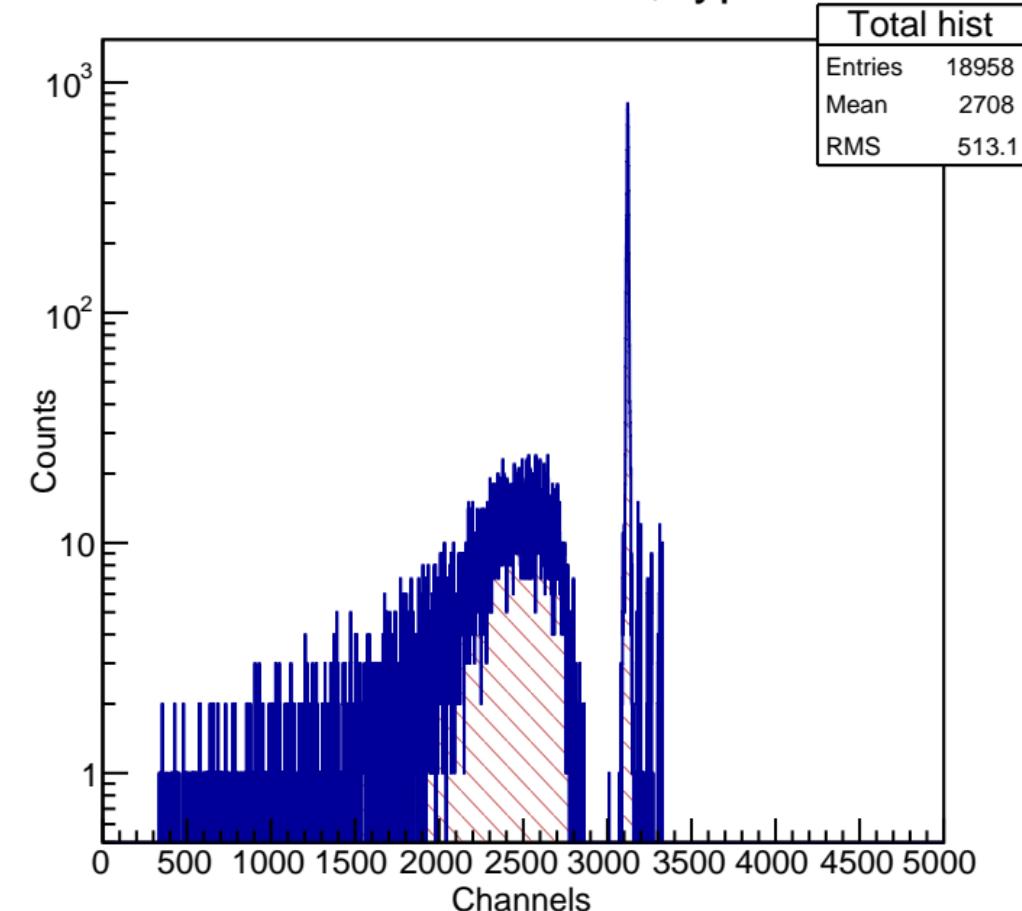
# TDCW Octet 99, type1



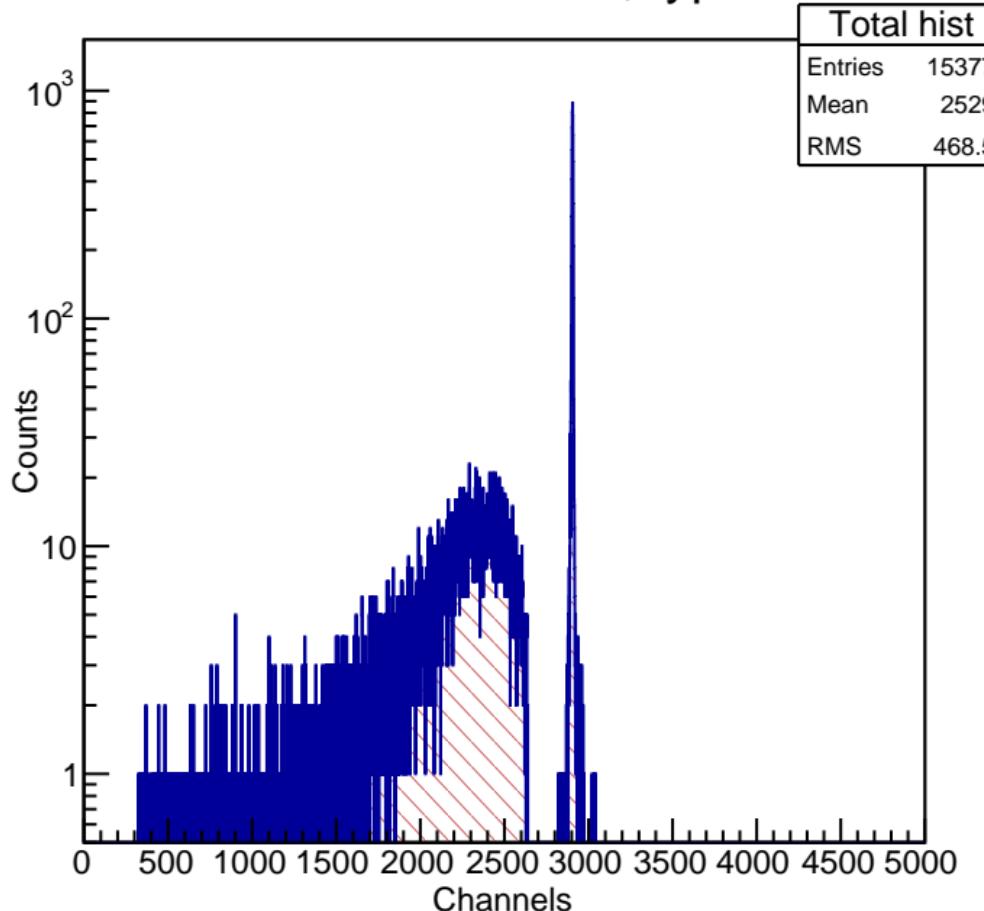
# TDCE Octet 100, type1



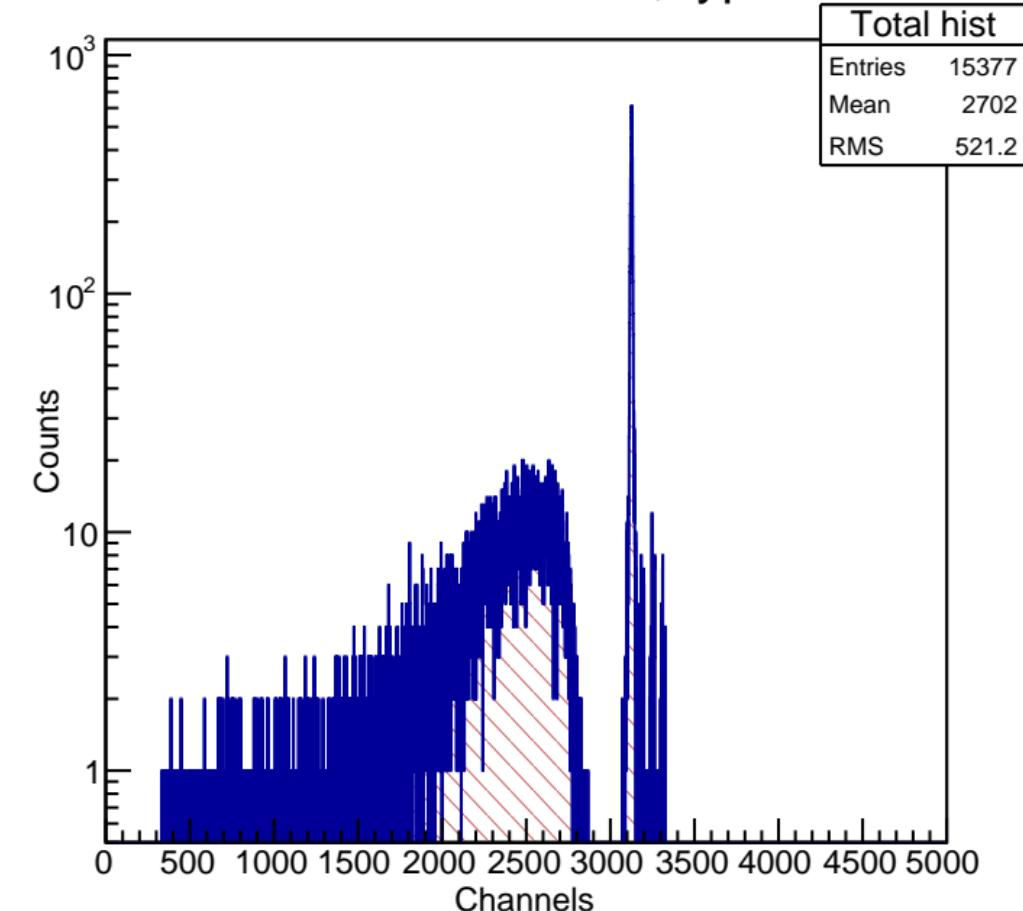
# TDCW Octet 100, type1



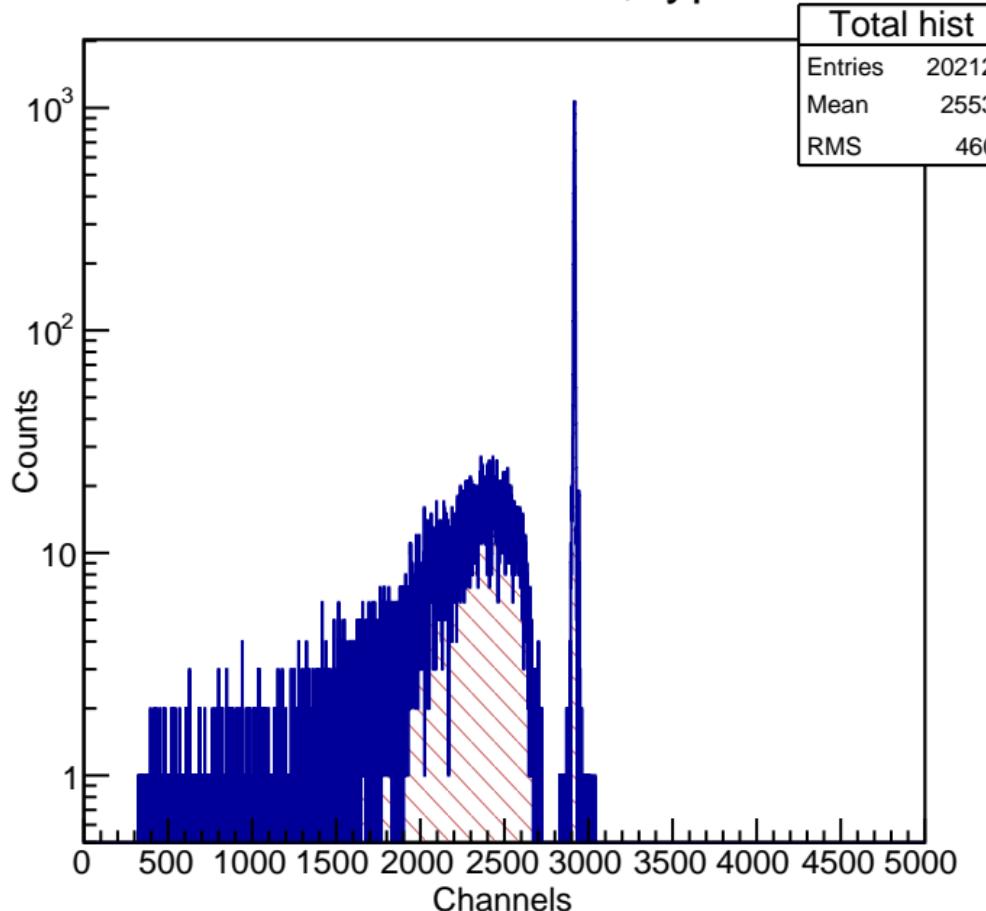
# TDCE Octet 102, type1



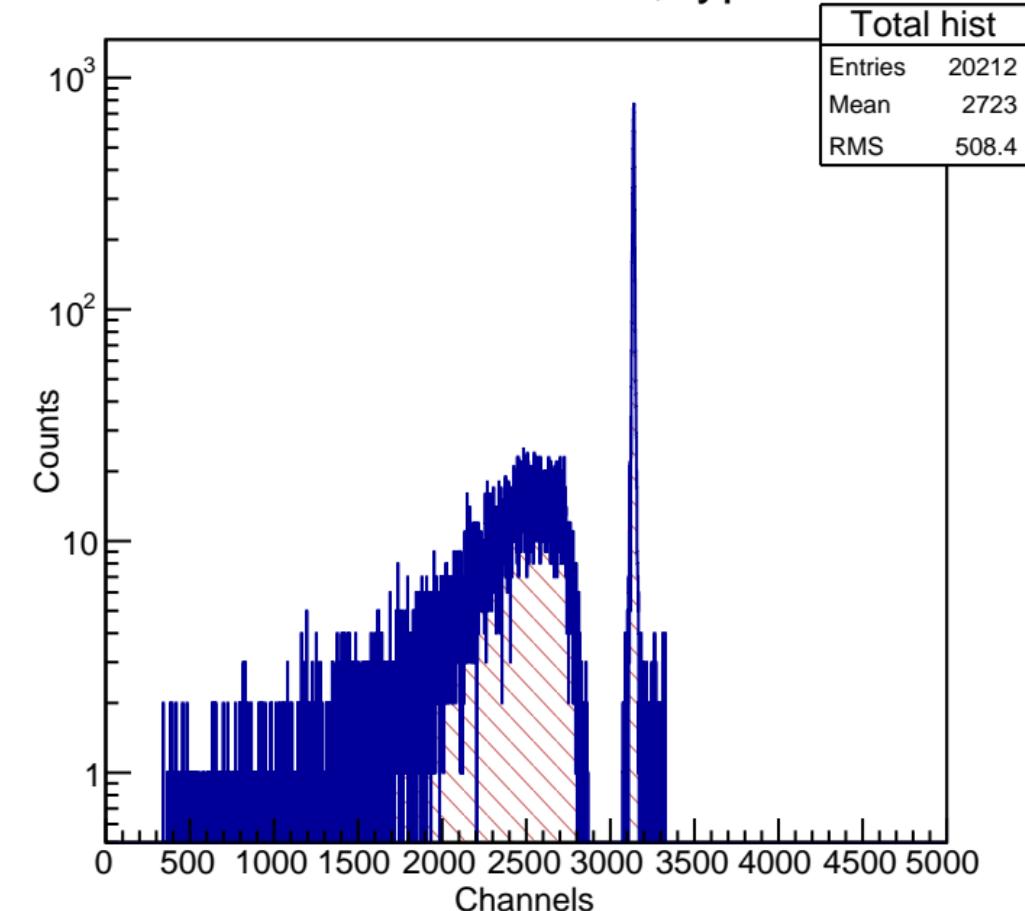
# TDCW Octet 102, type1



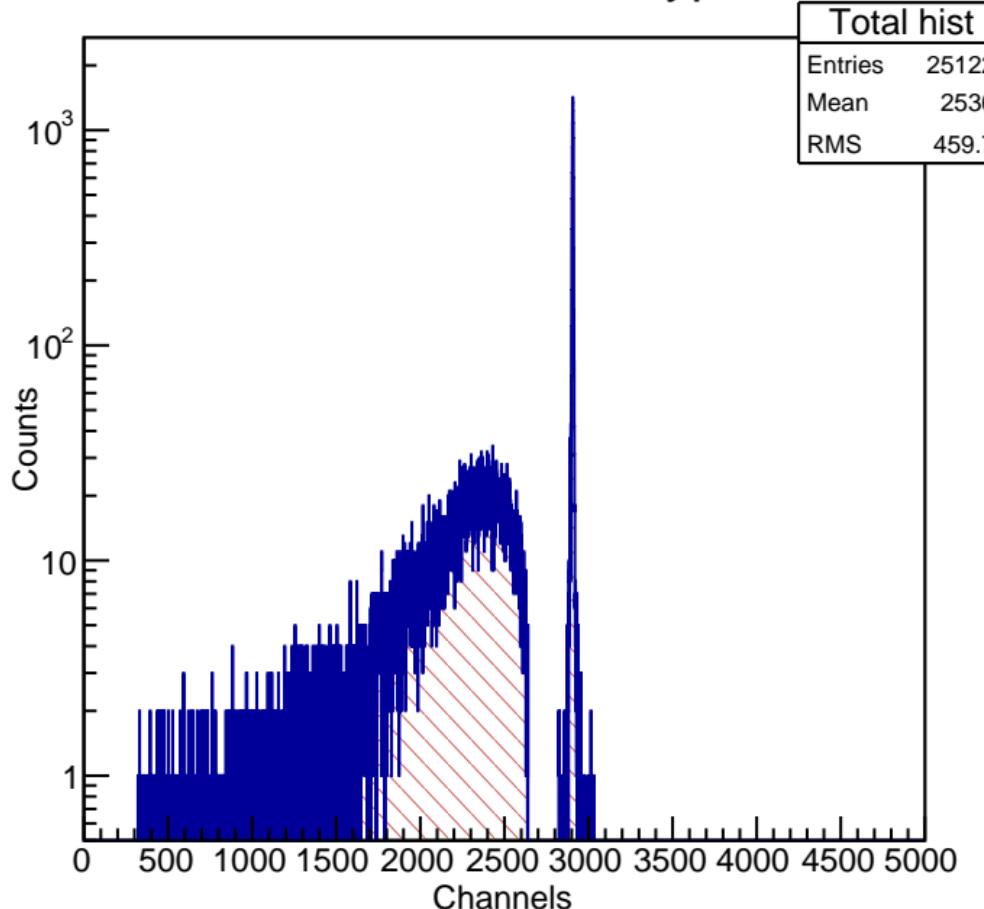
# TDCE Octet 103, type1



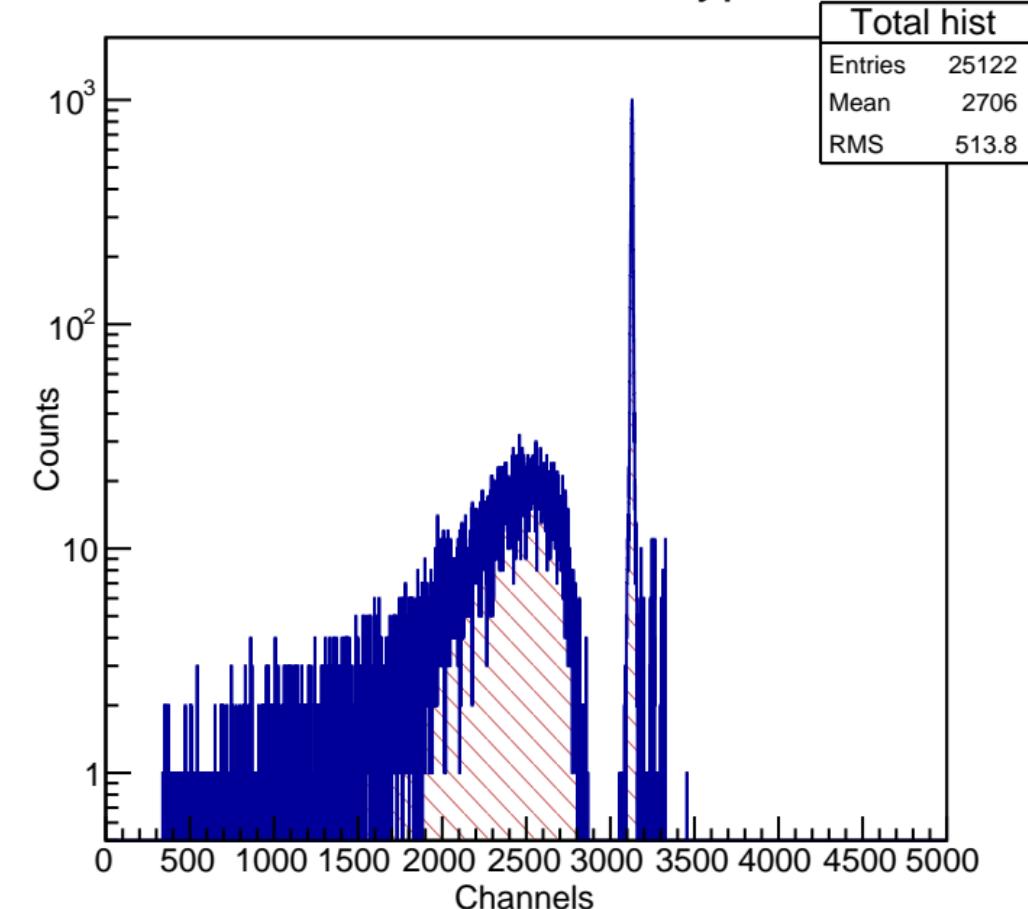
# TDCW Octet 103, type1



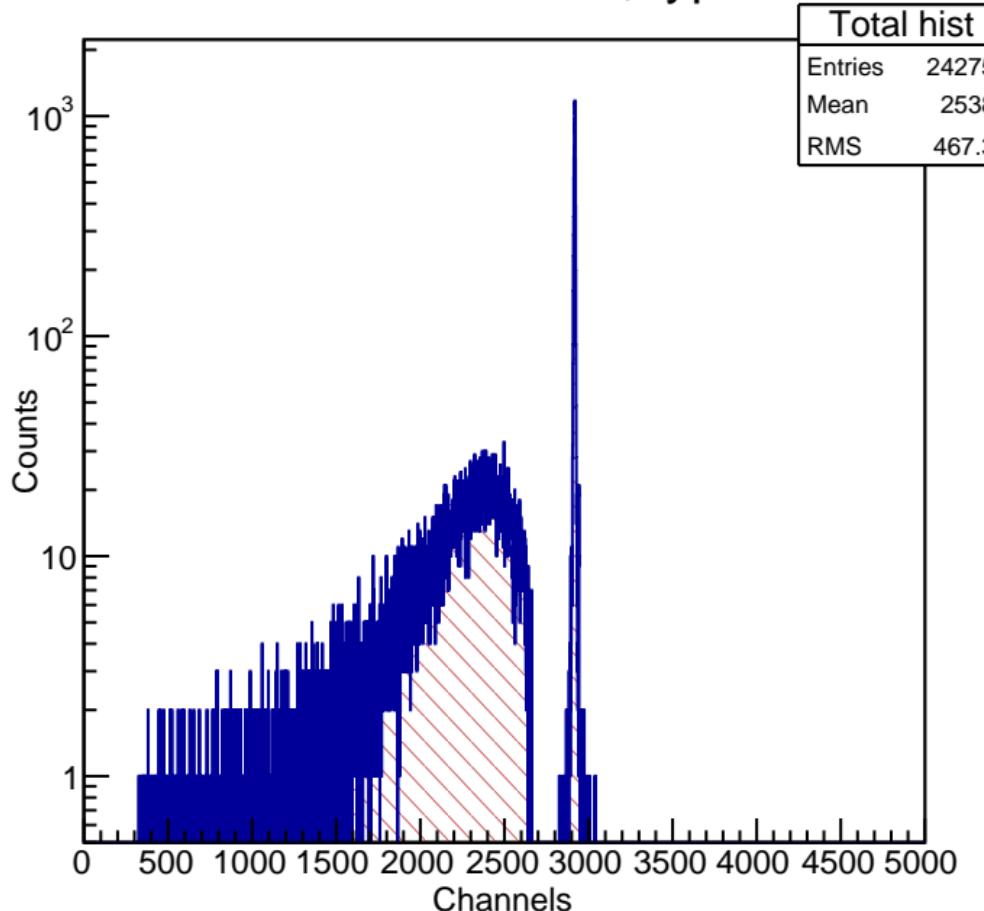
# TDCE Octet 104, type1



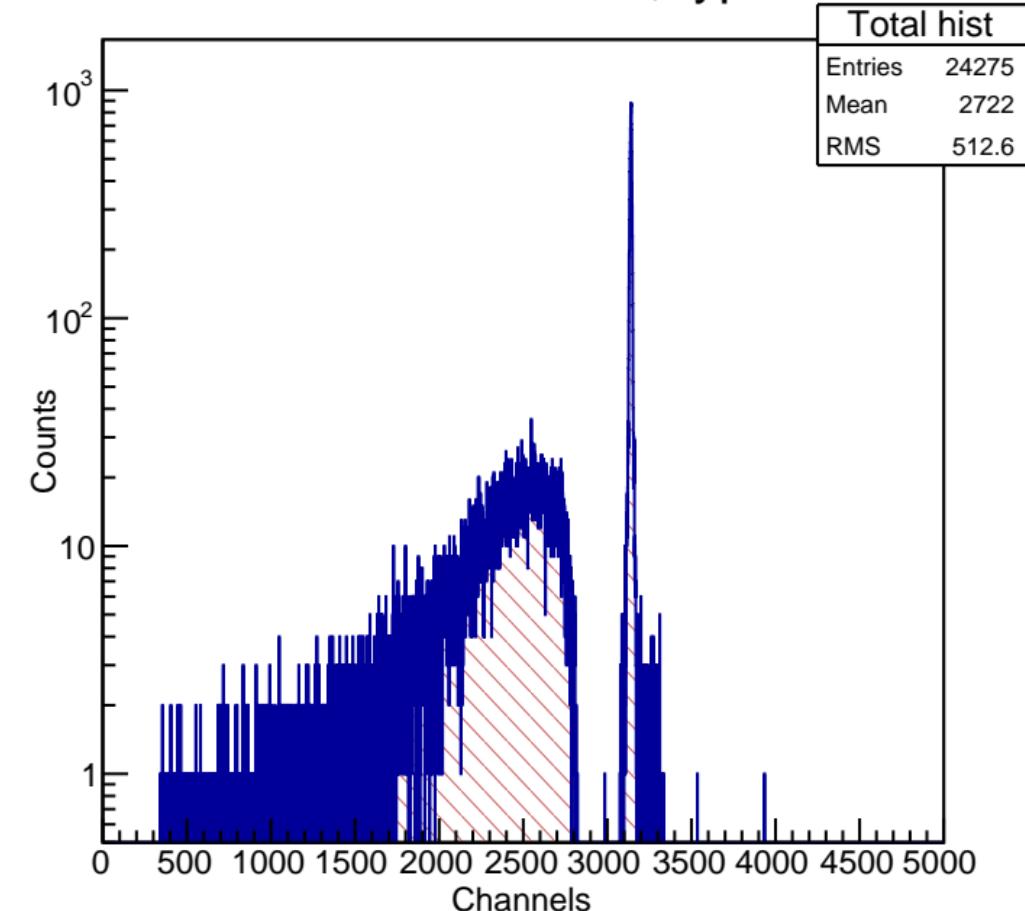
# TDCW Octet 104, type1



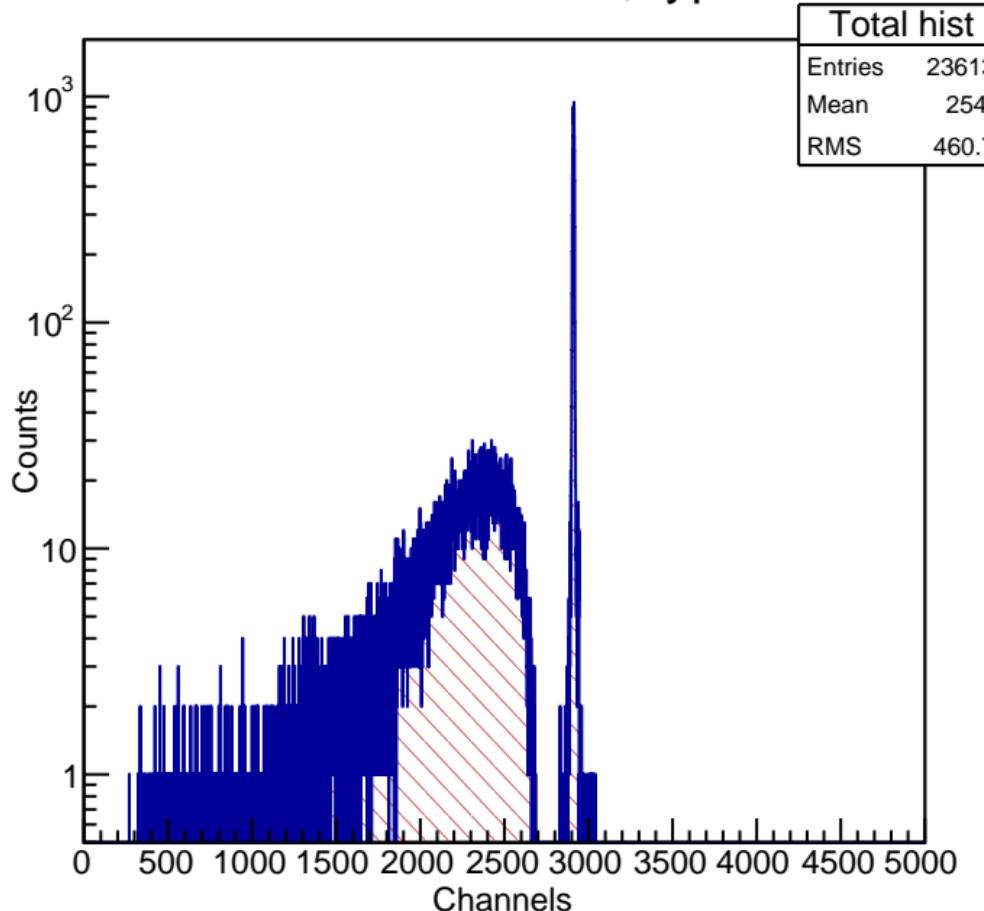
# TDCE Octet 105, type1



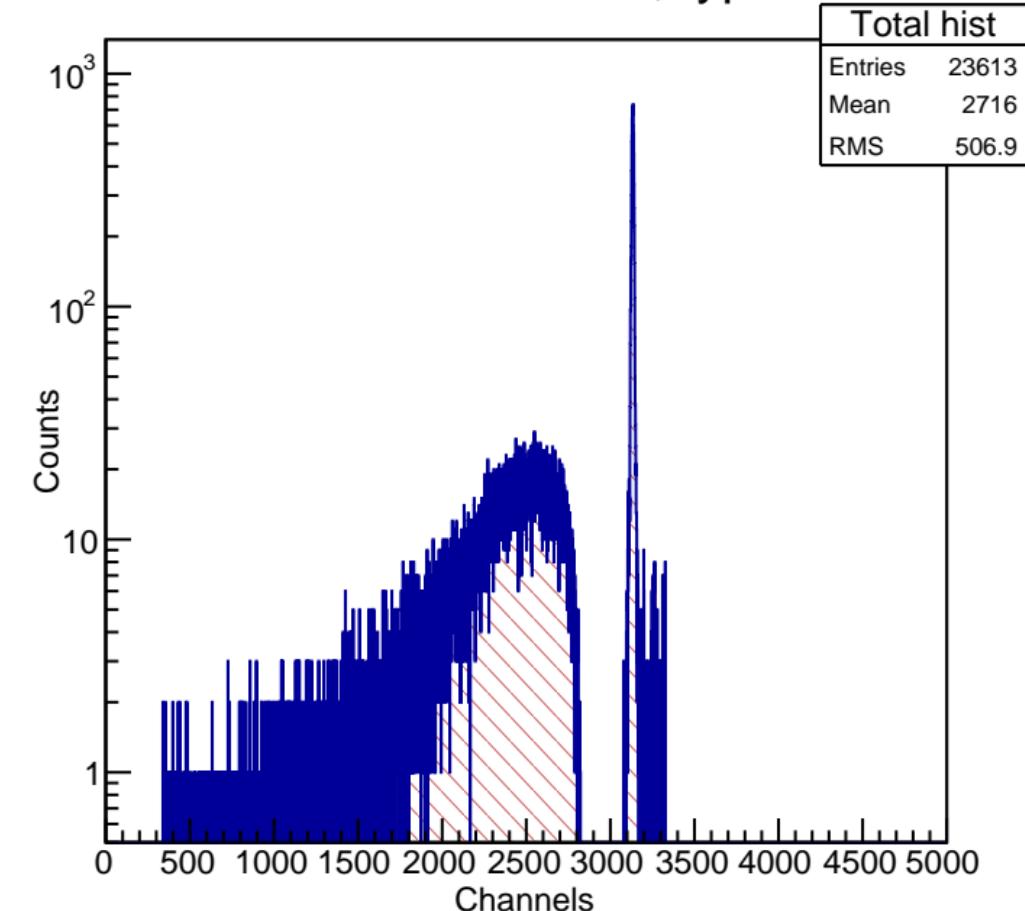
# TDCW Octet 105, type1



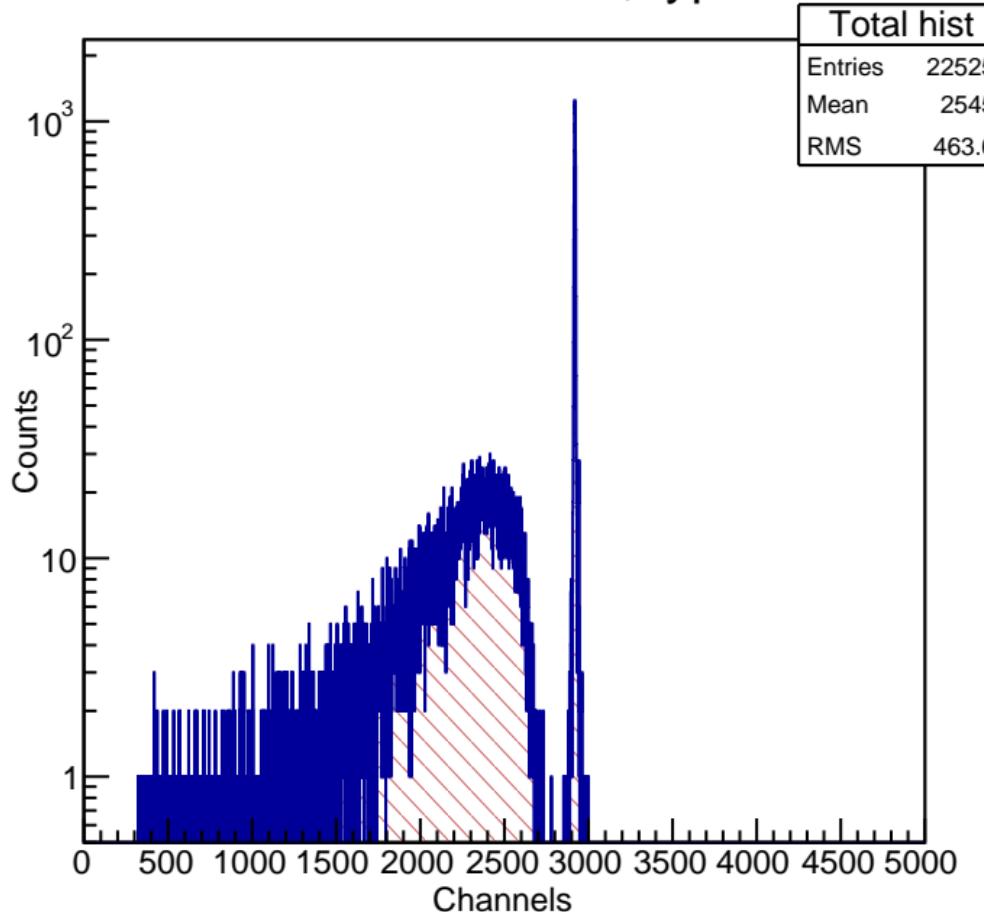
# TDCE Octet 106, type1



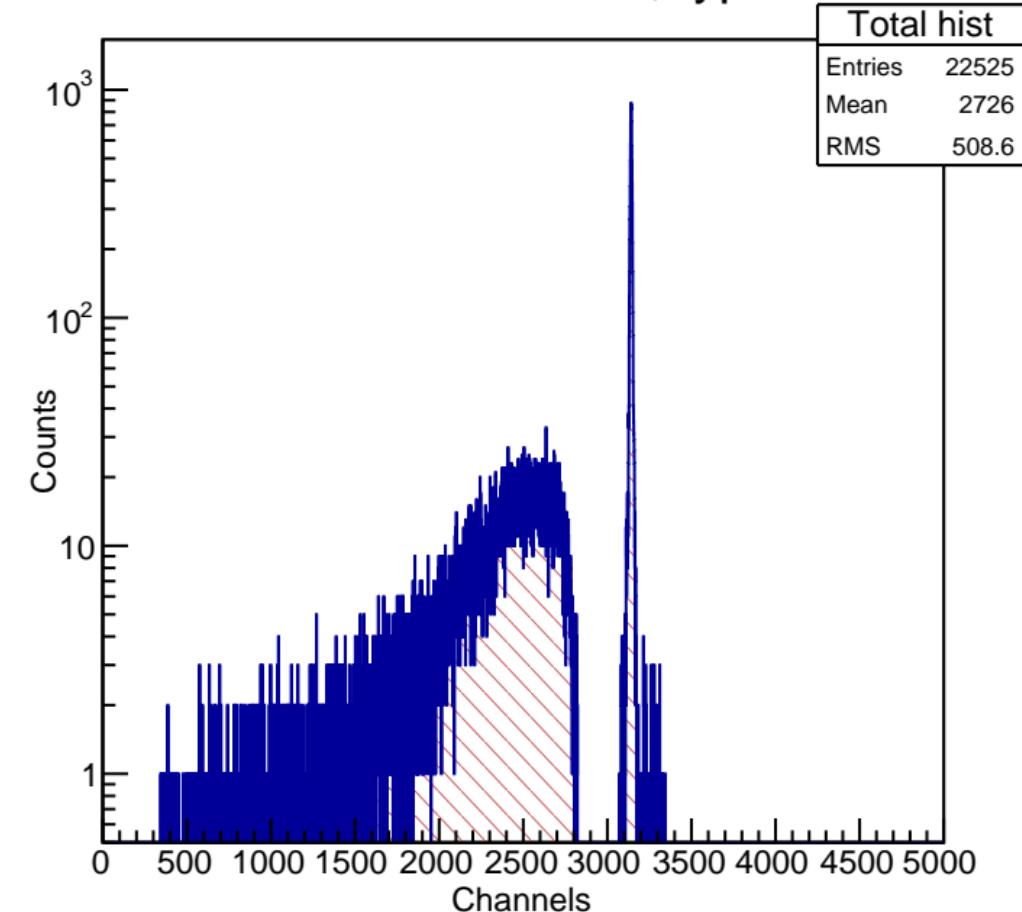
# TDCW Octet 106, type1



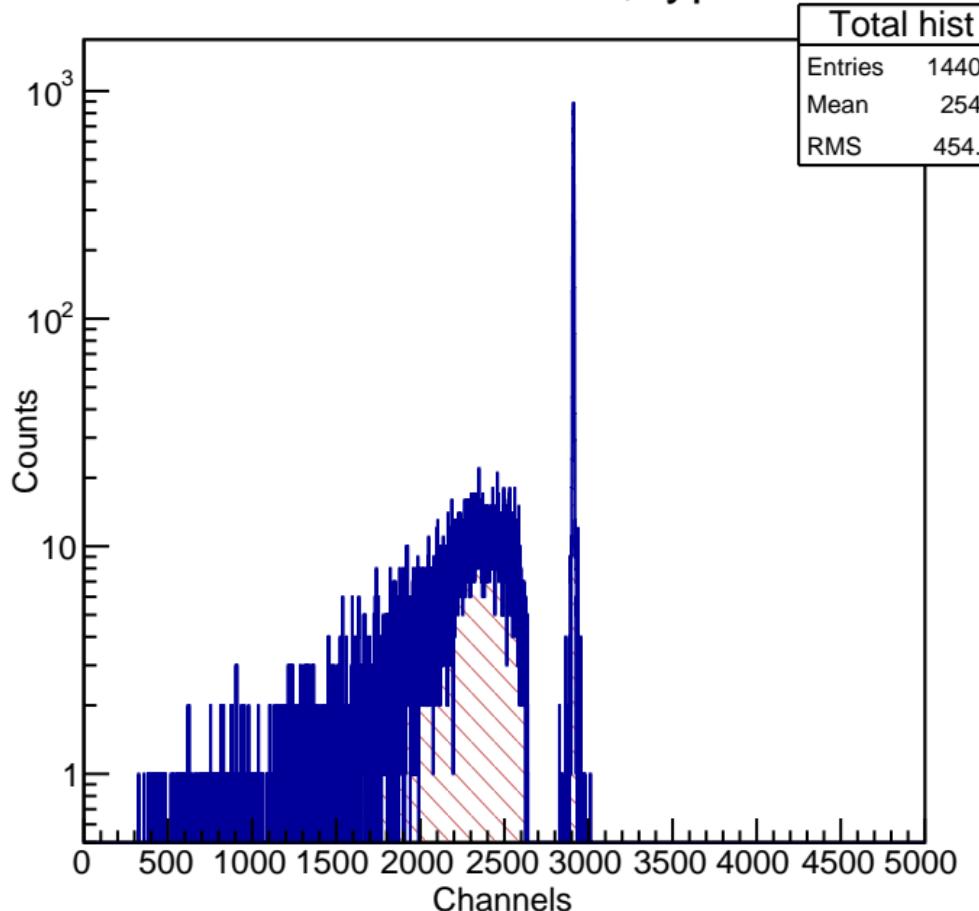
# TDCE Octet 108, type1



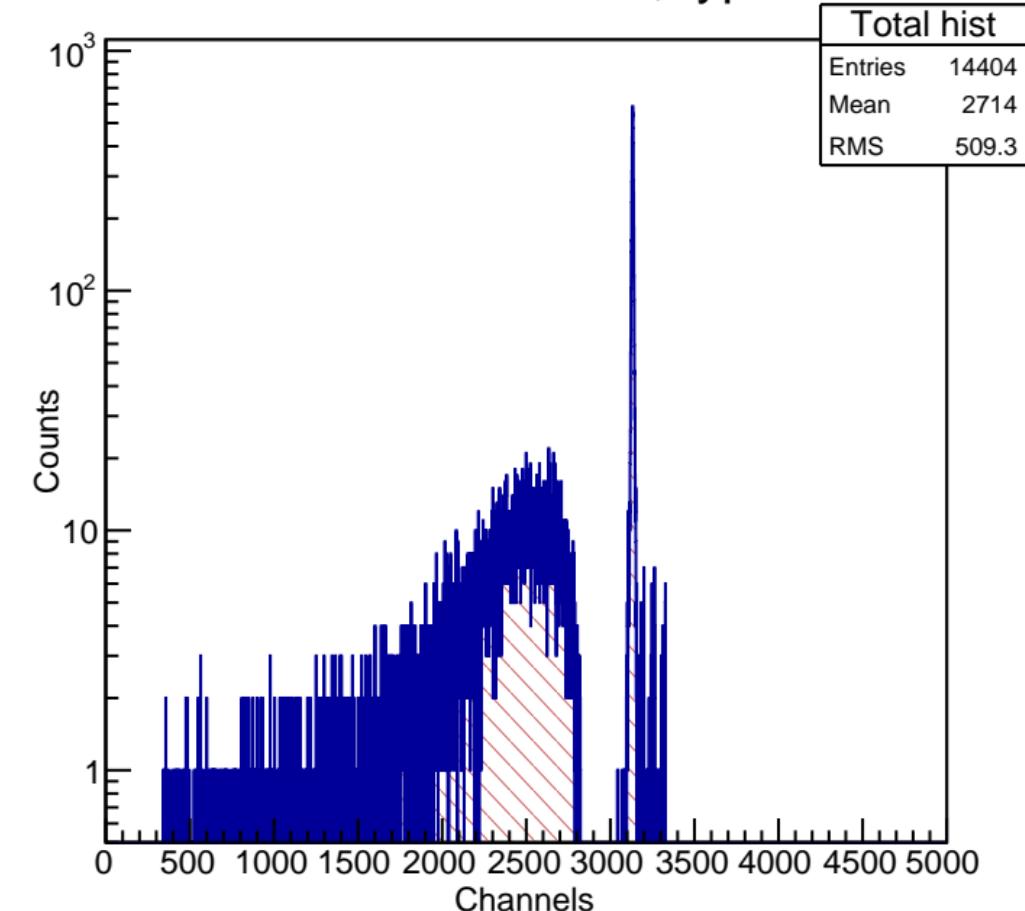
# TDCW Octet 108, type1



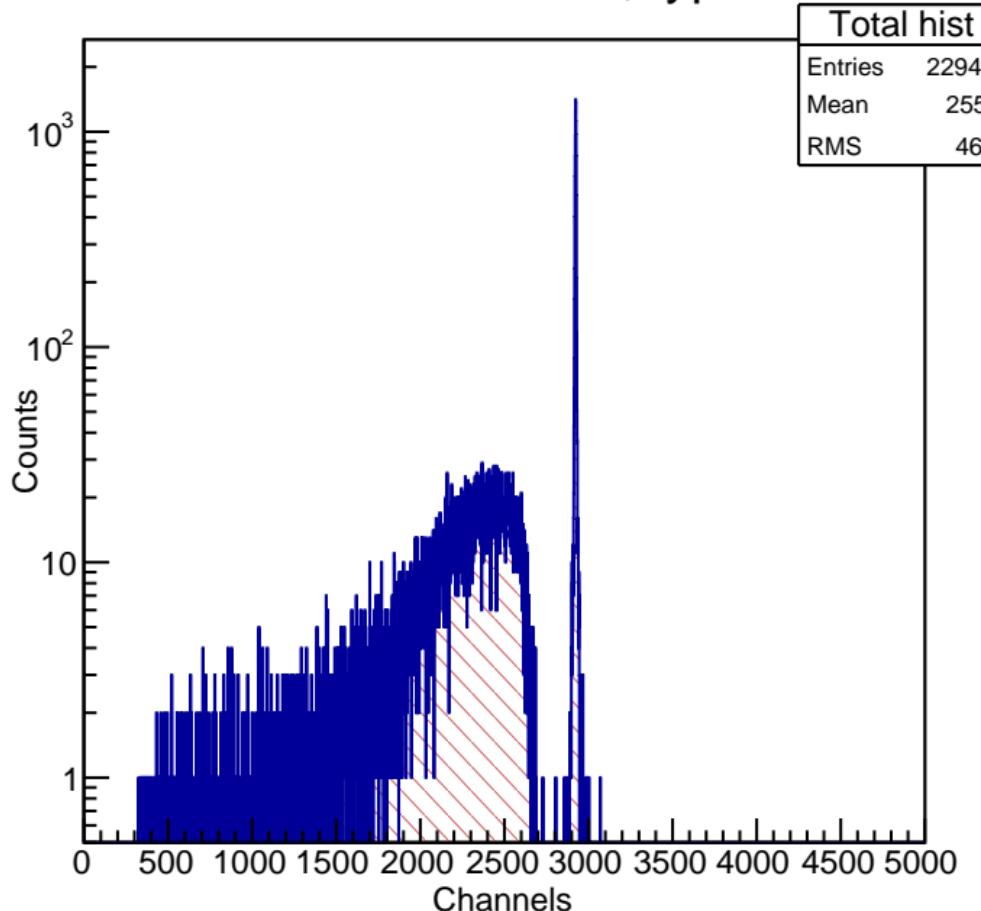
# TDCE Octet 109, type1



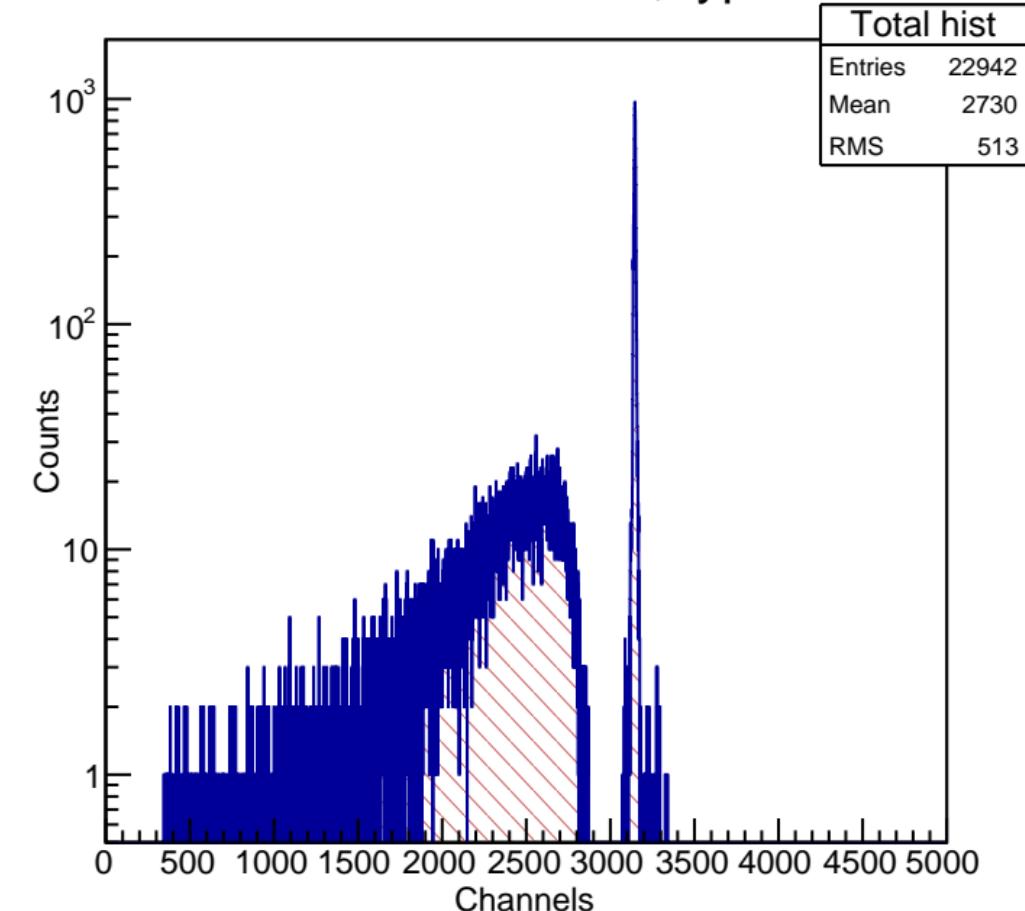
# TDCW Octet 109, type1



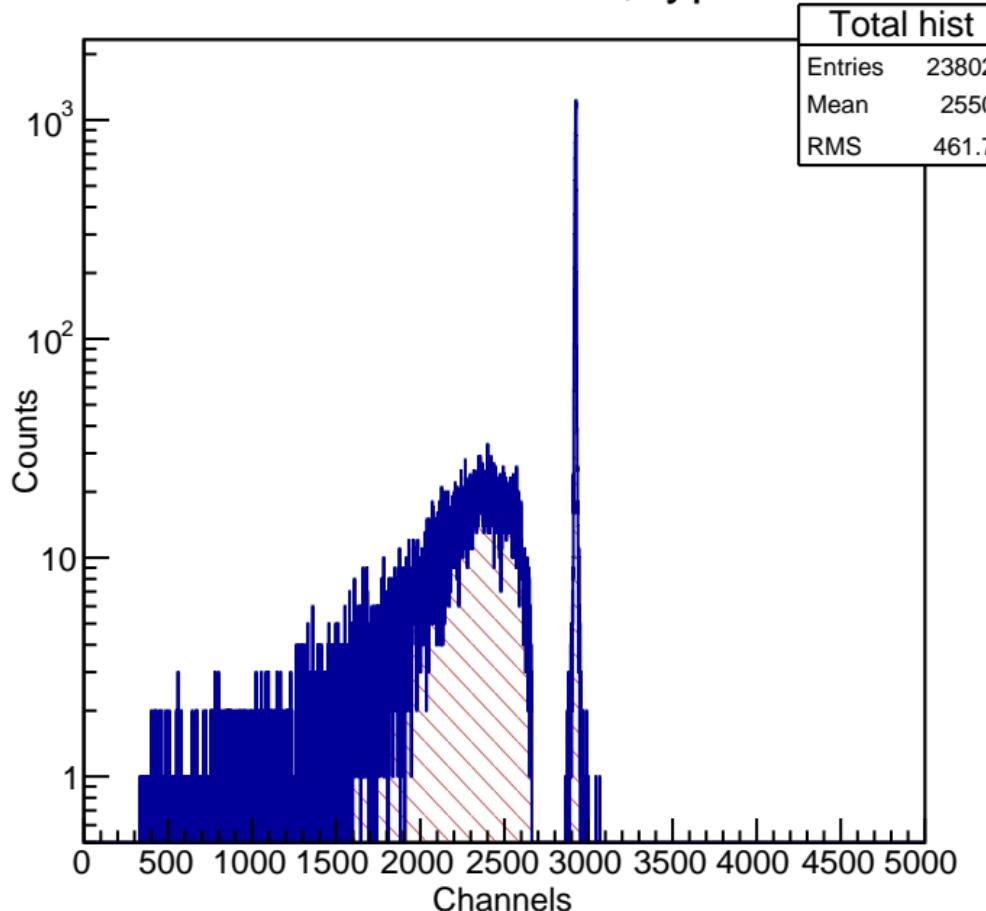
# TDCE Octet 110, type1



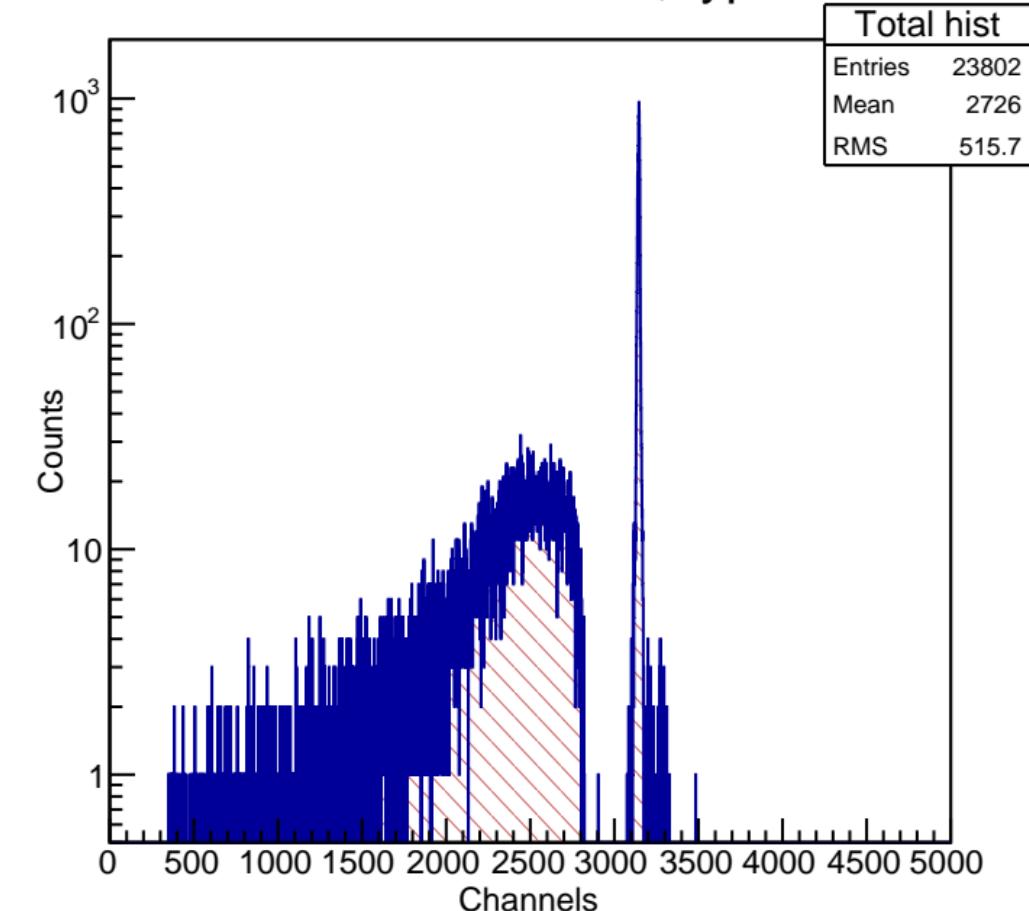
# TDCW Octet 110, type1



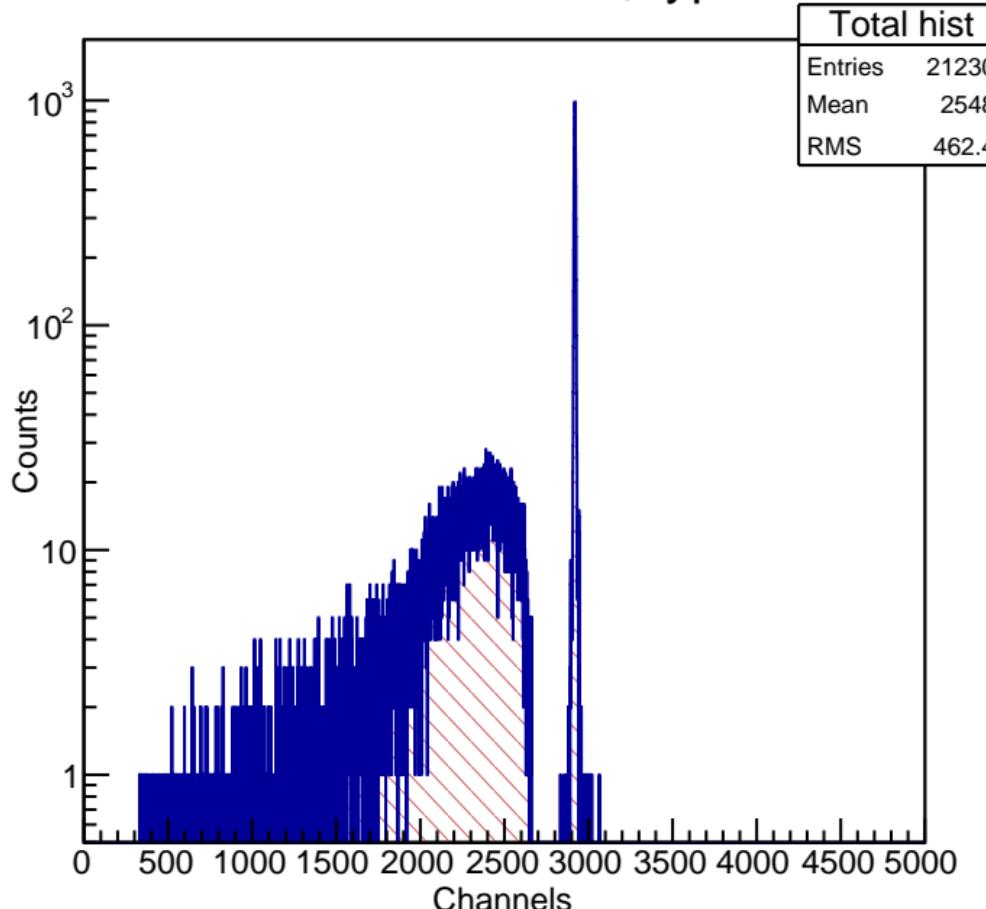
# TDCE Octet 111, type1



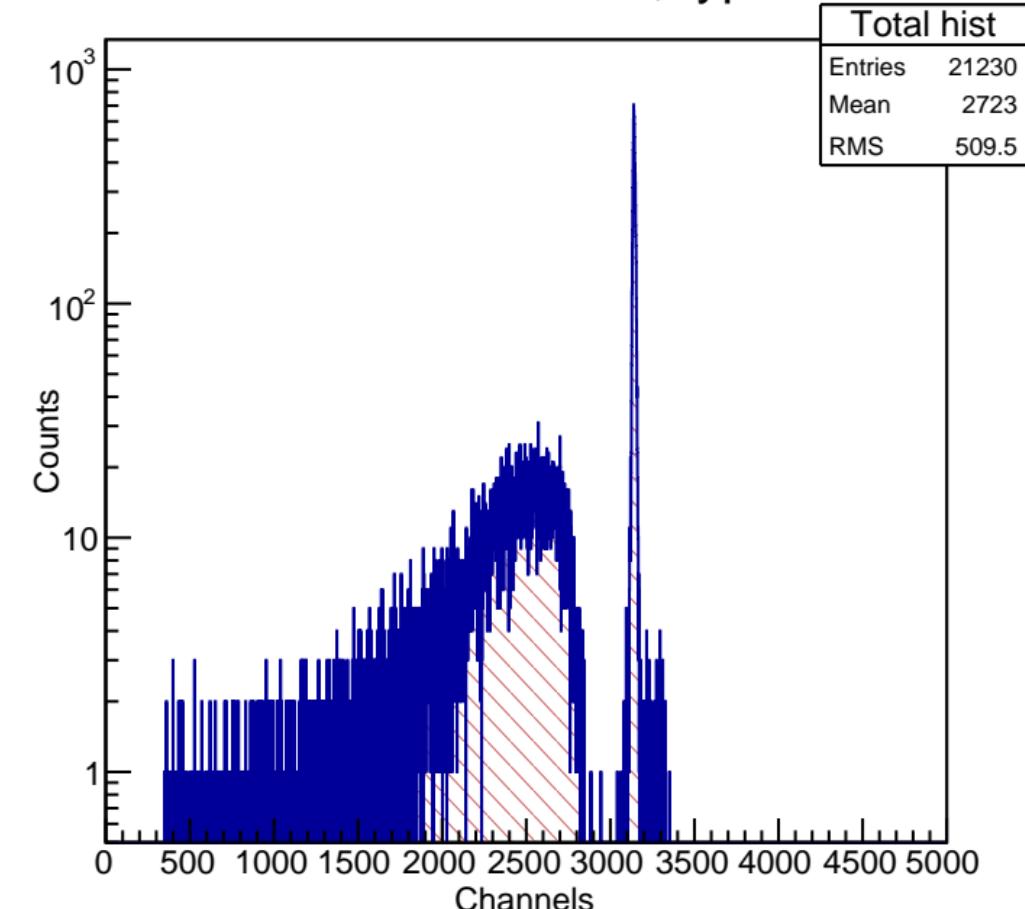
# TDCW Octet 111, type1



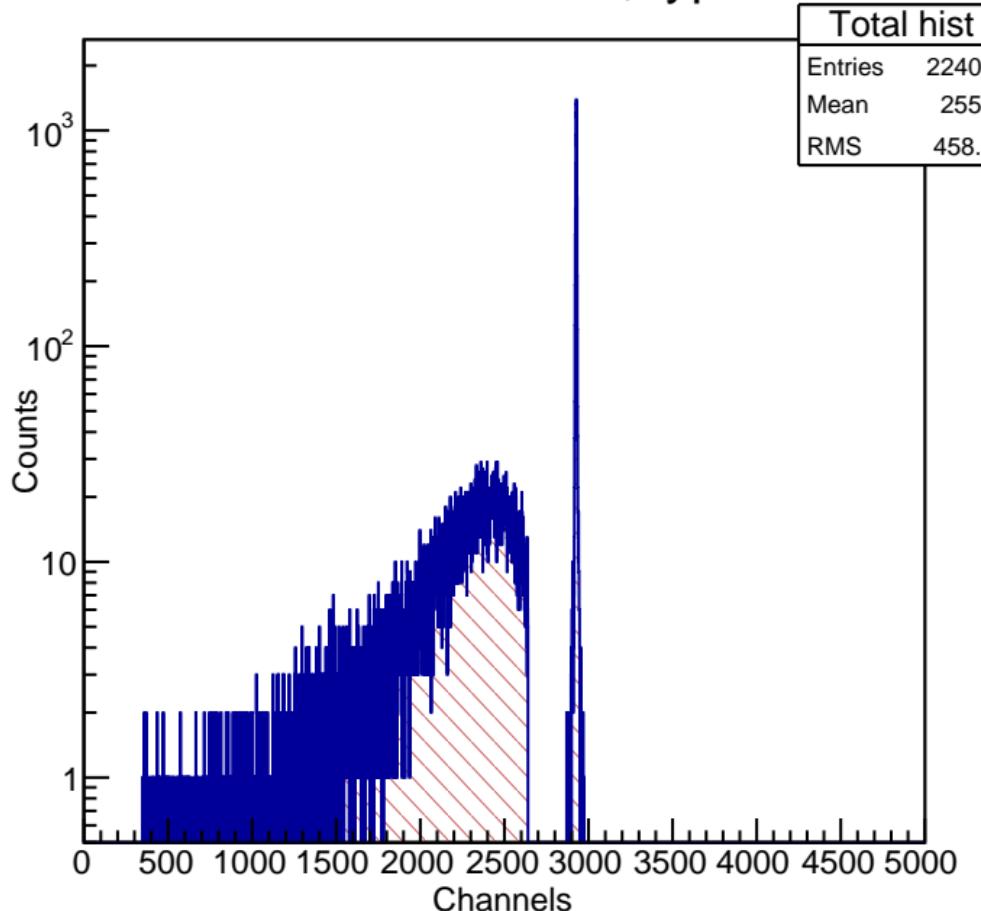
# TDCE Octet 112, type1



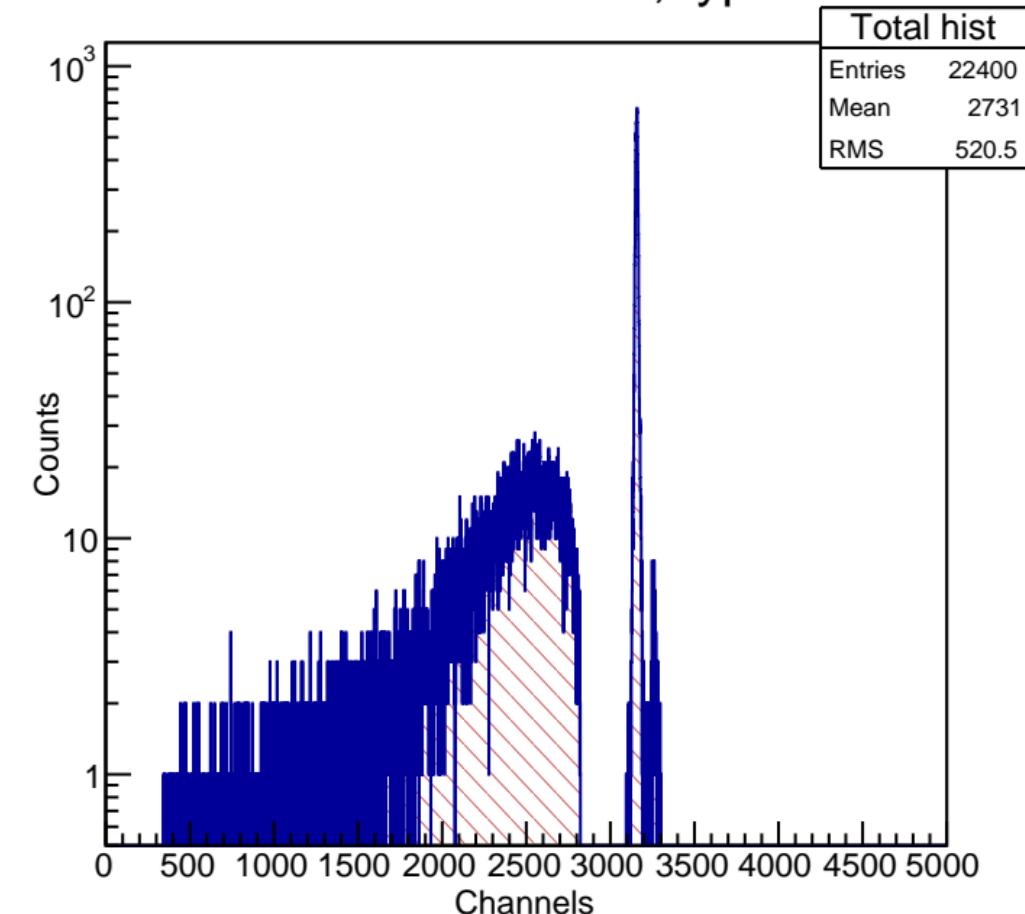
# TDCW Octet 112, type1



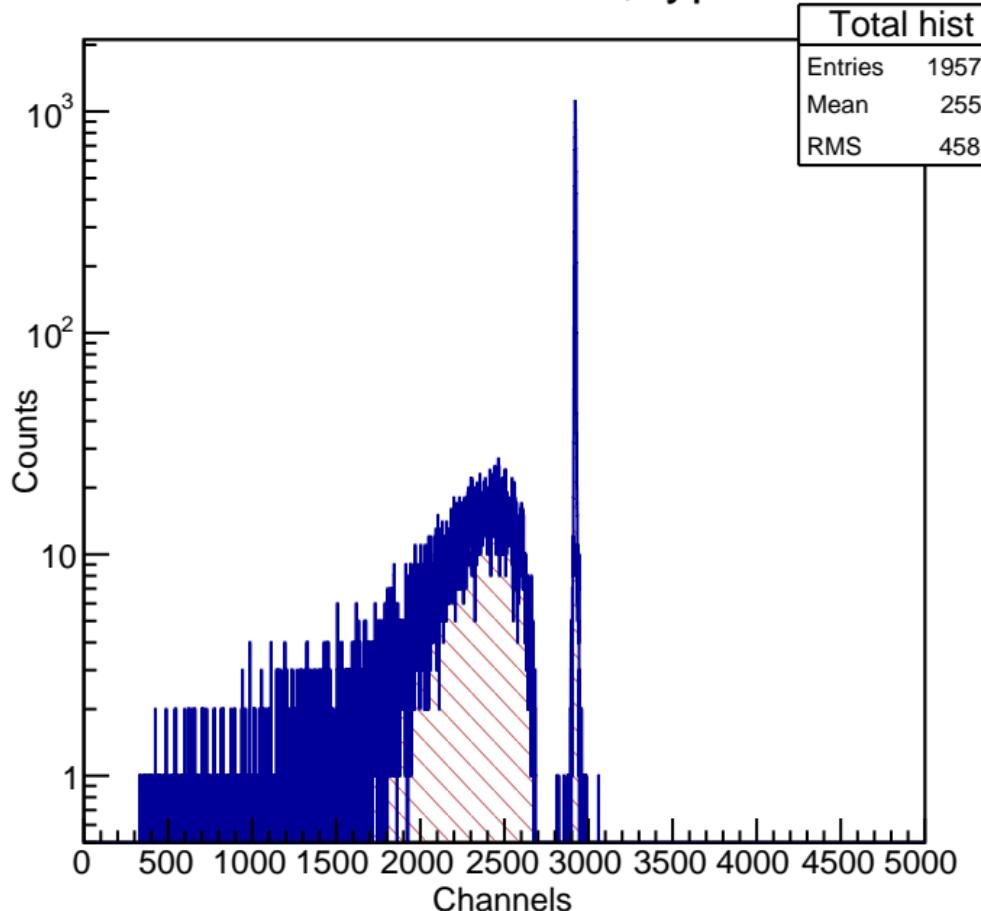
# TDCE Octet 113, type1



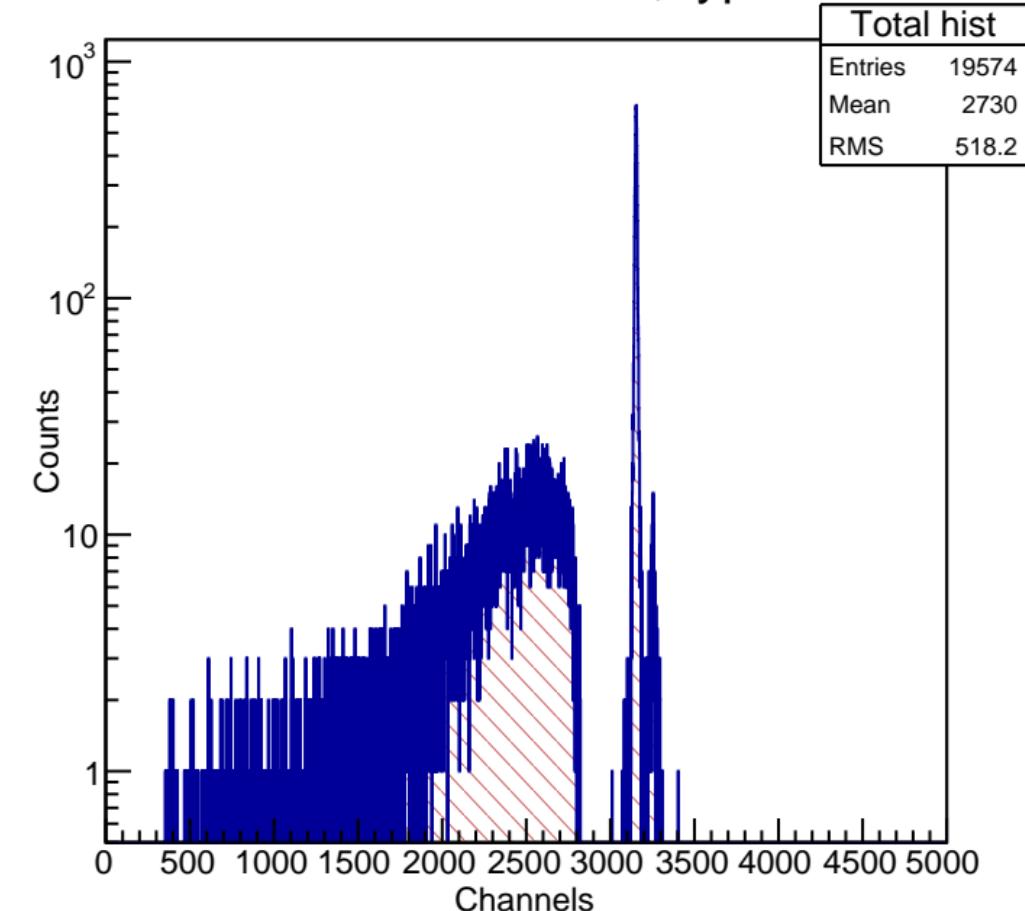
# TDCW Octet 113, type1



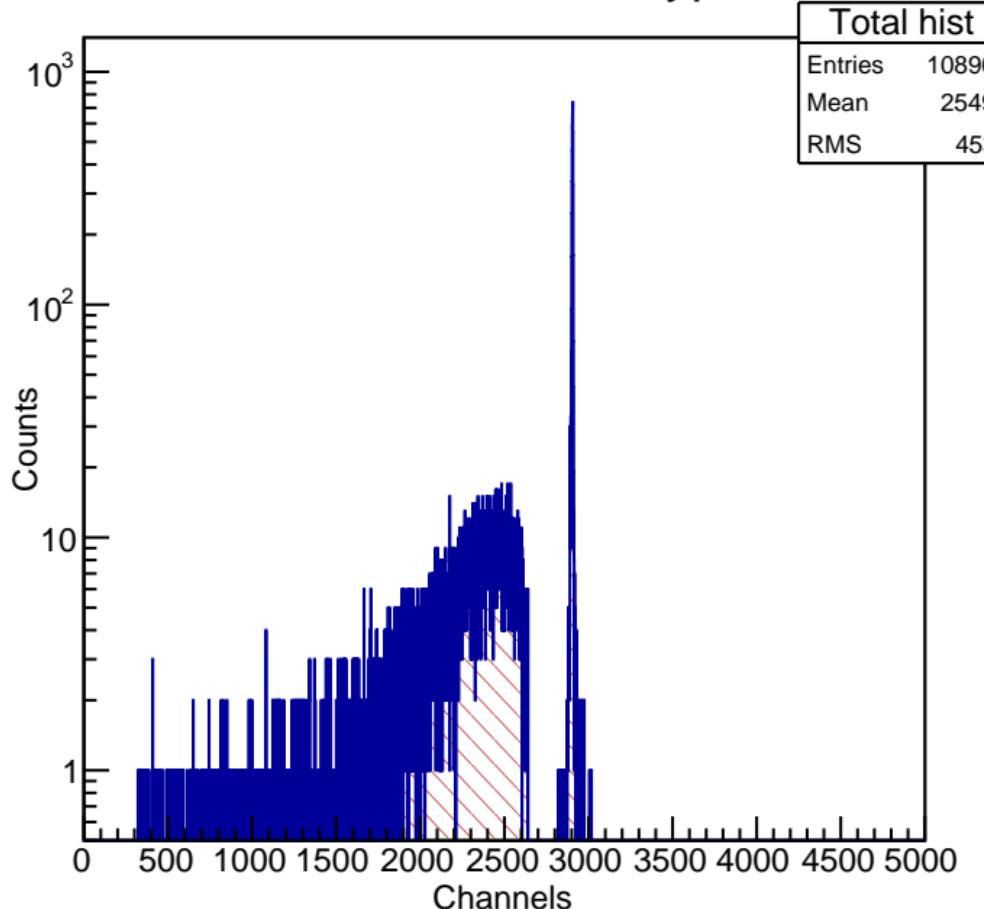
# TDCE Octet 114, type1



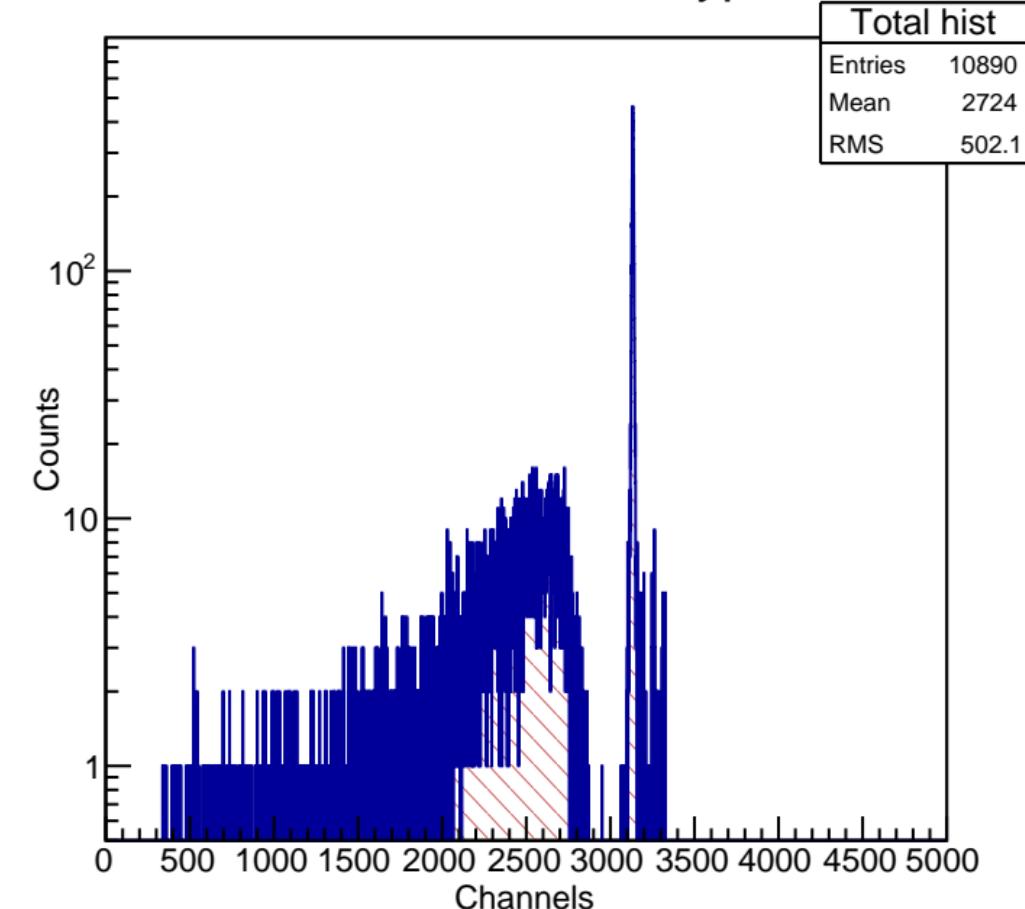
# TDCW Octet 114, type1



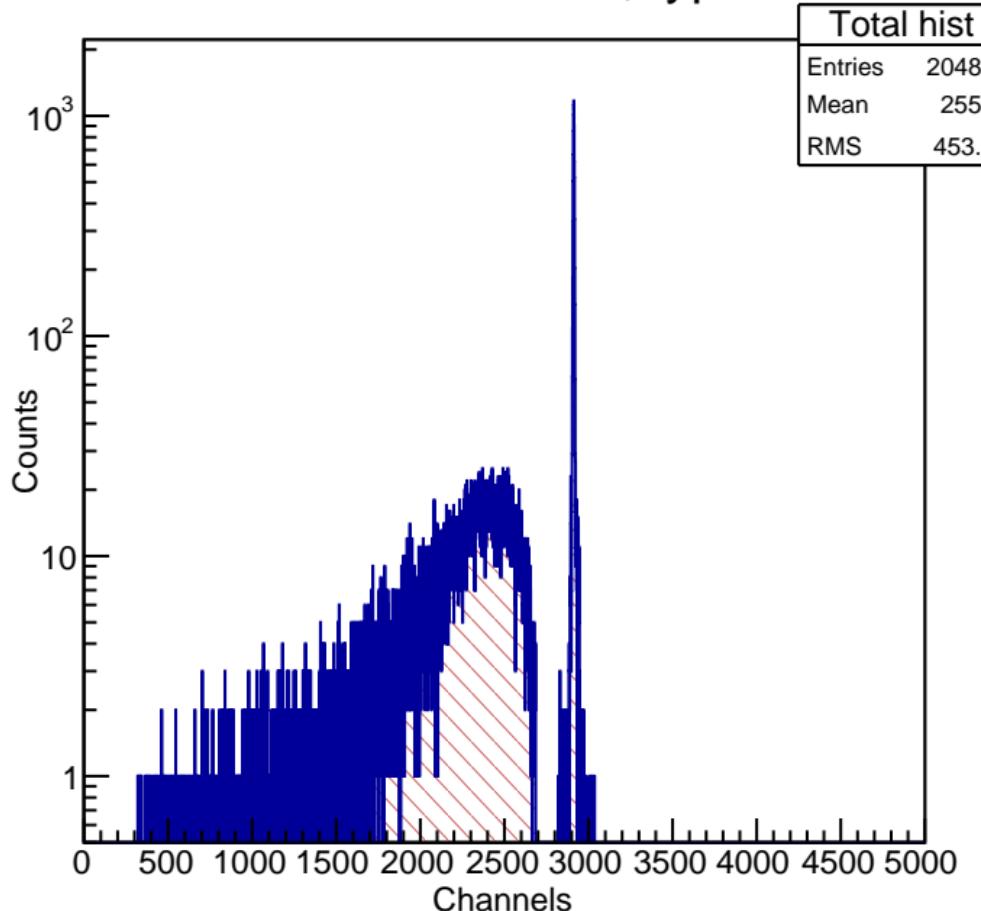
# TDCE Octet 115, type1



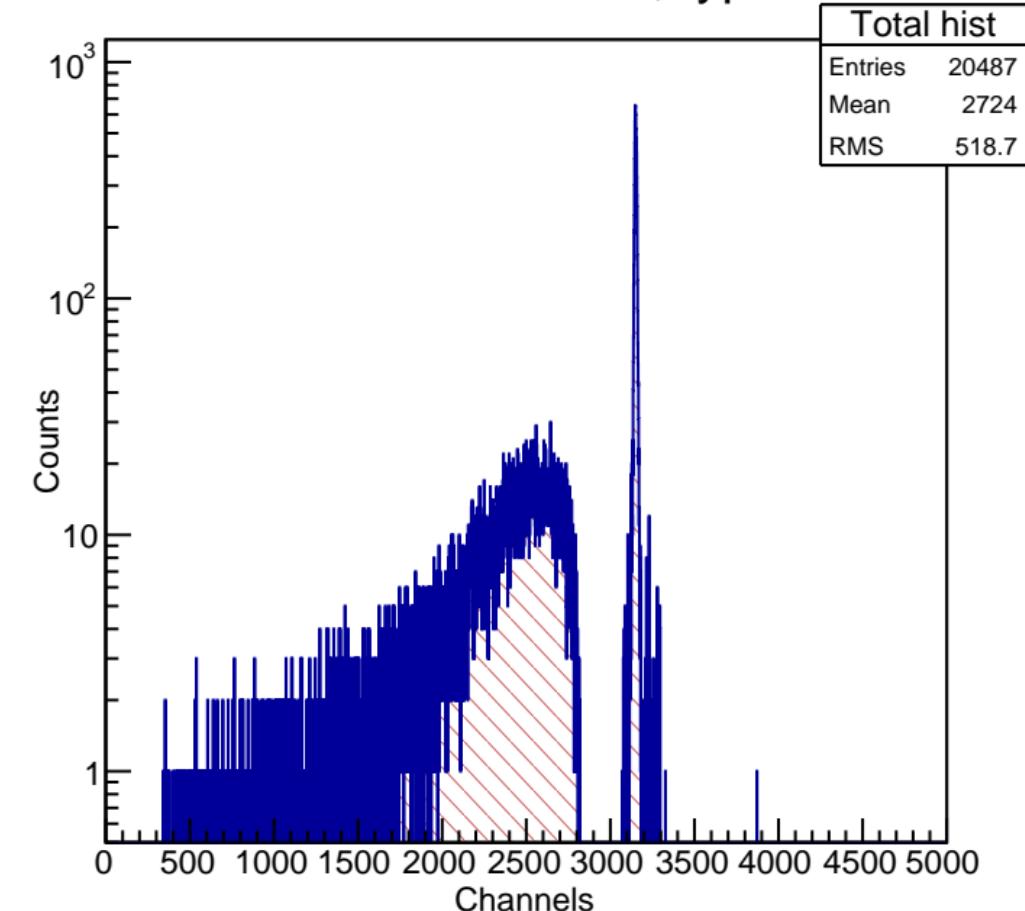
# TDCW Octet 115, type1



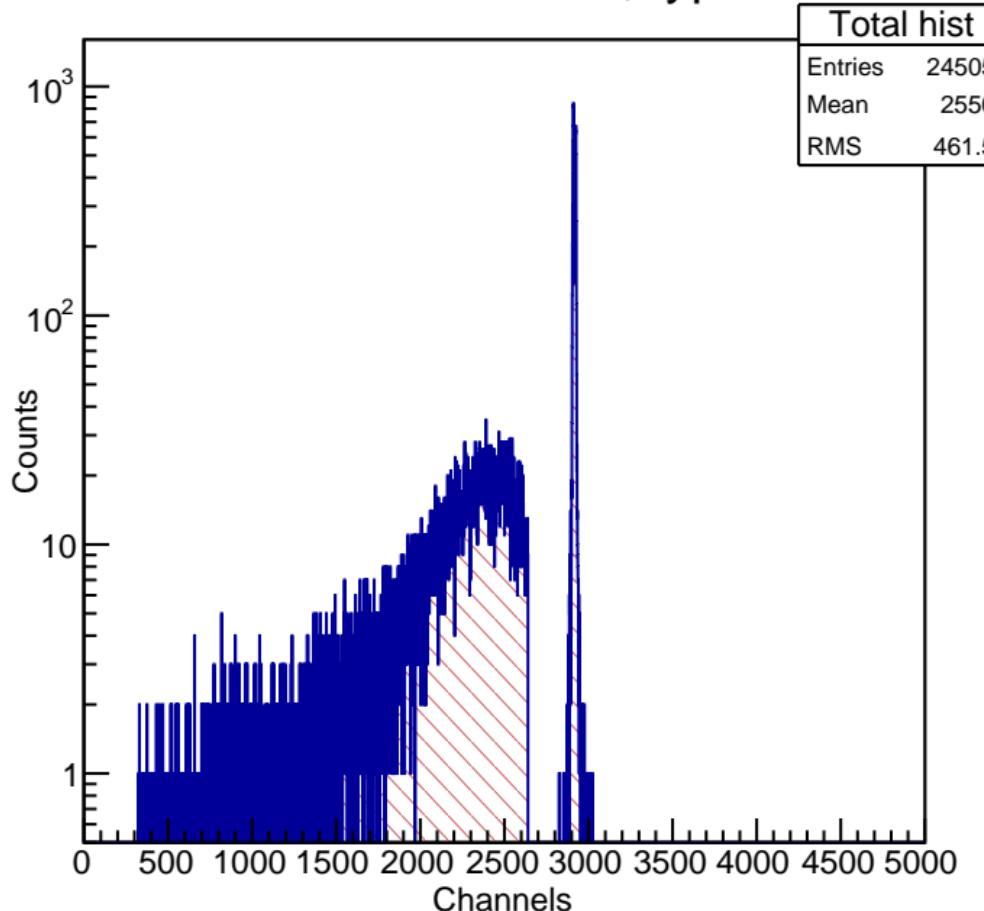
# TDCE Octet 116, type1



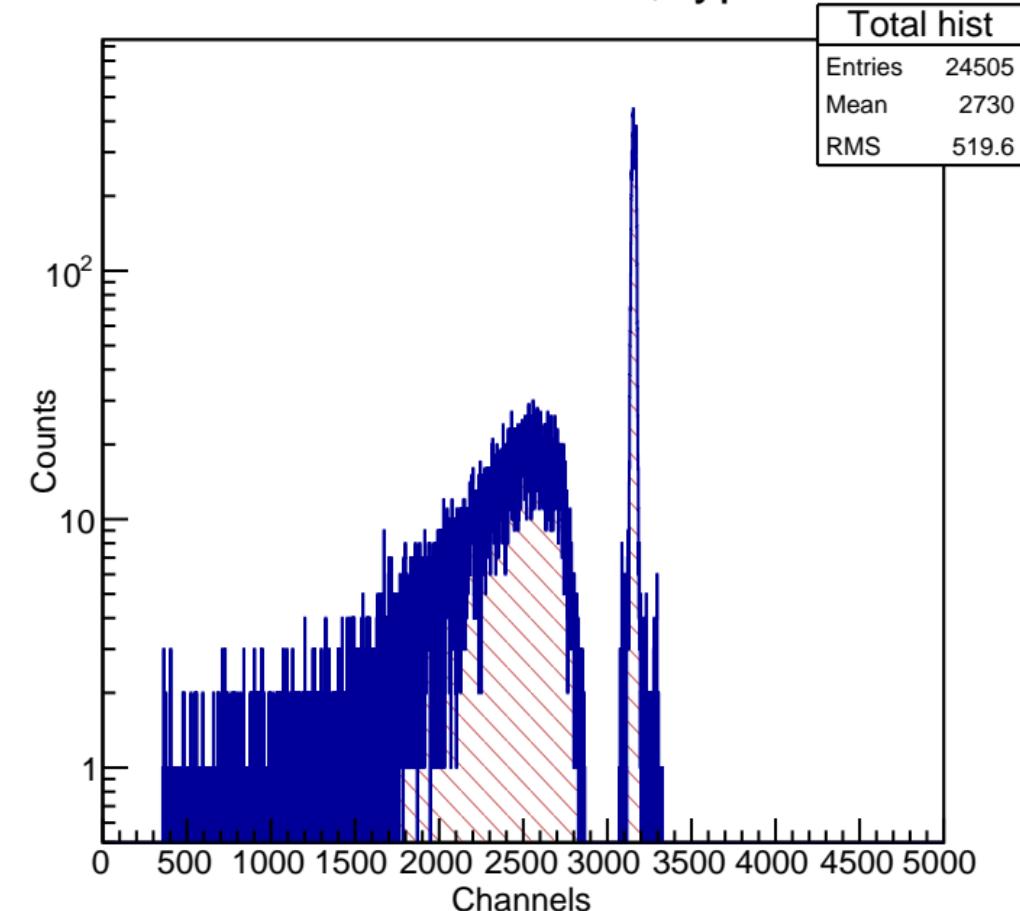
# TDCW Octet 116, type1



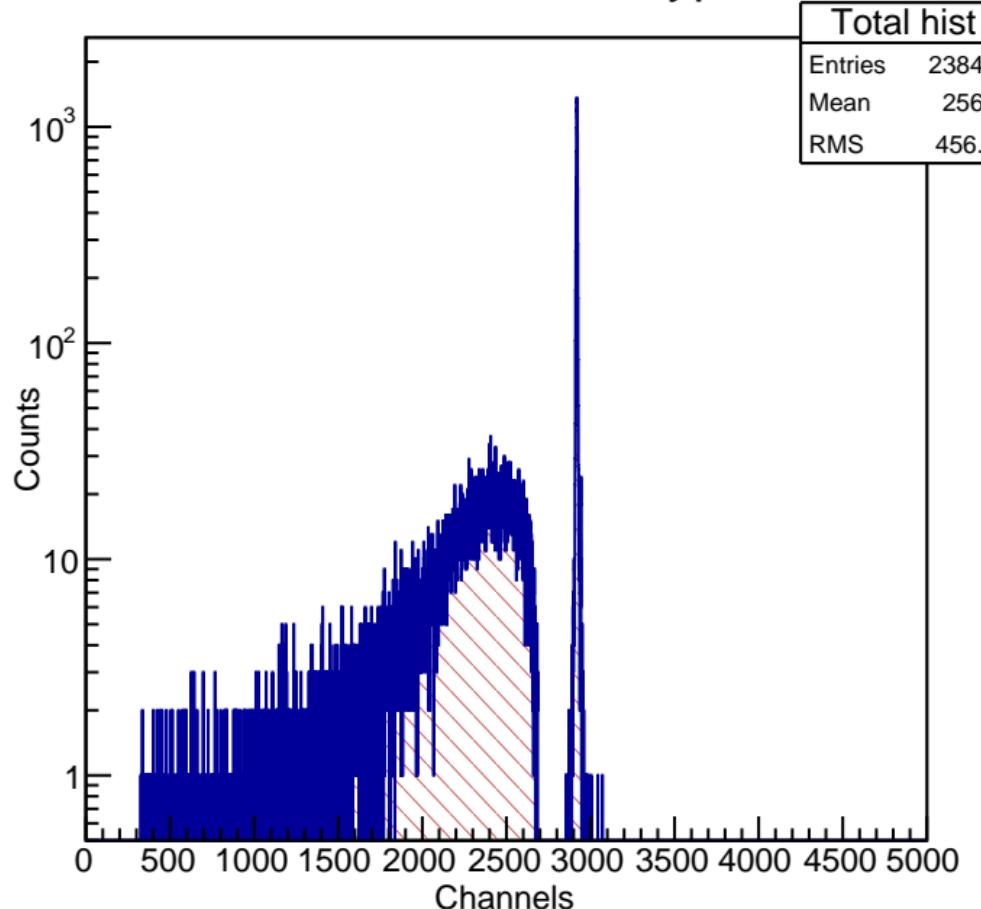
# TDCE Octet 117, type1



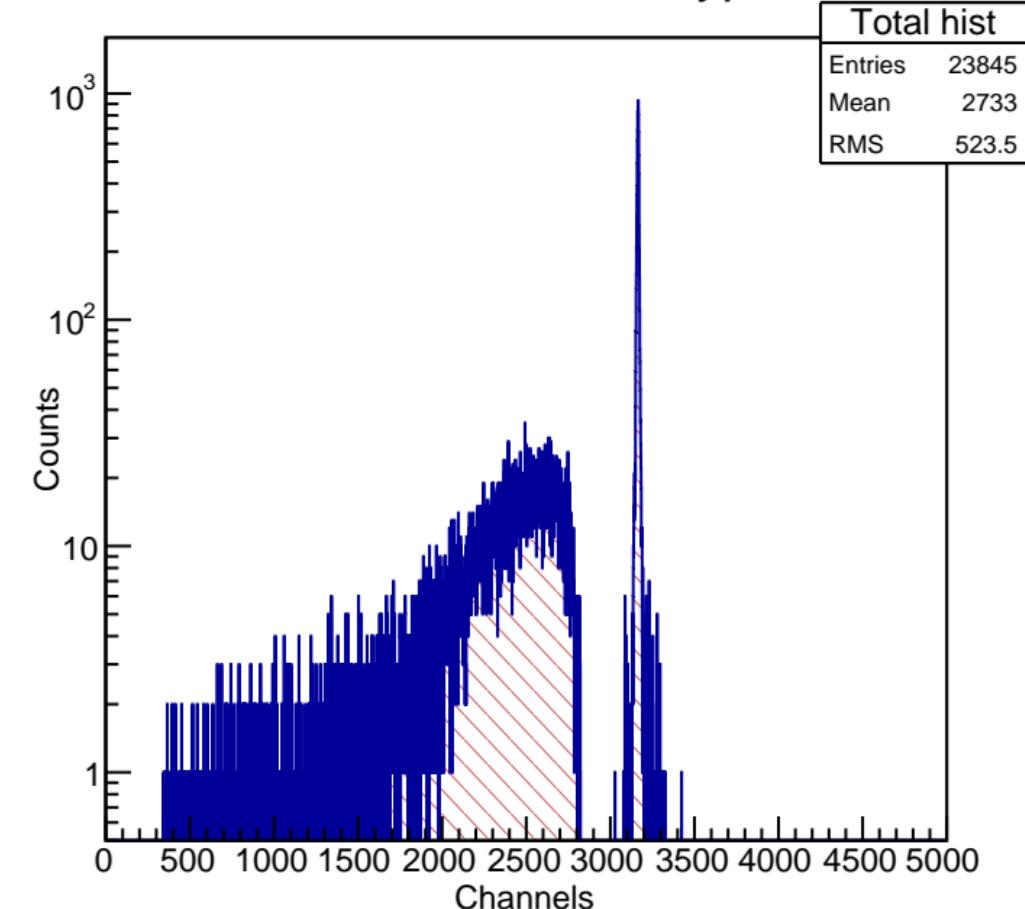
# TDCW Octet 117, type1



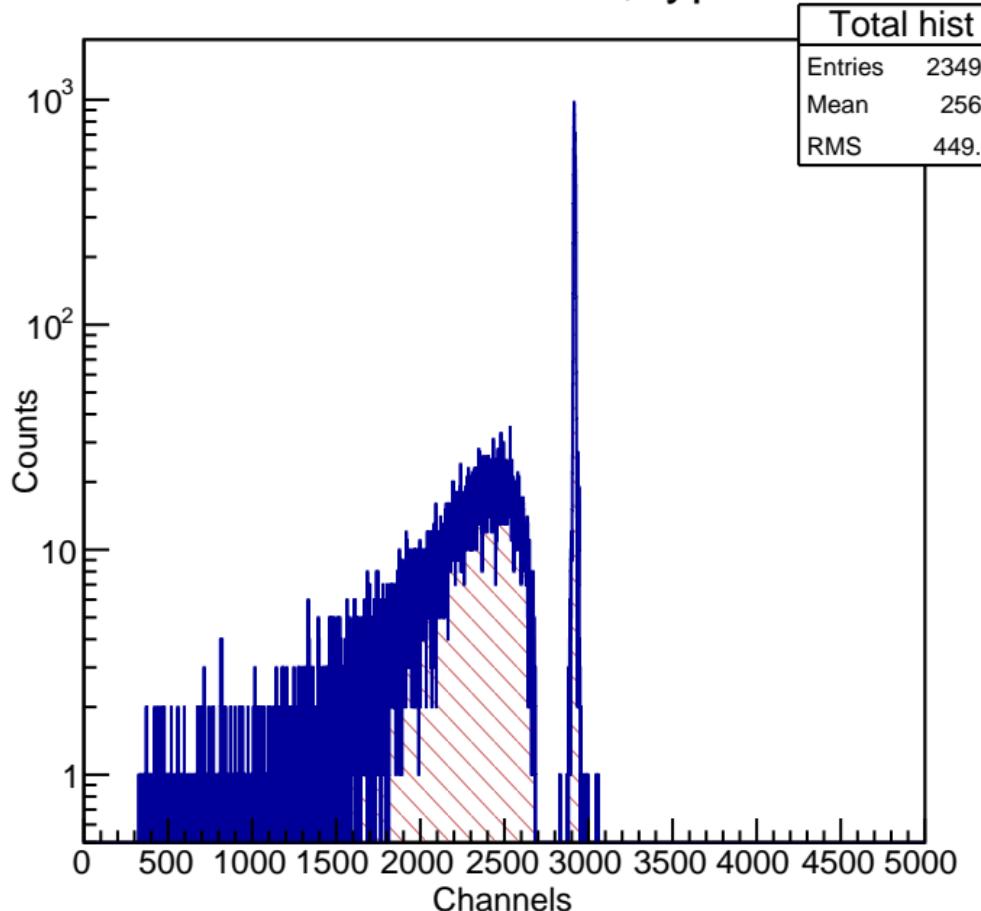
# TDCE Octet 118, type1



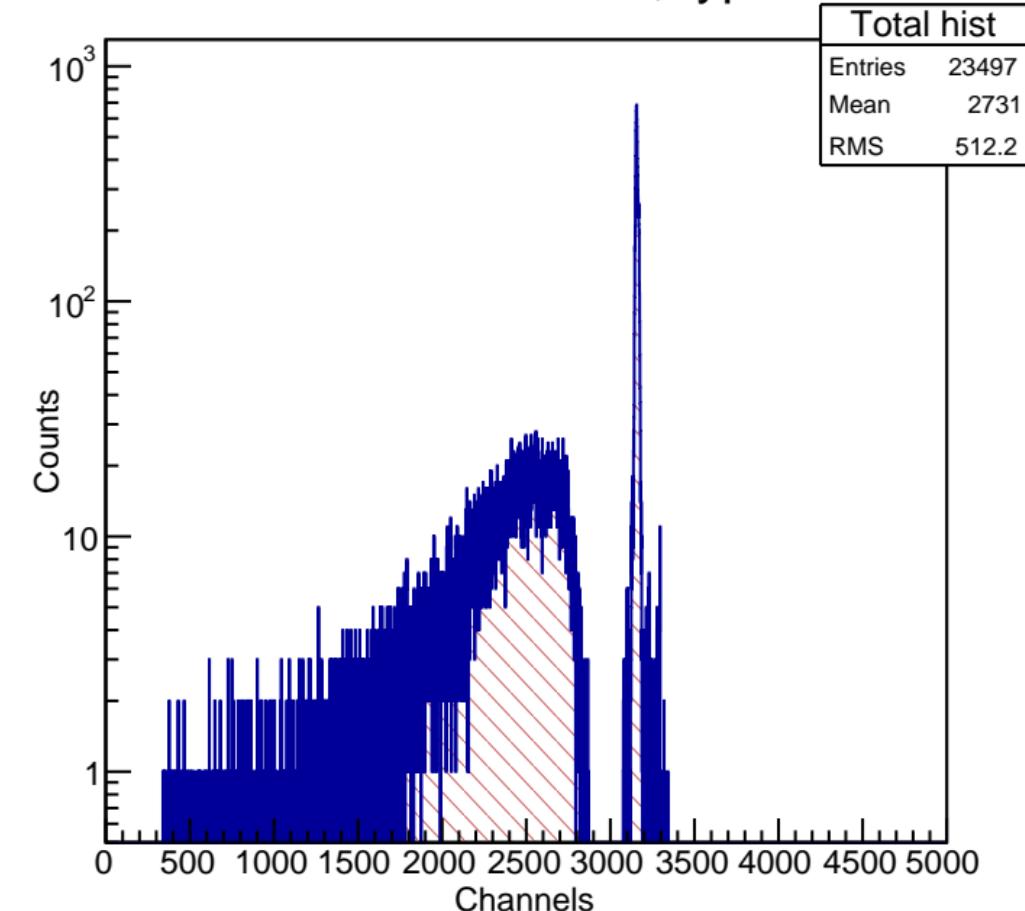
# TDCW Octet 118, type1



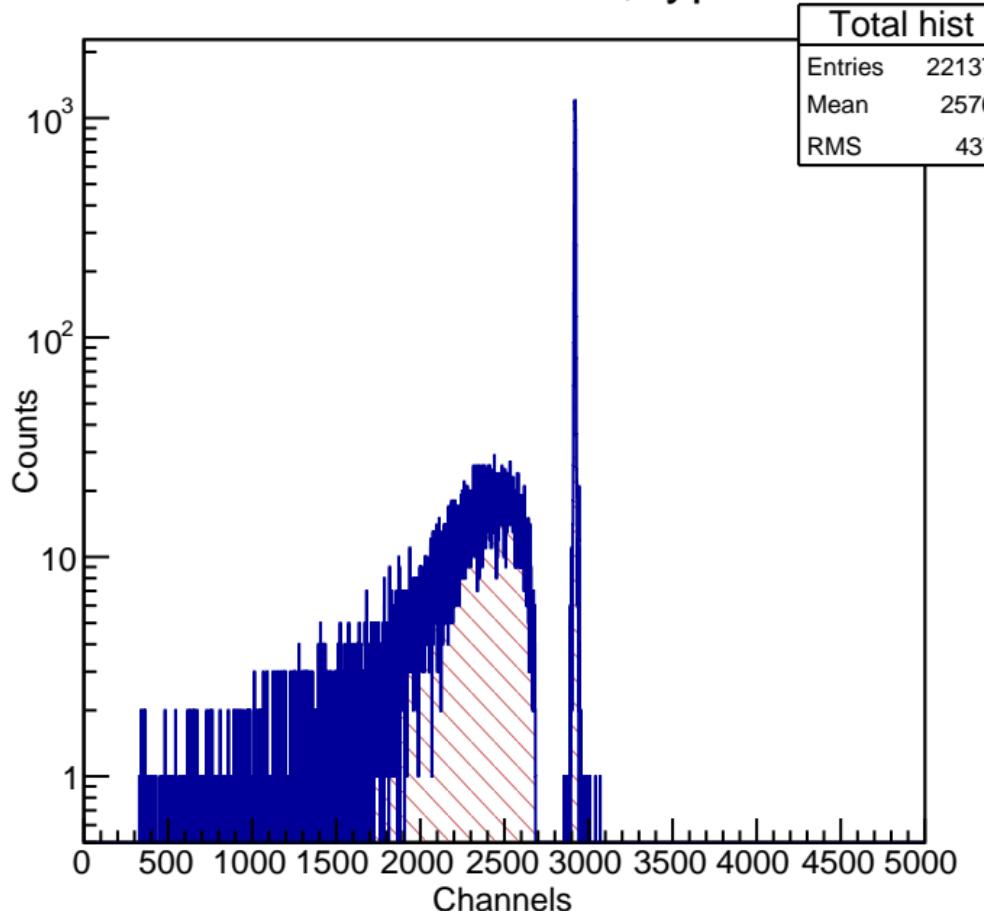
# TDCE Octet 119, type1



# TDCW Octet 119, type1



# TDCE Octet 120, type1



# TDCW Octet 120, type1

