

## **Zhaoyuan Liang**

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### **OBJECTIVE**

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To pursue an Entry level Full-time position in the field of Electrical Engineering specifically in electrical or electronics system, circuit, and HW platform design.

### **SUMMARY**

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Recent graduate with MS in Electrical Engineering and excellent background in circuit design. Experienced in IC design skills and hands on laboratory testing/debugging skills. Reliable, hardworking, highly-motivated and dedicated team player works well under pressure and multitasks.

### **EDUCATION**

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**University of Florida (UFL)**

Gainesville, FL, U.S.A

**Master of Science, Electrical and Computer Engineering, GPA (core courses) 3.63/4.00**

Aug. 2012– May. 2014

- Research divisions in Electronic Circuits Design and Signals & Systems

**Tsinghua University (THU), ranked #1 University in China**

Beijing, China

**Bachelor of Science, Electrical Engineering**

Sept. 2008– Jun. 2012

### **TECHNICAL SKILLS**

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**Programming Language:** C, C++, VHDL, Verilog

**Software and Tools:** Xilinx, Quatus II, SPICE, Matlab, Cadence, Microsoft Office Suite, AutoCad

### **SCHOOL PROJECT**

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- **Thesis Project: Synchronous Intelligent Terminal Utilizing in Smart Substation** *Sept. 2011– Jun. 2012*  
Managed a development team to research on industry standards, designed and implemented the Intelligent Terminal based on ARM and FPGA processor independently, specially achieved innovative communication features. Developed hardware system with schematic layout skills, and programmed with description language C and Verilog. The product was released in Sep.2012 and still in service.

### **INTERNSHIP EXPERIENCE**

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**Intern, COAMCI, U.S.A**

*Jul.2014–Feb.2015*

- Assisted Manager from China to create an initial investment team in North America and started up initial assessment by travelled and investigated on estate and other investment projects in several cities nation wide

**Intern, Symmetricom (Acquired by Microsemi), Beijing, China**

*Jun.2013–Aug.2013*

- Resolved problems in power supply module of a GPS synchronization device applied in subway systems with FPGA embedded system by utilizing PCB soldering and laboratory testing skills
- Volunteered in verification, simulations, and debugging of circuit and logic designs(schematics, analog, RTL) and write verification specifications, verification plans, and documentations

**Intern, XJ Electric CO., LTD (Major electrical equipment manufacturing in China),**

Xuchang, China

*Jun.2011–Sep.2011*

- Negotiated to Ultra High Voltage Transmission project team as Chief Engineer Assistant and assisted with calculations, preparing spreadsheets, updating project status and other detailing works

**Intern, Xi'an Electric Manufacturing Corp, (Major manufacturer of transformers in China)**

Xi'an, China

*Jul. 2010–Aug.2010*

- Operated 110kV circuit breaker under the direction of senior engineers
- Handled AC/DC circuit and power supply circuit testing/debugging