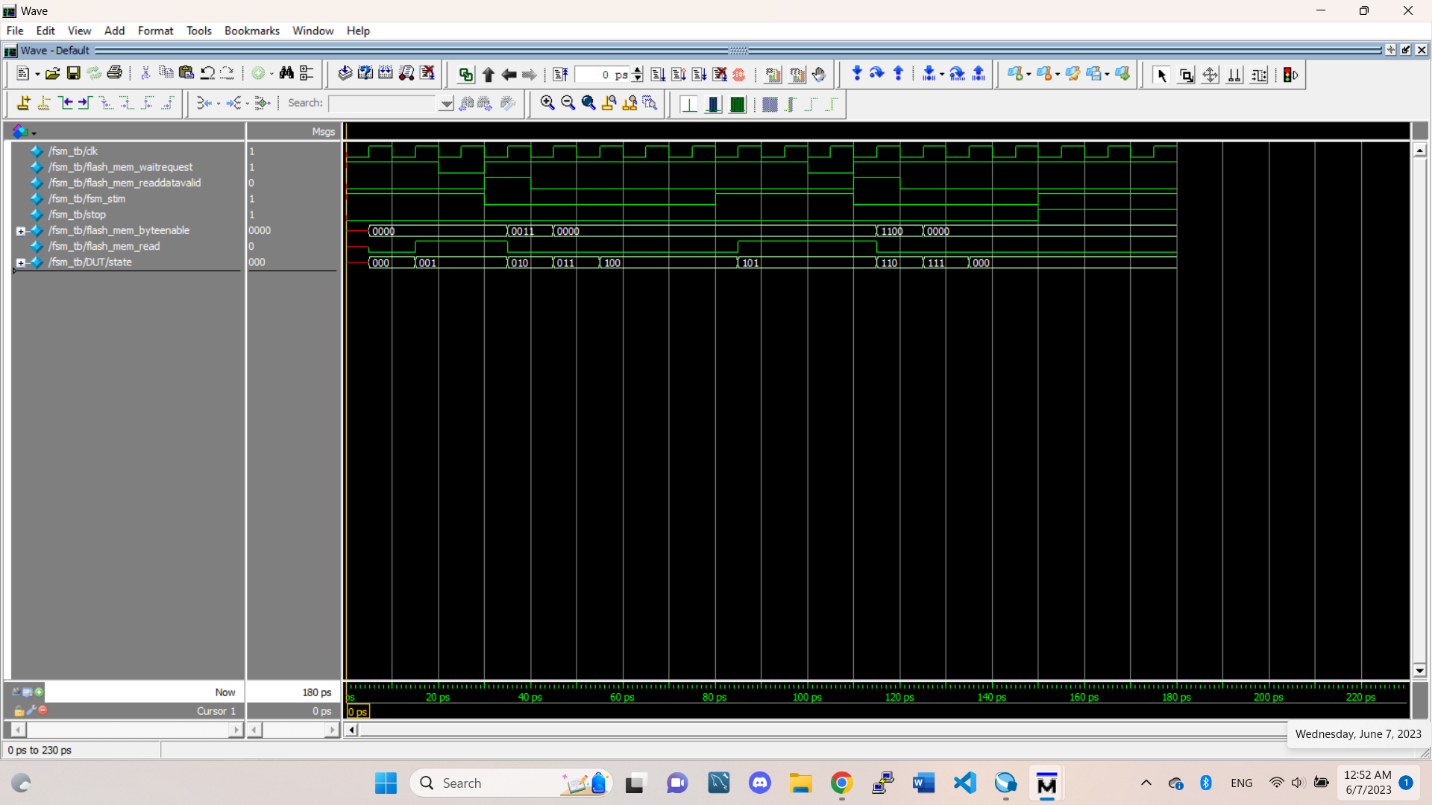
Xuan Tung Luu - 30236798 - README

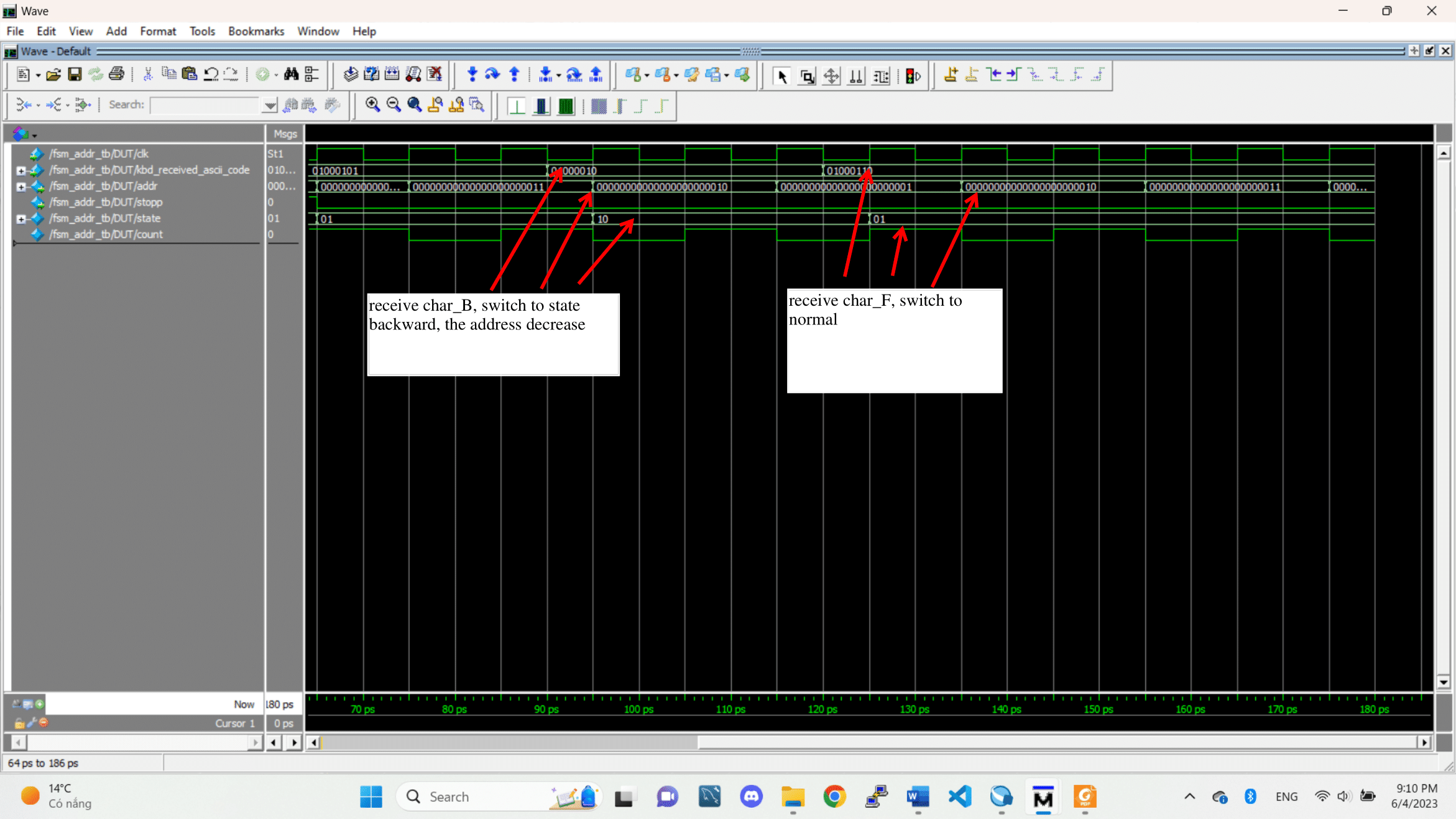
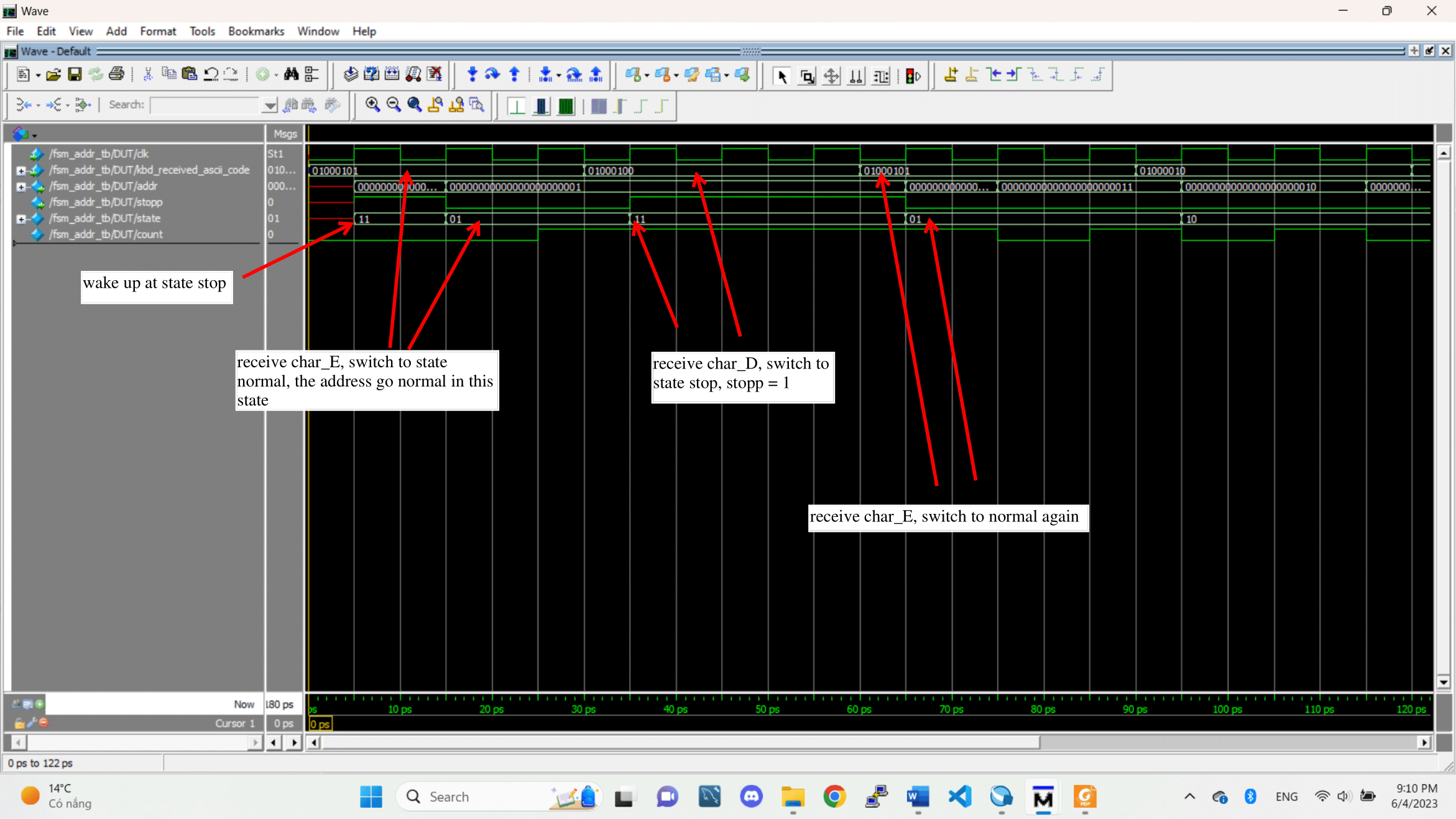
1. My SOF file is located at xuan\_tung\_luu\_30236798\_Lab\_2/rtl/simple\_ipod\_solution.sof

2. Everything works.

3. I have 2 FSM. The first one is to control the signals such as flash\_mem\_waitrequest, flash\_mem\_readdatavalid, flash\_mem\_byteenable, flash\_mem\_read to interact with the flash. This first module is called fsm. The other FSM controls the address, also sending signal to the first one if the music needs to stop. This second module is called fsm\_addr.

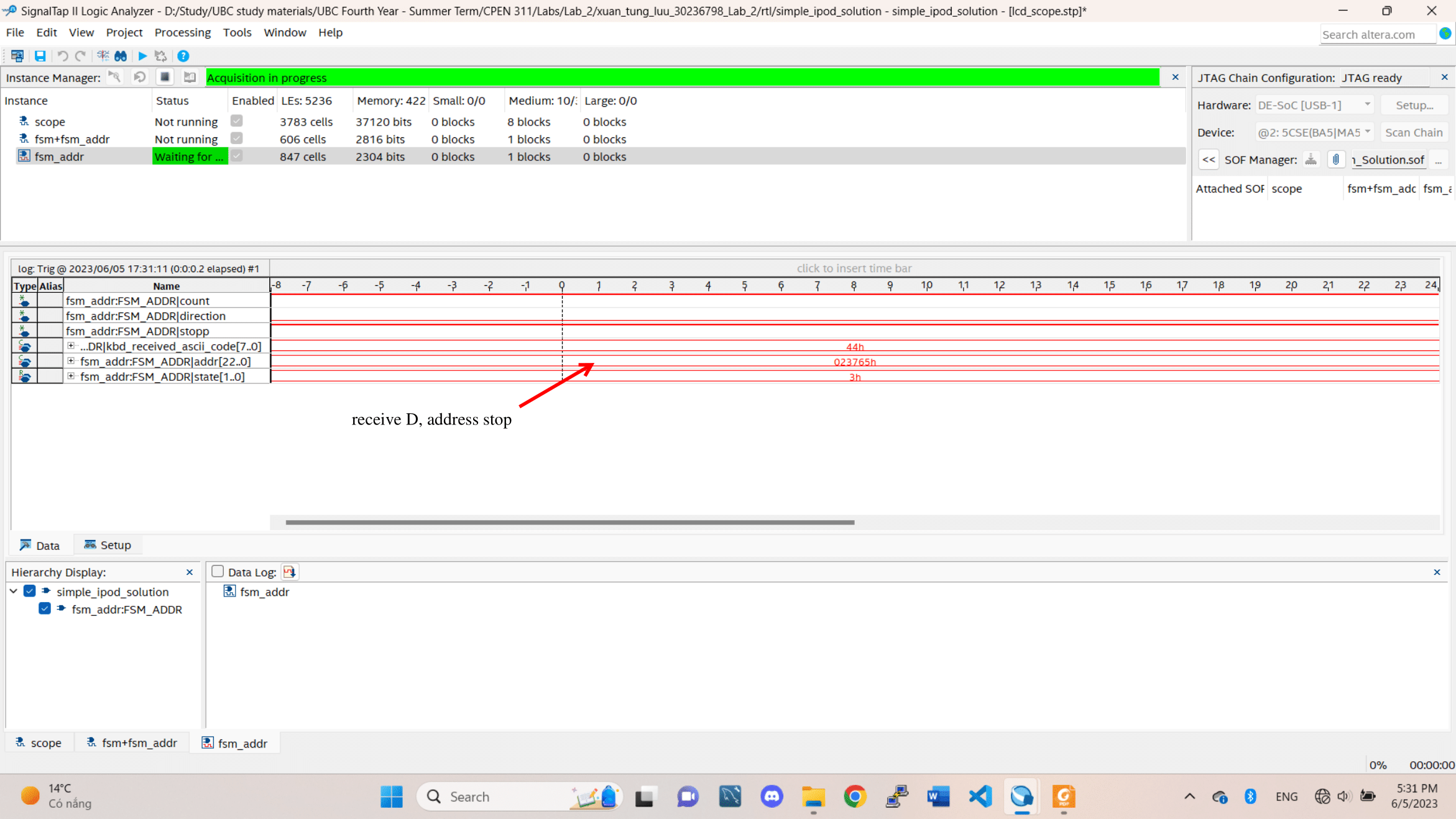
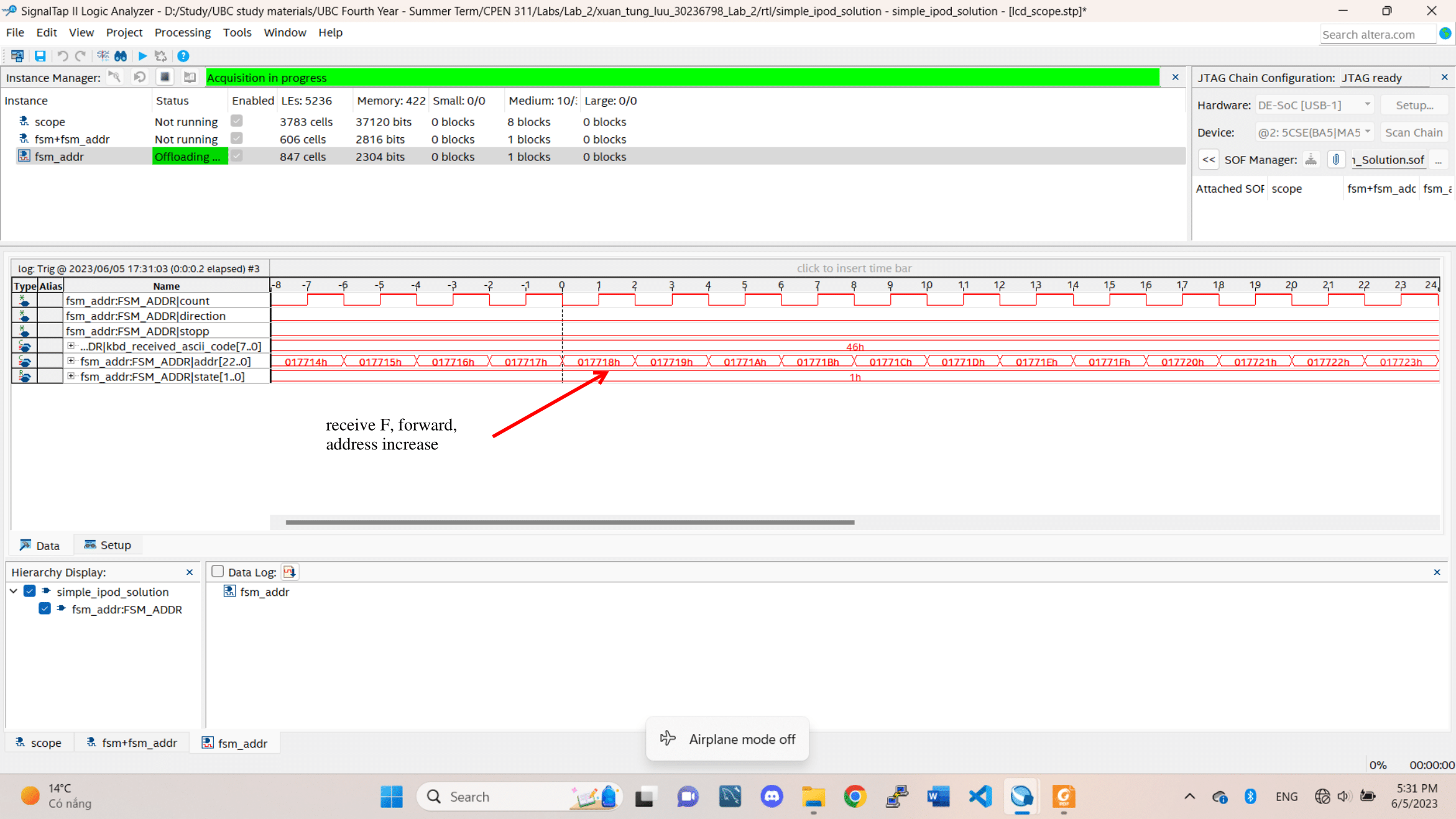
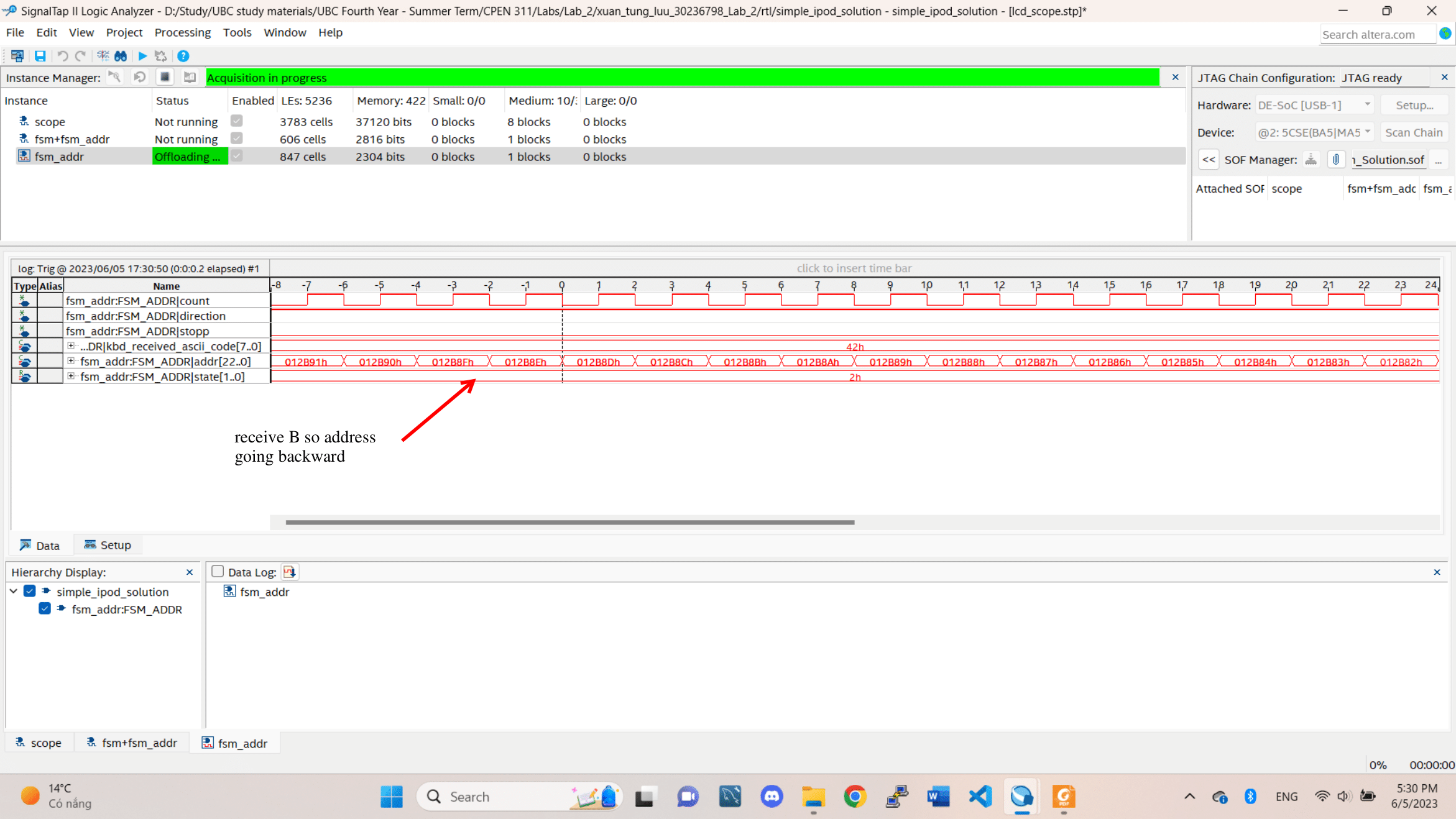
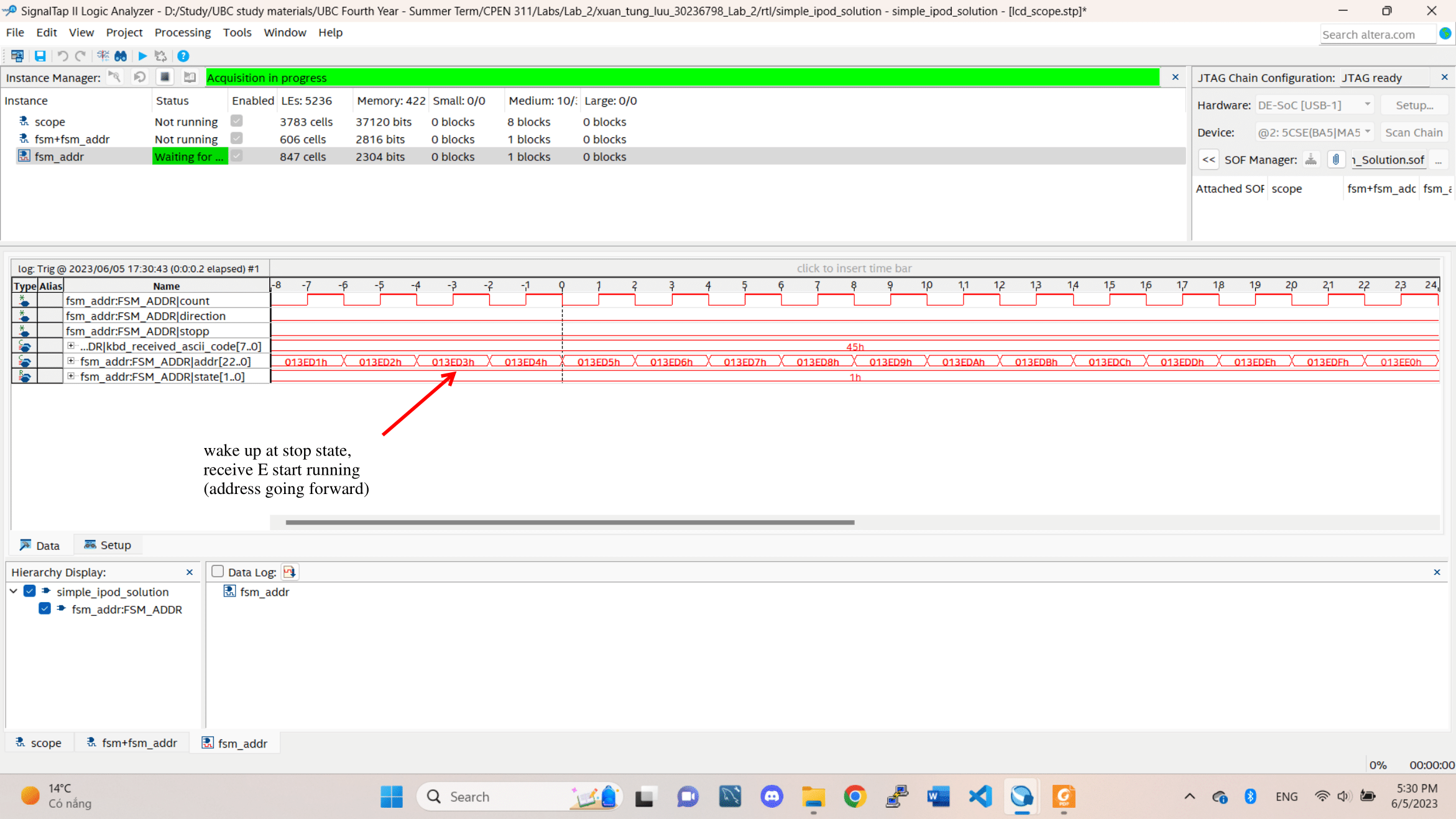


FSM for control signals with FLASH

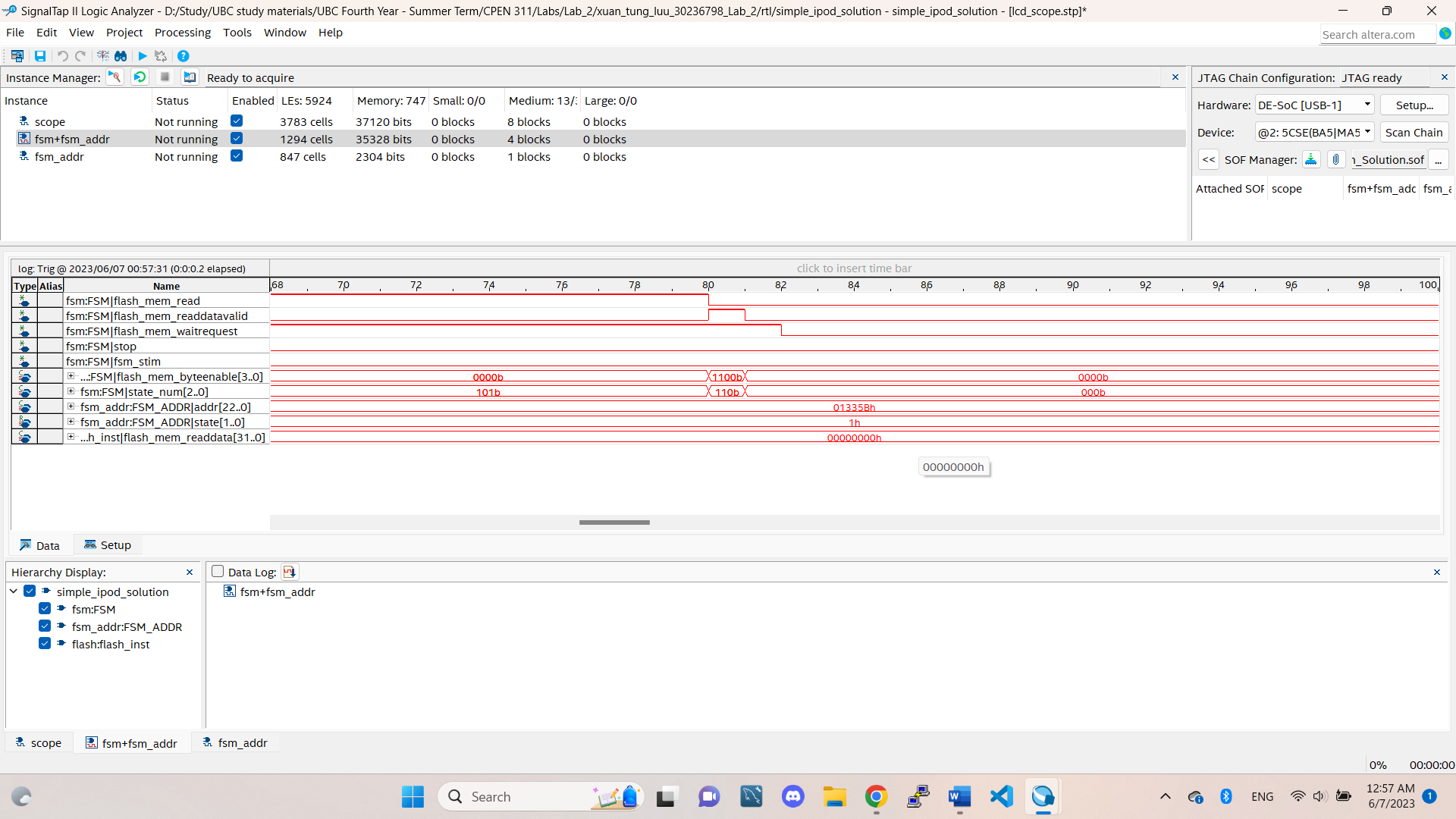


2 pictures of FSM controlling the address

4.Signal tap



FSM for controlling address



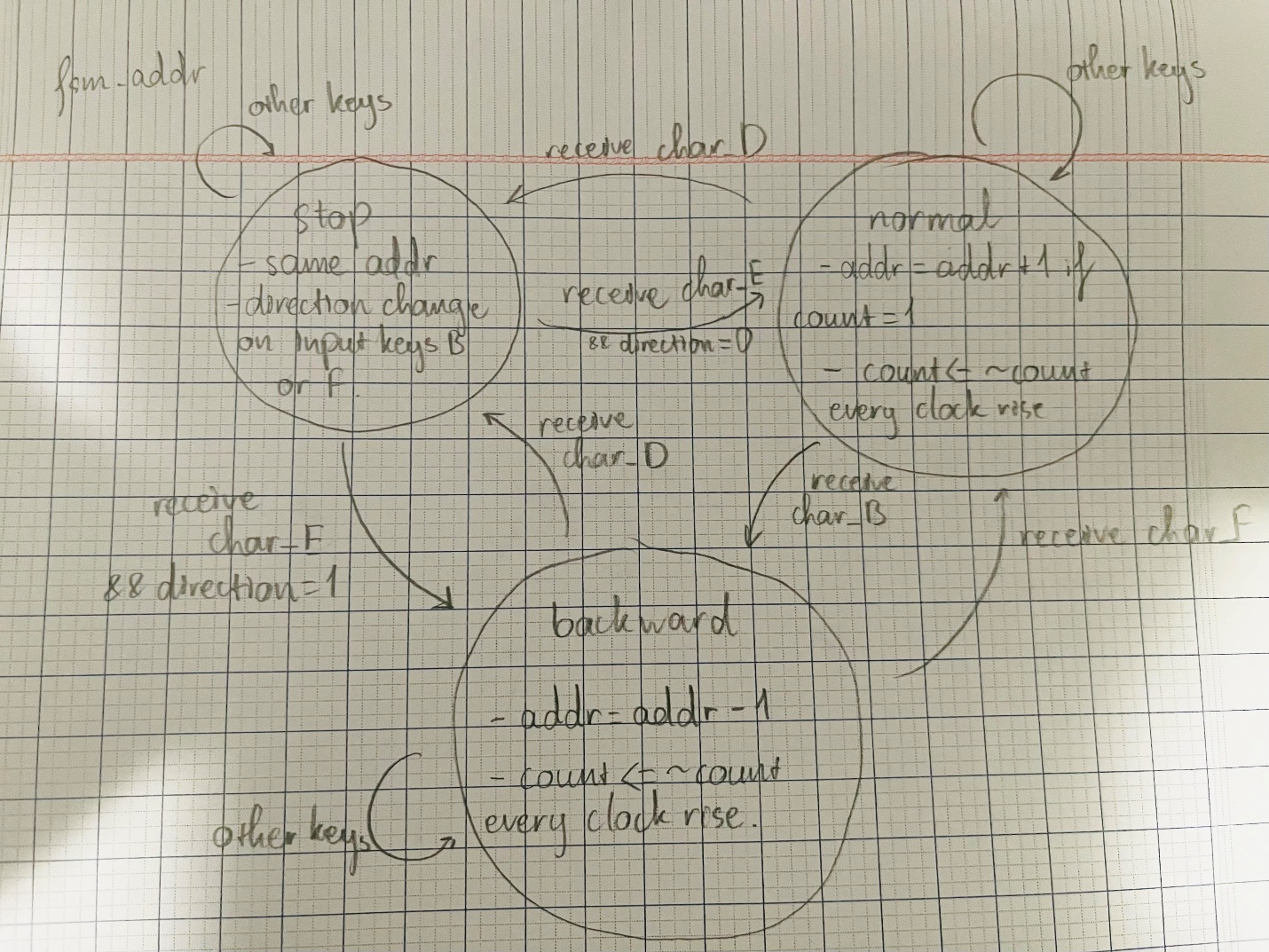
FSM for signals with FLASH

5. I use modelsim to run the simulation. There is a wave.do file in the same folder as the project.

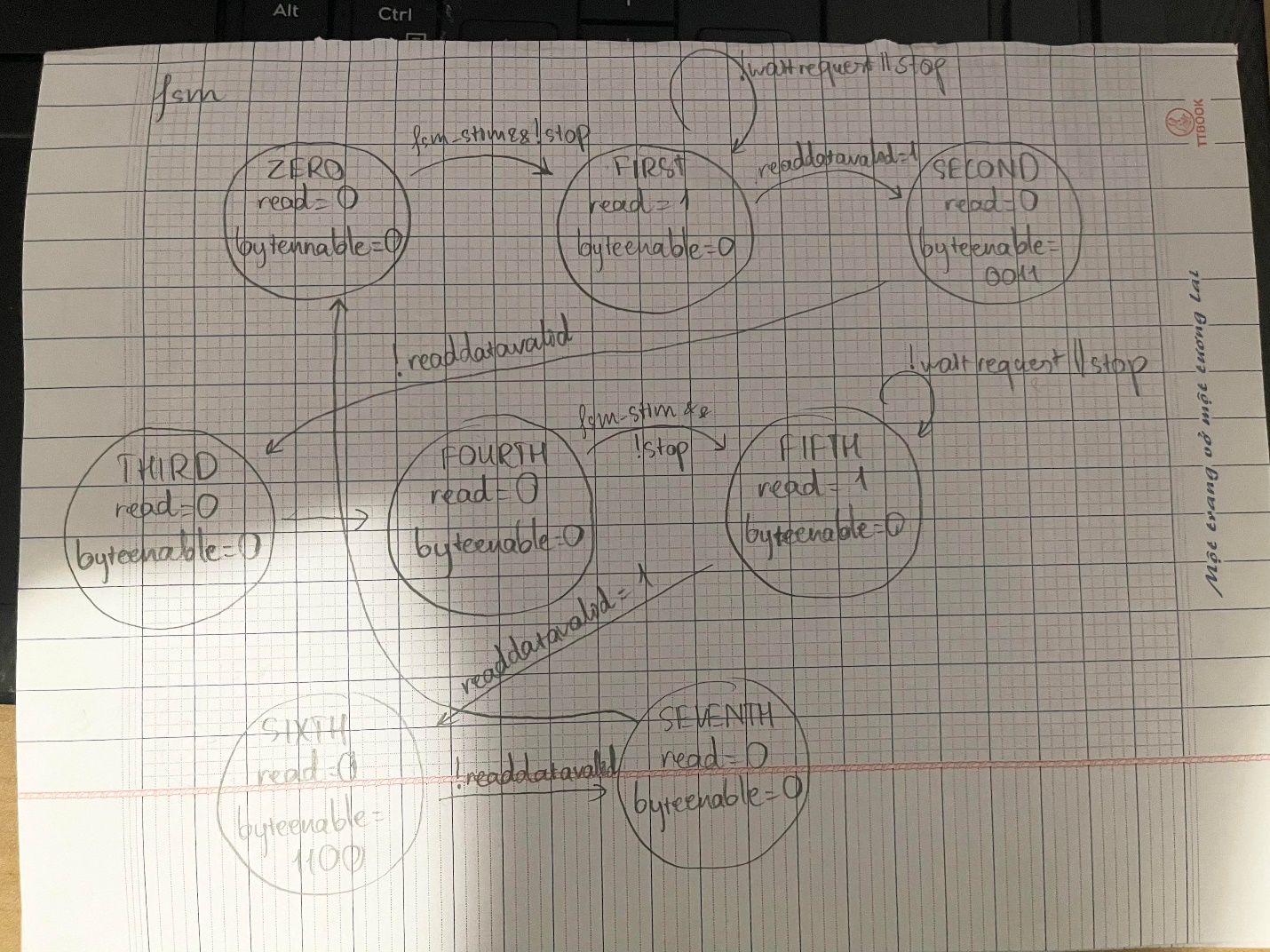
The modelsim project for the first FSM (fsm) is located at: xuan\_tung\_luu\_30236798\_Lab\_2/sim/fsm/fsm.mpf

The modelsim project for the second FSM (fsm\_addr) is located at: xuan\_tung\_luu\_30236798\_Lab\_2/sim/fsm\_addr/fsm\_addr.mpf

6. State diagram



State diagram for fsm\_addr (control address)



State diagram for fsm (for control signals to FLASH)